## EV/FiVDA <br>  <br> SEPTEMBER 1986

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10 - neon yaves- make good night


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 -25 watt pats 8 ofh
-25 watt pots 10009
- wire wound pots - 18, 33,5



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- heating paxa 200 warts mins

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 $1-634$ Dimm 10 wart speater and $3^{3}$ tweeter

- pliot bubse $8 . .5 .3$ A. Philips


- $12 Y$ dip proof reity -idieal foc car iot ios

13A tused and switched spurf for surface mounting ox can bee removed trom box








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enoush to Grive a rotatio arial or a tumbier for polishing stors
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$2 P 69-1250 v-0-250 \mathrm{vv} 60 \mathrm{~mA} \& 86.3 \mathrm{v} 5 \mathrm{~A}$ mains transformer +50 p post
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## VOL 15 No. 9 SEPTEMBER '86

## EVERYDAY <br> ニLEC ROMCS 

## ISSN 0262-3617

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COMMENT . . POPULAR FEATURES

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SIMPLE PRINTER BUFFER by W. Hunter
464
Allows continuation of data entry whilst previous info is printed out
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Keep your assets frozen
ELECTRONIC CANDLE
An "Exploring Electronics" project
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DIY KIT SERVICE

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Cyoervox (EE (Peverb (PE*
Cho-Reverb (PE*)
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Flanger
Frequency Doubler
Fuz (Smooth)
Guitar Overdfive
Hand Clapper
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Mixer 4ch Mono Simpl
Mixer 4ch Stereo (PE*)
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Noise Gate (PE*)
Sustain
Tone Control
Treble Boost
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# IIFRRA-RED BEAM ALARM 



## R.A.PENFOLD

# A simple alarm unit with various security applications 

IN THE late sixties and early seventies broken beam infra-red alarms were popular in television spy series and the like, usually with the hero defeating the alarm by avoiding the beam or shining an infra-torch onto the receiving sensor. Infra-red alarms are no longer the technological marvels that they once were, and are now regarded as relatively mundane pieces of electronics. They have moved on from those early days though, and are no longer restricted to use over very short distances, such as across a corridor. It was this short operating range which made the early types so easy to defeat. Although an infra-red beam is invisible to the human eye, the transmitting and receiving devices are not, and can be difficult to conceal really effectively. Once an intruder has detected the presence of the beam it is not difficult to avoid it.

Using modern devices and techniques it is possible to produce designs which will operate over much longer distances. In fact systems of this type are no longer restricted to spanning a doorway, or even a room, and they can be used out-of-doors over quite long distances if desired. Units of this type are often referred to as "infra-red fences". The system described here is designed to operate in conjunction with an existing burglar alarm system, and it can be constructed at quite low cost. An operating range of at least ten metres is possible, and by simply modifying the value of one resistor in order to increase the output power of the transmitter it is possible to boost the range to 50 metres or more.

## OPERATING PRINCIPLE

The obvious arrangement for a broken beam alarm is to have a light source aimed at a suitable sensor, with a simple circuit to detect the presence or absence of any significant output from the sensor. In practice this does not work very well as it is difficult to provide a strong enough light beam to reliably switch the sensor between two distinct output levels. The infra-red beam tends to become swamped by the background infra-red level.
The easiest way around this problem is to use a set-up of the type outlined in the block diagram of Fig. 1. Instead of using a


Fig. 1. Block diagram of the IR Beam Alarm.
continuous infra red beam, the special l.e.d. which generates the infra-red signal is pulsed on and off at a fairly high frequency (usually in the range 1 to 100 kHz ). The l.e.d. is driven from the pulse generator via a buffer stage which enables high l.e.d. currents to be provided. The drive signal is not usually a squarewave, but instead a short pulsed waveform is used. The general idea is to have the l.e.d. switched on for (say) ten per cent of the time so that a very high l.e.d. current can be used, but the average l.e.d. current is kept down to a safe level. For instance, with the l.e.d. pulsed at 500 mA with a signal having a 1 to 9 markspace ratio, the average l.e.d. current would only be 50 mA (which is quite safe for a large 1.e.d.).

There are several types of device which could be utilized as the sensor at the receiver, but the most common type for this application is a large area photodiode. These offer good sensitivity plus a reasonably wide bandwidth. This second factor is an important one as the received signal is not a continuous beam, but a series of narrow pulses. Although the pulses might be at a frequency of (say) 10 kHz , the narrowness of the pulses results in most of the
component frequencies being at much higher frequencies, and the system must have a bandwidth of at least ten times the fundamental input frequency in order to respond to the input pulses properly and make full use of the power economy at the transmitter.

Although reasonably sensitive, the output signal from the sensor diode is not likely to be particularly strong, and it could well be less than a millivolt r.m.s. However, as it is a pulse signal it is easily amplified to a more suitable level using ordinary audio type amplifier stages. Because a.c. coupling can be used there is no problem with drift or changes in the ambient infra-red level causing spurious triggering of the unit.

The amplified signal is rectified and smoothed to produce a d.c. signal that is roughly proportional to the strength of the received signal. When the beam is not broken, the received signal is strong and a strong d.c. output is produced by this circuit. This signal is used to drive a d.c. amplifier which in turn activates a relay. If the beam is broken, the d.c. signal rapidly subsides and the relay switches off. A pair of normally open relay contacts are wired into the burglar alarm circuit. Although the



Fig. 2. Simple lens system.
contacts are called normally open types, in this circuit the relay is normally switched on, and it cuts off when the beam is broken The relay contacts are thus effectively normally closed types, and are wired into the usual loop of normally closed switches in the main alarm circuit. This arrangement is fail-safe in that a failure in the alarm circuit will almost certainly result in the relay switching off, and the main alarm being activated.

## OPTICS

Although the range of a system of this type is much greater than a simple d.c. circuit, it is still difficult to obtain reliable operation at much more than two or three metres. It might be possible to refine the circuits to obtain some improvement, but a much better way of tackling the problem is to add an optical system. By focusing the output from the transmitter into a tight beam the usual fall off in strength as the range is increased is avoided. In practice there is some reduction in beam strength as range is increased due to imperfections in the focusing system and consequent spread of the beam, and the fact that air is not totally transparent. Even with quite a simple and inexpensive lens to focus the beam this can boost the range of the system by a factor of five or more. A lens at the receiver can be used to further boost the range by a similar factor.
Ordinary glass lenses, mirror lenses, or a combination of the two can be used in this application, but in its most basic (doubleended) form the lens system is as shown in Fig. 2.

At the transmitting end a plano-convex lens is used to focus the output from the l.e.d. into a narrow beam. Optically the receiving end of the system is just the opposite of the transmitter, with another plano-convex lens gathering up the output from the transmitter over a relatively large area, and then concentrating the received energy onto the infra-red sensor diode. For optimum results the l.e.d. and detector diode must be quite accurately aligned
behind their respective lenses, and they must be the correct distance from their lenses. The optimum spacing is equal to the focal length of the lens. You do not get something for nothing in the world of physics, and the price that has to be paid for the improvement in range is the highly directional nature of both the transmitter and the receiver. The practical result of this is that the system has to be carefully set up and mounted in a stable fashion if it is to function at all; but optical alignment of the equipment is not excessively difficult.
In the past there has been a problem for the home constructor with an arrangement such as this in that it has been difficult to obtain suitable lenses, and they have been quite expensive once located. Fortunately, suitable plastic lenses can now be obtained at low cost, making a project of this type perfectly feasible as a constructional project. Although in normal optical terms the quality of the lenses is, to say the least, extremely poor, image quality is unimportant in this context. The lenses need do nothing more than roughly focus the infrared signal in order to give a large improvement in performance

## TRANSMITTER CIRCUIT

The transmitter uses few components, as will be apparent from the circuit diagram which appears in Fig. 3.
1 Cl forms the basis of the unit, and this is the low power CMOS version of the well known 555 timer. It operates here in a form of astable (oscillator) circuit, but it is not quite the standard configuration as steering diode DI has been included. Normally this type of circuit operates by first charging timing capacitor C2 to two-thirds of the supply potential via both R1 and R2, and during this time the output at pin 3 goes high. C 2 is then discharged by way of R 2 and an internal switching transistor of the integrated circuit until the charge falls to one-third of the supply voltage. The cycle then starts again with C2 charging via R1 and R 2 . The output of ICl goes low during the discharge period.
In Fig. 3, TR 1 is a VMOS transistor, and it switches on the infra-red l.e.d. D2 when the output of ICl goes high and forward biases TRI's gate. In the standard 555 astable circuit the period during which the output goes high is longer than the time it spends in the low state, since C2 charges via both R1 and R2, but only discharges through R2. This is the opposite of what we require, as it results in the l.e.d. being switched on for more than 50 per cent of the. time, rather than the ten per cent or there-
abouts that is required. Diode Dl provides a simple solution by bypassing R2 during the charge portion of each cycle. The charge time is then controlled by R1, and the discharge time is determined by R2. By making $R 2$ about ten times higher in value than R I the required output waveform with a mark space ratio of about 1 to 10 is obtained. The oscillator operates at a frequency of about 3 kHz .
Little current is consumed by the oscillator circuit, and TRI requires no significant drive current. The current drawn from the battery supply is therefore little more than the average l.e.d. current, which comes to just over one milliamp. This low level of power consumption makes battery operation a practical proposition even if the unit is left operating for long periods of time, which is quite likely in a burglar alarm application. Each set of HP7 size cells should be capable of powering the unit continuously for well over a month, or using the unit around ten hours a day the batteries would only need replacement at approximately three monthly intervals.

## RECEIVER CIRCUIT

The full circuit diagram of the receiver unit is shown in Fig. 4. D3 is the detector diode, and this is reverse biased by R4. A small leakage current flows through D3, and the leakage level is dependent on the infrared level received by D3. The pulses of infra-red energy from the transmitter therefore cause small pulses or current to flow through R4 and D3, generating small voltage variations which are coupled by C4 to the input of the high gain amplifier. The latter is a straightforward three stage common emitter circuit which has capacitive coupling between all three stages. The full gain of all three stages is not required, and R12 introduces a large amount of feedback to TR4 in order to reduce its voltage gain to a more suitable level. The voltage gain of the circuit is still well in excess of 80 dB ( 10,000 times). The detector diode incorporates an infra-red filter which largely prevents problems with ambient light sources swamping the received signal. Tungsten lamps powered from the mains can generate a significant 100 Hz infra-red signal, but the coupling capacitors in the amplifier have been given low values so that they provide some attenuation at 100 Hz and reduce the risk of mains lighting holding the unit in the unactivated state.
Socket SK1 enables the output of the amplifier to be monitored using a crystal earphone or high impedance headphones. This is helpful when setting up the sytem as

Fig. 3. Transmitter circuit diagram.


Fig. 4. Receiver circuit diagram.

it enables the strength of the received signal to be gauged, making it much easier to set up the sytem for optimum reliability.

The output from TR4 is coupled to a conventional rectifier and smoothing circuit. The value of C 9 controls the time taken for the d.c. signal to decay to the point where the relay cuts off once the beam has been broken, and this time must be kept to just a fraction of a second so that the relay always cuts off before the person breaking the beam moves on and the signal is restored. On the other hand, a very short decay time could reduce reliability and cause spurious triggering of the unit. A decay time of about 100 ms seems to be a good compromise.

It is not possible to design the unit to have a very low stand-by current comsumption as the relay will remain activated except for any brief occasions when the beam is
broken. However, a relay which has a very low current consumption can be used, and the fact that the voltage and current ratings of the contacts are very low is not a problem in this application where only minute power levels are involved. The total current consumption of the unit is about ten milliamps or so. This will give around 100 hours of operation from HP7 size batteries, and rechargeable Nicad cells probably represent the most practical power source. Alternatively, four HP2 size batteries would be capable of powering the unit continuously for about one month, or for over two months with around ten hours use per day. Of course, if preferred, either or both units can be powered from mains power supply units that supply a well smoothed output at around five to six volts.

Note that protection diode D6 is an integral part of the relay which is a minia-

## 



TRANSMITTER
Resistors

| R1 | 100 k |
| :--- | :--- |
| R2 | 1 M |
| R3 | 390 |

All 0.25W 5\% carbon
Capacitors

| C1 | $100 \mu$ radial elect. 10 V |
| :--- | :--- |
| C2 | 470 p ceramic plate |

Semiconductors

| IC1 | ICM7555 CMOS timer |
| :--- | :--- |
| TR1 | VN10KM VMOS |
|  | transistor |
| D1 | 1N4148 silicon diode |
| D2 | TIL385mm IR l.e.d. |

## Miscellaneous

S1 SPST miniature toggle switch
B1 6 volt $(4 \times \mathrm{HP} 7$ size cells)
Plastic case about $150 \times 100 \times$ 60 mm ; Printed circuit board (available from the EE PCB Service, order code EE536); 30 mm diameter lens with 80 mm focal length (Maplin); 8 pin d.i.l. i.c. socket; battery holder for $4 \times$ HP7 size cells; PP3 style battery connector; wire and solder

## RECEIVER

| Resistors |  |
| :---: | :---: |
| R4 | 22k |
| R5 | 2M7 |
| R6,9 | 10k (2 off) |
| R7 | 2k2 |
| R8 | 2M2 |
| R10 | 1 M 5 |
| R11 | 4 k 7 |
| R12 | 1k |
| All 0.2 | 5\% carbon |
| Capacitors |  |
| C3 | $100 \mu$ radial elect. 10 V |
| C4,5 | $4 n 7$ polyester layer (2 off) |
| C6 | 2 n 2 polyester layer |
| C7,8 | 100n polyester layer (2 off) |
| C9 | $4 \mu 7$ radial elect. 63 V |

Semiconductors
TR2,3 BC549 silicon non (2 off)
TR4,5 BC547 silicon npn (2 off)
D3 TIL100 IR detector diode
D4,5 OA91 germanium diodes (2 off)
D6 Part of relay

## Miscellaneous

S2 SPST miniature toggle switch
RLA 1 5V, 500 ohm coil, 14 pin d.i.!. package with normally open contacts and protection diode. SK $1 \quad 3.5 \mathrm{~mm}$ jack socket B2 $\quad 6$ volt $(4 \times$ HP7 size cells)
Plastic case about $150 \times 100 \times$ 60 mm ; printed circuit board (available from the EE PCB Service, order code EE537); 30 mm diameter lens with 80 mm focal length; battery holder for $4 \times$ HP7 size cells; PP3 style battery connector; 14 pin di.i. i.c. socket; wire and solder, etc.

ture type in a form of 14 pin d.i.l. encapsulation.

## CONSTRUCTION

Starting with construction of the transmitter, details of the printed circuit board are shown in Fig. 5. Although the board may
seem to be rather over-size, it is in fact designed to fit into the guide rails which are moulded into the specified case. It fits into the pair of rails nearest the back of the case with its component side facing forwards. Of course, there is plenty of space on the board for mounting holes, and if a different case is used there should be no difficulty in mounting the board satisfactorily.

The board itself is easy to construct, and although ICl is a CMOS device, it has builtin protection circuits which render the usual anti-static handling precautions unnecessary. It is, nevertheless, advisable to use a socket for IC1. The leads of D2 should be trimmed quite short so that it does not protrude too far above the board. This is important because the distance from the

Fig. 5. Printed circuit board layout and
wiring of the transmitter.

EE58160

?


EE5676

lens to D 2 will be inadequate if the leads are left too long, giving a mediocre level of performance.

The lens is mounted on the front of the case, and it must be accurately positioned directly in front of D2. A large offset would impair the performance of the system and would give a peak response that would be well away from a rightangle to the front panel, making it relatively difficult to set up the finished units properly. A 30 millimetre diameter cutout is required for the lens, and this can be produced by first drilling a central hole of around 10 millimetres in diameter and then enlarging it to the required size using a reamer.

The lens has a rim which enables it to be glued in place on the rear face of the front panel using any good general purpose adhesive, but be careful to avoid smearing adhesive over the main part of the lens. There is a slight problem in mounting the lens in that some of the printed circuit guide rails moulded into the case tend to get in the

way. These can be carefully cut away using a small modelling knife, or the lens can be mounted on the front surface of the case (which will not look as neat).

Switch SI is mounted on one of the end panels, and then the unit is completed by wiring SI to the board and adding the battery connector. If the unit is powered by four HP7 size cells fitted in a plastic battery holder, connection to the holder is via an ordinary PP3 type battery clip.

Construction of the receiver is along much the same lines as that of the transmitter. Details of the printed circuit board are shown in Fig. 6. The leadout wires of D3 are bent at right angles so that its sensitive surface (the large one which does not carry the type number) faces forwards towards the lens. The SFH205 is a suitable alternative to the TIL100, and the sensitive surface of this device is the curved one.

Diodes D4 and D5 are the germanium type, and these are much more vulnerable to damage by heat then silicon devices. Therefore, when soldering these in place the bit should not be kept on the joint for any longer than is absolutely necessary. The relay has a 14 pin di.i.l encapsulation, but the middle three pins in each row are missing. The printed circuit board has all 14 holes drilled so that the relay can easily be mounted in a 14 pin di.i.l. socket if desired.

the tone to be peaked. If the clipping level is reached there will be no further increases in volume if a stronger input signal is obtained, but there will be a noticeable drop in the background "hiss" level
Setting up the sysem over a distance of several metres or more is a little more difficult, and the main problem is ensuring that the transmitter is aimed at the point where the receiver is situated. The receiver only has to be slightly outside the main beam in order to reduce the strength of the received signal to a totally inadequate level. The receiver can be used as a sort of field strength monitor to detect the precise direction of the beam, and the orientation of the transmitter can be adjusted by trial and error to home in the beam on the desired point. Alternatively, where possible it is easier if the transmitter can be aimed in the right general direction, and the receiver is then moved to a point within the beam. Try to get the units set up so that a strong signal is picked up at the receiver, as this gives optimum reliability.
The distance from each photocell to its lens is slightly less than the 80 millimetre optimum, but this only marginally reduces the maximum operating range, and has the advantage of making the system slightly less directional and easier to set up. A reliable operating range of 10 metres or more should be possible, and this can be considerably boosted by reducing the value of R3. This resistor can be made as low as $4 \Omega 7$ in value, giving a pulse current of around ' 500 mil liamps. This will increase the current consumption of the transmitter though (to about 50 milliamps with R3 at $4 \Omega 7$ ), and R3 should not therefore be any lower in value than is really necessary.

## IN USE

Initially the units should be tested at quite short range as they are then quite easilyaligned. However, even at short range the system is still highly directional, and they will need to be aimed accurately in order to obtain proper operation. Things are very much easier if a crystal earphone is used to monitor the output from SK 1, as the fairly high pitched tone from the receiver should be audible when the two units are even badly aligned, and adjustment of the receiver's orientation enables the volume of


# ENTERTAINMENT <br> BY BARRY FOX 

## Comdex

I spent a few days in Georgia talking modems with Hayes; the privately owned US company which has quietly succeeded in bringing some order into the chaos of computer communications and electronic mail. The Hayes protocol has become a de facto standard. More on modems and Hayes protocol in a future month; it's a story on its own. While in Atlanta I visited Comdex, the computer trade's exhibition and conference held at the World Congress Centre. Some snippets are worth passing on.
Film company Polaroid has cleverly stolen a march on the US computer industry with an offer to recover data from floppy discs which have been damaged by misuse. Most magnetic media companies will replace a faulty floppy, but they baulk if the disc has been mistreated. In any case you have still lost the data, which is usually far more valuable than the disc.
Polaroid now offers a Data Rescue Service in America. Anyone who has bought a Polaroid disc and damaged it can return it to the company. At no cost, and usually within 48 hours, Polaroid retrieves as much data as possible, copies it onto a new disc and posts it back to the owner
Actually this isn't all that clever. Floppy discs are only circular sheets of magnetic tape. The trick is to find a solvent which will shift sticky materials from the disc surface but not break down the resin which binds the oxide to the plastics base film. Polaroid, who make the discs-or in the past has bought in from other manu-facturers-is obviously in the best possible position to know the best solvent. As a publicity stunt Polaroid challenged the US computer press to try and find a way of contaminating a disc so that data was irretrievably lost. They tried everything from chilli sauce to ice cream. Polaroid cleaned it all off. The only loss was two per cent of data destroyed when one journalist made holes in the disc by stapling it to the sleeve. So far Polaroid isn't running the scheme in the UK.

## Big Blue Rules

Main impression at Comdex was that IBM rules the roost. Apple weren't there at all. Only Atari and Commodore, with the Amiga, were putting up a struggle against total domination of the PC industry by Big Blue.
The new IBM PC convertible lap-top, which will sell for 2000 dollars, used controversial new technology for the screen. IBM promises 8 hours operation from a single charge of the built-in nickel cadmium batteries. The only way to achieve this is with a passive screen of liquid crystal, which modifies reflected light instead of generating itś own.
The PC convertible screen is a dot matrix display capable of resolving 25 lines of 80 characters each or a graphics image built up from 640 by 200 individual picture elements or pixels. All I.c.d.s suffer from the same problem; the image has low contrast, and the text appears as dark grey
on a light grey background. Room light reflects off the glass front of the screen and degrades contrast even further.

But IBM believes it has solved the problem of glare and stray reflections. Conventional screens use a sandwich of two glass or plastics sheets, a thin sheet to contain the liquid crystal fluid and a thick sheet to protect the thin under-layer. It is the air gap in between which causes odd reflection effects in ambient light. The IBM lap-top screen uses a single layer of transparent. flexible plastics. The I.c.d. fluid backs directly on to this, so the screen is soft to the touch. It deforms when pressed and the image disappears where it is touched. Although this does not matter, because there is normally no need to touch the screen, IBM acknowledges that if someone prods the screen with a sharp object the plastic will puncture and the I.c.d. material ooze out.

## CD ROM

Sony believes that CD ROM, the new technology which relies on a laser-read 12 cm compact disc to store 600 megabytes of data instead of an hour of music, will be a consumer product by the end of the year. Sony began pressing CD ROMs at its US factory in Terre Haute, Indiana, in May. The company has also now started to sell CD ROM drives in bulk to other manufacturers for incorporation in home computers

There are two types of Sony CD ROM drive. The COU 5002 fits neatly into the slot in a computer which is normally used for a floppy disc drive. The CDU 100 is a stand-alone unit which plugs into the rear of a computer to offer an extra source of data. Sony is charging manufacturers a trade price of around 300 dollars for the CDU 5002 and 400 dollars for the CDU 100. An extra circuit board must be fitted inside the computer, but the extra cost to end user should still be well below 1000 dollars. For this premium on the price of a conventional computer, the customer will be able to read data from an optical disc as well as read and record with magnetic discs.

The stumbling block so far has been industry agreement on how a home computer will search through the data stored on a CD ROM. Although there is now a standard called CDI (CD Interactive Media) for the method of recording data on the disc so that it can be retrieved by any player, there is still no agreement on a protocol for searching through the retrieved data. In an effort to break the deadlock with market force and create a standard Sony has signed a joint venture deal with software company Knowledgeset of Monterey, California.
The joint venture, to be called Publishers Data Service Corporation, PDSC, will offer publishing companies a one-stop service. Sony's factory in Indiana will order and format the publisher's data, for instance, an encyclopaedia, and transfer it to digital tape. The same factory will then use this tape to produce a master disc from which CD ROMs are duplicated.

Knowledgeset will provide a retrieval program called KRS (Knowledge Retrieval System) so that a home or business computer can search through the data on the disc. KRS is compatible with any IBM PC. The joint venture promises a four week turnround, from raw data to finished CD ROMs ready for sale. The Indiana plant can press up to 1.5 million discs a month. They can be either music or data discs.

## Towering Inferno

Finally a warning. If you ever visit a Comdex show, don't whatever you do mention the word "fire", let alone shout it.

When the MGM hotel in Las Vegas went up in flames it was during a Comdex show. This year at Atlanta in Georgia the delegates were getting decidedly twitchy by the time they left the peanut state.

Both the Omni and Marriott Marquis hotels in Atlanta have those scarey glass lifts which run on vertical tracks up the outside of the walls. And this year both had fire scares. At the Omni hotel one of the lifts caught fire at 2am. When the fire alarms sounded no one took it seriously, until they started coughing on the kind of smoke which only a burning electric motor can generate.

At the Marquis the next day things were even more dramatic. All three of the lifts serving the 47 floors of bedrooms which overlook the biggest atrium in the world suddenly stopped dead. They then descended at high speed to the bottom floor

Alarm bells rang on each landing and flashing lights told guests to use the stairs Having heard about the Omni smoke they did, and weren't seen again for an hour Later I pieced together what had happened.

Sensibly the hotel designers built smoke detectors into the lift system at engine room level, down in the basement. One whiff of smoke and the whole things shuts down. Unfortunately, the hotel staff like to gather in the basement to puff on a few cigarettes, of one kind or another, and shoot the breeze about life in general.
'They aren't supposed to do it, but they do." a hotel receptionist confided, in between hoping I was having a nice day Once the crafty smokers are caught, the lifts go up again. Unfortunately this is little consolation to anyone who has just started out on a 47 floor hike down the stairs. They do not have such a nice day The Marquis management has not yet thought of a way of telling people on the stairs that the fire is out and the lifts are working. Maybe by next year's Comdex!

Amiga can hook up to a video source with Genlock-but can it compete with IBM?



## Alien zapping freaks! Build your own Custom Joystick

THIs month we shall follow the theme introduced last month by taking a brief look at the RAM chips used in the Spectrum. We shall also be introducing another of the Spectrum's system variables and showing how readers can make their own "custom designed" joysticks.

## RAM chips

From last month's memory map, readers will see that the Spectrum's memory is divided into ROM (addresses from 0 to 16383 decimal) and RAM (addresses from 16384 to 65535 decimal). The latter address space is, in fact, populated by two types of dynamic RAM; the lower 16 K being provided by eight $16 \mathrm{~K} \times 1$ bit chips ( 4116 , or equivalent) whilst the upper 32 K is provided by eight $32 \mathrm{~K} \times 1$ bit chips ( 4532 , or equivalent)

The pin-outs for the Spectrum's RAM chips are shown in Fig. 1. Readers should note that the 4116 (which are now somewhat elderly) require no less than THREE separate supply rails! The 4532 devices are, thankfully, a little more up to date and only require a single +5 V rail. The reason for the use of two different RAM types is simply that the Spectrum started life as a 16 K machine and only later became available as (or expandable to) a full 64 K machine.

At the time that the Issue 2 Spectrum èmerged, full-specification 32 K and 64 K RAM devices were rather expensive. Doubtless it was for this reason that the RAM devices fitted in the upper 32 K of the early 48 K RAM machines were "failed" 64 K devices in which just one half was "good"! These chips were coded with an " H " or an "L" and appropriate links provided on the p.c.b. Naturally, ALL chips in. the 32 K block MUST be of the same type; either all "H" or all "L"!

It seems likely that, on later issue Spectrums, the upper 32 K may be populated by fully functional 64 K RAM chips of which only half is actually put to use. With a little thought someone might even have managed to "page-in" the unused 32 K . So, if you have had a go at this, why not drop me a line so that I can pass on the relevant information.

## REPDEL

This month we put the REPDEL system variable under the microscope. REPDEL is at a decimal address of 23561 and it contains the time delay (in fiftieths of a second) that a key must be held down before it begins to repeat. Whilst REPDEL is normally initialised to 35 (giving a delay of $35 / 50 \mathrm{sec}$ ) the value can be readily changed by simply poking a byte into 23561 .
To produce a delay of exactly 1 second, for example, we would need to make the value in REPDEL equal to 50 . This can be achieved by a statement of the form:

POKE 23561,50
with very light but positive directional control used by the right hand with an entirely separate "fire" button operated by the left thumb. Furthermore, my "ideal" unit would be "hand-held" rather than having to rest on a flat surface.

## Circuit

The circuit diagram of the joystick is shown in Fig. 1. The four direction switches, S 1 to S 4 , are microswitches mounted on a "professional" joystick assembly.

Figs. 2 and 3 respectively show the circuit and internal wiring of the joystick. The four direction switches, SI to S4, are the microswitches. The switches provided on this unit are single-pole changeover types and, in this application, only the "make" (normally open) contacts are employed.

The exterior view of the prototype joystick, showing the control layout, is depicted in Fig. 4. Readers will doubtless wish to vary the layout to suit their own individual requirements. The Spectrum joystick connector (pin view) is shown in Fig. 5. This is


Fig. 1.. Spectrum RAM pin connections for the 4116 and the 4532 . Note that the 4116 requires three separate power supplies!

Readers would be forgiven for thinking that POKEing REPDEL with a value of zero would produce no delay at all. This, however, is not the case! Surprisingly, POKEing REPDEL with 0 results in the longest rather than the shortest possible delay. The reason for this apparent anomoly is simply that the Spectrum's operating system decrements the value contained in REPDEL until it reaches zero before exiting the key repeat delay loop. When POKEd with zero, REPDEL first decrements to 255 (i.e. -1) and thus the delay loop is repeated a total of 256 times.

## DIY Joystick

Several readers have accused me of taking a rather more serious approach to computing on the Spectrum than is really warranted by what has become accepted primarily as a "games" machine. To redress the balance here is a little project to warm the cockles of the hearts of all inveterate games players!

Most arcade games are greatly improved when using a good quality joystick. With this in mind, and having tried a number of commercially available joysticks with only moderate success, I recently set about producing my own "custom joystick"

Whilst hardened games addicts may disagree, my own preference is for a joystick
an "Atari standard" 9-pin D-connector in which pins 5, 7 and 9 are unused and simply left disconnected.

## Auto-fire unit

For "alien zapping freaks", Fig. 6 shows a further enhancement of the custom joystick which provides automatic operation of the fire button at a rate determined by the setting of VR1
This circuit uses a 555 timer'running in astable mode coupled to the joystick unit by means of an opto-isolator. This latter component is simply a standard single-transistor low-cost type housed in a conventional 6 -pin d.i.1. plastic package.
The prototype auto-fire circuit was built in a separate plastic case (identical to that used for the joystick unit) and the supply derived from an internal PP3 9V battery. The prototype auto-fire unit was coupled to the joystick box using a shon length of twin flex terminated with two 1 mm plugs. Whilst this arrangement will prove quite satisfactory for occasional use, some constructors will undoubtedly want to mount the autofire unit within the joystick housing. This should not present too great a problem however, since space is very much at a premium, careful attention will have to be paid to layout. Good luck-and happy "zapping"!

## CUSTOM JOYSTICK wrt AUTO FIRE



Fig. 2. Circuit diagram for the joystick.


Fig. 5. Standard joystick interface connections (pin view).


Fig. 3. Joystick internal construction and wiring.


Fig. 4. Control layout of the joystick.

## COMPONENTS

## Joystick Components

Plastic case (approx. $110 \mathrm{~mm} \times$ $60 \mathrm{~mm} \times 30 \mathrm{~mm}$ ); joystick switch assembly (S1 to S4); momentary normally open push-button switch (S5); sockets ( $2 \times 1 \mathrm{~mm}$ ); 9-way D-connector (to suit joystick interface); 6 -way flexible cable (approx. 1 m ).

## Auto Fire Components

Resistors
R1 10 k
R2 10 k
R3 270
All $0.25 \mathrm{~W} \pm 5 \%$
Potentiometer page
VR1 100 k linear carbon
Capacitors
$\mathrm{C1} 10 \mu$ p.c. elect. 16 V
C2 $100 \mu$ p.c. elect. 16 V

## Semiconductors <br> IC1 555 <br> IC2 Opto-isolator (see text)

## Approx. cost

Guidance only
£12


Fig. 6. Circuit diagram for the auto-fire unit.

If you have any comments or suggestions for inclusion in On Spec, please send them to:
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P.S. Don't forget to include a large (A4 size) stamped addressed envelope if you would like to receive a copy of our "Update"!

## Next Month

Next month we shall be reviewing some enhanced interpreters for the Spectrum and describing the construction of an "infallible" reset switch. This handy unit allows users to recover from BASIC and machine code program crashes without having to erase the entire contents of the Spectrum's memory. See you next month!

## Roloor

## MECHANICAL SERVANT

THE DREAM of the mechanical servant has been around for a long time, fostered by growing automation in industry and the imagination of a large number of writers, particularly in science fiction. The difficulties encountered so far in making the dream reality have hardly dented the belief that it is possible to make a mechanical human.

For the majority of people anything that falls short of the ideal cannot be called a robot and expectations are high that it can be achieved with the might of the "miracle" microchip. A brief look at what can be achieved with the present technology and knowledge is sufficient to show that mechanical humans are a long way from becoming a fact. However, there is still a lot happening in the world of robots and the aim of this new regular feature is to reflect the trends in what is called the "personal" end of the market.

It is always useful to start with a definition of the area to be covered. Personal robots can be a problem in that everyone knows when they see one but trying to set down criteria can be difficult. This column will try to concentrate on devices which can be programmed either directly from an on-board processor or indirectly from a host computer. I use the term program loosely to include instructions given to toys, like George Compurobot, through a keypad.

A simpler way of finding out what will be covered here is to split the available machines into three categories-arms, buggies or turtles and toys

## ARMS

Arms come in all shapes and sizes. Most are used in education and are usually smaller, cheaper versions of their industrial cousins with prices ranging to a maximum
of about $£ 4,000$. There are two types, articulated and Scara. The articulated are the dinosaur-like devices which operate in the vertical plane, whereas the Scara, which stands for Selective Compliance Assembly Robot Arm, works in the horizontal.

Within these two catagories there are many different versions with prices usually depending on their complexity. The degrees of freedom, which govern their flexibility, vary from three to six with the usual number being five.

In terms of the human body they are waist, shoulder, elbow and two wrist


The Ogre servo-assisted arm.
movements, rotation and up and down or pitch. The sixit degree is a sideways movement of the wrist or yaw. In addition there is an end effector, usually a gripper which is sometimes included in the number of degrees of freedom. Most grippers are two-fingered pinchers but there are threefingered versions and some machines can take other devices.

The drive systems are equally, if not more, varied. The older models use stepper motors and nylon cord transmission
systems. Steppers have the advantage that they can be instructed to move a certain number of steps without feedback to assess when the required movement has been completed. They are slower, however, than the now more popular d.c. servos, which do need feedback.

Scaras, such as the Cepek from Reekie Technology or the Ivax from Feedback Instruments, are mostly powered by d.c. motors with a toothed belt drive, nylon cord having been found to slip. Articulated arms go in for more exotic systems such as the servo-driven screw drives of the Ogre, the servo powered screw-driven pistons of Remcon's Teachrobot or the oil hydraulic system of Feedback's HRA 933 and 934 .

Positional feedback is usually obtained by optical encoders which use light reflecting from a disc on which there are black and white stripes

Most can be obtained in kit form or fully built, the advantage of the kits, apart from their being cheaper, is that a detailed knowledge of the device can be obtained as it is built. They come complete with software for operating the system either by way of their own processors or from popular 'micros like the BBC B or increasingly the IBM PC.

Most manufacturers, like LJ Electronics, maker of the Atlas, or Cybernetic Applications, maker of among others the Neptune and Serpent, consider that their arms are only part of a total robot system and have built-up work cells to show how their arms can be used in a manufacturing environment. They include conveyor belts and sorting and storing systems. Cybernetic has recently gone a stage further by including a milling machine on which small items can be made which are then checked and sorted by the rest of the system.

Although there have been some problems in the market recently with Colne

Sevmour Papert, reported to be the "father" of the turtle language-LOGO.

Even a child can teach a Neptune robot, when fitted with touch sensors.


Robotics being liquidated and Powertran Cybernetics assimilated into its parent company, Feedback Instruments, there are still a number of new developments.
Maker of the Ogre, L. W. Staines, is planning to launch a two-armed robot called Troll. It is based on the same robust technology as the Ogre and its two arms can perform tasks together. Recently a new name has appeared, Spectravideo. Already well-known for its computer peripherals, this company has announced a new low-cost four-axis arm with three clipon end effectors.

## BUGGIES

Buggies or turtles are small mobile com* puter peripherals which initially were designed to help in education, particularly the teaching of maths where abstract concepts could be made physical. The turtle name came from the innovatory work of an American, Seymour Papert, who wrote the language which they mostly use, LOGO, and used it to control small turtles on a screen before the mobiles were considered.

The devices are usually two wheeled, using stepper or servo motors, with a pen which can be raised or lowered to draw diagrams under the control of a computer.

There have been casualties here as well. Valiant Designs produced an infra-red controlled turtle which proved very popular but the company has since closed.

One of the earliest companies, however, is still around. Jessop Micro Electronics with its creatively-named Jessop Turtle, which looks like an upturned mixing bowl, is still selling steadily

Here again manufacturers are finding that having a robot is not sufficient and two of the newer companies have developed a large amount of support material. Clwyd Technics' school-designed Trekker comes with programs and documentation for a range of school courses in which the mobile can assist learning and has been well received. Zero from IGR has bump and sound sensors and a line follower and the company is hoping to move out of education with some spe-cially-written games.

## TOYS

At the cheaper end of the market are the "toys". Personal robots hit the headlines in this country about two years ago when Androbot's Topo was launched with the
expectation that RB Robot's RB5X would follow soon. Despite all the ballyhoo it quickly became apparent that they were little more than expensive, more than £ 1,000 , toys and they failed to sell.

The companies which went for more reasonably-priced toys, like Tomy of the US, proved more successful. Tomy robots can, with some persuasion, react to speech, be programmed in a simple way and are mobile, like George, the Compurobot. The company is now importing the Petster range, which has similar capabilities, but instead of looking like miniature R2D2s they resemble furry toy cats, the ultimate in cuddly robots.
The three-way split, however, leaves out two areas which do not fit easily into any category, the Heros and kits. Lego, Fischertechnik and more recently Meccano. are providing packages which though appearing to be toys allow quite sophisticated devices to be constructed.

A buggy can be built from Lego which can be controlled by a switch panel or by a computer. Fischertechnik has recently expanded its range of computing kits to include a simple arm and Meccano has been assessing the market.

All have attracted interest from the education market because of their relatively low cost and flexibility and increasing amounts of software are being written for them.

There is also a set of kits from Milton Bradley which though they are more obviously aimed at the toy market their movements can be controlled by a simple keypad and can give an easy introduction to many robotic concepts.

The Heros are much more complex, being intended for higher levels of education. When Topo was receiving much attention Hero 1 was also included as an example of how personal robots were developing, mainly because it was mobile and had a vaguely similar shape.
However, that was the end of the similarities. Hero had been designed with education in mind and had many more facilities than Topo. It had an on-board processor, with possibility of expansion, light, sound and movement sensors and a rangefinder, a four-axis arm, speech and a comprehensive robotics course based around it.

Made in the US by Zenith/Heathkit it is imported by Maplin Electronic Supplies and is still selling for about $£ 2,200$ readybuilt or $£ 1,200$ in kit. Recently its upgra-
ded version, the Hero 2000 became available in this country with far larger ROM and RAM, improved speech and a five-axis arm selling for $£ 2,200$ plus VAT for the kit.

## THE CHALLENGE

Robots have a long way to go before they match the ultimate fantasy but developments are being made all the time. As BP discovered when it held two "Build-ARobot" contests for schools and youth organisations, there is plenty of original thought around.
After its first contest, in which a block had to be found and collected in the shortest possible time, the organisers thought they would make the second one more difficult. A machine had to be built which would fetch a drink on request. It was thought this might prove too difficult, until the results were seen.
A similar "impossible" challenge has been set by Dr John Billingsley of Portsmouth Polytechnic-the building of a robot ping-pong player or Robat. That followed his Micromouse challenge which attracted interest from all over the world, lately in Japan.

The first prototype Robat machines have been seen and some have developed an efficient hand-eye co-ordination and been able to hit the ball. The next stage is to impart some direction to the resulting hit.

The big stumbling block to, any form of "human" robot is the development of an intelligence. How to react quickly to all the inputs which can be received through a series of accurate sensors or spoken instructions is a limiting factor in all robots. There is plenty of work going on around the world in the area of artificial intelligence and Britain is one of the leaders in the field.

The world of personal robots is wideranging and fascinating and I intend to cover it thoroughly. If you have any points you wish to make or any new products or developments which you think should have a wider audience I can be contacted through the magazine.

The Valiant infra red sensitive turtle-now defunct.


#  

$\square$ W.HUNTER

## An invaluable addition to any computer/printer set-up with a Centronics interface

APRINTER buffer is one of those items which you will not miss until you've used one. Any serious computer user will find the circuit described here is really useful. (However we do not recommend th is project to readers who have not built other projects successfully.) It allows the continuation of data or text entry whilst the previous letter, account, listing or what-have-you is printed out.

The commonest printer interface amongst cheaper printers and microcomputers is the Centronics Parallel Interface. It is available on the ubiquitous Epson range of printers, which can be driven by almost any computer. The printer buffer described in this article uses the Centronics Interface and has been designed for simplicity in construction and application. It fits between the printer cable from the computer and the printer itself. Whenever the computer wishes to send text to the printer, the buffer will accept the text as fast as the computer will transmit it, and then feed it slowly to the printer. The computer transmission will be at very high speed since the parallel interface is not constrained by a fixed Baud rate.
The buffer can accept text until its memory is full-that is 16 K characters, at
which stage it signals to the computer to stop. Meanwhile the buffer is feeding text out to the printer at the printer's maximum acceptance rate. Depending on the printer it might be 100 characters per second (cps) for a matrix printer down to 12 cps for a daisy wheel printer. Very often, the buffer's 16 K memory will be enough to accept all the computer's output at one go, freeing the computer for use in only a few seconds. The printer itself will be occupied for minutes as the buffer feeds out the full text at the printer's own speed. If the computer attempts to output more than the buffer can hold, it is halted at the moment the buffer is full. The buffer will then demand more text from the computer at the rate that the old text is sent to the printer.

## CIRCUIT

The heart of the buffer is the Motorola 6802 central processing unit (CPU) shown in Fig. 1. It is almost identical to the longrunning 6800 but the 6802 offers the advantage of an on-board oscillator, simplifying the circuit. The CPU executes a program that is held in the 2716 which is a 2 K byte EPROM. The 6802 CPU can access the computer and the printer through the 6821 parallel interface adaptor (PIA). This remarkably cheap device has its peripheral lines divided into two sets, known as Port A and Port B. Each set comprises eight data lines for either input or output, and two control lines for handshaking. In this design, port $A$ is used as an iniput from the computer whilst port B is the output to the printer. The PIA must be configured correctly to perform the startup sequence at switch-on.


COMPONENTS


10k (2 off)
R2 to R5 4 k 7 (4 off)
$\frac{1}{4} W \pm 10 \%$
Capacitors

| C1 | $47 \mu$ elect. 12 V |
| :--- | :--- |
| C2,C3 | 33 p (2 off) |
| C4 | 10 n |
| C5 to C8 | 100 n (4 off) |
| C9 | $470 \mu$ elect. 16 V |

Semiconductors

| IC1 | MC6802 CPU |
| :--- | :--- |
| IC2 | MC6821 PIA |
| IC3 | 2716 EPROM |
| IC4,IC5 | 6264P RAM (2 off) |
| IC6 | 74LS00 Quad |
|  | NAND Gate |
| IC7 | 74LS02 Quad NOR |
|  | Gate |
| IC8 | $7805+5 \mathrm{~V}$ |
|  | regulator |
| D1 | OA202 |
| D2-D5 | 1A Rectifier bridge |

## Miscellaneous

S1, Miniature panel
S2 mounting push button switches (2 off)
T1 Mains transformer 3VA rating, 7 to 9 volt output
FS 1 Mains fuse in fuseholder, panel rounting
SK1 Centronics type 36 way socket (IEEE488)

PL1 Centronics type 36 way plug, cable or IDC.
$X 1 \quad 4 \mathrm{MHz}$ crystal
Case, minimum size approximately $180 \times 120 \times 65 \mathrm{~mm}$; ribbon cable; double sided p.c.b. see Shop Talk; connectors, etc.


Fig. 1. Complete circuit diagram of the Printer Buffer.

Memory (i.e. RAM) is available in many different types of integrated circuits these days. The 6264 chips used in this design are part of the family known as "Byte-wide" memory chips, since they have eight parallel data lines. With these chips a byte of data can be read from one chip in one access cycle, making design simpler than with other types.
Each of the 6264 chips will hold 8 K bytes, making a limit of 16 K characters of text in the buffer. However the circuit and EPROM program are designed so that the buffer will run with only one memory chip if necessary, in case a constructor wishes to
keep the initial costs to a bare minimum.
The only remaining logic circuits are the NAND and NOR gates which are used for the decoding, shown in Fig. 2. In order to cover reset and interrupt vectors, the EPROM is decoded to the top of the memory map, occupying \$F800 to \$FFFF. The RAM is placed at the lowest addresses, the first (and possibly only) RAM chip being at $\$ 0000$ to $\$ 1 \mathrm{FFF}$, and the second at $\$ 2000$ to $\$ 3 F F F$. This leaves plenty of space for the PIA, which only requires four addresses-these are available at $\$ 8000$ to $\$ 8003$. Those readers familiar with address decoding will realise that the locations
given are not the only locations which could be used. In fact the PIA occupies all addresses from $\$ 8000$ to $\$$ BFFF, but there is no advantage in refining the decoding further for this project.

## HANDSHAKING

The Centronics parallel interface uses a system called "handshaking" in order to pass data from one device to another (see Fig. 3). Handshaking prevents the sending device (the source) from transmitting data too fast for the receiving device (the target) to deal with. At the start of transmission,


Fig. 2. Decoding of address space.
the source puts one byte of data on the eight parallel data lines. It then signals to the target that valid data is held on the lines, by sending a pulse called a strobe to the target. Since the data lines are all occupied, the strobe has to be sent on a separate line which is called the Strobe line. The source will now wait until the target has had time to read the data byte. The target signals that is has received the data, by putting a "high" voltage (i.e. 5 V ) on yet another line-the Busy line.
As soon as the source reads a high on the Busy line, it terminates its strobe pulse, if it hasn't already done so. The target now processes the data (i.e. prints or saves it) and then signals to the source that it is ready for another data byte. It does this by dropping the voltage on the Busy line back to zero: The Centronics interface provides for two types of target reply line. The Busy line can be replaced or assisted by an Acknowledge line. Some computers use only one or the other of these two lines. This design provides both, so it will interface with any computer with a Centronics interface.

## ACKNOWLEDGE

The Acknowledge line is held high normally, and when a byte of data has been read by the target, a short pulse to zero volts "acknowledges" the data. The source can then move on to the next byte. The width of the acknowledge pulse is not critical, and in this design a pulse of roughly 20 microseconds is produced. The pulse comes from a falling edge on the Busy line, through a 10 nanofarad capacitor, and signals that the buffer is ready to receive new data. If the buffer is full, the Busy line remains high and so there is no negative edge and no Acknowledge pulse, until space is available again.
Handshaking sounds complicated, but it achieves the fastest possible data transfer between variable speed machines. The interface needs two handshake lines (the Strobe and the Busy lines) to work, and these are just what the PIA provides. Port A and Port B each have two "control lines" in addition to the eight data lines. Remember that the PIA has to act as a target for the computer, and as a source for the printer. Control line CA1 is therefore a Strobe input, and CA2 is a Busy output for the target side of the PIA. Control line CB2 is a Strobe output and CBI is a Busy input when the PIA is a source to the printer.
The beauty of the Centronics interface is


EE559G

Fig. 3. Interface handshake timing diagram.
that when the buffer is temporarily full it can halt the computer's output, until space becomes available. This is achieved by holding the Busy line high. Similarly if the printer runs out of paper, it will halt output from the buffer in the same way.

## FUNCTION SWITCHES

Two function switches are provided -Hold and Flush. To keep the circuit simple, these switches are linked to the two interrupt lines of the CPU. A low on the Interrupt Request line (IRQ) will send the CPU off into a section of program which either suspends output to the printer, or resumes output if it is already suspended. The Non-Maskable Interrupt line (NMI) will divert the CPU to terminate output immediately and then reset its pointers to
effectively clear its memory. This is useful if printing needs to be stopped for some reason.

## PROGRAM

The program is held in EPROM and executed by the CPU. During normal operation, the program continually circulates through two routines, TRYIN and TRYOUT. One acquires data from the computer and the other despatches data to the printer -subject always to the limitations of buffer memory size and printer speed. Two pointers are used by the routines, DATEND points to the byte most recently stored in memory and DATBEG points to the byte most recently output (the next higher address is the next byte for output, i.e. the Data Beginning).



Fig. 4. Double-sided p.c.b. layout and wiring

While TRYIN is executed, the index register (IXR) points to the next available storage location. Subroutine INDAT puts the Busy line low and checks the input port (at PIAIN) to see if a data strobe has latched the CAI flag (bit 7 of PIAIN). If it has, the routine reads a data byte from the port and then puts the Busy line high again. TRYIN now stores the data byte, updates IXR and saves it in DATEND for the next round. Note that if no data strobe is detected or if memory is full, the pointer DATEND is not updated by IXR, so the current location is preserved for another try.
TRYOUT alternates with TRYIN and attempts to feed data to the printer. After loading the accumulator (A) with the next data byte (if there is data in memory), OUTDAT is called. This subroutine checks whether the previous output has been received and acknowledged by the printer (CBI, bit 7). If so, the new data is output via port B (PIAOUT). Finally the pointer DATBEG is updated. It can be seen that TRYOUT will not send new data if there is none to send, or if the previous byte has not been acknowledged. This latter condition causes a slight hiccup for the first byte, since there was no previous byte to be acknowledged in this case. FLAGl allows OUTDAT to circumvent the acknowledge condition for the first byte after power-on. The subroutine labelled STROBE provides the requisite handshaking pulses for OUTDAT.

When power is switched on, the reset line is held low for about 500 ms by the resistor and capacitor on the line. The reset vector
takes the CPU to an initialisation routine. First the stack pointer is set to point to the top of the stack area, then the PIA is set up appropriately with port $A$ as an input and port $B$ as output. CAI and CBI are set up to trigger on negative edges for handshaking. The memory at $\$ 2000$ is tested to find if the second RAM chip is fitted and the appropriate value ( $\$ 2000$ for $8 \mathrm{~K}, \$ 4000$ for 16 K ) is stored in MEMEND. The pointers DATBEG and DATEND are intialised to the start of buffer memory, above the stack at $\$ 007 \mathrm{~F}$. Eventually the main loop of TRYIN-TRYOUT-TRYIN, etc is started.
The interrupt vectors NMI and IRQ are used by the control buttons for Hold and Flush. The NMI routine simply resets the stack then jumps into the initialisation routine beyond the PIA set up section. This is to avoid the possibility of an inadvertent strobe to the printer if the PIA was set up anew. An interrupt request (IRQ) can cause the processor to run one of two routines, since the Hold button has a toggle action. The routine which is to be run is pointed to by IRQV2. Initially this is the routine called IRQR1 and it is this routine that halts printing. The first action taken by IRQRI is to put the Busy line high to stop output from the computer. It then goes into a 40 ms delay to ensure that the Hold switch has stopped bouncing.

Although the processor sets an interrupt mask when it receives an interrupt request, it is possible for a second interrupt request to be latched and acted upon when the
interrupt mask is cleared. IRQR1 gets around this by indicating that the IRQ routine has been run by setting a flag IRQFLG. This allows it to ignore a second interrupt. Finally, IRQR1 adjusts IRQV2 to point to IRQR 2 (the other interrupt request routine) and then enters the wait state. On the next press of the Hold button, the processor exits from the wait state and executes IRQR2. This allows printing to continue, by resuming the program at the point where it left off to service the first press of the Hold button. Thus no data can be lost by the Hold action.

## CONSTRUCTION

Construction of the printer buffer should present no problems. The power supply consists of a small transformer mounted separately in the case, driving a full wave bridge rectifier and smoothing capacitor. A fuse should be fitted in the mains input lead to the transformer, for safety. The d.c. supply is regulated at five volts by a 7805 positive regulator. Since current consumption is only 200 mA , power loss in the regulator should not require a heat sink to be fitted unless the transformer produces excessive voltage. The power supply components are mounted on the printed circuit board (p.c.b.). It is suggested that all the integrated circuits (apart from the regulator) should be mounted in di.i. sockets. This not only protects against destruction by high soldering temperatures, but also assists greatly if a component is faulty.

Before mounting any components on the p．c．b．connect all the pin throughs and solder on both sides of the double sided board－there are 73 of these．A few com－ ponents also require soldering on both sides
of the board．The p．c．b．track masters are not shown as this board is not recommend－ ed for home manufacture－see Shop Talk for a supplier．When the board has been constructed，the power supply regulation
can be checked before any expensive inte－ grated circuits are plugged in．Connection to the computer is through a Centronies socket mounted on the back panel of the case．Separate wires or ribbon cable can be

# PRINTER BUFFER SOFTWARE 


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－Sirobe outoui ocri

> * INTERRUPT ROUTINES

＊NDN－MASKABLE INTERRUPT ROLTINE


FBE9 3E

－INITIALISE PIA AND F：ND MEMORY SIGE

continued．．．

## PRINTER BUFFER SOFTWARE－continued

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| INI？ | FaC1 | 12d3：－ | FEEO | ： 2011 | F892 | ifalz | FGAE | IRGFLC | rue |
| IRGJMP | Faci | IRGR： | FeE | iRGRE | F39L | iRCU | FFFE | iROVE | 0000 |
| MEMENG | 0．000 | MEMET | 0060 | NML： | FEEO | vMiv | FFFC | CuTE： | 535 ${ }^{\text {c }}$ |
| OUTE | F367 | －Oltdat | FESi | Plaite | 8000 | FiAuLT | 3032 | AFSET | FFris |
| STACK | Cu7F | STAF： | ${ }_{1} 5000$ | STAFT1 | fevo | STRCEE | Fets | 3wif | $F \mathrm{BE}_{5}$ \％ |
| Swiv | FFFA | TWさiN | アセに | TקYOUT | FB07 | TSTMI | Fe） | TST：EM | r5Fe |

used to connect this socket to pads on the p．c．b．Output from the buffer to the printer is through a ribbon cable soldered direct to pads on the p．c．b．at one end，and terminat－
ing in a Centronics plug at the other end This plug should be either an insulation displacement type or soldered，with a cover． The ribbon cable to it should be clamped on
the back panel and lead out through a slot or recess．Two push button switches are mounted on the front panel for the Flush and Hold functions．

## CIRCUIT EXCHANGE

This is the spot where readers pass on to fellow enthusiasts useful and interesting circuits they have themselves devised．Payment is made for all circuits published in this feature．Contributions should be accompanied by a letter stating that the circuit idea offered is wholly or in significant part the original work of the sender and that It has not been offered for publication elsewhere．

## CAR WARNING OF THINGS LEFT ON

EVERY car owner knows the horrible feel－ E ing；you find you have left something on，and return to the car to find a flat battery．This circuit overcomes that prob－ lem on my Volvo，and has done so for about two years．
Take a typical journey．Get in，switch on and start，stop，switch off，after having put the sidelights on during the journey．The result is a buzzer sounding and a pilot light on to say what＇s left on．Switch off the lights and all is well．
If you want to leave them on for parking， flip a toggle switch and the buzzer stops． Come back again after parking with lights on，switch on，and the buzzer sounds to remind you to put the switch back to the original position．

Diodes D1，D2，D3 and D4 form an OR gate．Any one of the sensed circuits switched on produces 12 V to relay RLA and （via SI in the SET position）the buzzer．If ignition is off，the buzzer and RLA have an ＂earth＂return via lamp LP5；the buzzer sounds and the relay energises．
The relay contact RLA1 provides an earth return for indicators LP1，LP2，LP3，
and LP4 via diodes D5，D6，D7，and D8． The appropriate indicator lights up．Diodes D5，D6，D7，and D8 are needed to prevent incorrect indications via the low d．c．resis－ tances to earth of other sensed circuits．
If ignition is on，neither the buzzer nor RLA sees an earth return，as IGN is at 12 V ． The buzzer is silent，and no indicators light up．In a＂parking with lights＂situation， IGN is an earth return via LP5，and the junction of D1，D2，D3，and D4 is at 12 V ． Relay RLA is energised，so the appropriate indicator is on．

Putting SI to CANCEL silences the buzzer．When the car is started again，IGN becomes 12 V ；with S1 to CANCEL，this connects the buzzer between 12 V and earth． It sounds，as a reminder to set S 1 back to SET．

Lamp LPS may not be necessary．All that＇ is required is a permanent low－resistance connection between IGN and earth．Ori my Volvo，three added instrument illumination lamps provide this path．
Components are not critical．The circuit diagram shows suggested types．Indicators LP1，LP2，LP3，and LP4 are car accessory

shop 12 V indicators，any colour you fancy The diodes can be any IA 400 V type．The relay can be any small 12 V component with a coil resistance over 1000 ohms．Don＇t use car accessory relays，they take too much coil current．The buzzer can be any small 12 V type．

The indicators and the toggle switch are mounted on a small accessory panel．The rest goes on a small p．c．b．behind the dash． ＇The IGN input comes from any live line fed via the ignition switch．

The sensed points can normally be picked up by＂tee－in＂car accessory connectors near the switches of the sensed circuits．I sense side lights，headlights，spot lights，and a rear screen heater which is not fed via the ignition switch．Sense what you like；the circuit can have as many OR gate diodes and indicator lamps as you like．

C．Waltham
Southampton．
IF you＇re a regular reader and have not yet submitted an idea for Circuit Exchange， why not have a go now？We will pay $£ 40.00$ per page．for any article published．

We are looking for original ideas which may be simple or complex，but most importantly are useful and practical．

To help us to process articles which are offered for publication，all subject matter should conform to the usual practices of this journal．Special attention should be paid to circuit symbols and abbreviations and all diagrams should be on separate sheets，not in the text．Also manuscripts should be typed with wide margins and double line spacing or neatly hand written in the same fashion．

Just send in your idea to our editorial offices，together with a declaration to the affect that it has been tried and tested，is the original work of the undersigned and that it has not been offered or accepted for publication elsewhere．It should be empha－ sised that these designs have not been proved by us．


The books listed below have been selected as being of special interest to our readers, they are supplied from our editorial address (mail order only) direct to your door.

## DATA AND REFERENCE

## HOW TO IDENTIFY UNMARKED IC 8

K. H. Recort

Shows the reader how, with just a test-meter, to go about recording the particular signature of an unmarked i.c. which should enable the I.c. to then be identified with reference to manufacturers or other data. An i.c. signature is a specially plotted chart produced by measuring the resistances between all terminal pairs of ani.c.
Chart
Order code BP101
$\mathbf{~ O . 9 5}$

## RADIO AND ELECTRONIC COLOUR CODES AND

## DATA CHART <br> B. B. Babani

Although this chart was first published in 1971 it provides basic information on many colour codes in use throughout the world, for most radio and electronic components. Includes resistors, capacitors, transformers, field coils, fuses, battery leads, speakers, etc. It is
particularly useful for finding the values of old components.
Chart Order code BP7 £0.95
CHART OF RADIO, ELECTRONIC.
SEMICONDUCTOR AND LOGIC SYMBOLS
M. H. Babani, B.Sc.(Eng.)
llustrates the common, and many of the not-so-common, radio, electronic, semiconductor and logic symbols
that are used in books, magazines and instruction manuals, etc., in most countries throughout the world. Chart $\quad$ Order code BP27 $\quad \mathbf{E 0 . 9 5}$

## TRANSISTOR RADIO FAULT-FINDING CHART C. E. Miller

Used properly, should enable the reader to trace most common faults reasonably quickly. Across the top of the chart will be found four rectangles containing brief description of these faults, vis-sound weak but undistorted, set dead, sound low or distorted and background and following the arrows, carries out the suggested $\begin{array}{ll}\text { checks in sequence until the fault is cleared. Order code BP70 } \\ \text { Chart. } & \text { O.9.95 }\end{array}$

## DIGITAL IC EQUIVALENTS

AND PIN CONNECTIONS
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THERE are still people who remember hearing " 2 LO calling" on a crystal and catswhisker radio. More correctly known as the crystal detector, it was one of a number of devices used to "rectify" an alternating current signal.
Other devices, in order of importance, were: The Coherer, the Electrolytic, and the Magnetic detector; The Thermionic Valve; and, much later, the Transistor.
Here we are only concerned with crystal detectors, the more common of which, again in historical format, are:
Bornite and Zincite
Zincite and Tellurium
Carborundum and Steel
Galena and Tellurium
Copper Pyrites and Tellurium
Germanium and Copper
Iron Pyrites and Silicon
Several others worthy of mention are:
Cassiterite, Graphite, Hertzite, Malachite, and Molebdenite; but the possibilities are endless.

Even a piece of common coal can rectify-or the inner and outer cones of a coal-gas flame.
Since only germanium and silicon appear to have survived common usage into the 20 th century we will ignore all the others.

They possess a peculiar property where, when placed in contact with a foreign body, a current will flow much more easily in one direction than the other. This, in turn, changes a modulated pulse of radio-frequency voltage into a cumulative unidirectional current-which may be utilised to reproduce audio-frequencies from an earpiece or other electromechanism.

The property is only present where impurities exist in the crystal. Discovered by accident, it is variously described as rectification, detection, or demodulation. Potentiometric biasing may be used to select the straighter part of the characterstic curve; or a capacitor/resistor combination, so as to utilise the incoming signal.
The Greeks first spoke of "spirit" particles in 400BC and their electrostatic experiments with amber (Elektra) are well-known.

Experiments with rectifiers, however, did not begin until about 1835AD; then, around the turn of the century the silicon carbide rectifier was developed; commonly known as carborundum and catswhisker, this was the first use of silicon.
Shortly after the First World War, experiments began with silicon point-contact diodes. Ideal for u.h.f. detection, they were electrically similar to the thermionic valve, which was now fast replacing the crystal and catswhisker.

Germanium oxide can be obtained from the flue-ash resulting from burning common coal.
The advent of another war made miniaturisation a priority. Owing to material shortages, both germanium and silicon were used; the latter being less frequency-conscious.

## 1835 Munck Roschenschold: Rectifying action in

 solids.1906 Carborundum and Catswhisker was developed.
1924 Lossev Experiments with Point-Contacts and zincite with carborundum.
1930 Wilson and Mott published The Physical Theory of Conduction of Solids.
1945 J. Bardeen and W. H. Brittain began work on Control of Surface Charge Density, . . . and in
1948 published, The Transistor, a Semiconductor Triode.
1949 Bell Telephone Laboratories announce the $p-n$ junction Transistor.
(In Germany brass wire was substituted for the copper used in radios due to a shortage of the latter caused by allied bombardment.)
The instability of the catswhisker led to the study of "doping"-adding impurities to the junction-and conductivity was found to improve across the resultant alloy. Doping creates either a surplus or a deficiency of electrons in the crystal lattice. The former is referred to as the donor $n$, while the latter is called an acceptor $p$.
When $p$ and $n$ atoms bond together in a co-valent, or homogenous, union an energy exchange occurs as the two materials try to equalise, leaving a depleted area known as the "space-charge" region. An area of stress builds up in this region and is referred to as the $p-n$ junction. "Forming" a junction is a continuous process, so that the junction may become a chemically "single" crystal.
Since each side of the junction is of opposite polarity, it follows that it must possess capacitance. The extent of this capacitance has considerable influence on determination of the upper frequency limits.

Negative electrons have to be "balanced" by positive holes. (A hole is an atom lacking an electron and, therefore, carrying a positive charge.) Where the electrons greatly . outnumber positive holes the crystal is referred to as $n$ type.

If positive holes predominate the structure is called $p$ type.

The point-contact's catswhisker is pointed so as to concentrate the electric field. The same principle is applied to the lightning conductor.
Study concentrated on the effect of surface charges after the Second World War, and use of an electrolyte in contact with the surface of semiconductor material led to an additional electrode placed close to the first.

Soon afterwards, the Bell Laboratories announced the $p-n$ junction transistor. (This was named after TRANSfer + reSISTOR $=$ Transistor.) Which side of the junction the second contact is placed determines whether the resultant transistor will be pnp or npn.
The three transistor electrodes are called the emitter, the base, and the collector. Application of a forward bias between the emitter and base will cause holes (or electrons, depending on transistor type) to be emitted from the emitter into the space-charge region (the base). And a reverse bias applied from the collector to base will permit the collector to collect.
Direction of current flow has long been confused with the ambiguities of "current flow" and "electron flow"-the'one in direct contradiction to the other.

With the arrival of crystal "chemistry"-a branch of quantum mechanics-new, and yet more confusing terms have evolved. Current flow by hole movement; forward current, reverse current, saturation current, leakage current, and now $p-n$ or $n-p$ junctions!

## INTEGRATED

Soon two or more transistors were to be found embodied in the same envelope. (This was no new event, for in the
wartime German "people's" set four thermionic valves shared the same glass case.)

Transistor-pairs soon followed; the "Darlington pair" and the "Super-alpha pair" soon led to logic blocks and gating circuits, and to diode-transistor combinations (DTL). Then, to obtain greater speed, the transistor-transistor (TTL) logic spries evolved-and the integrated circuit was born.

Since the semiconductor industry concentrated on business processing and personal computers, integrated circuit development slowed down. Thermionic valves and other components were expensive in comparison with circuit wiring; whereas in semiconductors, chip area and wiring limit costs.
The advent of VLSI (very large scale integration) brought denser bulk memories, concentrating 256,000 ECL (emit-ter-coupled logic) devices onto a "chip" smaller than a baby's finger-nail, but with wide-ranging applications for solving design problems throughout the fields of physics and engineering.
The Government-sponsored Alvey directorate recently announced a $£ 63$ million, three year, research programme to a number of the larger manufacturers' research groups, to develop VLSI, which relies on a lithographic process. After the three years they move on to the next but one "chip" gen-eration-up to 10 million components on a single chip. One project will be to develop an advanced 1.25 micron CMOS (CMOS stands for complementary metal-oxide semiconductor), using a double level metal process involving VLSI.

By applying the laws of quantum physics, computer study of molecules large enough to be of biological significance has produced stable silicon compounds, called "polysilenes", in which silicon atoms bond together. These silicon-silicon bonds form a "living" tissue which will make the "biological chip" possible. New laws, both social and ethical, will be needed then to govern its use.

Devices such as those proposed will make the home computer seem a simple mechanism compared even with the calculators in use today.
That simple crystal might well be termed our building block to the future

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## Provides an audible warning of freezer failure

ADOMESTIC freezer is a very reliable piece of household equipment and generally runs for many years with no trouble at all. If a fault occurs, then there is usually time to get the machine repaired before the food is spoiled, providing the freezer is kept shut and the repair man is called in immediately. But what happens if the freezer is in an out of the way place like the garage when the fault occurs, and it is not noticed for some time? This happened to the author recently and he was left with a large amount of thawing food which had to be used up quickly before it spoiled. This prompted the design and construction of the freezer failure alarm described in this article.

## BASIS OF OPERATION

The circuit described here uses a silicon diode as the temperature sensor, and measures the voltage across the diode which changes as the temperature of the diode changes. The forward voltage of a silicon signal diode will depend upon the particular diode type, but it is usually around 0.7 volts.


Fig. 2. Complete circuit diagram of the Freezer Failure Alarm.

This vollage falls by about 2 mV for every Centigrade degree rise in temperature, and this is known as the temperature coefficient of the diode. Using this property, the diode can be used as a temperature measuring device. Fig. 1 shows the principle of the circuit used in the alarm.

The sensor diode D1 along with R2 forms one arm of a bridge circuit. The other arms are made up of R1, R3, and D2 plus R4. If all the resistors are of the same value, then the bridge will be in balance when the forward voltage drop of the two diodes is equal. Assuming that the two diodes have identical characteristics, then the bridge will be in balance if the two diodes are at the same temperature, and the voltmeter will read zero. If D1 is now cooled, then the voltage across it will increase by approximately 2 mV foreach Centigrade degree, the
bridge will be unbalanced, and the meter will show a positive reading.

## CIRCUIT

In Fig. 2 the circuit of the alarm is shown. The voltmeter is replaced by an op-amp which amplifies the difference in the voltage between the arms of the bridge. Because the amplifier has no d.c. feedback its gain is extremely high, and its output will switch from low to high with the slightest imbalance in the bridge. The output of the amplifier then drives the base of TRI which in turn causes the audible warning device to sound. Variable resistor VR1 is used to balance the bridge when the sensor diode is at the required temperature. Capacitor C 2 is required to make the amplifier switch on rapidly. It supplies positive feedback to a.c. signals so that as the output voltage begins


Fig. 1. Principle of operation.
to change, this is fed back to the positive input and speeds up the transition. The circuit requires very little current, and is supplied by half-wave rectifying the 12 V from the transformer and smoothing with capacitor Cl .

## CONSTRUCTION

The circuit was built on a printed circuit board, the layout of which is shown in Fig. 3. The circuit board is mounted in a diecast box. All components are mounted on the board except the audible warning device -which is mounted on the outside of the box-and the sensor diode.

The diode is soldered to a pair of fine twisted wires and sleeved to protect the connections. The sleeve, which covers the diode itself, not only gives physical protection to the diode but also gives some thermal insulation, so that the diode does not warm up too quickly. This prevents the alarm from going off when the freezer door is opened, but will allow the diode to warm up sufficiently quickly if the freezer should fail.
The audible warning device (X1) was screwed onto the outside of the box in the author's alarm, but could be mounted remotely from the alarm and connected to the main unit with a long lead if required. The warning device uses a very small current, so there should be no appreciable voltage drop unless the connecting lead is very long or the wire is very thin. In the prototype XI was a piezo ceramic device obtained from Verospeed (type SM2), but virtually any

## COMPONENTS



Semiconductors

| D1 | 1N4001 |
| :--- | :--- |
| D2,D3. | 1N4148 (2 off) |
| IC1 | 3140 op. amp. |
| TR1 | BC108 |

## Miscellaneous

XI Audible warning device ( 15 to 20 V operation)
T1 12V 1.5VA mains transformer
LP1 Mains neon indicator
FS $1 \quad 20 \mathrm{~mm}$ mains fuseholder and 100 mA fuse
Diecast case, $113 \times 63 \times 31 \mathrm{~mm}$; p.c.b. available from the $E E P C B$ Service, order code 534; connecting wire (see text), sleeving (see text), fixings, etc.

## Approx. cost

 Guidance only


Fig. 3. Printed circuit board design and component layout and wiring. Make sure the anode of the sensor diode is connected to the upper pin. T1 should be connected to the mains via a fuse and mounted in an earthed metal case.
similar device could be used as long as it is rated for use at $15-20$ volts.

## TESTING

Once the circuit has been built and checked, it can be tested. This is easily done by switching on with both diodes at room temperature, and adjusting VRI until the alarm sounds, then back-off VR1 until the alarm just stops. If D2 is now warmed up by holding above a hot soldering iron (take care not to touch the diode with the iron or it may crack) the alarm should begin to sound. If VRI does not have enough adjustment to either switch the alarm on or off then the bridge must be well off balance. This may be due to an error in construction or a component that is faulty. It is a simple matter to check the components with a meter to ascertain where the fault lies.

When the circuit has been tested, the sensor diode can be put in the freezer, and the connecting wires brought out over the door seal at a convenient point. If the wires are thin (use $7 / 0.1 \mathrm{~mm}$ wire), then the seal of the freezer will not be impaired.

The diode should be left in the freezer for at least fifteen minutes to allow it and the sleeving to cool to the surrounding temperature before the alarm is adjusted. During this time the alarm should be switched on and the box lid closed to allow the diode in the other side of the bridge, D3, to reach its working temperature. This is necessary because the bridge circuit compares the voltages across the diodes, and cooling D3 has the same effect as warming D2. The potentiometer should, of course, be adjusted so that the alarm is not sounding during this wait. After this time the alarm can be adjusted in the same way as it was during testing. The alarm is now ready for use and the box can be closed up.

## RELIABILITY

Because of the simplicity of the circuit, it should be very reliable and should give no trouble, but for peace of mind it might be worthwhile testing the alarm once or twice a year. This may be done by removing the sensor from the freezer and checking that the alarm sounds after a small delay. This also provides an opportunity to check the calibration of the alarm. The author's alarm has been in use for more than six months and has given no trouble apart from a small recalibration when it was tested at four months. The adjustment required was very small and would not have caused any problem had the freezer failed.

## NEXT MONTH-WATCH OUT FOR THIS SYMBOL



IT COULD SAVE YOU MONEY


## Part 3 Another transistor switch

This series is designed to explain the workings of electronic components and circuits by involving the reader in experimenting with them. There will not be masses of theory or formulae but straightforward explanations and circuits to build and experiment with.

THIS month we build another circuit based on a simple transistor switch. This circuit introduces you to ideas that are very important in circuit de-sign-the idea of the potential divider and the idea of feedback. It also introduces another kind of semiconductor device, the light-dependent resistor.

## POTENTIAL DIVIDERS

Sum of resistors $R_{1}$ and $R_{2}=R$ (Fig. 3.1a).

Potential across $A C\left(V_{A C}\right)$ is $V_{A C}=I R$ $=\mathbf{I}\left(R_{1}+R_{2}\right)$ so $I=V_{A C} / R$


Fig. 3.1. Potential divider arrangements.


Fig. 3.2. Circuit diagram for an "Electronic Candle". The practical layout is shown opposite.

Potential across $\mathrm{BC}\left(\mathrm{V}_{\mathrm{BC}}\right)$ is $\mathrm{V}_{\mathrm{BC}}=\mathrm{IR}_{2}$
$=\frac{V_{A C}}{R} \times R_{2}=V_{A C}\left(\frac{R_{2}}{R}\right)$
The potential across $A B$ is divided, and a fraction of it ( $R_{2} / R$ ) appears across BC.
This assumes that we draw no current from point B. If a small current is drawn, there will be little change, but if a large current is drawn the potential across BC may fall greatly.

In Fig. 3.1b $\mathrm{V}_{\mathrm{BC}}$ can have any value between zero and $\mathrm{V}_{\mathrm{AC}}$, depending on the position of the wiper of the variable resistor, VR

## LIGHT DEPENDENT RESISTOR

The light dependent resistor (l.d.r.) is a resistor made from a semiconducting material such as cadmium sulphide. When light falls on it, the energy of the absorbed light causes additional electrons to be set free. This provides more
charge carriers, so current can flow more readily. The effect of this is that the resistance of the material decreases. The brighter the light, the lower the resistance. Its resistance may fall from 10 megohms in darkness to as low as 20 ohms in bright light.
The light dependent resistor (l.d.r.) is also known as a photoconductive cell.

## ELECTRONIC CANDLE

The electronic candle to be described is a novelty to amuse your family and friends. The "candle" has an electric lamp instead of a wick. When you strike a match and hold it close to the lamp, the lamp lights. When you blow, the lamp goes out. The candle works best in a dimly lit room.



## HOW IT WORKS

The lamp is turned on and off by a transistor used as a switch. The base current to the transistor comes from a potential divider. This is made up of R1 and R2 (Fig. 3.2). One of the resistors (R1) in the potential divider is a light dependent resistor. In the dark this has a resistance of several hundred kilohms. As the amount of light falling on it is increased, its resistance decreases. In bright light it has a resistance of only about 100 ohms. In the dark the potential at $A$ is low, almost zero volts. No base current goes to TRI, so TRI is off and the lamp is dark. If we shine a light on R1 (or hold a burning match near to it), its resistance falls. The potential at $A$ rises and base current flows to TR1. The transistor is turned on, and the lamp lights. In the electronic candle, the lamp and l.d.r. (light dependent resistor) are close together. The light from the lamp keeps the resistance of R1 low, so once
turned on the lamp stays on. Even when we take the match away, the lamp stays on. To the uninitiated it appears that when we "light" the candle, using a match, it stays lit just like an ordinary candle. The "signal" from this circuit is the light coming from the lamp. Part of this signal is fed back to the I.d.r. The way the circuit is arranged ensures that light from the lamp keeps the lamp switched on. Conversely, absence of light from the lamp keeps the lamp switched off. This is positive feedback.
The electronic candle has a small shutter of card which rocks sideways and partly covers the l.d.r. when we blow on the candle. This partly breaks the beam of light between the lamp and l.d.r. As this hapens the resistance of R1 increases sharply, the potential at A falls and TRI is turned off. The lamp goes out. Now, even though we have stopped blowing, the amount of light reaching R1 from the surroundings is


COMPONENTS

## Resistors

MKY7C38E or ORP 12 lightdependent resistor 560

Transistor
ZTX300 npn junction transistor

## Miscellaneous

LP1 6V 0.06A lamp in holder; materials for making candle and concealing circuit if required; breadboard, e.g. Verobloc.

## Approx. cast

Guidance only
not enough to turn on the lamp again. Just like an ordinary candle, it stays out when it has been blown out. The unit can be built up and tested as shown in Fig. 3.2.

## CONSTRUCTION

There is a lot of scope for your imagination in making this circuit look like a real candle. The drawing (Fig. 3.3) shows one way of doing it, but there are lots of other equally good ways.

## FEEDBACK

Most electronic circuits have some kind of output signal. The output signal may be sound, movement, electric current, or (as in this month's circuit) light. A small part of this signal may be taken and fed back to the circuit from which it came. The signal fed back is used to modify the output of the circuit.

Feedback may be positive or negative. If it is positive, an increase in the output signal leads to a further increase in output signal (until it reaches its maximum). If it is negative, an increase in output signal leads to a decrease in output signal (until it is zero or reaches a stable level).

This month's circuit shows positive feedback. We shall discuss negative feedback in later issues.

Next Month: Using a Thermistor.
You will Need...
R1 Thermistor, type VA 1040 (or any similar type with a resistance of about 150 ohms at $25^{\circ} \mathrm{C}$
VR1 Variable resistor 1 k
LP1 Filament lamp 6V 0.06A with holder
D1 TIL209 or similar light emitting diode
B1 Battery-box with four 1.5 V cells

Breadboard (e.g. Verobloc); pointer-knob for VR1.

# CARTIMER 



## $\square$ D.BUTLER

$\square$

Don't choke your car or collect parking tickets. The choice is

## yours!

The Car Timer project was originally designed as a "choke-on" reminder for cars which do not have any such indication. However, other applications can easily be found, as the circuit is a timer with an alarm. The timer is fully adjustable, as is the alarm note, it consumes little power and occupies a modest amount of space.

## CIRCUIT DESCRIPTION

The timer is formed around IC1 (Fig. 1), a 4060 14-stage ripple counter and oscillator. Components R1, R2 and Cl set the oscillator frequency, hence the timing period. The reset pin (pin 12) is provided with a pulse every time the supply is connected by the action of C2 and R3. In this way the timer is always initiated at the same point: The timer is activated by applying +12 V to the supply line. Particular ways of achieving this will be considered later. The actual timing period is adjusted by connecting an oscillator output to the alarm circuit described below.

## PIEZO ALARM CIRCUIT

The alarm circuit produces a twin tone which may be modified by changing componenis C3, R5 and C4, R6. The transducer


Fig. 1. Complete circuit diagram for the versatile Car Timer. Note timing link LK.1.
(WD1) is a ceramic piezo-electric element which generates tones when a 3 V peak square wave is applied. Rigid mounting methods ensure maximum volume levels, which is necessary in the environment of a car. The element has no moving parts, is robust and consumes little power.

A final mention should be made of diode D1, which protects the circuit from reversal of power connections.


| Table 1 |  |
| :---: | :---: |
| Connect LK1 to: | Approx. time |
| a | $\frac{1}{2} \mathrm{sec}$ |
| $b$ | 1 sec |
| $c$ | 2 sec |
| d | 4 sec |
| e | 8 sec |
| f | 18 sec |
| $g$ | 36 sec |
| h | 2 min 23 sec |
| i | 4 min 45 sec |
| j | 9 min 30 sec |

NOTE: The times given are also the "on" time of the alarm tone, e.g. LK 1 to J. Alarm activates after 9 min 30 sec . Alarm turns off after 9 min 30 sec unless supply is removed first.
Figures quoted are for the following conditions:

$$
\begin{array}{lc}
R 1=4 k 7 & \text { Supply voltage } \\
\text { R2 }=39 k & =12 \\
C 1=1 \mu &
\end{array}
$$

## CURRENT CONSUMPTION

Based on the component values given above and 12 V supply: Timer on $=$ 0.75 mA excluding l.e.d.; with alarm $=$ $1: 7 \mathrm{~mA}$. (Alarm tone alters current drawn from 1-5 to 3 mA as frequency increases.)

[E5T06


Fig. 2. Component layout and printed circuit master (full size), see EE PCB Service. Note a link wire should be attached to LK1 and one of the timing pins.

## COMPONENTS

| Resistors | See |  |
| :--- | :--- | :--- |
| R1 | $4 k 7$ |  |
| R2 | $39 k$ |  |
| R3 | 470 k |  |
| R4 | 100 k |  |
| R5 | 1 M | page 485 |
| R6 | 8 k 2 |  |
| R7 | 470 (if required) |  |
| $1 \mathrm{~W}+5 \%$ | carbon types |  |

## Capacitors

| C1 | $1 \mu 16 \mathrm{~V}$ tant. bead |
| :--- | :--- |
| C2 | 100 n polyester |
| C3 | 100 n min. layer <br> polyester |
| C4 | 22 n min. layer <br>  <br> C5 |
| polyester <br> 100 n polyester |  |

## Semiconductors

| IC1 | 4060 ripple counter <br> and oscillator <br> 4011 dual input |
| :--- | :--- |
| IC2 | NAND gates |
| D1 | 1N4148 diode <br> D2 |
| I.e.d. with mounting <br> clip |  |
| WD1 | piezo transducer <br> PB2720 |

## Miscellaneous

Printed circuit board (available from the EE PCB Service Order Code 538) or Veroboard; case (if required) (Verobox part no. 202-21024B $71.5 \times 49 \times$ 24.5 mm ); mounting hardware; spade connectors; switch to activate timer, e.g. microswitch, mercury tilt, push button (see text).
gly


## EEgT16

Fig. 3. Component layout and details of breaks to be made in the underside copper strips of the stripboard version.


The completed stripboard version. Note the link wire (LK 1) attached to one of the timing pins.

## CONSTRUCTION

The circuitmay be constructed using the p.c.b. layout (Fig. 2) or the Veroboard layout (Fig. 3). No special construction techniques are used, except that care should be taken when soldering in ICI and IC2 (sockets could be used). Remember to add a wire link from LK1 to one of the oscillator outputs using Table 1 for the intended timing period. If an l.e.d. indicator is used to show the timer is on, connect it to any
spare oscillator output via a 470 ohm current limiting resistor R7.

The piezo sounder should be firmly mounted to obtain maximun sound levels. Connection to the outside world, and the type of project case used depends largely on the application. The prototype was mounted in a small Verobox (part number 20221024 B ) and connections made using spade terminals and "Scotch-block" break-in connectors.

## SUGGESTED APPLICATIONS



Fig. 5. Heated rear screen circuits, (a) without relay (b) with relay.



Fig. 4. Circuit of choke switch with warning light. The mechanical arrangement is shown left: 1 using microswitch and 2 using reed relay with magnet.

## APPLICATIONS

As mentioned earlier, the project was originally designed as a "choke-in" reminder and, therefore, the unit needs to be activated when the choke control is used. Obviously it is not possible to give detailed switching methods used on every type of car, but Figs. 4 to 6 illustrate several alternatives.

The timer may be activated easily if the choke control already has a switch included, usually for a dashboard warning lamp. In many cases this switch is connected to earth which means that the timer +12 V should be permanently joined to +12 V via the ignition. Using the choke cable is slightly more difficult, and sections (1) and (2) show switching methods.
(1) Shows the choke cam plate on the side of the carb. A miniature micro switch is mounted where indicated by "*". The normally closed contacts are used as the microswitch lever will be released as the choke is turned on.
(2) Shows the choke cable near the control knob. A bracket ( X ) is attached to the control knob mounting thread, on which is a reed switch $(\mathrm{Y})$. This is connected to +12 V via ignition, and the +12 V switched line of the timer. A small magnet is attached to the cable using twisted wire and insulation tape. The distance from the reed switch is judged by pulling the choke out and seeing whether the reed switch has engaged. Push the control back in and check that the switch has turned off. You may be surprised how far the magnet has to be fastened from the reed, as they are very sensitive. The reed and magnet method may be employed at either the control knob or carb end.

Fig. 5 also shows how the timer may be used on the heated rear screen. If the alarm goes off and the heater is still needed, just turn the switch off and then back on. The alarm will be cancelled for another time period. Finally Fig. 6 shows a parking meter and egg timer using two Veroboxes back to back.

Use two miniature Veroboxes without lids, fix lower sections using dowel, e.g. matchsticks. A 9 V battery fits in top section, the circuit board in the lower section. Activate timer using an ultra-miniature toggle or slide switch. Adjust timing components to suit application, e.g. increase C2 to increase the time periods of the outputs.


## 10W AUDIO



AMPLIFIER

Designed to be extremely versatile and useful, this amplifier provides 10 watts r.m.s. sine wave output power ( 20 watts peak) and will accept a wide variety of inputs. There are two "flat" inputs, one for dynamic and electret microphones, guitar pick-ups and other low signal sources. The other input accepts signals at standard "line" levels between 100 mV and one volt. A third, completely independent input is provided with full disc RIAA equalisation for use with moving magnet pick up cartridges. The inputs can also be mixed to blend announcements with music, etc.

Three projects under one title-all simulations of the Knight Rider lights from the LIGHT RIDER TV series. The three are: a lapel badge, using six l.e.d.s, a larger l.e.d. unit with 16 l.e.d.s and a mains version capable of driving six mains lamps totalling over 500 watts

$$
1000000000
$$



The Micro-Tracer shows an interesting way in which a computer and two integrated circuits can be used as a signal injector and tracer. The software has been written for the BBC, C64 and PET series of computers.


EV W

## ...from the wol

## YOLAM ELECTROMHI OESHMER RWAROS

## $\star$ Umpire's decision wins top prize, and a job!, for Stephen and Howard

## $\star$ Tim breaks sound barrier to win the Intermodiate Class

$\star$ Gareth "gets on his bike" to win the Junior Category

Now in its second year, the Young Electronics Designer Awards Scheme is run by Cirkit Holdings in conjunction with cosponsors Texas Instruments and Electronics Times-a trade magazine. The competition challenges students to design and construct an electronic device with a possible application in everyday life.

After the preliminary rounds, the final aspiring designers gathered in the Great Hall at the Westminster School, London, recently to hear whether they were to be amongst the prizes. The top prize in the Senior Category carried a reserved place in the Texas graduate intake-a job!

This year, the competition was divided into three sections, senior (19-25), intermediate (15-18) and junior (under 15). Texas also donated a business computer to the establishment sponsoring the winning entries in the older age groups and 30 TI calculators to the school sponsoring the winner in the junior class.

The 1986 winners were announced by Maggie Philbin, presenter of BBC's Tomorrow's World, and presented by the Rt. Rev. Michael Ashley Mann, The Dean of Windsor, and Dr. Robb Wilmott CBE, Chairman of European Silicon Structures.

Final Results
After the opening ceremonies and speeches the big moment arrived and the judges' final decisions were revealed.

The winners in the senior category (19-25) went to Stephen Osborne (24) and Howard Mitchell (21) of Essex University for a Netball Umpire's Electronic Scorer. They received $£ 500$, $£ 450$ a year sponsorship to complete their studies, a vacation job and a


Dr Robb Wilmor CBE and the Rt. Rev. Michael Ashtey Mann congratulate Stephen and Howard on becoming the winners of the Senior Section
reserved place in TI's graduate intake; upon graduation.
Second place ( $£ 250$ ) went to Michael Scott (21), David Boyd (20) and Douglas Mackay (20) from Thurso Technical College for their Robotic Rehabilitation Arm. Third position ( $£ 100$ ) was awarded to Adrian Travis (23) for his entry Electronic Teaching Bricks.

## Intermediate Class

In the intermediate category, the winner was Tim Price who received $£ 350$ for his design of a Digital Audio Processor using a 16 bit processor and 128 K of RAM
The runner up in this section went to the only girl entrant, Rosemary Erskine (18) of Norwich High. Her Digital Anemometer won Rosemary $£ 200$. The $£ 75$ third prize went to James Lucy (16) for his BBC Micro Stage Lighting System.

## Junior Class

Winner of the junior class ( $£ 250$ ) was Gareth Arthurs (13) for his novel Bicycle Lighting Unit. The unit switched the cycle lights between dynamo and battery according to the dynamo output, preventing them from being extinguished when the cycle stops at road junction, traffic lights or is parked.

Second spot ( $£ 150$ ) went to David Marshall (14) for his Wheelchair Controller project and a Thermometer for the Blind or Partially Sighted won $£ 50$ for Jonathan Ibbotson (14).
For details of how to enter next year's competition, write to: Carla Sharyk, Young Electronic Designer Awards, Standard House, 16-22 Epworth Street, London, EC2A 4SX.

(above) The Robotic Arm is demonstrated to Maggie Philbin (BBC) and Peter van Cuylenburgh (far right) of TI by Michael, Douglas and David.
(left) The Digital Audio Processor that won first prize for Tim Price in the Intermediate Category.
(right) Gareth (13) with his trophy and bicycle lighting unis that won top prize in the Junior Class.


# of electronics 

## INDUSTRY YEAR AWARDS

A $£ 115.000$ scheme to encourage closer links between industry and education has been announced by Minister of State for Industry, Peter Morrision.

Mr Morrison said: "1 am pleased to announce that my department is launching the Industry Year Award to encourage collaboration between Industry and institutions of further and higher education We are offering $£ 115,000$ in cash prizes, to include one for the best example of collaboration with a small firm.
"Prizes will be given in three areas of collaboration: course development, training programmes and technology transfer. The winner in each section will receive. $£ 25,000$, Runners-up $£ 10,000$ and $a$ special prize of $£ 10,000$ will be offered for collaboration with a small firm.

## RADIO DATA

Radio Data System (RDS) is the new system, due to commence broadcasting in September, that will, it is claimed, make it easier for listeners to find their favourite radio programmes.

An inaudible signal will be added to v.h.f./f.m. transmitters which will enable a new generation of receivers to perform a variety of automatic functions. These range from advanced automatic tuning with a readout of the station name, a clock that is always accurate, instant switching to pick up traffic messages on other channels to the provision of a visual readout of music details of the concerto you are listening to.

The signals to control these functions are broadcast as digital codes in parallel with the main programme.

Sir George Jefferson, Chairman of British Telecom, announced that the group had achieved a profit of $£ 1,828$ million for the year ended March '86. This was 20 per cent up on the previous year

Of this, $£ 18$ million was being allocated to the new employees' profit sharing scheme.

## FREE MODEMS

Following the purchase of substantial stocks of modems, Micronet have announced that they will be issuing them free to members subscribing for one full year in advance to both Micronet and Prestel.
Following the example of the French, they hope that this precedent for the UK will encourage Prestel to do likewise

## Just Tandy

It has been reported that Tandy is to close half of its Computerworld stores because they are not making enough money. Instead certain areas will be covered by sales staff working from home, supported by their nearest centre.

Eight stores are affected and cover the areas of Bradford, Crawley, Croydon, Hull, Leicester, Liverpool, London's Victoria and Southampton. This move will leave around 40 staff to be redeployed, retrained or made redundant.

## The Watford Gap

One of the UK's leading mail order suppliers for BBC Micro accessories. Watford Electronics, is to break new ground by setting up a dealer network.

The move means that in future Watford's vast range of products comprising of hardware, software and peripherals will be available from selected dealers.

Commenting on this new venture, Watford's managing director Nazir Jessa said, "There are a lot of people who don't like to purchase via mail order and who would gladly pay a little extra if only they could buy from their local dealer."

## Comet's New Dish

Comet became the first major national retailer to enter the satellite age when two complete satellite TV receiver packages went on sale at the end of last month (July).

A basic single satellite system, offering up to eight additional TV channels, is available for the sum of $£ 890$ and a multi-satellite system, offering 14 extra channels, is being offered at $£ 1190$. The prices include full installation, 12-month guarantee, and a "lifetime" satellite dish licence.
Pilot stores to carry stocks of the satellite packages are: Hull; Norwich; Leeds and Rochester. There is no extra charge for presite visits the north of England. with ropes and pulleys. around him.

Claimed to be the largest European exhibition of musical instruments, hi-fi, in-car and TV/Video products, the 20th SIM-HI.FI-IVES show will be staged in Milan from 4 to 8 September 1986

## ROYAL SOCIETY FELLOWSHIPS

British Telecom has agreed to sponsor Royal Society Fellowships for Chinese scientific research workers to visit the UK to work in universities and research institutes. Awards will be made in fields of particular interest to BT, such as telecommunications and information technology.

Fellowships will normally be for periods of six months. Awards will cover living expenses in the UK, but not international fares which candidates will be expected to find from their home organisations.

> Mr. David Pentecost is to be British Telecom's new Chief Executive, Procurement, in charge of the company's $£ 1,900$ million a year purchasing programme.

## CALLING OF THE CHURCH

Five Jlagpoles adorn the roof of Holy Cross Church in Knutsford, Cheshire, but only one is real. The others are aerials which bring British Telecom's Cellnet cellular mobile radio network to this part of

The church tower is one of the highest points on the local landscape, and an ideal location for the aerials. Concerned not to spoil the view, BT decided to disguise them as Jagpoles, complete

Pictured here, the Vicar of Holy Cross Church, the Reverend Paul Moulton, stands next to the real flagpole with the four Cellnet aerials

I am pleased that the Church agreed to placing the aerials here. They are absolutely out of the way and attract no attention-it's a good example of caring for the environment", he says.


## AURORA

Many radio amateurs have rotatable antennas enabling them to transmit their signals in the direction of the station they wish to contact. Sometimes, however, it is more effective to beam in a completely different direction

An example of this is auroral propagation, which becomes possible when a solar flare releases energy from the sun. Energetic charged particles are carried to earth by the solar wind, impinging on the upper atmosphere and ionizing the Elayer in the auroral zones around the poles.

The resulting aurora act as reflecting layers, and by directing their VHF antennas northwards amateurs make contact with distant stations who are also beaming to the riorth. To take full advantage of the phenomenon, advance warning is necessary. Beacon stations nearest to the auroral zones can provide the first signals to be reflected to more southerly climes, and some enthusiasts have receivers monitoring beacon frequencies continuously for any sign of an auroral event.

Others tune to Leicester University's experimental auroral monitoring radar station at Wick, on 153.2 MHz , and others to Scandinavian TV video carrier transmissions around 50 MHz . Immediately signals are heard above a certain strength, warning networks are activated to alert others of possible aurora.

Events are sometimes repeated after 27 days, coinciding with the rotation period of the sun. Some amateurs keep a 27 day calendar to record occurrences and forecast future events. Aurora follow a seasonal pattern, peaking around March and September, although they can occur at any time throughout the year. When radio aurora occur at night in clear weather, there may also be visible aurora.

Amateurs in northern locations have the greatest success, but in major events stations in the south of England, and in Europe as far south as Italy, can participate. Signals received have a distinctive rough sounding note, and are slightly off frequency due to Doppler shift. This effect becomes more noticeable the higher the frequency of the signals, which are receivable up to 432 MHz on occasions. Morse is the favoured mode of transmission, as weaker signals are easier to copy, but s.s.b. speech can be used, especially on 50 MHz where Doppler shift is minimal.

Last February, despite the current low point in the solar cycle, there was an unexpected major aurora, causing great excitement among many amateurs.

British v.h.f. stations, even in the south, made exceptionally long distance contacts into eastern Europe. Scandinavia, and USSR call-areas. In the USA, the effect of the aurora reached nearly as far south as Florida, with record-breaking contacts of up to 1348 miles on two metres, and stations spread out across normally sparsely populated bands trying to find clear frequencies.

Auroral working is a fascinating activity, combining the possibility of outstanding success with some degree of uncertainty. Next time you hear on the news that the aurora borealis can be seen from your area, think of all the local amateurs rushing to their rigs. Better still, if you know anyone with amateur v.h.f. receiving facilities, get round there fast, and listen in to the action!

## NICE DAY OUT

In 1984, the Dunstable Downs Amateur Radio Club hit on the idea of holding a national amateur radio car boot sale, with few traders, just amateurs selling to amateurs at bargain prices. They held it at Old Warden Aerodrome, Biggleswade, Beds, home of the Shuttleworth collection of

## QUESTION CORNER

## Q. Is there any type of simple receiver I can build to receive amateur

 transmission?A. Traditionally, the regenerative receiver has been recommended for beginners to listen to amateur signals. Simple designs using just one or two transistors have appeared in EE, and this type of circuit will receive single sideband speech, Morse, and a.m. transmissions surprisingly well.

In recent years there has been a lot of interest in direct conversion receivers which are equally easy to make, although there is a need for a stable variable frequency oscillator in the circuit. A simple receiver of this type is generally claimed to give better results than a regenerative circuit.

historic aircraft, and it was an instant success.
.Last year the event was repeated, with over 100 car boots offering components, accessories, receivers, transceivers, computers, even household and motoring items. Now an annual event, this year's sale, on Sunday, 21 st September, promises to be even bigger and better, and an outing for all the family.

The aircraft and motor museum is an attractive venue in its own right. Combine this with an opportunity to find out something about amateur radio at first hand, including a special-event talk-in station, GB4SC, visit the RSGB's bookstall and information stand, and pick up some useful components or other goodies for that next EE project, and you have a fine day outl

For 50p admission, deductible from the museum admission charge of $£ 1.50$, plus free car parking, it must also be one of the bargain outings of the year!

Apart from organising this event, the Dunstable club's activities include radio DF (direction finding) hunts; amateur TV; construction contests; and visits to places of interest. They participate in radio contests from a site on Dunstable Downs; provide the call-in station for the big RSGB rally at Woburn in August; and have a good , programme of lectures throughout the year.

Newcomers to amateur radio are welcome in all these activities. Contact Phil Morris, G6EES, on Dunstable 607623, for further information, mentioning this column

The diagram shows the basic idea. The wanted r.f. signal is mixed with the output of a local v.f.o. This oscillator is tuned slightly off the frequency of the incoming signal, and the difference between the two signals is at audio frequency, which can be filtered to provide any desired degree of receiver selectivity before audio amplification.

A disadvantage of d.c. receivers is that they provide double signal reception for Morse, as the sidebands of the wanted transmission can be resolved as audio signals on either side of zero-beat, the tuning point where the incoming signal and the local signal cancel each other out.

This turns to an advantage in receiving single sideband signals, when the appropriate sideband can be selected to give good reception. A.M. signals are resolved by tuning to zero-beat.

Obviously, the simpler a d.c. circuit is, the more its limitations, but even in its simplest form it can provide a useful introduction to amateur radio. Improvements can be made by using r.f. amplifiers/attenuators, improved detectors and audio filters, and by other means, and high quality performance then becomes possible.

A particular attraction of the d.c. receiver to amateurs is that the local oscillator can also serve as the v.f.o. for a low power transmitter. This arrangement is particularly popular for Morse transmission, and some of the QRP operators I have mentioned previously used transceivers of this type to extremely good effect.

# SHOP TALK if 澥 <br> <br> BY DAVID BARRINGTON 

 <br> <br> BY DAVID BARRINGTON}

## Eureka!

Leading supplier to the BBC Micro market, Watford Electronics has unveiled a new RAM Expansion Card called the Eurekal

Announcing the latest addition to their range of BBC Micro expansion products Mr. Nazir Jessa MD said "The Eureka Card would have made Archimedes jump out of his bath twice as quickly!
Not only is the Eureka claimed to be cheaper than a Second Processor, but it also provides up to 14 K more RAM than a 6502 Second Processor gives to the " Hi " versions of View Basic and Wordwise Plus. Shadow RAM is provided automatically

The card fits into the processor socket of the BBC Micro and is controlled by software contained within a standard sideways ROM. It is fully buffered, not only to the on-board RAM but also to the BBC Micro as well.

A new set of OSWORD calls are provided with the card to allow blocks of extra RAM to be written to and read from directly.
We hope to review the Eureka card in a future issue (we also hoped to include a photograph, but being so new Watford were unable to supply one). In the meantime further information and price may be obtained from: Watford Electronics, Dept. EE, 250 High Street, Watford WD1 2AN.

## Infra Red Beam Alarm

One or two items could cause purchasing problems for constructors of the Infra Red Beam Alarm project.

The lenses for focusing the infra red beam are stocked by Maplin. The I.e.d. type TIL38 and the detector diode TIL 100 are currently listed by Greenweld, TK Electronic, Maplin and Cricklewood Electronics. The VMOS power f.e.t. transistor type VN1OKM, called for in the transmitter, is stocked by Maplin and Cricklewood.
The relay used in this project is a low voltage, DIL package, "Reed" relay, with an internal diode connected across the coil. Other types of relay may be used provided they are able to operate down to about 4 V and have a coil resistance of 500 ohms.
However, the layout of the printed circuit may have to be altered to accommodate other relays and a diode will have to be "hard wired" across the coil contacts. In case of difficulty in locating a suitable relay, the one used in the prototype was purchased from Maplin: code FX88V

The printed circuit boards for this project may be purchased through our PCB Service: code EE536 and EE537

## Freezer Failure Alarm

The MOS/FET operational amplifier and the warning device used in the Freezer Failure Alarm should be stocked by most of our advertisers.

The printed circuit board may be purchased from our PCB Service: code EE534.

## Car Timer

The "spade" connectors for the Car Timer project should be available from any good motorists' accessories shop
The 4060 ripple-counter, with internal oscillator and the 4011 dual-input NAND gate devices are now fairly common and should not cause buying prọblems
The printed circuit board may be purcha sed from our PCB Service: code EE538.

## Simple Printer Buffer

Ready programmed EPROMs ( $£ 8.50$ ) and a double-sided printed circuit board ( $£ 9.75$ ) for the Simple Printer Buffer are available from Tayside Microsystems, Dept EE, 55 Causewayend, Coupar Angus, Perthshire, Scotland, PH13 9DX.

Most of the semiconductors and microprocessor crystal are also stocked by Maplin, CMC Components and Cricklewood Electronics. They can also supply the Centronics plug and socket.

## Spectrum Joystick

Looking through our collection of components catalogues, we cannot foresee any problems with locating a 6-pin optosolator for the Spectrum Joystick-this months On Spec project. The D-connector and 6 -way flexible cable should now be available from most of our advertisers
The quoted price for this project is based on the RS "microswitch joystick mechanism". Of course, this sum may be reduced by obtaining this part from a local computer shop or you can make one up from separate microswitches.

## Exploring Electronics

A suitable "test bed" for the Exploring Electronics experiments would be the circuit block used in our Teach In ' 86 series

## Scratch Blanker

The delay line chip type TDA 1022 called for in the Scratch Blanker project should be available from most component suppliers, such as: C.P.L. Electronics, Marco, TK Electronics, Greenweld and Cirkit.
The CMOS switch 4066BE is currently listed by Maplin, Magenta and Cricklewood. When specifying the 555 timer for IC9, be sure to order the low power, suffix L, or the CMOS ICM7555 device.

The printed circuit board may be ordered through the EE PCB Service: code EE539.


## .... Beeb....Bee....Beeb....Bee

 $-3$TEMS of test equipment that would once Ihave been technically difficult and horrendously expensive to produce are now available at prices which are not beyond the means of dedicated electronics enthusiasts. This includes such things as digital multimeters, dual trace oscilloscopes, and autoranging capacitance meters. With a computer having the versatile interfacing capabilities of the BBC machines it is possible to produce sophisticated test gear with the aid of very inexpensive add-ons which make conventional budget priced equipment look positively expensive. The two simple add-ons described in this month's article demonstrate this point very well. Both use only a handful of inexpensive components, and one converts the Beeb into an auto-ranging resistance meter while the other converts it to an auto-ranging capacitance meter. In the previous article we consider ways of using the user port to enhance the capabilities of the analogue port, and these two add-ons both exploit this idea.


Fig. 1. Arrangement used for Resistance to Voltage conversion.

## R to V Conversion

The analogue port does, of course, measure voltage, and in order to measure any other commodity using the analogue inputs it must first be converted to a proportional voltage. Resistance is something that is quite easily converted to a proportional voltage, and there is actually more than one way of tackling the problem. The most commonly used approach, and the one which is probably best suited to our current requirements, is the simple set-up shown in Fig. 1.
A constant current is fed to the test resistor, and this produces a voltage across the resistor. Ohm's Law states that "voltage $=$ current $\times$ resistance", and with the current at a fixed value the output voltage becomes proportional to the test resistance. For example, if you assume a current flow of one milliamp and work out the voltage produced with test resistance of $1 \mathrm{k}, 2 \mathrm{k}, 3 \mathrm{k}$, etc., you will find that the answers are one


Fig. 2. Circuit diagram for the auto-ranging resistance meter.

```
    10 REM AUTO-RANGING
    20 REM RESISTANCE METER
    30 REM J.W.P. 6/96
40 TeFEG2=7
    ?&FEG2
    60 MODE 7
    70 REPEAT
    OO PROCRANGE
        90 PROCSHOW
        95 FOR }X=1\mathrm{ TO 1000: NEXT
        UNTIL FALSE
    110 END
1000 DEF PROCRANGE
1010 v%=-1
1020 REPEAT
1030 V%=V%+1
1040 ?&FE60=V%
1045 FOR }X=1\mathrm{ TO 500: NEXT
1050 UNTIL ADVAL1 DIV 64<1023 OR V%>4
1060 ENDPROC
1070
2000 DEF PROCSCREEN
2010 PRINTTAB (5,5);CHR$(141);"RANGE"
2020 PRINTTAB (5,6);CHR$ (141);"RANGE"
2030 PRINTTAB(11,12);CHR$(141)
2040 PRINTTAB (11,13);CHR$(141)
O50 ENDPROC
2060
3000 DEF PROCSHOW
3005 V ==STR$ (V%+1):IF V%>4 THEN V = " = = 
3010 PRINTTAB (13,5);V$
3020 PRINTTAB (13,6);V$
3030 DISP$=FNREADING
3040 PRINTTAB(13,12);DISP$
3050 PRINTTAB(13,13);DISP&
3060 IF V%=0 THEN PRINTTAB(5, 20) "Reading in DHMS
3070 IF v%=1 OR v%=2 OR v%=3 THEN PRINTTAB (5, 20) "Reading in KILOHMS. "
3@B0 IF U%=4 THEN PRINTTAB(5, 20)"Reading in MEGOHMS."
3990 ENDPROC
3100
4000 DEF FNREADING
4005 IF U%>4 THEN R$="OVERLOAD":=R$
4010 R=ADVAL1 DIV 64
4020 IF V%=0 OR V%=3 THEN D=1
4030 IF V%=1 OR V%=4 THEN D=100
4040 IF V%=2 THEN D=18
4050 R*=STR$ (R/D)
4060 P=INSTR(R$,".")
4 0 6 5 ~ I F ~ P = 0 ~ T H E N ~ P = L E N ( R \$ ) ~
4070 R$=LEFT$(R$,P+LOG(D))
4080 IF LEN(R$)<B THEN R$=STRING$ (B-LEN(R$)," ")+R$
4090 =R 
```

volt, two volts, three volts, etc. It is important that any device measuring the output voltage has a very high input resistance, as this resistance is in parallel with the test component and will impair the accuracy of the system if it has a significant shunting effect on the test resistance. The output voltage is therefore taken via a buffer stage having an ultra high input impedance.

In theory this arrangement has an infinite operating range, but in practice several measuring ranges have to be included in order to cover a wide resistance range with good accuracy. With (say) just one range going from 0 to 10 megohms, most resistors would give an insignificant output voltage which could not be measured accurately, especially when using a digital voltage measuring circuit with a strict limitation on its resolution. Several ranges can be covered simply by having a number of different current levels available. For instance, a tenfold increase in the test current reduces the full scale resistance value by a factor of ten-reducing the test current by a factor of ten increases the full scale resistance by the same factor.

## Resistance Meter Circuit

Fig. 2 shows the circuit diagram for a very simple but effective resistance meter addon for the BBC machine, and with appropriate software this is capable of auto-ranging.
Transistor TR1 is at the heart of the system, and this is connected as a conventional constant current generator. The output current is controlled by the resistance


Fig. 3. A simple capacitance to voltage converter set-up.
between TR1's emitter and the positive supply rail. In this case there are five switched resistors giving the circuit five measuring ranges with full scale values of $1.023 \mathrm{k}, ~ 10.23 \mathrm{k}, 102.3 \mathrm{k}, 1.023 \mathrm{M}$, and 10.23 M . This covers all the resistor values commonly used in electronics.
Each emitter resistance actually consists of a fixed resistor and a preset type wired in series. The preset resistors are adjusted to give good accuracy on each measuring range. Selection of the emitter resistance could be made by way of manual switching, but in order to achieve auto-ranging it is obviously necessary for range selection to be under the control of the computer. In this case the switching is provided by three lines of the user port and a 4051 BE CMOS eight way analogue switch (ways " 5 " to " 7 " being

10REM ẢUTO-RANGING
20 REM CAFPMCITANCE METER
उDREM J.W.P. 6/B6
40
50? FFE62=7
GOMODE 7
65 FROCSCREEN
65 FROCSC
70 REPEAT
7OREPEAT
BOPROCRANGE
BDPROCRANGE
9ØPROCSHOW
100 UNTIL FALSE
$110 E N D$
$10 D 0 D E F$ PROCRANGE
1010 V\%=-1
102 DREPEAT
$1030 \mathrm{~V} \%=\mathrm{V} \%+1$
1040 ? \& FE $6=$ V\%
1045 FOR $x=10$ TO 2000:NEXT
$1050 U N T I L$ ADVALI DIV $64<1023$ OR V\%>4
106 EENDPROC
1060 E
1070
2000DEF PROCSCREEN
200DDEF PROCSCREEN
$2010 P R I N T T A B(5,5) ;$ CHR\$ (141) "RANGE"
$201 \triangle P R I N T T A B(5,5)$; CHR $(141)$ "RANGE"
$2020 \operatorname{RINTTAB}(5,6) ;$ CHR $(141)$ "RANGE"
2020FRINTTAB (5, 6); CHR\$ (141); "RANGE"
203@PRINTTAB (11, 12); CHR\$(141)
$2040 \mathrm{FRINTTAB}(11,13) ;$ CHRs (141)
2050ENDFROC
2060
SOOODEF PROCSHOW
300SV $=$ STR $(V \%+1)$ :IF $V \%>4$ THEN $V \$="="$
3010FFINTTAB $(13,5): V \$$
3020PRINTTAB (13,6);V
3030DISP\$=FNREADING
3040FRINTTAB (13,12); DISP\$
3050FRINTTAB (13,13); DISP\$
300OIF $V \%=0$ THEN PRINTTAB $(5,20)$ "Reading in PICOFARADS "
3070 IF $V \%=1$ OR $v \%=2$ OF $v \%=3$ THEN PRINTTAB (5, 20) "Reading in NANDFARADS.
$3080 I F V \%=4$ THEN PRINTTAB $(5,20)$ "Reading in MICROFARADS"
$30801 F$ V $=4$
$3090 E N D P R O C$
3100
4000DEF FNREADING
4005 IF $V \%>4$ THEN Fi s="OVERLOAD": =R
$4010 F=A D V A L I$ DIV 64
$4020 \mathrm{IF} \quad V \%=0$ OR $V \%=3$ THEN $D=1$
4030 IF $\quad V \%=1$ OR $\quad V \%=4$ THEN $D=100$
$4040 \mathrm{IF} \quad \mathrm{V} \%=2$ THEN $\mathrm{D}=10$
$4050 R=5 T R \$$ (R/D)
4060P=INSTR (R\$, ".")
$40651 \mathrm{~F} P=0$ THEN $P=$ LEN(R) $)$
$4070 R \$=L E F T \$(R \pm, P+L O G(D))$
40日® IF LEN (R\$) <B THEN R\$=STRING\$ (日-LEN(Rक) " " ") +R
$4090=\mathrm{R}$ \$

> Listing 2: CAPACITANCE METER
left unused). IC1 has a significant resistance of about 200 ohms through whichever switch is activated, and this resistance forms a large part of the emitter resistance on the lowest range. This is unfortunate as it does not aid good accuracy and stability on this range, but in practice results seem to be perfectly adequate in both respects.

IC2 is the output buffer stage, and this is a MOS input device which provides an input impedance of over a million megohms. It therefore ensures good linearity even on the 10.23 megohms range. On the face of it the buffer stage is unnecessary as the analogue inputs of the BBC computer have an extremely high input resistance anyway. Despite this it is often better to include a buffer in add-ons added to the analogue port so that signals in the connecting lead to the computer are at a low impedance and not a high impedance of several megohms. This avoids problems with excessive noise pickup in the connecting cable.

## Software

Suitable software for the resistance meter add-on is given in Listing 1, and this supports auto-ranging. This facility operates in essentially the same manner as the auto-ranging voltmeter circuits described in the previous article, and a description of the process will not be repeated here. In order to calibrate the unit five test resistors are required, and these should be one per cent types having values which correspond to roughly half the full scale value of each range (e.g. $470,4 \mathrm{k} 7,47 \mathrm{k}, 470 \mathrm{k}$, and 4 M 7 ). Start with the preset resistors at about half resistance, then connect each resistor in turn and adjust the appropriate preset for the correct reading. The display indicates the range in use, and VR1 to VR5 operate on ranges 1 to 5 respectively.

## Capacitance Measurement

There are several ways of providing capacitance to voltage conversion, but these are mostly variations on the basic method of using a C-R timing network with the test capacitor as the capacitive element in this network. Fig. 3 shows the most simple type of converter, and this is the type which is used in this capacitance meter add-on.
The pulse generator produces a constant stream of pulses which are used to continuously trigger the monostable multivibrator. Each time it is triggered the latter provides an output pulse having a duration that is determined by the $\mathbf{C - R}$ timing network, and which is proportional to the value of the capacitor. With a small test capacitance the output from the monostable is a series of brief pulses, giving a low average output voltage. A high test capacitance gives a long


Fig. 4. Circuit diagram for the auto-ranging capacitance meter.
output pulse and a high average output voltage. In fact the average output voltage is proportional to the value of the component under test provided the value is not so high that new trigger pulses are received before each output pulse has finished (which would give a simple frequency divider action and a low output voltage).

Although the average output from the monostable is proportional to the test capacitance, the signal here is a series of pulses which the analogue converter in the BBC machine will read as zero, something beyond full scale value, or somewhere between these two extremes if a pulse starts or finishes during a conversion. This problem is easily overcome, though, and all that is required is a lowpass filter at the output of the monostable to integrate the pulses and provide a reasonably ripple free d.c. output, signal.

## Circuit Operation

The full circuit diagram of the capacitance meter add-on is shown in Fig. 4. ICl operates as the pulse generator, and this is a standard 555 astable circuit. The values of RI and R2 have been arranged so that the output signal is a series of narrow negative pulses, which is the type of signal required for the correct triggering of the monostable. The monostable is based on IC3 which is another 555 device, but this time the low power CMOS (7555) version is specified. This is not a matter of reducing the current consumption, and the advantage of the 7555 in this case is its lower self capacitance. This gives improved accuracy with low value capacitors, but the self capacitance is still sufficient to boost very low readings and give poor accuracy. Values of a few tens of picofarads or less are little used,

and so this is not a major drawback, but to some extent it can be counteracted by deducting the self capacitance from readings on range one. The amount by which readings should be offset is the capacitance reading obtained on range one with no test capacitor connected.

Range switching is again accomplished by a 4051 BE controlled by three user port lines. The five ranges have full scale values of $1.023 \mathrm{nF}, 10.23 \mathrm{nF}, 102.3 \mathrm{nF}, 1.023 \mu \mathrm{~F}$, and $10.23 \mu \mathrm{~F}$.

## Software

The program for use with the capacitance meter add-on is given in Listing 2, and this is just a slightly revamped version of the resistance meter program. Again, for calibration purposes close tolerance components having values roughly equal to half the full scale value of each range are required (e.g. $470 \mathrm{pF}, 4 \mathrm{n} 7,47 \mathrm{nF}, 470 \mathrm{nF}$, and $4 \mu 7$ ). A compromise may have to be accepted on the highest range as a suitable calibration component might be difficult to obtain. As before, it is just a matter of connecting each calibration component and then adjusting the appropriate preset for the correct reading.
The circuits shown here and in the previous article should give some idea of the versatility available by using the analogue and user ports together. Those who like experimenting might like to try combining the circuits to produce an auto-ranging multimeter, and the basic techniques used in these designs can be applied to other measurement applications. Where possible electronic switching should be used as it offers high speed, low cost, and low power consumption. Its disadvantages are a lack of isolation between the control signal and the signal being switched, and what is likely to be of more relevance in this context, around 200 ohms resistance through each switch. Reed relays offer a better alternative where isolation and (or) low "on" resistance is needed.

If you have any comments or ideas for inclusion in the Beeb Micro pages, please send them to: Everyday Electronics, 6 Church Street, Wimborne, Dorset BH21 IJH.

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## METER SHUNT <br> CALCULATOR

THE alignment chart, shown in Table 1, enables you to adapt a current meter to a higher current range. To do so, a resistance "shunt" must be connected across the meter terminals. By selecting the appropriate shunt resistance the current range can be multiplied by any desired factor.

To use the chart all you need to know is the internal resistance of the meter (usually marked on the dial) and the multiplication (M), see Fig. 1. A straight edge or thread stretched across the chart then gives the required shunt reistance.

EXAMPLE: A $0-10 \mathrm{~mA}$ meter is required to read $0-30 \mathrm{~mA}$. This means that the multiplication factor is 3 . If the meter resistance ( Rm ) is 20Q, what shunt resistance (Rs) is needed?

As shown dotted on the chart, when $M=3$ is joined to $R m=20 Q$ the line cuts the Rs scale at $10 \Omega$ and this is the required value of shunt.
This kind of chart cannot be read reliably when the multiplication needed is large For multiplications of more than 11 (where this chart runs out), calculate Rs from the formula:

$$
R s=\operatorname{Rm} /(M-1)
$$

EXAMPLE: A $100 \mu$ A meter with a resistance of $1 k$ is to be adapted to read 100 mA . In this case $M=1000$ and $R s=1000 / 999=1.001 \Omega$.

In practice a $1 \Omega$ resistor could be used. Its tolerance should be as close as possible, preferably 1 per cent or better.

Fig. 1.


## MEASURING METER RESISTANCE

When Rm is not marked on the meter it can be measured. Ohmmeter measurement is not accurate enough funless youhave a digital meter) and may pass a damaging amount of current through the meter.

Some current meters are designed to drop a standard voltage (such as 100 mV ) when passing full-scale current. If this voltage is known, the resistance can be calculated. For a 100 mV full-scale drop, a 1 mA meter has a resistance of $100 \Omega$; a $100 \mu \mathrm{~A}$ meter 1 k ; a 10 mA meter 10 Q ; and so on. However, meter resistances vary, so if there is no dial indication of either the resistance or the full-scale voltage drop a measurement is needed
The method shown in Fig. 2 may not be the last word in accuracy but it calls for the minimum of standards of comparison. WARNING: it only works on meters with linear scales
With S1 open, the meter current is adjusted by VR1 to produce a deflection at or near full scale. Note the reading carefully, Close switch S1 and connect close


Fig. 2. Current meter resistance circuit.

Table 1: CURRENT METER RANGE EXTENSION CALCULATOR

tolerance resistors R2 until you find one which reduces the reading by very roughly half. Note the new reading.

Divide the first reading by the second to give a number, $M$. Join this number on the M scale of the chart to the value of your R2 resistor on the central (Rs) scale. The line now cuts the Rm scale at the meter resistance.

## CIRCUIT VALUES

The meter test circuit will operate from any voltage above 3 V . The only requirement is that this voltage should not change when the current changes. You can use fresh dry cells (U2 size), or a car battery, or a stabilised mains power unit.

Resistor R1 must be chosen to suit the voltage and the l.e.d. Red l.e.d.s drop about 1.6 V when lit, and the silicon diode D1 drops about 0.7 V . These voltages add up to about 2.3 V and R 1 receives the difference between 2.3 V and B 1 .
So if B 1 is 6 V , the remaining 3.7 V appears across R1. In this case, if the l.e.d. needs 10 mA to light, R 1 must be around $370 \Omega$. (Use $330 \Omega$ or $390 \Omega$.) For other voltages and l.e.d.s, calculate on the same lines.

The circuit will cope with meters up to about 100 mA full scale. It is convenient to gang S2, the on/off switch, with potentiometer VR1. If VR1 is of the "inverse log." (reverse log.) type, meter current in-

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Wanted: replacement pick-up cartridge for Dynatron Espresso model GR5 record player. Good price paid. Rhydian Blackmore, Can-y-Lloer, Fformers, Llonwith, Dyfed, Wales. Tel: 05585429.

Advance J3B signal generator sine/ square wave, output $10 \mathrm{~Hz}-100 \mathrm{kHz}$. Includes cables and manual. Only $£ 100$. Richard Hind, 13 Cobwell Road, Retford, Notts. DN22 7BN. Tel: 0777701040.

D×200 Communication Receiver. Unused. £100. Aquarius computer. New. £40 o.n.o. Computer terminal keyboard. Excellent value $£ 15$. J. Howells, 118 Heritage Park, St. Mellons, Cardiff, S: Glam. Tel: 0222797956.

Hobbyist has thousands spare components for disposal. New and used. Sell cheap or swap for W.H.Y. J. De-Almeida, 106, Thorney Park, Wroughton, Swindon, Wilts. SN4 OQT. Tel: 0793 812566/812291 ext. 252.

EE (Dec.) Digital Capacitance Meter in full working order with engraved front panel. Also pcb. Offers. Tel: 0248722697.

2532 EPROMS E2 4116 50p. Rhythm Ace Electronic Drums $£ 40$. Commodore Plus 4 disk plotter-new, £250. J. Howells, 118 Heritage Park, St. Mellons, Cardiff, S. Glam. Tel: 0222797956.

Stepper Motors 10 V 4-phase 0.48 A $7.5^{\circ}$ per step $£ 10$ each. Harris digital data book (CMOS) £3. C. Orpin, 27 Cowleaze, Chinnor, Oxon. Tel: Kingston Blount 52405.

Wanted: Electronics Monthly December 1984, January 1985, February 1985 to buy or on loan. Tom Munnelly, 16 Seabury Road, Malahide, Co. Dublin, Eire.

Maplin Frequency Counter $10 \mathrm{~Hz}-500 \mathrm{MHz} .8$ digit variable time gate. Mains/battery operated. Fully calibrated £ 140 (o.n.o.). D. Pratt, 2 Slades Lane, Meltham, Huddersfield, W. Yorks. Tel: 0484850327.

Wanted: Manual or circuit for video circuits V31A. Tube booster or anything for TV repairs. Peter Makin, 6 Fairisle Close, Clifton Estate, Nottingham. Tel: Nottingham 215460.
Wanted: Mullard E10-1 1GH CRT for Solartron scope. Also plug-in amps (CX1270 ) if possible. Tel: Weybridge (0932) 44154.

Complete set Everyday Electronics November 1971 to May 1986. Three binders. £75. Tel: Brighton 562119.

Transistor tester logic probe plus many components, switches, leads etc. All unused. £25. Tel: Tunbridge Wells 29033.
creases with clockwise rotation. Single inverse log. potentiometers may be hard to find, but one section of a stereo balance pot is usually reverse log, or something like it.

## OSCILLATION

The purpose of capacitor C 1 is to discourage h.f. oscillation, which can occur in this type of circuit, especially at high currents. If erratic results are obtained, this may indicate that the circuit is oscillating despite C1. Try a different value or put a second capacitor between base and collector of TR2.

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Everyday Electronics June 1981 to December 1985. Some missing. Individual sale. Reasonable offers considered. D. Strawn, 25 Stephens Drive, Inverkeithing, Fife KY11 100. Tel: 0383417695 (after 5 p.m.).

Wanted: Philips 22 RR5 22 Portable Ra-dio-Recorder. Complete or the R/Rec. section switch p.c.b. board. Mr. R. Barlow, 55 Carolyn House, Durrington, Worthing, Sussex BN13.

Wanted: Any modern TV which has I.R. controls and multi-standard switching (two required if possible). Richard Shidhu, 115 Streatfield Road, Kenton, Middx. HA3 9BL. Tel: 01-204 4838.

Newnes Radio \& Television books, servicing manuals, service sheets, magazines and books-send s.a.e. for list. Mr. F. D. Brown, 6 Ryan Close, Ferndown, Wimborne, Dorset BH22 9TP.

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# SCRATCH BLANKER 

## Bring your old records back to life

THe compact disc, with its click-free reproduction, is rapidly gaining in popularity and looks likely to replace conventional dises in due course. This is cold comfort to anyone who owns a large collection of microgroove recordings, since the cost of replacing them with compact discs would be enormous and many recordings are never likely to be released on compact dise anyway. In fact compact dises are not totally immune to scratches, but except in very bad cases scratches fail to have any obvious effect on the audio output signal. This is due to error detection and correction circuits that are built into every player.

Somewhat similar techniques can be applied to ordinary discs, and so-called "click eliminators" can provide reasonable results from a badly scratched and otherwise useless record. There is a limit to the degree of improvement that can be produced using a relatively simple click eliminator circuit, and the problem is not so much eliminating the click as replacing it with something. In a simple click blanker it is replaced with a period of silence, and although the gap in the signal is too short to be heard as such, it is sufficiently long to produce an audible glitch. It is a very minor glitch when compared to the effect of a bad scratch, and in some cases it can actually be inaudible. In most cases though, it sounds like and is roughly analagous to tape drop-out.

## DESIGN

This design is for a reasonably simple and inexpensive scratch blanker which provides good results. It has to be pointed out that a blanker is inevitably much more complicated than a scratch filter circuit which simply provides top-cut filtering. This type of filter relies on the fact that surface scratches produce signals that are predominantly at high audio frequencies (about 7 kHz or more). Severely attenuating these high frequencies gives a great reduction in the surface noise and leaves a passable standard of reproduction. Bad scratches can not be efectively combatted using filtering as they produce strong signals well down into the middle audio range, and an ordinary scratch filter would leave many of the frequency components unattenuated.

Using a lower cut-off frequency would give good attenuation of the scratch signal, but would result in a totally inadequate

bandwith for music reproduction. Incidentally, a scratch blanker does not necessarily provide any reduction in audio bandwidth at all, and the unit featured here provides a full 20 Hz to 20 kHz bandwidth. The blanker connects between the preamplifier and the power amplifier, or it could be driven from the pick-up via a suitable preamplifier.

## BLANKING

In principle noise blanking is perfectly straightforward, and merely entails cutting the signal path for a short period when a noise spike is detected. The length of the blanking pulse depends on the application, but in the present context about three or four milliseconds is adequate. The problem is complicated by the need to blank the noise pulse before it starts to appear at the output of the circuit, and in practice this requires the use of a delay line. The basic arrangement is to feed the signal to the output via the delay line first and then the
electronic switch, but to drive the pulse detector circuit from the non-delayed signal. It takes the pulse detector under a millisecond to fully activate and break the signal path, and the delay line therefore only needs to provide a delay of about one millisecond in order to ensure that the noise pulse fails to reach the output.
Another slight complication is that of smoothing over the break in the signal. This is usually done using a sample and hold circuit, which is the method adopted in this design. A sample and hold circuit provides the effect shown in the waveform of Fig. 1(a). Here the signal is simply maintained at whatever voltage it happened to have at the instant the signal was cut off, and at the end of the blanking pulse it is immediately switched back to the current signal level. This does produce a slight glitch, in that at the instant the signal is switched on again the sample leve! and the signal level will usually be somewhat different. It is only a

Fig. 1 (a). A sample and hold circuit is used to maintain the signal at a constant voltage during the blanking period. (b) Without the sample and hold circuit the blanking could make matters worse rather than better.

minor glitch though, and the switch in amplitude must always be below the peak to peak signal amplitude at the time. This ensures that under low signal conditions the glitch is always small and can never be dominant over the wanted signal.

What has to be avoided is a situation of the type shown in Fig. 1(b), where the signal is taken to a certain potential during the blanking period (or simply allowed to drift to any potential). This could produce a worse noise spike than the one which is being suppressed.

## SYSTEM OPERATION

The block diagram for the signal processing stages, is shown in Fig. 2 while Fig. 3 shows the block diagram for the blanking pulse generator circuit. Starting with the signal processing stages, there are two identical circuits, one for each stereo channel. An amplifier at each input provides buffering, plus a small amount of voltage gain (about 6 dB ). The voltage gain is needed as the input level to the delay line would otherwise be significantly less than that required for optimum noise performance. This voltage gain also compensates for small losses through the delay line, and the overall gain of the unit is little more than unity.

The delay line is a standard "bucket brigade" or CCD (charge coupled device) type. This consists of a series of capacitors and electronic switches which sample the input voltage, and pass the samples along the chain of charge storage capacitors through to the output of the device. There is obviously a delay between each sample being taken and it finally appearing at the output, and this delay depends on the number of capacitors in the line as well as
on the rate at which samples are passed from one capacitor to another. The rate of progress is controlled by a clock oscillator which is common to both channels, and it operates at a little over 200 kHz . The delay line is a 512 stage type. The delay (in ms ) is equal to the number of delaying stages divided by twice the clock frequency (in kHz ), which works out at slightly more than one millisecond in this case.

## LOWPASS FILTER

A lowpass filter is needed at the input and output of each delay line. The input filters are needed to prevent any high frequency signals present on the input signal from entering the delay line where heterodyne tones could be generated by them interacting with the clock signal. In this case the clock frequency is well outside the audio range, and it is only radio frequency signals appearing on the input due to stray pick up that might cause problems. The output filters are needed to remove the clock signal. Apart from a slight clock breakthrough, the sampling process results in a stepped output signal, and the filtering is needed in order to smooth out the steps and give a proper output signal.
Each sample and hold circuit is really just an electronic switch feeding into a charge storage capacitor. The capacitor is normally driven from a low impedance source so that it has no significant effect on the circuit. However, when the switch breaks the signal path the capacitor retains whatever charge potential it happened to have at the instant the switch opened, giving the required "hold" action. A buffer amplifier ensures that there is no significant loading on the capacitor so that it is not discharged during the blanking period.


Fig. 2. Block diagram for the signal processing stages.
Fig. 3. The arrangement used to generate the blanking pulse.


## BLANKING PULSES

Turning to the blanking pulse generator stages (Fig. 3), the first two stages are a unity gain inverting amplifier and a mixer. The right hand channel is fed direct to the mixer, but the left hand channel is inverted first. This gives a differential amplifier action with signals that are in-phase on the two channets tending to cancel out one another. Signals in antiphase on the two stereo channels are added together to give a strong output signal. Signals near the centre of the sound stage will be in-phase, and this results in the main signal being attenuated to some degree. On the other hand, the noise pulses will either be out of phase, or will only affect one channel, and in either case this results in a strong output from the mixer.

This does no more than slightly boost the noise spike in relation to the programme signal, and further processing is needed before strong noise pulses can be reliably distinguished from the main signal. This is achieved using a high slope highpass filter. The noise spikes have a strong high frequency content, whereas the main signal has relatively little signal content at these frequencies.

The filtered signal is amplified and then used to trigger a monostable multivibrator. The latter provides the blanking pulse to the sample and hold circuits, after its output signal has been inverted to give a pulse of the correct polarity for the sample and hold circuits.

As it stands, this arrangement seems to be capable of differentiating reliably between strong noise spikes and the main signal. However, if the sensitivity is adjusted to the point where relatively minor scratches are blanked, there is then a danger of sections of programme material which have a strong high frequency content activating the unit. This must not be allowed to occur as it would result in passages of music being virtually eliminated. There are no easy and totally effective solutions to this problem, but compromise solutions are possible. One would be to limit the blanker to just one operation in a given period of time. This would ensure that frequent spurious operations could not occur, and that sections of music could not be blanked out. Unfortunatley it would also prevent the unit from blanking out the second noise spike where two occur in rapid succession.

## INHIBIT CIRCUIT

Another approach, and the one used in the final circuit, is to have an inhibit circuit which prevents the blanker from operating if a strong continuous output from the highpass filter is present. This is achieved by amplifying the filtered signal, then rectifying and smoothing it to produce a positive bias which drives a switching transistor. If the output from the fitter is sufficiently strong, the transistor is biased into conduction and it effectively holds the output of the monostable in the low state, disabling the blanker action.

There may seem to be a flaw in this arrangement in that the inhibit circuit could be operated by the noise spikes, thus preventing them from being blanked. In practice this is avoided by having the attack time of the circuit just long enough to permit noise spikes to be blanked before the inhibit circuit becomes operational. This still leaves a real flaw in that the blanker is rendered inoperative during passages which have very strong high frequency content. This is not a major drawback though, since

## COMPONENTS

Resistors
R1,2,33,101,102
R3, 15, 18, 19, 20, 21, 25, 26, 103
R4,5,10, 12, 13, 14, 16, 17,27,28,29.
30, 104, 105,112,113,114
R6, $7,35,106,107$
R8.23.108
R9,109
R11,31,111
R22.24
R32
R34
All $\frac{1}{4}$ W $5 \%$ carbon film
Potentiometers
VR1,101
VR2,102
VR3
VR4

## Capacitors

C1,15,101 C2, 102 C3,26,103
C4,8,17,18,19,20,104,108 C5,7,105, 107
C6
C9, 109
C10, 14, 16,24,110
C11,22,111
C21,23,25
C12,28,29
C13
C27
Semiconductors

| IC1.101 | LF353 (2 off) | IC8 | LF351 |
| :--- | :--- | :--- | :--- |
| IC2,102 | TDA 1022 (2 off) | IC9 | L555CP or ICM7555 |
| IC3 | 4047 BE | IC10 | UA7815 |
| IC4,6,104 | 1458C (3 off) | TR1.2 | BC549 (2 off) |
| IC5 | 4066 BE | D1.2 | 1N4148 (2 off) |
| IC7 | 741 C | D3.4 | 1N4002 (2 off) |

Miscellaneous
SK 1,2,101, 102
S1
FS1
T1

4k7 (5 off)
100k (9 off)
10k (17 off)
5k6 (5 off)
15k (3 off)
1 k (2 off)
47k (3 off)
6 k 8 (2 off)
39k
1M5

22k sub-min horizontal preset (2 off)
1 k sub-min horizontal preset (2 off)
1 M lin.
4 k 7 log.
$4 \mu 7$ radial elect. $63 \vee(3$ off)
330 nF carbonate (2 off)
$2 \mu 2$ radial elect. 63 V ( 3 off )
2 n 2 carbonate ( 8 off)
1 nF carbonate (4 off)
100 pF ceramic plate
150 pF ceramic plate ( 2 off)
47 nF carbonate ( 5 off)
$10 \mu \mathrm{~F}$ radial elect. 25 V ( 3 off)
100 nF carbonate ( 3 off)
100 nF ceramic ( 3 off )
$100 \mu \mathrm{~F}$ radial elect. 16 V
$2200 \mu \mathrm{~F}$ radial elect. 35 V

Phono sockets (4 off)
Rotary mains on/off switch
500 mA 20 mm quick-blow fuse Mains primary, twin 15 volt 200 mA secondaries

Case about $230 \times 133 \times 63 \mathrm{~mm}$; printed circuit board (available from the EE PCB Service, order code EE539); 20 mm chassis mounting fuseholder; three control knobs; eight 8 pin di.i.l. i.c. holders; two 14 pin di.i.l. i.c holders; two 16 pin d.i.l. i.c. holders; mains lead; Veropins; connecting wire; etc.
scratches are relatively unimportant and not so noticable during these passages. This system works well in practice, and enables the unit to be operated with a substantially higher level of trigger sensitivity than would otherwise be usable.

## CIRCUIT OPERATION

Refer to Fig. 4 for the circuit diagram of the signal processing stages, and to Fig. 5 for the blanking pulse generator circuit. Fig. 4 only shows the circuit for one channel, and most of the components are duplicated in the second stereo channel.
The input stage is an operational amplifier (ICla) used in the non-inverting mode and having a voltage gain of two times. VRI provides a variable bias voltage, and as the circuit is direct coupled from the input to the output this sets the bias level for all the subsequent stages as well. In practice it is set to obtain optimum large signal handling ability from the delay line circuit.

IClb acts as the buffer amplifier in the input lowpass filter, which is 12 dB per
octave type. Although delay lines often require very high slope filters at the input and output in order to obtain passable results, the high clock frequency used in this case enables excellent results to be obtained using relatively simple filters, and a 12 dB per octave roll-off above 20 kHz is more than adequate. The high clock frequency results in an excellent signal to noise ratio without the need for any form of noise reduction circuit.
The delay line chip is a TDA 1022 (IC2) with its clock signal provided by IC3. The latter provides the clock signal for both channels incidentally. IC3 is a CMOS 4047 BE astable/monostable device which operates in the free running astable mode in this circuit. The delay lines require a twophase clock signal, but with its $Q$ and $\bar{Q}$ outputs the 4047 BE can provide these without the need for an external inverter to the second phase.

Resistors R8 and R9 provide a bias signal to IC2, while VR2 acts as a simple mixer to combine the outputs of stages 512 and 513
of IC2. The TDA 1022 only has 512 delaying stages, but there is a 513 th stage which is used to maintain the output level while the 512 th stage is taking a sample from the previous stage, and is unable to provide a valid output level. Preset VR2 is adjusted to minimise clock breathrough, but most of the clock attenuation is provided by the third order ( 18 dB per octave) filter based on 1C4a.

The sample and hold circuit just consists of CMOS analogue switch IC5a and charge storage capacitor Cl 0 . The switches in the 4066BE device have low "on" resistances, especially when, as here, a 15 volt supply is used. Together with the low output impedance of IC4a this ensures that Cl 0 does not introduce any significant high frequency roll-off. IC 4 b is the output buffer amplifier.

## UNITY GAIN

Moving on to Fig. 5, IC6a is the unity gain inverting amplifier, and IC6b functions as a summing mode mixer. The mixed signal is then fed to a fourth order ( 24 dB per octave) highpass filter based on IC7. This has its cut-off frequency at approximately. 10 kHz , which seems to give about optimum results in practice. The next stage is an inverting amplifier built around IC8, and this has a voltage gain which is adjustable from about 100 times with VR3 at maximum resistance, to zero when it is at minimum resistance; VR3 is the trigger sensitivity control.

The monostable is a 555 type (IC9), but in this circuit a low power version (L555 or 7555) is used to ensure that noise spikes are not modulated onto supply lines, from where they could possibly break through to the output. Pin two of IC9 is the trigger input, and under quiescent conditions this is held at about half the supply voltage. To trigger IC9 pin two must be taken below one-third of the supply voltage, and scratches will produce a suitably strong input signal to do so on negative half cycles. IC9 then provides a positive output pulse which is set at approximately four milliseconds in duration by R32 and C23. It is a negative output pulse that is required, as the analogue switches are turned on by a "high" control signal, and switched off by a "low" control level. This problem is overcome by using one of the analogue switches (IC5c) as an inverter. The fourth switch in IC5 is left unused.

The inhibit circuit functions by switching on TRI when there is a strong, sustained, high frequency input. TRI then holds the control input of IC5a low, thus holding the two sample and hold circuits in the "pass" state. The control signal for TR1 is obtained by amplifying the output of IC7 using a high gain common emitter amplifier based on TR2. Its output is then rectified and smoothed by D1, D2 and C22 to give the positive d.c. control signal for TR1. VR4 is the inhibit sensitivity control.

## POWER SUPPLY

A well smoothed 15 volt supply is required, and this is provided by the mains power supply circuit shown in Fig. 6. This has T1 to provide isolation and voltage step-down, with push-pull rectification and a considerable amount of smoothing provided by D3, D4 and C27. A monolithic voltage regulator (IC10) then provides regulation and electronic smoothing. The total current consumption of the blanker circuit is around 60 milliamps.



## CONSTRUCTION

Construction is greatly simplified by the use of a single printed circuit board to accommodate all the components apart from the controls, sockets, fuseholder, and mains transformer. Details of the printed circuit board and wiring are shown in Fig. 7 This board is available from the $E E P C B$ Service: code EE539.

The board is in most respects perfectly straightforward to construct, but there are a few points worthy of note. Firstly, in both Fig. 7 and the components list the components in the right hand channel have the same

Fig. 6 (left). Circuit diagram of the mains power supply
identification numbers as those in the left hand channel, except that one hundred has been added to the number (e.g. Cl 108 is the right hand channel equivalent of C8). A few components are common to both channels, and both channels are of course served by a single blanking pulse generator. There are a number of link wires, a dozen in fact, and these are made from 20 or 22 s.w.g. tinned copper wire. The two long link wires between IC2 and IC3 should be kept quite taut, or insulated if necessary, so that there is no risk of them short circuiting together.
Components IC2, IC3 and IC102 are MOS devices, and consequently require the standard antistatic handling precautions.

Fig. 7. Component layout and printed circuit board master (full size)


EE58<6


Although IC9 is also a MOS type, it has built-in protection circuits that render handling precautions totally unnecessary. IC10 will become quite hot in operation, and it is a good idea to fit it with a small aluminium fin to provide heatsinking and ensure that there is no risk of overheating. Most of the non-electrolytic capacitors are carbonate or miniature polyester types having a lead spacing of 7.5 millimetres, and it is essential that the correct type are used if they are to fit onto the board properly. Veropins are fitted to the board at all the points where connections to off-board components will be made.

## HOUSING

An instrument case having approximate outside dimensions of 230 by 133 millimetres is suitable as the housing for this project. Any case of about the same size should suffice, but do not choose one significantly smaller on any dimension. The sockets are mounted on the rear panel and phono types are probably the best choice for these, although a different type could obviously be used if this would be more convenient for use in your particular set-up. A hole for the mains !ead is made in the rear panel, well towards the right hand end of the panel, and this should be fitted with a cable securing grommet.

The printed circuit board is mounted on the base panel using M3 or 6BA screws and fixing nuts. Spacers about 10 or 12 millimetres long are used to keep connections on the underside of the board well clear of the metal case. The board is fitted as far to the
left as possible, so as to leave space for TI and the fuseholder at the right hand end of the base panel. A solder tag is fitted on one of TI's mounting bolts to provide a chassis connection point. For safety reasons the mains earth lead must connect to this tag, so that the case is earthed. The three controls are mounted on the front panel, and obviously on/off switch SI should be fitted adjacent to T1, see Fig. 8 and photo.
To complete the unit the hard wiring is added, and as this is a mains powered project it is important to proceed carefully with this, and to thoroughly check the completed wiring.


Before and after oscillograph showing the effect of blanking.

## ADJUSTMENT AND USE

Presets VR2 and VR102 are adjusted for minimum clock breakthrough at the output of their respective channels, but due to the high clock frequency used in this circuit there is minimal clock breakthrough at any


Fig. 8. Interwiring to the printed circuit board.


Completed Scratch Blanker showing interwiring to circuit board.


Fig. 9. Adding a bypass facility requires a 4-pole 2-way switch.
settings of these components. It is therefore quite in order to simply set these presets at a roughly mid-setting. If an audio signal generator and an oscilloscope are available, VR1 and VR101 are adjusted for symetrical clipping on each channel. If suitable test gear is not to hand it is acceptable to simply adjust these to find any settings which give an output signal that is free from any obvious distortion. Provided the circuit is not overloaded it provides a typical total harmonic distortion level of well under one per cent.
Just where in the audio system the unit is connected depends on the facilities offered by the system concerned. If a tape monitor facility is available, this can provide a convenient way of selecting the unit or switching it out, as desired. With some amplifiers and receivers preamplifier outputs and power amplifier inputs are available, and the unit can then be connected between these. A bypass switch in the blanker unit would then be needed, and all that is required is a four pole changeover switch connected as shown in Fig. 9 (a three way four pole rotary switch with the end stop set for two way operation is suitable for S2). A third alternative is to feed the unit from the cartridge via a suitable preamplifier, and ready-made preamplifiers of the appropriate type are available if a homeconstructor design can not be located. The output of the unit would feed into any high level input of the amplifier or receiver, such as a "Tape" or "Aux" input. With this method it would again be advisable to include the bypass switching.

If only very bad scratches are to be processed the trigger level control will not need to be advanced very far, and results will probably be perfectly adequate without needing to advance the inhibit sensitivity control at all. With the trigger level well advanced the unit will process relatively minor scratches, but the inhibit sensitivity control must also be well advanced or it is likely that some passages of music will be blanked out. However, this is something that depends on the nature of the recording, and it is really necessary to experiment a little with the settings of the two controls to determine what works best for a given recording.
The degree of attenuation applied to the scratch pulse is massive, and there is no significant breakthrough of this signal at all. As mentioned earlier, there may be a slight audible glitch when the blanker operates, or there may be no audible effect at all. This depends on the relative signal levels at the beginning and end of the blanking period, but there is never less than a very substantial improvement in the signal. The accompanying oscillograph shows "before" and "after" traces, which clearly show the effect of the blanking.

# Gocungly <br> Doing it!! 

1F you have followed this series thus far you should now be at the stage where you can build up projects, taking them through to a neat and fully finished article. Although one could reasonably be forgiven for thinking that this is the end of things, there is often one final stage on the way to success-getting the finished project to work. Had I been writing this series a few years ago I would have emphasized that the chances of getting a project to work first time, even for an experienced constructor, were not great. These days the situation is somewhat different, with the widespread use of ready-made printed circuit boards greatly reducing the risk of errors, and making first time operation much more likely. There are still plenty of things that can go wrong, though, and if on completing your first project it fails to operate you should not be too surprised or disappointed. In more than 90 per cent of cases where a newly constructed project fails to work the problem is a simple and straightforward one which can easily be rectified by someone with little technical knowledge and little or no test gear.

## THE OBVIOUS

Start by looking for obvious problems which are easily overlooked by one's eagerness to switch on and try out the new gadget. Is the battery fitted, and if it is, is it properly connected to the battery clip. The latter are a common cause of problems, and it is worthwhile gently pulling the two leads to ensure that one of them has not become detached from its press-stud. Sometimes one press-stud connects to the battery properly, but the other is a loose fit and power fails to get through to the circuit. In these cases the offending female press-stud can be crushed slightly with a pair of pliers to make it a reliable fit.

You should have thoroughly checked for errors such as integrated circuits fitted around the wrong way before switching on the unit, but probably few constructors are able to control their enthusiasm to test the new device, and most projects undergo little in the way of real checking prior to switch-on. Fortunately most modern components are very tolerant of abuse, and quite serious errors will often not result in any casualties. However, if at all possible it is obviously better to track down errors before switch-on rather than afterwards.

For one thing, if an error such as an integrated circuit fitted round the wrong way is not discovered until after the unit has been tried out, and on correcting the error the unit still fails to work, this introduces a strong element of uncertainty. It could be that there is another and unrelated error, or the integrated circuit might have been damaged. This means either spending a lot of time searching for a fault which might not exist, or replacing what might well be a perfectly good device. Some basic checking prior to switch-on
can save a lot of wasted time and money.
The hard wiring is where any errors are most likely to occur, and the most common causes of problems here were discussed in previous articles. If there is something in the wiring that you were not sure about when constructing the project, it would be a good idea to take another look at this to see if you might have misinterpreted something, and if it looks as though you might have done so, try the alternative method of connection.

## THE LESS OBVIOUS

More often than not a quick check over the unit will reveal some simple little error that is the cause of the problem, but some faults can be more difficult to spot. Printed circuit mounting capacitors of the carbonate or polyester layer varieties are a common cause of trouble. The standard fault is where the lead spacing is just fractionally narrower than the hole spacing in the board. On pushing the component down into place on the board one of the leads gets ripped away from the body of the component, but there may be little outward sign of this.

A close visual inspection will normally reveal this problem, though, as will gently pushing on the side of each component. If this fault is found it is often possible to solder the lead back in place, but this could affect the accuracy of the component as well as giving dubious long-term reliability. If this is done it is therefore best to regard it as only an interim measure, and to replace the component with a new type at the earliest opportunity, taking care not to give a'repeat performance.

With projects constructed on stripboard the main hazard is accidental short circuits between copper tracks due to what are often quite small blobs of excess solder. With custom printed circuit boards this is less of a problem, but it can still happen, especially on areas of the board where there are a lot of connections in a small area (d.i.1. clusters etc.). Solder blobs and splashes (thin streaks of solder across the board) can be difficult to spot visually, and it is helpful to use a spirit-based cleaner to remove any excess flux from the board; a magnifying glass might also be very helpful.

## CONTINUITY CHECKER

I have often come across short circuits which defy visual detection even with close and detailed inspection, and my preferred way of doing things is to use a continuity tester to check for short circuits between adjacent tracks or pads which should not be connected. This really requires the use of a tester which will not respond to very low resistances or to forward biased semiconductor junctions, as otherwise a constant stream of misleading indications are likely to be obtained. Suitable continuity checker designs appear in Everyday Electronics from time to
time, or a multimeter set to a range having a full scale value of about 10 or 20 k should suffice.

Do not be tempted into the classic mistake of building up a continuity tester from a torch battery and bulb wired in series. The problem with this arrangement is that it can force quite high currents through the circuit being tested, and this could result in the unit producing more faults than it locates. The simple continuity checker circuit of Fig. 1 is a sort of modern equivalent of the old torch bulb type, and the use of an ultra-bright l.e.d. enables the current to be kept to a suitably low level.

You may occasionally track down a short circuit with a continuity tester, but find yourself in the position of not being able to visually locate the offending piece of solder. Running the blade of a modelling knife between the two copper strips a few times over their entire length should cut through the solder and cure the fault.


Fig. 1. Simple but safe continuity checker.

Soldering is a subject that has been covered in previous articles, and it is one which has to be mentioned again here since inadequate soldering it possibly the most common cause of projects failing to work. Good soldered joints on a circuit board should have a sort of mountain shape with a shiny surface to the solder. A rounded shape (often accompanied by a dull surface to the solder) usually indicates that the solder has not flowed over the track and lead properly. A dull and crazed finish to the surface of the solder indicates the lead has been moved as the solder solidified, causing numerous fractures in the solder

- Another problem that can occur is where a leadout wire has been trimmed slightly too short so that very little of the lead (or nothing at all) protrudes on the copper side of the board. This can result in a cap of solder over the leadout wire without it actually being soldered in place. Joints of this type can often be spotted because they are very much flatter than ordinary joints, with no sign of the leadout wire.
This is another case where a detailed visual inspection will often reveal the fault, but it can not be relied upon to do so. It is much better to use a continuity checker to test for a proper connection between each leadout wire and its copper track. Simply giving the components a firm pull will often reveal inadequate joints as the offending leadout wire may pull clear of the board, and it is worth giving this a quick try before spending a lot of time testing each connection with a continuity checker

If a dry joint is located, do not simply try applying further solder to it. First clean off the original solder using a desoldering tool or solder wick, then make sure that both surfaces are clean and in a fit state for
soldering (with any excess flux being removed), and finally resolder the connection. It would be advisable to recheck the new joint with a continuity checker. If the problem was due to inadequate leadout protrusion, it might simply be a matter of applying the iron to the joint and pushing the component fully down and into place on the board. If the lead has been trimmed too short, one way around the problem is to solder a short piece of tinned copper wire in place, and then connect the leadout to this on the component side of the board. A neater solution is to simply replace the component. I have come across d.i.l. integrated circuit holders with very short pins that are barely a board's thickness in length. It can be very difficult to produce reliable connections with these, especially when using a fairly thick piece of board, and they are best avoided.

## COMPONENTS

Faulty components are quite rare these days, and unless you fail to take due care when connecting components it is extremely unlikely that a "dud" will be responsible for the problem. Therefore, if you are careful when constructing the unit and ensure that there are no faults of what could really be termed mechanical rather than electrical in nature (e.g. short circuits and integrated circuits fitted around the wrong way) the finished project is almost certain to work. It is useful to keep this in mind, and a positive approach to things gives a far better chance of success

If a thorough check fails to reveal a fault the next step is to try checking the components, and at the very least a multimeter is needed for this. Even with the aid of a multimeter only fairly simple components
can be tested. As a quick initial check, ensure that the supply is getting through to the circuit board, and to all the integrated circuits, etc., that connect to one or other of the supplies. Broken printed circuit tracks are not a common fault, but they do occur.

Any multimeter should be capable of measuring over a wide resistance range, and testing the resistors should not be a problem. Do not overlook the fact that incircuit checks may not be very accurate due to the presence of components connected in parallel with the one you are checking. What this means in practice is that some readings may be on the low side, but a higher than expected reading is certainly indicative of a fault. If a low reading is obtained, swopping the test prods over may give a more accurate one, but the only certain way of eliminating errors due to effect of other components is to temporarily disconnect one leadout wire so that the component is effectively'isolated from the rest of the circuit.

A multimeter cannot thoroughly test inductors, but a broken wire will be indicated by a very high resistance through the winding. Similarly, capacitors cannot be properly tested, but the most common fault is a short circuit through the component, and a multimeter set to a resistance range will detect this. When first connecting the meter to the capacitor the needle might give a slight kick as the component charges up. The size of the kick depends on the value of the capacitor (higher values giving larger deflections), and this can be used as a rough guide to whether or not a component is functioning properly. However, very low values will not give a significant kick of the needle even with the
multimeter. set to the highest resistance range, and this method is not applicable to digital multimeters.

Diodes are easily checked using a multimeter set to a resistance range. Connecting the test prods one way round should give a low reading, while a high reading should be obtained with the prods reversed. As a quick check of transistors, there should be a diode action between the base-emitter and base-collector leads, and a very high resistance across the collector-emitter leads with the test leads connected either way around. All these tests should ideally be carried out with one lead of the component under investigation disconnected from the board (or two leads in the case of transistors), and results are largely meaningless unless this is done.

Mistakes can occur in magazines (but very infrequently in EE\&EM), and any corrections for errors in constructional articles normally appear an issue or two after the copy in which the project was published. If all else fails you can write into the magazine asking for help, but do try to include as much useful information as possible. Simply stating that a project has failed to work and asking why is very much a "how long is a piece of string?" type question. If the project does something, even something like a component getting warm, then give details of this. Even if it does nothing at all then this fact should be stated and might prove helpful. If you have a multimeter take a few voltage readings and include these. Most important of all, do not forget to include a stamped addressed envelope for the reply.
Robert $\mathscr{P}_{\text {enfold }}$


## Spot on

Sir-In reply to David J. M. Lloyd's letter "Back to the beginning" (July '86 issue) I would like to say that as a beginner I find your magazine "spot on". My attention was first drawn to your magazine when I noticed a Waa-Waa Pedal project. I purchased my first small batch of components and completed the project in one day . . . all due, of course, to your easy to follow instructions and clear pictorial layout and diagrams. I was immediately hooked to electronics as a hobby.
I now have a regular order for your magazine. I am especially interested in your musical effects projects as I play electric guitar in a band. Car projects are of little interest to me as we do not own a car . . . we're a family of dedicated motorcyclists! "Home projects" like burglar alarms, intercoms and games are very interesting to build and useful to the whole family.

I learn something new about circuit layout and building with every project I complete. Learning from building projects and also from your "Teach In " series has helped me to gain a wealth of knowledge about electronic circuitry.

However, I do agree with Mr. Lloyd on the subject of computers. There are enough magazines covering technical information and "add-on" projects for computers. Anyway, most beginners would prefer building the more "conventional" projects rather than tackling the more difficult computer projects. That of course is just my opinion.

I disagree with Mr. Lloyd on the subject of formulae, calculations, etc. I find these of great interest and as I like to experiment with circuit design, formulae and calculations are a necessity.

Overall, I find Everyday Electronics and Electronics Monthly an excellent magazine for the beginner and not-so-beginner alike. I shall keep buying it . . . computer projects included... or not. Keep up the good work!

Billy McCoy,
Newcastle West,
Co. Limerick.
of popular i.c.s, how to actually make them work and tailor them to our own needs. A good example of this is the Exploring Electronics series-it shows us the principle of transmission, but, how would you build a modulator and r.f. amplifier??

Also, why not have more projects that can be built from readers' junk boxes? I'm sure there are thousands of us with 741 s , 7400 s, 4011 s in our junk boxes, but unable to use them as they now seem to be "obsolete" and EE projects use only CA3240s etc., where a good ol 741 could do the trickl

Just look at your readers' circuits. You'll see lots of $741 \mathrm{~s}, \mathrm{BC} 109 \mathrm{~s}$ etc. etc. Could it be they're trying to tell you something? Also let's have a bit of "boring" theory so we can design our own circuits-no, perhaps not-it might put you out of a job!
G. Finney,

Pontypool,
Gwent.

## No more please!

Sir-I read David Lloyd's letter (July 86 issue) with some interest.

The July issue was the first electronics magazine that I have bought for several years, although I have "pottered" with circuits during that time. I agree entirely with his views upon computers. The reason that I rejected the other magazines was that they were packed with computer circuits-no more please!
I would, however, like to see more articles on circuit design and applications


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