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To your system

The Searth for Extra-Terrestrial Infeligence

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64K EPROM EMULATOR Test Eprom software on a PC first

PLUS

Electronic Ding/Dong
Fast Fivers
Higher Degrees in Electronics

ELECTRONIC A-Z Wordgames, Joker and Dice



Vol 26 Issue: 9 | 15 August | 1997 | £2.50 | USA \$4.95

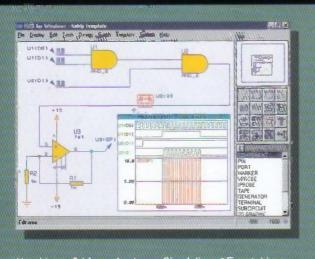
PROTEUS

Schematic Capture

MIZE Cir Denies Edi Josh Dongs Gran Togoldo Sutre HNREG Version DE ANALOG # 11 RI a 100n ME 02 50184 R2 DIGITAL

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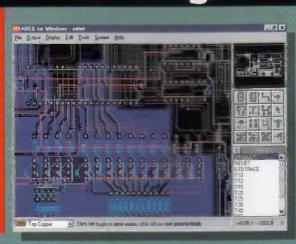
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Version



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with its rip-up-and-retry **EWW January 1997**



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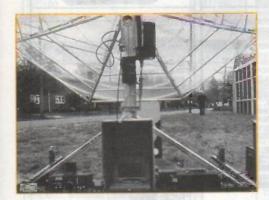
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Volume 26 No.9



Next Issue 12th September 1997



Electronic A to 2

Given twenty-six letters you could play nearly any word-game that your imagination could devise, says Roy Bebbington (and he adds some suggestions). The A to Z has a joker and dice function as well.

H. Paul Shuch argues that Amateur Radio Astronomers are now in the front line of the search for communications signals from space beyond our Solar system, and

SETI and the Amateur Radio Astronomer

Higher Education Special

A proportion of electronics students graduating successfully from a first degree course are opting to return for one to three years to pursue a higher degree. ETI looks at three research and higher degree prospectuses for UK universities.

Digitally Controlled Power Supply

describes the sky-search work of the SETI League

Take a letter - an Electronic A to Z

Robert Penfold's power supply with PIC-controlled stability will not suddenly provide a high voltage if you make the classic mistake of connecting the supply to a low-voltage circuit without turning it down first.

64K Eprom Emulator

Following the popular ETI Eprommer, Keith Wardill's matching eprom Emulator allows software to be written and compiled on a host computer, downloaded to the emulator and tested before burning into the eprom

Quickroute Systems CAD competition

We have £2500-worth of high quality PCB Computer Aided Design software for the winners of our competition - just answer three simple questions about QS software.

Electronic Ding Dong Door Chimes

Terry Balbirnie's electronic chimes have a major advantage over the two-bar electrical type - you can set them to repeat only as often as you want them to:

Fast Fivers - A Musical Booby-trap (5)

If you have a bottle of Victorian port to protect, "Twinkle twinkle, little star" may do the trick, says Owen Bishop.

Sorry - Part 2 of Speed Control in DC Motors and Part 2 of the Valve Characteristic Tester have had to be held over this month. We'll aim get them in the next issue.

Regulars

News **PCB** foils

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DIGITAL MULTIMETERS

CM2300 DIGITAL MULTIMETER

- . 3.5 LCD DISPLAY
- HEIGHT 12mm MAX READING 1999
- MAX HEADING 1999
 MY INDICATION FOR HIGH VOLTAGE
 SINGLE MANUAL ROTARY SWITCH FOR
 FUNCTION AND RANGE OPERATION
 ALL RANGES OVERLOAD PROTECTED
 10A DC CURRENT TEST.

- DC VOLTAGE 2V/20V/200V/500V

- AC VOLTAGE 200/500V
 DC CURRENT 200mA,
 RESISTANCE 2k\(\Omega\) /20k\(\Omega\) /20k\(\Omega\) /20k\(\Omega\) SUPPLIED WITH TEST PROBES **ORDER CODE: CM2300**

PRICE: 975p

CM2400T DIGITAL MULTIMETE R WITH TEMP MEASUREMENT



- FEATURES:
 3.5LCD DISPLAY
- HEIGHT 12mm
- MAXIMUM READING 1999
- 10A DC CURRENT TEST

- 10A DC CURRENT TEST
 DC VOLTAGE 200m/VZV/20V/200V/1000V
 AC VOLATE 200750V,
 DC CURRENT 0.2mA/200mA/20mA/200mA/20A
 RESISTANCE 200Ω /2KΩ /2δ(Ω /200KΩ /2MΩ)
 SUPPLIED WITH TEST PROBES
 TEMPERATURE MEASUREMENT
 CONTRIBUTY TEST

- CONTINUITY TEST
 DIODE TEST & CONTINUITY CHECK
 ALL RANGES OVERLOAD PROTECTED

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CM2900 PACKET DIGITAL MULTIMETER

- 3.5 LCD DISPLAY
 COMPACT AND LIGHTWEIGHT POCKET SIZE
 MAXIMUM READING 1999
 DC CURRENT 7 RESISTANCE OVERLOAD

- PHOTECTED
 SCHOE SWITCHES FOR FUNCTION AND RANGE
 OPERATION
 SUPPLIED IN WALLET WITH TEST PROBES
 DC VOLTAGE 20/201/2001/5001
 AC VOLTAGE 2001/5001
 DC CURRENT 200MA
 RESISTANCE 2KΩ /200KΩ /200KΩ /2MΩ

ORDER CODE: CM2900

PRICE1150p

CM2700 AUTORANGING DIGITAL MULTIMETER



- FEATURES:

 3.75 LCD DISPLAY WITH DECIMAL POINT

 33 SEGMENT BARGRAPH DISPLAY
- OVERRANGE INDICATION
- ROTARY SWITCH FOR FUNCTION

- SELECTION
 AUTO POWER OFF (APPROX 15 mins)
 AUTO POLARITY WITH INDICATION
 DIODE TEST & CONTINUITY TEST WITH
- BUZZER ALL RANGES OVERLOAD PROTECTED

- LOW BATTERY INDICATION
 SUPPLIED WITH TEST PROBES
 DC VOLTAGE: 320mV/3.2V/32V/320V/600V
 AC VOLTAGE: 320mV/3.2V/32V/320V800V
- DC CURRENT A: 320µA/3200µA/32mA/ 320mA/10A
- AC CURRENT A: 320µA/3200µA/32mA/
- 320mA10A RESISTANCE: 320Ω/3.2KΩ/32KΩ/320KΩ/ 3.2MΩ/32MΩ

ORDER CODE: CM2700 **PRICE: 4050p**

CM3230 DIGITAL **CAPACITANCE METER**



- 3.5 LCD DISPLAY

- HEIGHT 18mm MAUBRUM READING 1999 CAPACITANCE 9 RANGES FROM 200pF-
- 20000µF
 MEASURING FROM 1µF 20000µF
 SINGLE MANUAL ROTARY SWITCH FOR
 RUNCTION AND RANGE OPERATION
 ZERO ADJUST KNOB

ORDER CODE: CM3230 PRICE: 3950p

CM3900A DIGITAL MULTIMETER

1444



FEATURES

- LARGE LCD DISPLAY

- LARGE COO DISPLAY
 HEIGHT 18mm
 MASIMUM READING 1999 + UNIT
 SINGLE MANUAL ROTARY SWITCH FOR FUNCTION AND RANGE OPERATION
 AUTO POWER OFF (APPROX 15 min)
 DIODE TEST FUNCTION

- DIODE TEST FUNCTION
 ALL RANGES OVERLOAD PROTECTED
 SUPPLIED WITH TEST PROBES
 DC VOLTAGE; 200mV/2V/20V/200V
 700V ACCURACY ±0.5%
 AC VOLTAGE; 200mV/2V/20V/200V/700V
 DC CURRENT &: 200ju/20ma/200ma/2A/20A
 AC CURRENT &: 200ju/20ma/200ma/2A/20A
 RESISTANCE : 200g/2kti/200kti/2Mci/200kti/200kti/

ORDER. CODE: CM3900A PRICE: 2900p

CM3920 DIGITALMETER WITH **TEMP MEASUREMENT**



FEATURES

- TEMPERATURE MEASUREMENT

- TEMPERATURE MEASUREMENT
 DIODE & TRANSISTOR HEE TEST
 LARGE LCD DISPLAY
 MEIGHT 18 mm
 MAXIMUM READING 1999 + UNIT
 SINGLE MANUAL ROTARY SWITCH FOR
 FUNCTION AND RANGE OPERATION
 AUTO POWER OFF (APPROX 15 mins)
 DIODE TESWIT FUNCTION
 ALL RANGES OVERLOAD PROTECTED
 SUPPLIED WITH TEST PROBES
 DC VOLTAGE: 200m/VZ/VZOV/ZOV/

- DC VOLTAGE: 200mV/2V/20V/200V/

- DO-VOCIAGE: 200mV/2V/200V 1000V ACCURACY 9.0 5% AC VOLTAGE: 200mV/2V/20V/200V/700V DC CURRENT Z-200mA/200A AC CURRENT X-200mA/20A RESISTANCE: 2000/2K0/200kQ/2MQ/20MQ/

CAPACITANCE: 2nF/20nF/200nF/2µF/20µF **ORDER CODE: CM3920**

PRICE: 4100p

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SATELLITE POWER SUPPLY REPAIR K

SAT660	SATPSU2
	CODE
AMSTRAD	
SRD510, SRD520, SRD540, SRD550	SATPSU3
SRDR45	-
SRD500	SATPSU4
SRX320, SRX340, SRX345, SRX350	SATPSU5
	SATPSU6
SRX100	
SRD600	SATPSU14
SAT250, SR950, SRD700, SRD950,	SATPSU16
SRX1002, SRX2001, SRX301,	
SRX501, SRX502	1 152
	CATDOLIAD
SRD2000	SATPSU18

BRITISH TELECOM	CODE
SVS300	SATPSU17
	CODE
IRD150	SATPSU12
IBD155	SATPSU19

CHURCHILL	CODE
D3MAC DECODER	SATPSU7

ECHOSTAR	CODE
SR5500 EARLY PSU WITH ADJ	SATPSU12
6500, SR7700, SR8700	SATPSU13
Federal College Colleg	
SRD 5, SRD16	SATPSU1
SRV1	SATPSU2
SRDE4	SATPSU11
FINLUX	CODE
SR5700	SATPSU12
GOODMANS .	CODE
ST700	SATPSU1
GRUNDIG	CODE
	0.1700111

STR1 GIRD200, FIRD3000	SATPSU1 SATPSU2
MANHATTAN 850, 950	SATPSU1
MASPRO SRE250S/1, SRE350S/1 SRE250S, SRE350S, SRE450S	SATPSU1 SATPSU2

(ITS	
SOPRENSON TYPE PSU ONLY	SATPSU15
NETWORK 9000, 9200	SATPSU2
NOKIA SAT1500	SATPSU2
PACE	
PRD800, PRD900, PSR800, PSR900 MRD920, SS9000, SS9010, SS9200, SS9210, SS9220	SATPSU1 SATPSU2
D100, D150, MSS100	SATPSU6 SATPSU8
APOLLO, MSS200, MSS300 MSS500, MSS1000	SATPSU9 SATPSU10
PHILIPS	CODE
STU802/05M STU801	SATPSU1 SATPSU2
	CODE
THOMSON SRS4	SATPSU2
TOSHIBA	GODE W
SAT99 TU-SDU200	SATPSU1

CODE	PRICE	CODE	PRICE	CODE	THE PROPERTY	CODE	MICE
SATPSU1 SATPSU2 SATPSU3 SATPSU4 SATPSU5	650p 650p 650p 650p	SATPSU6 SATPSU7 SATPSU8 SATPSU9 SATPSU10	650p 650p 730p 900p	SATPSU11 SATPSU12 SATPSU13 SATPSU14 SATPSU15	1735p 3125p	SATPSU16 SATPSU17 SATPSU18 SATPSU19	730p 850p 1175p 650p

SRE250S, SRE350S, SRE450S

PACE SATELLITE TUNERS				
MODELS	CODE	PRICE		
PRD800, MSS200 (2GHZ) (221-2077062)	TUNER01	1650p		
PRD900, MSS500, MSS1000 (2Ghz) (221-2177012)	TUNER02	1650p		

PACE SWITCH MODE TRANSFORMERS				
MODELS	CODE	PEE		
PACE9000 PACEPRD800, PRD900	PACE9000 PRD800	800p 550p		

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THE SATMETER IS A PROFESSIONAL PORTABLE SATELLITE STRNGTH METER DESIGNED FOR THE INSTALLATION AND MAINTENANCE OF SATELLITE TV SYSTEMS. THE SATMETER CAN BE USED AS STAND ALONE METER WITH POWERING THE LNB AS WELL AS IN LOOP. THROUGH OPERATION WITH SATELLITE RX POWERING THE LNB.

ACOUSTICAL SIGNAL: ON SIGNAL STRENGTH

INPUT IMPEDENENCE: 75 Ohm MAX.INPUT SIGNAL: -10 DBM

LED INDICATOR: VERTICAL/HORIZONTAL POWER AMPLIFIER: 18 DB

FREQUENCY RANGE: 900 TO 2050 MHZ DETECTION RANGE: -60 TO -10 DBM

ORDER CODE: TOOL 22 PRICE: 8500p

SATELLITE LNB'S

		PODICE	MAKE & MOREL		
Cambridge AE22/AE5 0.8dB standard 10.95-II.70 GHz Gctd Range Cambridge AE14 Universal LNB 10,7-11.7/11.7-12.75 GHz Cambridge AE21/AE5 Single O/P Switching LNB 1.0dB Standard Cambridge AE19/AE6 Single O/P Switching LNB 1.0dB Enhanced Cambridge AE24/AE12 0.8dB Enhanced 10,7-11.8GHz Gold Range Cambridge AE8 Dual O/P H-V Separate Enhanced	LNB1 LNB2 LNB3 LNB4	2160p 2500p 2050p 2050p	Cambridge AE7 Twin O/P H+V Both Enhanced Cambridge AE2 Dual Q/P H-V Separate Enhanced Grundig Super Universal 'Anis' 10.7-12.75 GHz 0.8dB Grundig Universal 'Anis' 10.7-12.75 GHz 1.0dB Cambridge AE1 Twin O/P H+V Both Standard	LNB7 LNB8 LNB9 LNB10 LNB11	4000p 3550p 2600p 2250p 4000p

FUSES

	TIME LAG (20)	VIM)	QUICK BLOW	(20MM)
CURRENT RATING	ORDER CODE	PRICE	ORDER CODE	PRICE
100mA	FUSE36	75p	FUSE37	60P
160mA	FUSE01	75p	FUSE17	60p
250mA	FUSE02	75p	FUSE18	60p
315mA	FUSE03	75p	FUSE19	60p
400mA	FUSE04	75p	FUSE20	60p
500mA	FUSE05	75p	FUSE21	60p
630mA	FUSE06	75p	FUSE22	60p
800mA	FUSE07	60p	FUSE23	60p
IA	FUSE08	60p	FUSE24	60p
1.25A	FUSE09	60p	FUSE25	60p
1.6A	FUSE10	60p	FUSE26	60p
2A	FUSE11	50p	FUSE27	60p
2.5A	FUSE12	50p	FUSE28	60p
3.15A	FUSE13	55p	FUSE29	50p
4A	FUSE14	- 55p	FUSE30	50p
5A	FUSE15	60p	FUSE31	50p
6.3A	FUSE16	60p	FUSE32	50p

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CERAMIC PLUG TOP

CURRENT RATING	ORDER CODE	PRICE
3A	FUSE33	1000
5A	FUSE34	100p
13A	FUSE35	100p

20mm CERAMIC TIME LAG

CURRENT RATING	ORDER CODE	PRICE
6.3A	FUSE38	100p
8A	FUSE39	100p
10A	FUSE40	100p
3.15A	FUSE41	85p
4A	FUSE42	85p
5A	FUSE43	85p

38mm CERAMIC TIME LAG

CURRENT RATING	ORDER CODE	PRICE
10A	FUSE48	815P
10		

32mm CERAMIC SLOW BLOW

CURRENT RATING	ORDER CODE	PHRA
8A	FUSE44	185P
10A	FUSE45	185p
15A	FUSE46	185p
20A	FUSE47	210p

ALL THE ABOVE PRICES ARE FOR PACKS OF 10 FUSES

			SISTORS	A CHARLES AND A STATE OF THE ASSESSMENT OF THE A	
AC125 30P	PART PRICE	## PART ### PRICE ### BU409 ### BU412 ### BU412 ### BU413 ### BU433 ### BU433 ### BU500 ### BU500 ### BU500 ### BU500 ### BU5050 ### BU5060 ### BU5080	BUX848A	PART	PART PROCE 2N3553 500P 2N3553 500P 2N3703 9P 2N3704 9P 2N3706 9P 2N3706 9P 2N3706 9P 2N3706 9P 2N3707 9P 2N3709 9P 2N3809 9P 2N3809 9P 2N3809 9P 2N3809 9P 2N3800 9

	DAY 6					TRANS	ISTORS					200	A STATE	2.5
PART	PRICE	PAR	PF	RICE	PART	PRICE	PART I	RICE	PART	PRI		PART	PR	
L	C. KETS	1A/50V W01		18p	TIC116C 8A/300V	59p	8156 8224	300p 240p	4075 4076		13p 42p	7430 7 43 7		25(28) 30(38) 60(22) 10(25) 25(90)
		1A/100	/	0.204	TIC116D	70p	8226 8250	240p 750p	4077		13p	7438 7442		38
PIN I PIN	41 54		,	19p	8A/400V TIC126D	75p	8251	200p	4081		13p 13p 13p	7447		60
PIN	6	W04		210	12A/400V		8253	160p 220p	4082 4085		13p 36p	7450 7451		10
PIN	91	W06	/	23p	TIC126M 12A/600V	90p	8257 8271	3400	4086		30p	7454		25
PIN	121	LA/600	/	10000	C106D	28p	8279	270p 400p	4089		75p 18p	7473 7481		25 90
PIN	13		,	28p	4A/400V BP103	37p	8283 8284	440p	4094		44p	7482		80 25 75 35 48
PIN	15			33p	BR303	85p	8287	260p	4094 4098		58p 50p	7485 7489		75
		2A/100 BR82D	1	33p	BT106 BT119	180p 100p	8288 82C206PLCC	650p 500p	4099		42p	7493		35
ODES		2A/200	/	14	17088	200p	8748	700p 800p	4501 4502		26p	7495 74132		42
	WAT	BR84D 2A/400	,	37p	17089 1 71 27	200p	8755 8726	950	4504		36p 35p	74141		55
7 TO 39V	5 S			43p	15/80H	230p	8T28	110p	4505 4506		80p 58p	74145 74157		42 55 70 45
	WAT	T 2A/600	/		15/85A SG 264	230p 800p	CMOS IC's		4506		30p	74160		50
7 TO 39V	9	P 8R88D 2A/800	,	43p	SG613	1500p			4508		67p		74HC SERIES	
	TAGE	BR32		43p	COMPUTER	Cin	4000 4001	13p 13p	4510 4511		32p 30p		IANO DENIES	10
REGUL	LATORS	2A/200 BR34		43p			4002	13p	4512		38p	74HC03 74HC08		18
105	25	P 2A/400	1		Z80ACPU Z80ADMA	100p 200p	4006 4007	34p 13p	4514 4515		65p 65p	74HC10		20
906 908	25 25	P BR36 P 2A/600	V	44p	Z80ADMA Z80ACTC	140p	4009	20p	4516		36p	74HC11 74HC14		14
12	25	P BR62		80p	280ASIO-1	210p	4010 4011	21p 13p	4517 4518		100p 36p	74HC20		15
15	25 25		1	72p	Z80ASIO-2 75107	210p 65p	4012	130	4519		28p	74HC27		21
118	25	P 6A/400	V		75110	750	4013	19p 32p	4520 4521		36¢ 86¢	74HC51 74HC73		20
05	25	P BR251	01/	150p	75113 75122	100p 110p	4014 4016	18p	4526		38p	74HC74		2
06 08	30 30	P 25A/10 P BR252	04	165p	75154	100p	4018	300	4527		41p 38p	74HC76 74HC77		2
112	30	P 24A/20	OV	407-	75162 75182	700p 95p	4019 4020	28p 33p	4528 4529		65p	74HC85		33
915 918	30		nv	1B5p	75183	95p	4021	360	4532		48p 140p	74HC86 74HC107		29
924	30	P BR215	5	200p	75195 2114	185p 150p	4022 4023	36p 13p 25p	4553 4555		29p	74HC123		3
BL05 BL08	24	P 25A/60 P BR258	0V	2400	2532	200p	4024	25p	4556		36p	74HC125		35 25 26 33 33 33 33 34 34 36 36 36 36 36 36 36 36 36 36 36 36 36
BL12	24	P 25A/80			26LS32	75p 100p	4025 4026	13p 60p	4557 4583		140p 60p	74HC126 74HC132		3
BL15 BL18	24	P BR351 P 35V/10		185p	2716 2732	200p	4027	18p	4584		30p	74HC133		3
BL24	24	P BR352		200p	2732A	220p	4028	29p 34p	4585 40103		40p 120p	74HC137 74HC138		3
9L05	35			220p	2764 27C64	150p 200p	4029 4030	17p	40105		140p	74HC147		4
9L08 9L12	35 35				27128	150p	4032	52p 60p	40106 40107		35p 50p	74HC153 74HC154		3
9L15	35			230p	27256-25 27512	150p 300p	4033 4034	76p	40110		170p	74HC157		3
M309K M317T	100			260p	4116	40p	4035	42p 46p	40114 40160		180p 55p	74HC158 74HC160		3
M323K	350	IP 35V/80		40p	4164-15 4164-12	80p 90p	4038 4040	30₽	40161		55p	74HC161		4
BH09KC 9H12KC	800 700				41256-15	80p	4041	36p	40174 40192		48p 48p	74HC162 74HC163		4
9HGKC	800	P BY176		40p	41256-12 41256-10	100p 110p	4042 4043	30p 36p 72p	40193		48p	74HC164		4
L.E.D	.'s 3mm	1.5A/8			41464-12	150p	4045	72p	40194 40257		58p 120p	74HC165 74HC168		5
		100	TRIACS		6116 6264-10	80p 210p	4046 4047	42p 45p	40207		(EOD	74HC174		3
ELLOW		Sp TIC20	iD Oi	60p	62256-12	300p	4048	26p		74 SERIES		74HC175 74HC190		4
REEN		3p 4A/400	V		6502A 65C02	360p 930p	4049 4050	18p 20p	7400		20p	74HC192		5
mm		TIC225		69p	6522	280p	4051	380	7401		16p 18p	74HC193 74HC194		4
ED		Sp TIC22	SD O	68p	6800 6802	210p 220p	4052 4053	35p 35p	7402 7403		20p	74HC195		- 4
REEN		8A/400 TIC23		85p	6803	500p	4054	53p	7404		35p 10p	74HC221 74HC238		8
		12A/4	IOV .		6808	500p 500p	4055 4056	52p 52p	7405 7406		30p	74HC240		4
	NGULAR ED's	TIC24 16A/4		105p	6810	150p	4060	52p 40p	7407		30p	74HC241 74HC242	F 15 4 11 11 1	4
		TIC25	30	190p	6818	380p 130p	4063 4066	52p 20p	7408		25p 20p	74HC243	3	5
mm z 2.5mm		20A/44 TIC26	ID VO	2050	6821 6840	290p	4067	120p 13p	7413		30p	74HC245		4 2 4 5 4
ELLOW		8p 25A/4		Loop	6845	200p	4068 4069	13p	7414 7416		45p 32p	74HC251 74HC257		4
REEN		Вр	THYRISTORS		6850 74F244	90p 35p	4070	13p	7417		32p	74HC259		2
BF	RIDGE				8085A	300p	4071	13p 13p 13p 13p	7420 7421		20p~ 25p	74HC273 74HC280		6
REC	TIFIER	2N506 0.8A/6	1	20p	8086 8088	500p 480p	4072 4073	13p	7425		15p	74HC283	3	6
V005	1	6p 0.8A/6	UV					T. Call	A Charles				THE LATE OF STREET	

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News...

16-bit microcontroller with on-chip flash memory

Hitachi has extended its range of F-ZTAT microcontrollers with on-chip flash memory with its first 16-bit device. Based on the H8/300H CPU core with a 125 nanosecond instruction cycle, the new H8/3048 has 128 Kbytes of flash memory, system support functions and a mix of peripherals.

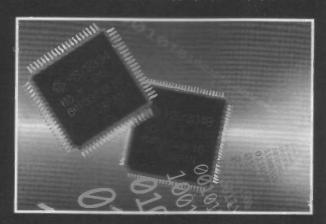
The on-chip flash memory enables engineers to reprogram the microcontroller in the system, avoiding the need to remove and replace the device for software changes. Manufacturers can also build uncommitted production units and program the flash memory just before delivery. This can be used, for example, to set different languages in units destined for different countries. The flash memory also allows units to be reprogrammed in the field in incorporate new standards or product upgrades.

The H8/300H CPU core has 32-bit internal architecture with sixteen 16-bit general purpose registers and a concise instruction set designed for speed. Other on-chip functions include up to 4 Kbytes of ram, a 16-bit integrated timer unit, a programmable timing pattern controller, a watchdog timer, two serial comms interfaces, an 8-channel, 10-bit A/D converter, a 2-channel, 8-bit DAC, 78 I/O pins, a DMA controller and a refresh controller. The H8/3048 series is supported by development software and a low-cost evaluation kit including a

development board with the device, a flash programming board and software, a GNU C compiler and C debugger, a Windows debugger and a CD-ROM with full documentation and tutorials.

The H8/3048 is also available with prom or masked rom memory.

For further information contact Vince Pitt, Hitachi Europe Ltd., Whitebrook Park, Lower Cookham Rd., Maidenhead, Berks SL6 8YA. Tel. 01628 585163 Fax 01628 585160.



MODSMODSMODSMODSMODS

Issue 2 1997 Low Cost Digital Frequency Meter. In figure 2, D2 1N4148 should be the other way round. In figure 3, R15 should appear between X1 and pin 15 of IC1. Figure 4, the component layout, is correct.

Computer AutoSwitcher Issue 5 1997 In the bottom left hand corner of figure 2, a connection is shown between the neutral line and the 0 tap of T1 secondary. The connection should be between the 0 tap and the earth line immediately above it on the diagram. The PCB, and the component overlay shown in figure 3, are correct. A sheet listing the published MODS from the September 1996 issue of ETI onwards is available from Nexus House (see page 74) for an SAE or International Reply postal coupon.

Eprommer Eprom Programmer (Issue 7 1997). In figure 9 on page 55, the component labelled C15 should be IC13, the 317LP voltage regulator, with three connections, not two as marked. The tracks and pads on the PCB foils are correct. In figure 6, page 54, the two "blank" pads just to the left of R3 are for an extra 10 nF spike-precaution capacitor between pin 28 of the eprom socket and ground. The references to figure 6 (the main component overlay) and figure 8 (case construction) are a little scrambled, but fortunately the two diagrams are not easily confused. In figure 6, the through holes are marked by single pads with a black dot, not by an X as in the text.

PCB supplies for hobbyists

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For more information, write to Etch-Tech Boards, PO Box 1566, Salisbury, Wilts SP1 3XX.

Antique Wireless 155

The Antique Wireless Newsheet Is out and about again. Editor Tudor Gwilliam-Rees, formerly of The Vintage Wireless Co. Ltd., is publishing news of manuals, magazines and service data from Savoy Hill Publications, 50 Middon St., Bideford, The Little White Town, North Devon EX39 2EQ. Tel/Fax 01237 424280.

Subscriptions are UK £5 for 12 issues, overseas £7.00 for 12 issues. Recognised clubs, magazines and societies, please write and enquire.

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(utrf) transceivers. Chris Lorek gets to grips with the latest
miniature transceivers.

Study for the RAE on your PC
Paul Simpson GORUR reviews the Ludd Radio Amateur
Examination Tutorial software by George Butter G4BXU.

A Storno 4000 scanning modification
Gary Franklin G4GHD describes a simple drawit for a
scanning function that automatically increments through
programmed channels, stopping when it finds a busy
channel. This could quite easily be used to give a scan
function in any set which has an up/down button for
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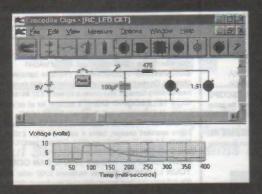
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News...

Crocodile migrates to Windows and Mac



Crocodile Clips circuit simulation software, widely used in UK schools for National Curriculum courses in Science and Technology, is now available for Apple Macintosh computers, and for Windows running on PCs.

Users can design and test their own circuits on screen with a range of components, in effect providing a software-based breadboarding system before real components are brought into use. Packages are also available to allow the user to import Crocodile Clips circuits and use them to design PCB layouts.

The makers of Crocodile Clips believe that the software, which has proved useful in schools and industrial training, is also useful to constructors designing and making their own projects and PCBs.

Demonstration versions are available from the web site www.crocodile-clips.com/education/ or by calling 0131 226 1511.

Police get one-touch computer comms in-car

Police in Lincolnshire are putting into action a county-wide Installation of mobile data terminals with satellite-based location detection. This will allow the Force to pinpoint the exact location of their patrol cars, and allow Police Officers to access the Police National Computer from patrol cars.

The system, designed for Lincolnshire Police by systems integrators APD Communications of Aylesbury and Havant-based data storage and networking manufacturers Xyratex in partnership, will provide officers in up to 120 vehicles with direct live connection to central criminal records and car registration details, via purpose-designed ruggedised personal computers.

Superintendent Duncan Gerrard of Lincolnshire Police said, "Not only do officers have effortless access to key data on suspect vehicles and their drivers, but they can obtain this information directly from the PNC, within seconds, combined with direct despatch to incidents from the command and control system, linked to a full GIS mapping system. This speeds up their throughput, and dramatically decreases the number of voice request for information to the control centre, freeing up those operators to deal with other urgent requirements."

The in-car Mobile Data Terminal, deisgned and made by Xyratex, is a full-spec portable comuter capable or running all standard PC DOS and Windows-based applications. The Police units also have a new manoeuvrable LCD 7.4 in touch-screen mounted on the vehicle dashboard. The touch screen allows single-touch commands for one-man police vehicles, and can be angled to prevent visibility to others. The system has been rigorously tested to make sure that it is shock-proof, rugged and able to withstand extremes of temperature.

Tony Waddington, Special Accounts Manager at APD Communications, who designed the system, said, "This is the first operation system in the UK which offers fully-

integrated mobile data and vehicle location to a police force by utilising their own existing private mobile radio network."

For more information contact Damion D'Souza at Xyratex Tel. 01705 486363 or Greg Wale at APD Communications Tel. 01296 435831.





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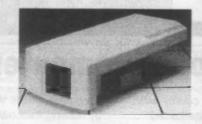
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SETI and the AMATEUR RADIO Astronomer

The Search for Extra-Terrestrial Intelligence is now in the hands of Amateur Radio astronomers.

H Paul Shuch describes the aims and the sky-search activities of the SETI League

he electromagnetic Search for Extra-Terrestrial Intelligence (SETI) has been the subject of considerable interest and attention within the amateur radio astronomy community, ever since the United States Congress terminated the NASA SETI program in 1993. I am going to discuss the more tangible aspects of amateur SETI, emphasising the similarities and differences between a SETI station and a typical radio telescope. It will become clear that amateur radio astronomers already possess much of the equipment and expertise necessary to mount a scientifically credible SETI effort. Just as optical astronomers are responsible for the discovery of most comets, the amateur radio astronomer should be in an ideal position to be the first to detect coherent signals from distant, technologically developed civilisations.

Early SETI

The notion of humankind's uniqueness in the universe had been challenged by philosophers since before Copernican times. Nevertheless, it is only within the twentieth century that the existence of other technologically advanced civilisations in space has become a possibility accepted within the scientific establishment, and far more recently still that the feasibility of detecting such other civilisations has entered mainstream thinking.

The first scientific paper seriously contemplating surveying nearby stars for intelligently directed microwave signals, Searching for Interstellar Communications by Cocconi and Morrison was published in 1959 (Nature 184 pp 844-846, 19 September 1959). Unbeknownst to the authors, as they were writing their pivotal paper, a young radio astronomer was preparing to perform the very experiment that they were describing. That scientist, Dr. Frank Drake, launched his Project Ozma search from the National Radio Astronomy Observatory (NRAO) facility at Green Bank, WV in 1960, ushering in the era of modem SETI.

Project Ozma must be considered the very first SET study: it surveyed two nearby sun-like stars, for just a few weeks, at just one frequency, and detected no extra-terrestrial intelligent signals. Nevertheless, Ozma served as a model for dozens of later SETI projects.

The world's first SETI meeting was convened at Green Bank by Dr. Drake in 1961. As the agenda for that conference, Drake drafted an equation for estimating the number of possible communicative technologies in the cosmos. The Drake Equation is today the primary probabilistic tool whereby SETI scientists assess their prospects of success. Drake himself considers it a way of quantifying our ignorance. The exact equation is worthy of a chapter of its own, and in fact whole books have been written about it. Suffice it to say that its seven factors encompass cosmology, planetology, atmospheric science, evolutionary biology, psychology, technology, and sociology. Thus SETI is possibly the most interdisciplinary of all sciences.

In the nearly four decades between that first meeting and today, of the order of fifty different SETI projects have been conducted around the world, with frequency coverage extending throughout the microwave, millimetre-wave, and optical spectra. These searches have been attempted by Government agencies, educational institutions, non-profit scientific organisations, and, more recently, by amateurs.

Although no definitive proof of extraterrestrial intelligence has yet been received, SETI has achieved scores of tantalising hints that such signals might indeed exist. Many candidate signals have been attributed to terrestrial, aircraft and satellite interference, others to equipment malfunction and natural astrophysical phenomena, but a few defy explanation. Since these signals have failed to repeat or otherwise eluded our attempts at verification, we can draw no conclusion save that there is much to be learned about the universe we inhabit.

The Sky Survey — Amateur SETI's Rightful Role

Before its funding was terminated by Congress in 1993 NASA's SETI program consisted of two distinct but complementary research elements: a targeted search of nearby sun-like stars, and an all-sky survey for interesting signals of unknown origin. The former, which involves aiming at likely candidate stars for long periods of time, is well suited to large, steerable dishes with their narrow beam-widths and high sensitivities. If we guess right as to which stars constitute likely candidates, the targeted search will provide us with the greatest likelihood of immediate

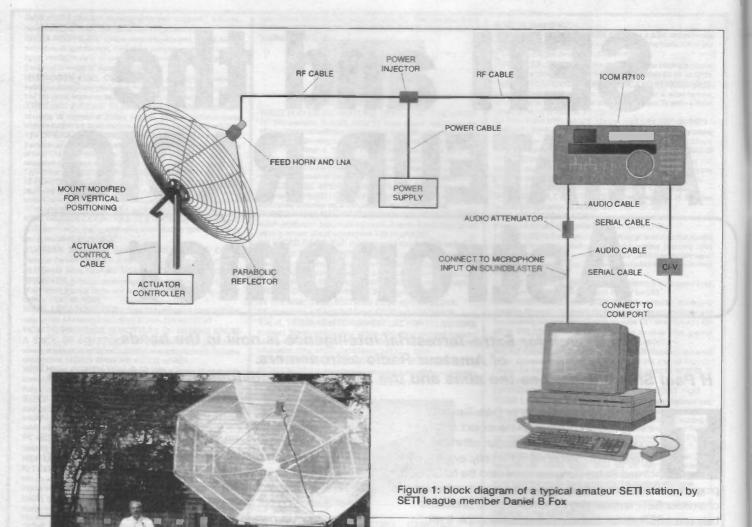


Figure 2: SETI League life member Orville Greene welcomes members and guests to the Project Argus launch in April 1996, featuring the Project Argus SETI station described in this article. (Photo: Gerry Fleming)

success. But since only a limited number of relatively nearby candidate stars is known to us, concentrating our search in their direction may cause us to miss an equally good star of which we happen to be unaware.

An all-sky survey, on the other hand makes no a priori assumptions as to the most likely direction to explore. The sky survey attempts to sweep out the entire sky which can be seen from a given location. No antenna tracking is required, since it is the entire sky, rather than individual stars, which we seek to scan. While targeted search antennas must be constantly moved, sky survey radio telescopes are operated in meridian transit, or drift-scan mode, in which it is the Earth's rotation which turns them.

NASA's late targeted search has been resurrected by the non-profit California-based SETI Institute. Their *Project Phoenix* effort employs some of the world's finest radio telescopes, aiming them sequentially at promising targets from a catalogue of nearby sun-like stars. But since large antennas have quite narrow beam-width, they see only a small portion of the sky at a given time. To sweep out the whole sky with such large antennas would consume inordinate amounts of time. A sky survey effort, by contrast to a targeted search, would be better performed with antennas of moderate size.

Smaller antennas can see more sky within their beam patterns, but have correspondingly less gain. We achieve reasonable sensitivities through A digital signal processing, but the antennas need to remain fixed on their targets for relatively long integration times. Fortunately, when used in meridian transit mode, small antennas, with their relatively wide beam-widths, provide us with far greater signal acquisition time than do the larger antennas typically used for targeted searches.

The sky survey approach seems ideally suited to the community of amateur radio astronomers desiring to pursue SETI. The non-profit, membership-supported SETI League, Inc. has designed and Initiated just such a sky survey. A grass-roots effort which will ultimately grow to thousands of amateur radio telescopes world-wide, the SETI League's *Project Argus* sky survey was initiated in April of 1996. When fully deployed early in the next century, it will provide (for the first time ever) real time full-sky coverage, looking in all directions at once, across all four pi steradians of space.

Selecting the magic frequency

Our Earth is currently surrounded by a sphere of microwave radiation roughly fifty light years in radius, which is readily detectable over interstellar distances utilising technology such as is today available to amateur radio astronomers. This radiation, emanating primarily from our planet's UHF TV transmitters and long range search radars, would mark our planet as inhabited to any similar technological society within fifty light years. Within that range are found hundreds of stars, tens of which are sufficiently sun-like to probably host one or more habitable planets.

The distance over which we are detectable is limited only by the time since we first began transmitting sufficiently strong signals in the appropriate frequency range. Fifty years from now, we will be detectable out to 100 light years distance. At that point our signals will have engulfed thousands of stars, including hundreds of potential life sites. With every successive doubling of elapsed time (out to 1,000 years or so), the number of civilisations which our radiation signature can potentially reach goes up by a factor of eight. Sooner or later, our signals may well reach a distant radio telescope.

SETI hypothesises that other technological civilisations are similarly surrounded by a detectable sphere of microwave radiation, the radius of which will be limited only by the length of time such civilisations have possessed sufficiently advanced radio technology. We depend upon our ability to intercept and recognise (though not necessarily decode) such a radiation signature to achieve the existence proof of other intelligent civilisations which SETI seeks.

The problem with seeking incidental radiation is that the unknown factors exceed the known. We can only guess as to where physically to point our antennas, when to listen, and on what frequency. The time dimension is resolved by starting to look now, and continuing until we detect something noteworthy. A large enough number of coordinated stations, effectively looking in all directions at once, resolves the pointing uncertainty. And we can narrow the search space in the frequency dimension by recognising the range of frequencies which are least attenuated by planetary atmospheres and the interstellar medium. This, however, leaves us with most of the microwave spectrum, and much of the optical, as likely frequencies.

Since there are no "wrong" frequencies to search, The SETI League has avoided establishing a policy of dictating search frequencies to *Project Argus* participants. One person's guess is as good as another's, so whatever frequency at which you can assemble a workable radio telescope is fair game. Amateur radio astronomers have long explored the 406 MHz, 610 MHz, 1.42 GHz and 10.6 GHz radio astronomy bands, and I can think of no good reason why they should not pursue SETI in those spectral regions as well.

The foregoing, however, applies only to the problem of scanning for incidental radiation from the distant civilisation. What if another intelligent race were making a deliberate, concerted attempt to signal its presence to its interstellar neighbours? Is there a particular frequency, or range of frequencies, which would be self-evident to the receiving clvilisation? Can we narrow the search space?

Cocconi and Morrison thought so when they published their 1959 Nature article. They reasoned that 1420.405 MHz, the precession frequency of neutral hydrogen atoms, was a good place to start looking for deliberately beamed interstellar beacons. This frequency, which falls in the quietest part of the radio spectrum, is marked for all to see, by nature herself There is nothing geocentric about hydrogen radiation; perhaps, they reasoned, selecting it for interstellar communication is a mark of intelligence, in and of itself

Drake had arrived at the same conclusion independently, and indeed monitored a narrow band of frequencies encompassing the hydrogen line (also known as H1) during his *Project Ozma* search. Today, nearly four decades later, the hydrogen line region still looks like a good bet to many

SETI professionals.

Fortunately for amateur SETI, much amateur and professional radio astronomy already goes on at the hydrogen line. Equipment for use at this frequency is abundantly available, and much of it can be readily adapted to SETI use. There are indeed other likely "magic frequencies" which are being scanned for signals of possible intelligent extraterrestrial origin, and once again, one person's guess is as valid as another's. Nevertheless, many of the world's amateur radio astronomers are already scanning the hydrogen line for natural astrophysical phenomena, and it's a small step to make their receivers search for artificial signals as well. The following sections discuss the hardware, software, and human considerations of amateur SETI.

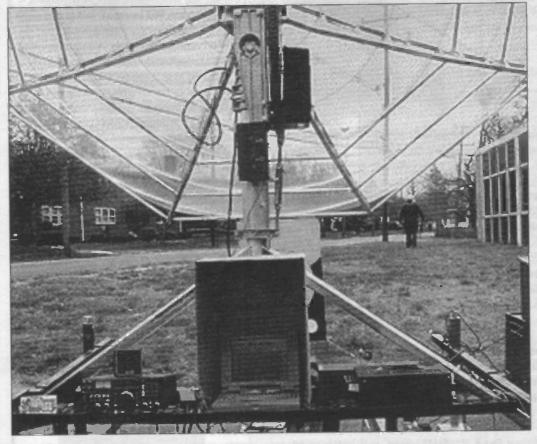


Figure 3: The business end of the first Project Argus radiotelescope. The VDU is showing a hydrogen line weak signal source (visible in the photo) for system testing. The Icom 7000 receiver, and a multimedia laptop computer for digital signal processing are also part of the station. (Photo: Gerry Fleming)

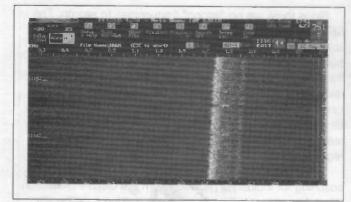


Figure 4: An anomalous signal detected by SETI League members Trevor Unsworth and Ken Chattenton at 1472.5 MHz using a home-made 3.5 metre dish. The signal exhibited digital modulation, with a 270Hz bandwidth. Its Doppler shift of -25 Hz/min marks it as RFI from a low earth orbit (LEO) satellite. Though clearly not of extra-terrestrial origin, this signal gave Project Argus its first real workout, testing both the sensitivity of our receiving stations, and our ability to recognise terrestrial and satellite interference. (Image by Trevor Unsworth GOECP, by permission.)

What is The Drake equation?

Is there a way to estimate the number of technologically advanced civilisations that might exist in our Galaxy? While working at the National Radio Astronomy Observatory in Green Bank, West Virglnia, Dr. Frank Drake conceived a means to mathematically estimate the number of worlds that might harbour beings with technology sufficient to communicate across the vast gulfs of interstellar space. The Drake Equation, as it came to be known, was formulated in 1961 and is generally accepted by the scientific community.

N = R* fp ne fl fi fc L

where

iN = The number of communicative civilisations iR* = The rate of formation of suitable stars (stars such as our Sun) ifp = The fraction of those stars with planets. (Current evidence indicates that planetary systems may be common for stars like the Sun.) ine = The number of Earth-like worlds per planetary system ifl = The fraction of those Earth-like planets where life actually develops ifi = The fraction of life sites where intelligence develops ifc = The fraction of communicative planets (those on which electromagnetic communications technology develops) iL = The "lifetime" of communicating civilisations.

Frank Drake's own current solution to the Drake Equation estimates 10,000 communicative civilisations in the Milky Way. Dr. Drake, who serves on the SETI League's advisory board, has personally endorsed SETI's planned all-sky survey.

Quoted by permission of Steve Ford, WB8IMY, from QST, August 1995, page 38.

SETI with a radio telescope

"I already own a sensitive radio telescope," many an amateur radio astronomer has noted. "Why can't I use it for SETI?" The short answer is, you can!. An antenna and preamplifier adequate for radio astronomy might potentially detect intelligent signals from space. To achieve this adequate sensitivity, we design the preamplifier circuitry so as to generate minimal device and thermal noise. And we design the antenna so as to minimise the noise contributions of our warm planet, instead responding primarily to the cold sky above. Most any successful radio telescope meets these conditions. But for SETI, we also need to pay special attention to the receiver, and the post-detection hardware and/or software, if we are to maximise our admittedly slim chances of success. This section will deal with receiver considerations. Signal processing is addressed in the section which follows.

Any amateur (or professional) radio astronomer pointing a sufficiently sensitive radio telescope at the sky will encounter a wide variety of naturally occurring radio phenomena. Prominent among these will be solar radiation, or sun noise, which spans the spectrum. All stars emit this broadband signal, though it will be most pronounced, and most easily detected, from our nearest stellar neighbour. In addition to solar noise, Hi radiation emanates from the roughly one hydrogen atom found per cubic centimetre of interstellar space. While concentrated at the 21 cm (1420 MHz) line, it is Doppler shifted both up and down in frequency by the random motion of the interstellar medium.

Though hydrogen dominates all of space, countless other atoms and molecules, both inorganic and organic, permeate the interstellar medium, and many emit characteristic signals which are similarly Doppler shifted across the spectrum. These natural emissions, the signals which radio astronomy seeks, are present in receivers pursuing SETI as well, but in this case represent not signals at all, but potential interference.

Fortunately, all known natural radio phenomena emanating from space are inherently broadband in nature, none being narrower than a few hundred kHz. Intelligently generated mlcrowave signals, on the other hand, are characterised by their relative spectral purity or coherence, and (depending upon their modulation mode and information content) might be very narrow band indeed. So spectral coherence is one of the hallmarks of artificiality which SETI seeks, and which helps us to distinguish between a SETI signal and natural "noise."

Most microwave receivers used for classical radio astronomy tend to be relatively broadband. If the signal energy we seek represents a natural astrophysical phenomenon (which we can expect to occupy a broad slice of spectrum), then it makes good sense to employ broadband receivers, so as to intercept as much as possible of the signal energy. Such is not the case for SETI.

Narrow band "bins"

SETI tends to utilise extremely narrow-band receivers (only at the post-detection level. That is, our radio frequency (rf) circuitry might scan wide spectral expanses, but we process the received signals in some way, into very narrow channels or "bins", in search of artificial phenomena. These bins tend to be tens of Hertz to tenths of Hertz wide. This has significant implications if we try to adapt existing (presumably broadband) radio telescopes to SETI.

We could, for example, modify any superheterodyne radio astronomy receiver for narrow-band reception, simply by adding a narrow if (intermediate frequency) filter. But unless the

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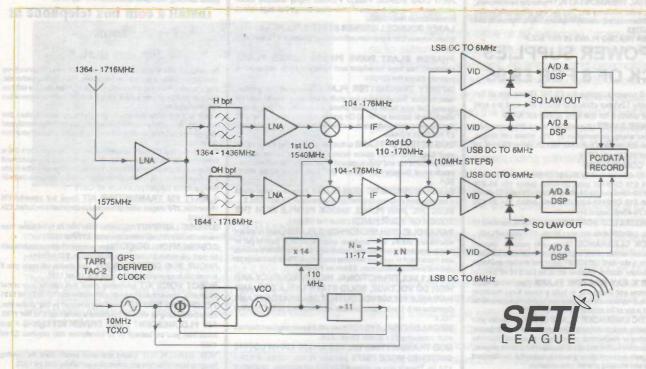


Figure 5: sample block diagram: the Mini-Meta spectral and temporal analysis receiver, a typical SETI receiver

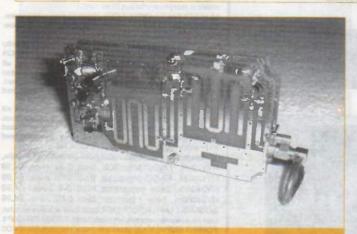


Figure 6: the new SETI League hydrogen line (1420 MHz) to 2-meter (144 MHz) downconverter, first demonstrated at the Annual Meeting in March 1997. At the Southeastern VHF Conference two weeks later, the prototype measured in at 1.85 dB noise figure and 49 dB conversion gain. This converter gives over 50 dB image rejection and 30 dB spurious rejection and will be offered in kit form by Down East Microwave (See References)

LO (local oscillator) used to downconvert the incoming signal frequency is sufficiently stable, the signal may not stay within the if passband long enough to process. Thus the radio astronomy receivers which hold most promise for SETI applications will be those with crystal-controlled LO chains. And to reduce thermal drift, an oven-stabilised crystal is highly desirable. (See also figure 5.)

Many of the more capable microwave receivers employ digital frequency synthesis of the local oscillator frequency. Synthesisers generally provide us with ample frequency stability, in that they involve phase-locking a free-running oscillator to a highly regulated, temperature-controlled crystal reference oscillator. Unfortunately, all but the most sophisticated synthesisers suffer from marginal spectral purity. This is because synthesisers tend to generate a plethora of phase-noise sidebands only a few tens of dB weaker than the desired LO frequency.

Phase noise limits the SETI receiver's ultimate sensitivity, by adding noise prior to the detector. But it has an additional detrimental effect, in that noisy LOs might generate spurious receiver responses, giving us multiple opportunities for a false indication of a coherent signal where none is in fact present. A high level of falsing can be expected for SETI anyway, due to the polluted nature of our planet's Mr environment. Why complicate the situation with receiver-generated false hits? It is probably better to avoid synthesised receivers, unless they have been designed for the lowest possible phase noise. Another LO concern deals with long-term stability. In order to maximise the sensitivity of a SETI receiver, it might be necessary to integrate the signal (in either hardware or software) for many minutes. The LO must hold still so that the received signal remains in the bin width for the entire integration period. All but the most carefully designed oscillator circuits will exhibit excessive long-term drift.

In summary, radio telescope receivers may prove useful for SETI, with modification. A narrower bandwidth if filter is usually called for, and it is often necessary to employ an external, crystal-controlled and temperature regulated LO chain exhibiting the very highest possible frequency stability, and the very lowest possible phase noise. Such an LO is the most critical element of a sultable SETI receiver.

Signal processing considerations

OK, so we've come up with a radio telescope which employs an acceptable LO, ample if filtering, and adequate sensitivity to recover the weakest of signals. We're still not done. We now need to process the recovered signals into narrow bins, and identify within them those signals which might emanate from distant technological civilisations.

The earliest SETI receivers employed filter-bank technology. That is, the if was split into multiple filters, each with a bandwidth of a few kHz, on adjacent frequencies. Each filter drove its own square-law detector circuit, and any signal which appeared at the output of one filter channel, but not the adjacent ones, was considered narrow enough in bandwidth to

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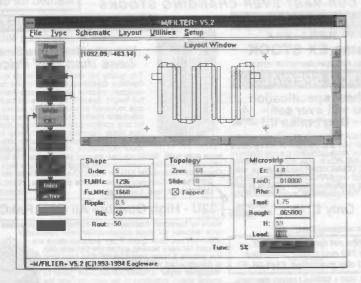


Figure 7: a design simulation of a type of microwave bandpass filter suitable for SETI use. (Compare this with figure 6)

constitute a SETI candidate. This is the very scheme employed at the Ohio State University Radio Observatory in 1977, when the so-called "Wow!" signal (the most tantalising SETI candidate signal to date), was detected.

Fortunately, our technology has advanced significantly since then. Today the favoured tool for SETI signal analysis is digital signal processing (9SP), employing computers executing fast Fourier transform (FFT) algorithms. Implementing such techniques in custom, dedicated DSP microcircuits, the SETI research community has for some time concentrated on developing sophisticated multi-channel spectrum analysers (MCSAs) capable of scanning millions of bins, over hundreds of megahertz of spectrum, in real time. The current state of the art in MCSA technology is probably BETA, developed at Harvard University by physicist Dr. Paul Horowitz, with funding from the Planetary Society and other private and corporate donors. BETA now analyses several hundred million bins, each less than one Hertz wide. Such technology is, unfortunately, well beyond the reach of the amateur SETI community at present. But we can learn from it, and emulate it on a small scale.

Personal computer technology today makes it possible for the amateur radio astronomer to scan thousands of bins, over tens of kilohertz, at virtually negligible cost. The audio output from a SETI receiver must first be digitised for signal analysis, and this is accomplished in any of a number of inexpensive computer sound cards. SETI League members have developed a variety of shareware FFT programs to sort this audio output into bins, and display the results on the computer monitor as histograms, waterfall displays, or any number of alternative formats.

Early amateur SETI systems are digitising a 12.5 kHz audio bandwidth, and applying DSP software to break it down into 1024 individual bins, each about 12 Hz wide. It remains to be seen whether these values are optimal, but the beauty of the PC-driven DSP approach is that the search parameters are readily changed in software. As faster personal computers and more advanced sound cards become available, it becomes possible to reduce the width of individual bins, increase the total number of bins scanned, or increase the bandwidth of the audio spectrum which is being monitored.

Since sensitivity of radio telescopes increases with the square root of integration time, small-aperture amateur

instruments generally time-average a very large number of observations to achieve reasonable performance. Long integration would similarly improve the sensitivity of amateur SETI systems, but with a complication. We are observing the heavens from a rotating platform, which imposes on all received signals a characteristic Doppler shift related to the Earth's motion. Depending on frequency and declination angle, this Doppler shift can be ten to hundreds of Hertz during the time it takes a signal to transit the antenna's beamwidth. For wideband radio telescopes, the Doppler shift is minute compared to the signal bandwidth, hence we can integrate for the entire transit time. Narrow-band SETI receivers, on the other hand, are integration-limited by Doppler to the time it takes the signal to drift between bins. Given, for example, a 10 Hz bin width, and a Doppler rate due to the Earth's rotation of 10 Hz/min, we would be limited to only one minute integration periods. Beyond that, the signal would find itself in the next bin of the digital signal processor. This Doppler phenomenon significantly limits the maximum integration time we can utilise, hence the maximum sensitivity we can achieve.

There is a partial solution to the above problem. The same computer which performs signal analysis can compute the Doppler rate, as a function of the frequency scanned and the co-ordinates of the antenna. Many microwave receivers can be tuned if the receiver's local oscillator is properly chirped (that is, tuned slowly in frequency) at exactly tine Doppler rate, the effects of the Earth's rotation can be nullified, and longer integration becomes possible.

Unfortunately, chirping the receiver's LO only compensates for the rotation of our own planet. A valid SETI signal would most likely be emanating from a similarly rotating planet, which would impose a Doppler shift on the transmitter which we can in no way predict. It is hypothesised that any civilisation producing a deliberately beamed interstellar beacon would solve the problem for us, by drifting their transmitter's frequency so as to compensate for their own Doppler. However, we can expect no such assistance in the case of intercepting a civilisation's leakage radiation, hence our practical integration times are likely to be limited.



Dr. H. Paul Shuch attempts to measure sun noise with a portable radio telescope. This system serves as a test-bed for the hardware and software to be used for the Project Argus all-sky survey. The actual antennas used for SETI are much larger.

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Software is currently under development to automate the signal analysis and verification process, by alerting the operator (and through the Internet, other SETI participants) when a signal meets a set of user-programmed criteria. Terrestrial and satellite interference have already generated false alarms for our early participants. But through the application of artificial intelligence (AI) techniques, it is expected that the system will ultimately learn from its false detections, so that in time, it will only respond to those signals which represent the most viable candidates for SETI success.

Assembling a prototype system

Let's start by defining the minimum equipment necessary to do a credible job at microwave SETI, as depicted in the typical system block diagram developed by SETI League member Dan Fox (figure 1). You will want to acquire, at a minimum, a dish antenna and feed horn, a low-noise preamplifier, a microwave receiver, and a suitable computer running some kind of digital signal processing software. A number of useful accessories will round out the SETI station. There are sections in the SETI League Technical Manual corresponding to each of these areas, but the choices are so diverse as to boggle the mind. Is there anything we can do to narrow things down a bit?

In fact, there is. I can tell you exactly what hardware and software I used in the first *Project Argus* station at SETI League headquarters. I didn't go with the least expensive choices in each category, or necessarily the best. I opted for expediency in order to get a station on the air in time for our April 21, 1996 launch ceremonies and, yes, I cut a few corners in the process. You probably won't want to duplicate my station exactly as I implemented it, but at least this will give you a starting point. As more stations come on the air, better solutions to the problems of amateur SETI will make themselves known. Some of these will come from you, and I hope you'll share them with your fellow League members.

The headquarters station is depicted in figures 2 and 3. Though just about any surplus satellite TV dish in the 3- to 5-meter diameter range would suffice, the antenna we chose for our first system is a Paraclipse Classic 12, with horizon-to-horizon mount. This 3.7 meter diameter dish has a focal length to diameter ratio which makes it easy to illuminate with a simple cylindrical waveguide feed horn from Radio Astronomy Supplies of Atlanta. Feed efficiency is on the order of 50 percent. As the antenna is slightly under-illuminated, sidelobes and antenna noise temperature are reduced. We are exploring the possibility of adding a choke ring to this feed horn in the future, to improve both illumination efficiency and sidelobe performance. The robust Paraclipse mount and chain-drive rotor were modified for meridian transit mount with full 180 degree elevation rotation.

A SETI League-designed GaAs MMIC (gallium arsenide monolithic microwave integrated circuit) low-noise amplifier, as manufactured by Down East Microwave, is mounted directly on the feed with a male-to-male type N coaxial adapter. The next generation preamp, now in the design phase, will employ a GaAs PHEMT (pseudomorphic high electron mobility transistor) device in front of the existing MMIC stage, for a significant reduction in front-end noise. At present, no bandpass filter is being used behind the preamp, although in Mr polluted areas it might be wise to add one. Though not yet commercially available, a microstrip filter such as the one described in the Technical Manual is probably a good bet. We expect to add such a filter to our station at a later date.

Twenty-five feet of RG-8 coaxial cable, with type N connectors installed, connect the LNA to an Icom 7000 microwave receiver. (Production of this receiver has been discontinued in favour of the new model 8500. At time of writing the SEI league has not evaluated the new design, but it should perform at least as well as the model 7000 series. We had considered replacing this receiver with a homebrew downconverter driving a VHF scanner, although the Icom is performing so well that we would be hard pressed to recommend any other approach at this time. Receiver audio output is applied to the microphone input of a Texas Instruments model 560CDT multimedia laptop computer, which uses a 75 MHz Pentium CPU. In fact, much less costly computers of the 486DX variety would be perfectly acceptable, at a fraction of the price. The DSP software we are currently using is a commercial product called Spectra Plus, although any of the low-cost shareware programs listed in the Appendix are certainly worthy of consideration. We have yet to obtain suitable SETI logging software, so at present one must stare at a computer screen and evaluate the incoming signals. This is a weakness in the first Argus systems which we hope our members will help us to overcome.

It must be emphasised that this station is not the only, indeed not necessarily the best, approach to amateur SETI It does, however, achieve all design objectives in terms of frequency coverage, stability and sensitivity. If all components are purchased new, it can be duplicated in its entirety at a cost of about \$7,000 US. (Although half of that cost is tied up in the particular multimedia laptop computer we chose.) This is certainly quite a bit more than one need spend for an effective SETI station. In fact, using a more modest computer and dish, the price quickly drops in halt for no discernible difference in performance. And if one uses an existing computer, a surplus dish, and builds some of the RF hardware from kits rather than purchasing it assembled, then the basic design is duplicable for well under \$1000 US. Thus, the system just described should be considered as a proof-of-concept effort, nothing more.

Search coordination and verification

The search space for SETI involves temporal, directional, and frequency dimensions, and it's probably unrealistic to expect any search to encompass all possibilities. Nevertheless, the greater the number of participants, the more frequencies and directions we can hope to monitor per unit time. Thus The SETI League has concentrated its efforts on devising a global network of thousands of participating stations. The publication of these pages constitutes a part of that effort. But an infinite network will avail us little if all members end up searching on the same frequency, in the same direction, at the same time.

As discussed in the Magic Frequencies section above, there are good arguments against dictating frequency coverage at present. Sky coverage, on the other hand, can be readily co-ordinated. If all the amateur radio telescopes being devoted to SETI are operated in meridian transit mode, then by judicious assignment of declination angles, full shy coverage becomes a feasible goal. Based upon the beam-widths typical of amateur radio telescopes, scanning all four pi steradians of sky in real time will require something on the order of 5,000 participants. This goal seems elusive, when viewed from the perspective of around 24 active stations. But The SETI League Is adopting a

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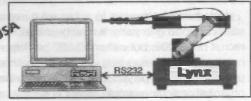
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longer view. If we provide the necessary co-ordination between participant stations, we can hope to achieve full sky coverage early in the next decade.

A major concern of SETI professionals is whether amateur radio astronomers have the training and discipline to separate the electromagnetic wheat from the cosmic chaff. That is, will we be fooled by astrophysical phenomena and manmade interference which might masquerade as intelligently generated extra-terrestrial signals? The concern is a valid one; even professionals are sometimes fooled by their equipment or the environment. When Frank Drake first swung his *Project Ozma* dish toward Epsilon Eridani, he was excited to be greeted by a strong, stable, clearly artificial signal. "Can it really be this easy?" he wondered. It took several days of repeat observations for Drake to figure out that he was being tantalised by manmade interference, most likely from a military aircraft or spacecraft.

Every subsequent SETI study has encountered similar false alarms. Our planet is now encased in a shell of orbiting communications and navigation platforms, all generating signals across the microwave spectrum which could easily be mistaken for interstellar communications. The Project Phoenix targeted search has received hundreds of these false alarms, and has had to employ a sophisticated follow-up detection mechanism Involving the use of two widely separated radio telescopes, in order to eliminate interference from consideration The SETI League has been similarly fooled by our increasingly rf-polluted environment.

Our *Project Argus* sky survey kicked off on 21 April 1996 initially with a mere five participating stations. Less than three weeks later, on 10 May, two of our members in England reported receiving a candidate signal in the 1.4 GHz

band (figure 4). Follow-on analysis indicated that the signal's Doppler shift was far too rapid to be explained by the Earth's rotation, but was consistent with that expected from a low-Earth satellite orbit. It appeared that the SETI League's search had fallen prey to what Frank Drake calls Spectral Gridlock.

Fortunately, we have a variety of tools in our arsenal to guard against such false alarms. If we employ computer-controlled receiver tuning as outlined in the previous sections, then such satellite interference as experienced in England will spread itself across several adjacent DSP bins, and be essentially ignored by the computer. But we can envision interfering signals which emulate even the Doppler signature of interstellar communications, and must take steps to guard against drawing false conclusions.

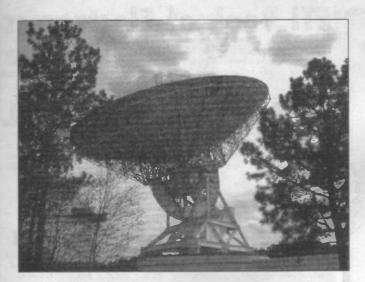
The Follow-Up Detection Device (FUDD) approach utilised by Project Phoenix, to which we alluded earlier, holds promise for amateur SETI as well. Just as the professional SETI community can pair up spatially disparate research-grade radio telescopes for signal verification, so can amateur SETI pair up two widely separated lesser telescopes. If properly co-ordinated, they can form what I call a "zeroth-order" interferometer.

The idea is for two member stations, displaced in longitude by several hundred kilometres, to both view the same celestial co-ordinates, at the same frequency, all the time. Linked through the Internet, the two stations can continually compare notes. Any signal detected by only one of the stations is deemed terrestrial interference. Any signal which fails to exhibit the precise (and readily calculable) differential Doppler signatures which a true interstellar source would generate at the two particular observing sites is dismissed as aircraft or spacecraft interference. Only if the amplitude and frequency patterns match those calculated for the two locations is a signal deemed a viable SETI candidate.

Unfortunately, in order to achieve full sky coverage by stations working in pairs, it now becomes necessary to recruit not 5,000, but rather 10,000 participants. Such numbers, though daunting, are not altogether unprecedented. The various radio amateur satellite (AMSAT) organisations, for example, boast in excess of 10,000 members worldwide. They provide, however, a service to the radio amateur community: the design, construction, launch and operation of a network of communications satellites, which permit members to better pursue their hobby. It remains to be seen whether a sufficient number of SETI enthusiasts will similarly regard the co-ordination of a global search as a service worthy of their involvement and support.

Sadly, In recent years the Search for Extra-Terrestrial Intelligence has been attacked not just in the halls of Congress, but in the electronic hobbyist press, as being a waste of time and money. There may be a grain of truth to this, especially if SETI efforts ultimately fail to achieve positive results. An Important consideration of a "privatised" search, however, is that no government entity Is wasting the time and money of its citizens. Rather, it is our individual members who choose to waste their own time and money, to varying degrees, for their individual purposes.

Besides, by definition, doesn't "waste of time and money" properly describe all pastimes? (Serious scientific research undertaken in this area has concluded (I paraphrase) that life would be a waste of time and effort without the presence of personal pastimes.- Ed.)



The SETI Institute's Project Phoenix targeted search of nearby sun-like stars resumed in May 1997, from the National Radio Astronomy Observatory, Green Bank WV. In addition to the 140 foot NRAO radio telescope, the group is employing this 100 foot dish at Georgia Tech's Woodbury Research Facility, as a follow-up detection device, or FUDD.

Conclusions

The world's amateur radio astronomers are in a unique position to make major contributions to the ongoing Search for Extra-Terrestrial Intelligence (SETI). Their radio telescopes already contain much of the hardware and software which comprises a credible SETI station. By paying careful attention to LO stability, if filtering and DSP techniques, they can achieve sensitivities adequate to detect signals of likely power level out to perhaps several hundred light years.

Our signal analysis capabilities are presently limited primarily by the power of our computers. But that's a very good place to be limited. Computer power has been roughly doubling every year for the past few decades. If the technological trend continues, within ten years our available computers will be about 1,000 times as powerful as the ones we use today. At that point, there may well be no place in the Milky Way galaxy which evades our gaze.

Lacking a concentrated, Government-sponsored SETI program, success will most likely require thousands of individual stations in a co-ordinated effort. The SETI League is one organisation willing to provide the needed co-ordination. But discipline on the part of the participants is also crucial. Fortunately, the optical astronomy community has already showed us that amateurs have the discipline necessary to make significant scientific contributions. Why should it be otherwise in the radio spectrum?

Those amateur astronomers interested in pursuing the SETI challenge are invited to join the non-profit, membership-supported SETI League, Inc. The SETI League maintains an extensive Internet presence; publishes quarterly newsletters, how-to manuals, and other technical documents; assists its members in locating equipment and software, as well as setting up their SETI stations; provides co-ordination of frequency and sky coverage; and provides a medium of communications for participants in its Project Argus all-sky survey.

Contacting SETI

Our best information contact (and membership details) are on our Web site at http://www.setileague.org/, by email from joln@setileague.org. Our postal address is The SETI League Inc., PO Box 555, Little Ferry, NJ 07643, USA. Tel (Fax only) (US) 201 641 1771. H. Paul Shuch, PhD is Executive Director of The SETI League, Inc.

Within the USA you can call the League's toll-free membership hotline, 1(800) TAU-SETI.

References

The SETI League Technical Manual (ISBN 0-9650707-2-7) Available for a \$10 US contribution (\$12 for foreign delivery) to The SETI League, Inc.

Equipment suppliers

These are sources for the equipment used to assemble the SETI receiving station described in this article. The list is by no means exhaustive; it merely serves to document one particular prototype system. For additional hardware and software sources, the SETI League Technical Manual, or refer to our World Wide Web site mentioned above.

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Antenna:

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Preamp:

SETI-LNA

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Receiver:

IC-R7000, IC-R7100, IC-R8500

Icom America, 2380 116th Avenue NE, Bellevue, WA 98004, USA Tel 206 454 8155 Fax 206 454 1509.

Software:

FFTDSP

Mike Cook, 501 E Cedar Canyon Rd., Huntertown, IN 46748, USA 219 637 3399.

SETIFOX

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DSP Blaster

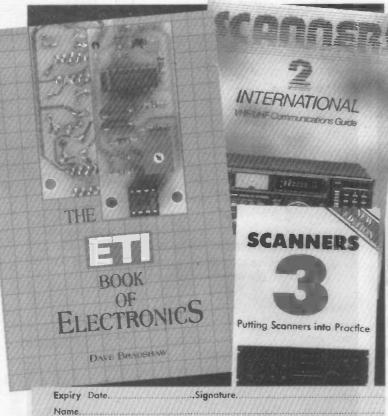
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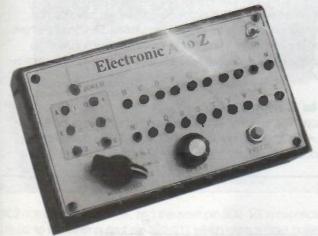
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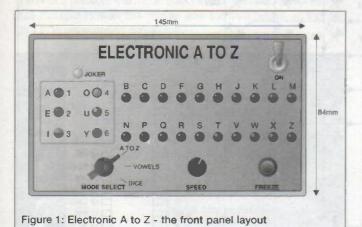
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Electronic A to Z



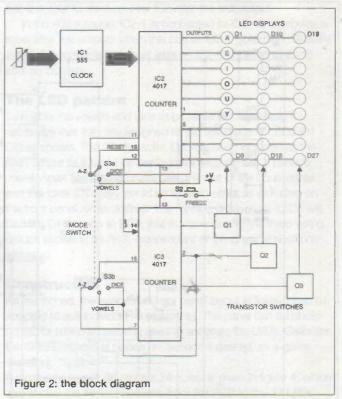
iven twenty-six different letters you could write a best-seller - or a symphony for twelve letters, if you can arrange them in the right order. Perhaps this is too tall an order, but the Electronic A-Z has some more realistic mind-bending challenges to offer with its series of random letters at the touch of a button. Many popular TV quiz shows and board games call for a random selection of letters; for instance, 'Countdown', 'Catchword', 'Lexicon', Scrabble, and so on. Although a number of alphabetic characters can be electronically displayed using eye-catching 7-segment displays, it is not easy to reproduce the full alphabet in this way.



This project describes a simpler A to Z selector, achieved by using single LEDs, one for each letter. Figure 1 shows a suggested front panel layout. The five vowels can be selected separately, and an electronic dice (or die, to be strictly correct), is also 'thrown-in' as a bonus; it is useful in many board games and saves all that scrabbling about with conventional dice that never seem to stay on the board. Besides the 26 letters of the alphabet, a 'joker' is included, which is useful in some word games to represent any letter - like the blank tile in 'Scrabble'!

At switch-on, the display LEDs flash through the selected characters repeatedly at a rate determined by the position of the SPEED control. When the FREEZE button is pressed, the display will pause on the particular character that is being scanned at that instant. On release, the LEDs will resume flashing. The circuit consumes only 6 mA at 9 V.

Take a letter - any letter. Add a joker and you have a recipe for an evening of popular word games. By Roy Bebbington



The block diagram

The block diagram (figure 2) consists basically of an oscillator providing clock pulses to a counter circuit that drives LED displays representing the alphabetic characters. The popular 555 timer and two 4017 decade counter cmos ics are used in this A to Z version to activate 27 LEDs (all 26 letters of the alphabet and the 'joker'). You are probably thinking that, mathematically, something doesn't add up if we expect to scan 27 characters from two decade counters - we don't! All 27 characters, in three groups of nine LEDs, are actually scanned by one decade counter, the second counter merely switching the groups into circuit in the correct sequence. The available characters are selected by the MODE SELECT switch S3, operating broadly as follows:

In the 'A to Z' position, all 27 LEDs, arranged in three groups of nine on the outputs of the first counter IC2, are activated to run in sequence. The second counter, IC3, clocked by the 'carry' output (pin 12), operates three transistor switches TR1-TR3 in sequence to turn on the three groups of LEDs in the right order (D1-D27). In the 'vowel' position all 5 vowel LEDs on the first five outputs of the counter, are activated in sequence. In addition, the reset (pin

15 of IC2) is connected to the sixth output pin (IC2-1), which causes the counter to reset after the first five outputs are scanned so that only the five vowels are selectable

In the 'dice' position the link from the reset (pin 15 of IC2) is switched to the seventh output pin (IC2-5). This causes the counter to reset after the first six outputs are scanned; as the five vowels and the first consonant (Y) are also labelled 1 to 6, a dice facility is available.

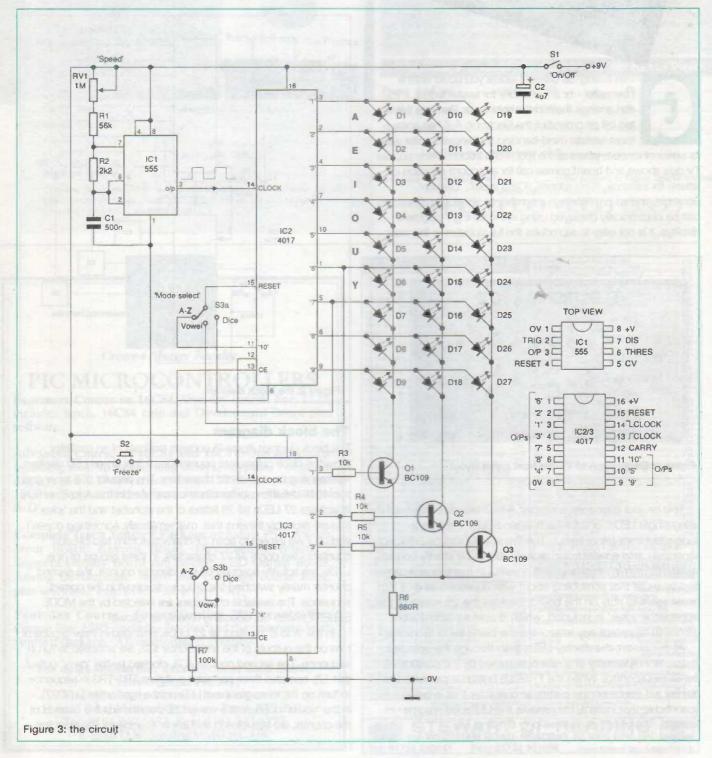
The circuit

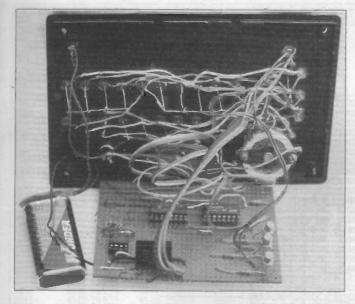
The 555 stage, IC1, provides the rectangular clock pulses to operate the counter stages, IC2 and IC3. A potentiometer RV1 has been included in the timing circuit, designated SPEED, to allow adjustment of the scan speed of the LEDs (approximately 2 to 14Hz). If the speed selected is slow, then it could be possible to anticipate and 'freeze' a letter (or number) of your choice - a useful

facility for some games, especially when handicapping is needed. The timer IC1 is connected in the astable multivibrator mode and positive-going output pulses are available on output pin IC1-3. The speed is determined by the setting of RV1 and the values of R1, R2 and C1. These timing pulses are applied to input pin IC2-14 to 'clock' the counter IC2. The MODE SELECT switch S3 determines which outputs from the counter are activated and therefore which LEDs are available for display. The functions have been briefly covered in the block diagram description, but the details are now discussed with reference to the circuit diagram in figure 3.

A to Z mode

When the A to Z mode is selected on S2, the nine outputs from counter IC2 go high in sequence at a speed determined by the clock pulses. Cycling continues because the clock enable pin





(IC2-13) is held low via R7, and the reset pin (IC2-15) is connected via S2 to the tenth output pin (IC2-11), which gives a reset pulse when it goes high.

The second counter, IC3, in conjunction with the three transistor switches Q1, Q2, Q3, selects the three groups of LEDs in sequence, that is, D1-D9, D10-D18, D19-D27, to provide a continuous running display of the 27 letters in this mode. The divide-by-ten output pin (IC2-12) is used as the clock input to IC3-14 to provide three outputs (IC3-3, IC3-2, IC3-4) to switch the three transistors in sequence. These transistors provide the 0V return, via R6, to activate the three groups of LEDs. This resistor limits the current through the LEDs and also allows IC2-15 to go sufficiently high to achieve reset in the vowel and dice modes. In the A to Z position, the mode select switch S3b returns IC3-15 to IC3-7 output to reset the counter after the third output, so that all 27 LEDs are scanned sequentially.

Resistors

R1 56R R2 2.2R R3,R4,R5 10k R6 680R

R6 680R R7 100k

RV1 1M lin pot.(SPEED)

Capacitors

C1 500nF polyester C2 24.7uF 10V radial elect

Semiconductors

IC1 NE555 timer
IC2, IC3 4017 decade counter
Q1 to Q3 BC109 or equivalent
D1 to D5 5mm green LEDs (vowels)
D6 to D26 5mm red LEDs (consonants)
D27 5mm yellow LED (joker)

Switches

LIST for the Electronic A-Z

S1 spst (ON/OFF)

S2 push-to-make (FREEZE)

S3 2-pole, 3-way rotary (MODE SELECT)

Miscellaneous

Suitable project box eg Maplin MB5 (145 x 95 x 57.5 mm); stripboard (36 strips x 24 holes), 9V battery (PP3); dil holders (8-pin, 2 x 16-pin), connecting wire, solder, etc.

The clock enable pins of the IC2 and IC3 are normally held to 0V by resistor R7. However, when the FREEZE pushbutton S2 is operated, +9V is applied, the clock is disabled and the LED activated at that instant is displayed, while the S2 button is held down. To guard against the 'button-jabbers', who may release S2 before the selected character has been observed, a small circuit modification is suggested. An electrolytic capacitor can be wired in parallel with resistor R7 to continue the freeze action momentarily after S2 is released. A 10uF capacitor holds the selected LED on for a further second before sequencing resumes. Obviously, this modification is effective in all modes.

Vowel/dice mode

In the vowel and dice modes, only the first group of LEDs requires to be activated so that the 5 or 6 characters (vowels or dice) can run continuously in sequence. This means that only transistor switch Q1 needs to be in circuit, so S3b now returns IC3-15 to IC3-2 output to reset counter IC3 after the first output.

In the vowel mode, IC2-1 is connected to IC2-15 to provide a reset after five outputs when this output IC2-1 goes high. Similarly, in the dice mode, a high logic level on IC2-5 is routed to IC-15 after six outputs.

The LED pattern

To enable the vowels and dice to be switched separately, it is necessary that they are assigned to the first group of LEDs (D1 - D6) as shown. The 'Y' character, D6, has been included as the sixth output as it could be useful in some games as a pseudo vowel. Apart from these characters, the rest of the consonants and the joker (D7-D27), could be labelled either as indicated on the front panel, or as desired. The running order of the LEDs will be from D1 through to D27; that is, according to the three wiring groups shown in figure 3, independent of the physical positions allocated.

Construction



As mentioned, the suggested front panel layout in figure.1 can be adapted to suit individual requirements. The panel can be photocopied or rubdown lettering used to annotate the LEDs. Calibrate the SPEED control in pulses per second if desired, as a game 'handicap' facility.

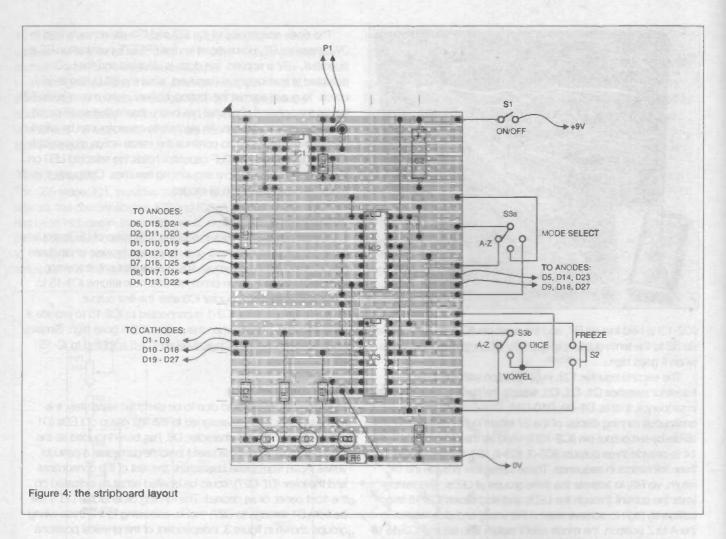
A stripboard layout (36 strips x 24 holes) is given in figure 4, which also shows the interconnections from the PCB to the 27 LEDs and the switches. Use multi-strand wire to keep these connecting links flexible. Only the component side is shown; the breaks in the copper strips on the underside are Indicated by crosses (x). Make sure that no whiskers of copper are left, and there are no excess blobs of solder to cause short-circuits. The integrated circuits should preferably be mounted in dil sockets to avoid overheating and should be retained in their original wrapping until required, that is, fitted last, to avoid the risk of damage by static charges.

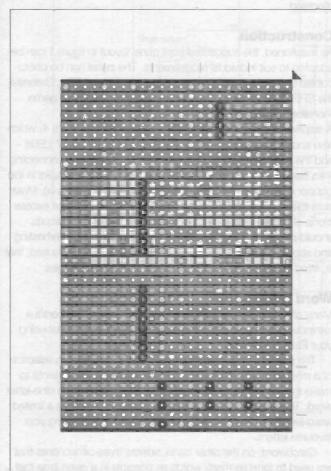
Word games

Many of the popular word games are well-known, but here's a reminder of some of them and a few new suggestions for using your Electronic A to Z.

The popular TV game Countdown offers the random selection of a mixture of nine letters, choosing vowels and consonants to make the longest possible word with bonus points for a nine-letter word. This allows plenty of scope for variations, such as a limited word-length for children and a slower speed for capturing your favourite letters.

Catchword, on the other hand, selects three consonants that are used to form as many words as possible in a given time that





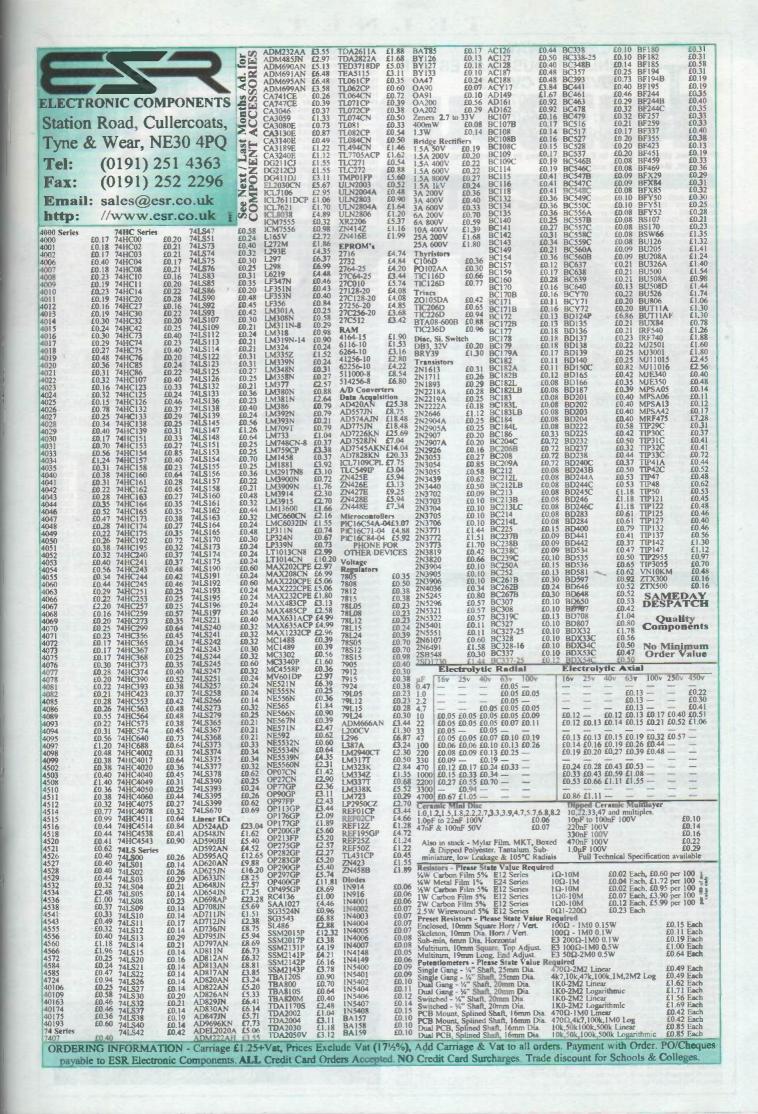
start with the first letter and include the other two in the given order. For example, LND could produce LINED, LOANED, LAMINATED, and so on, a bonus being given for the longest word. In this game, any vowels displayed could be ignored, but the joker could be used to advantage to stand for any desirable consonant.

Another game is called Well-known Initials. A pair of letters can be selected in the A-Z mode to see whether contestants can conjure up famous, or infamous, initials of people in history, films, music, TV, sport, etc. For example, Winston Churchill, Mickey Mouse, Russ Abbot, Andy Cole.

The game Word Chain consists of selecting a sequence of letters in the A-Z mode, which are written down by the contestants. Like the proverbial typewriting monkeys, this chain of letters will mostly produce jibberish, but occasionally a word will be formed. A contestant scores points if a word is spotted before the next letter is selected.

In Wordsmith, the skill is to make as many words as possible from six or seven letters chosen at random. It will be necessary to select a couple of vowels if none is forthcoming from the A-Z mode. As an elegant variation, one of the consonants may be chosen as the key letter, which must occur in all the words.

Finally, don't forget the pencil and paper game of *Categories* where, starting with a chosen letter, lists are made by each contestant of such categories as a girl's name, boy's name, town, county, country, animal, flower, fish, TV star, and sport. The difference is that with the Electronic A to Z, a different starting letter can be randomly selected for each category. Players score 2 for a suitable category name not duplicated by any other player, and one if another player (or more) shares the same answer.



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Rohde & Schwarz LFM2 - 60MHz Group Delay Sweep Gen	£1600
Rohde & Schwarz SMFP2 - 1GHz Radio Comms T/set	£2500
Rohde & Schwarz UPSF2 - Video Noise Meler.	£1400
Honde & Schwarz UHE - HMS Voltmeter (10HZ-25MHZ)	£300
Rohde & Schwarz SUF 2 Noise Generator	2300
Rohde & Schwarz UPGS - Psophometer	£150
Rohde & Schwarz SMDU - 15MHz to 525MHz Signal Gen (FM & AM)	Σ500
Schaffner NSG 203A Line Voltage Vanation Simulator.	£950
Schaffner NSG 223 Interference Generator	£850
Schaffner WSG 431 Electrostatic Discharge Simulator	£1250
Schaffner NSG 223 inserference Generator Schaffner WSG 431 Electrostatic Discharge Simulator Schumberger 4923 Radio Code Test Set. Schlumberger 4923 Radio Code Test Set. Schlumberger 4931 – 1GHz Radio Comms Test Set. Schlumberger 2720 1250MHz Frequency Counter Schlumberger 7050/70657075 Multimeters	0263
Schlumberger 4031 – 1GHz Radio Comms Test Set	£7000
Schlumberger 7060/7065/7075 Multimeters	from £350
Solartron 1250 – Freq. Response Analyser	2500
Stanford Research DS 340 – 15MHz Synthesised Function (NEW) and arbitrary	61200
waveform generator. System Donner 6030 – Microwave Frequency Counter (26.5GHz) Spanning CET Counter Received	£1200
Tektronix TM5003 + AFG 5101 Arbitrary Function Gen	£1750
Tektronix 1240 Logic Analyser	2500
Tektronix DAS9100 - Senes Logic Analyser. Tektronix - Plug-ins - many available such as SC504, SW503, SG502, PG508, FG504, FG503, TG501, TR503 + many more	LOUU
PG508, FG504, FG503, TG501, TR503 + many more	EPOA
Fektronix 577 Curve Tracer	£1150
Tektronix 577 Curve Tracer Fektronix AM503 + TM501 + P6302 – Current Probe Amplifier Fektronix P6506 + TG501 + SG503 + TM503 – Oscilloscope Calibrator Fektronix P6506 + TG501 + SG503 + TM503 – Oscilloscope Calibrator	£1995
Tektronix AA5001 & TM5006 M/F - Programmable Distortion Analyser	£2500
Toldrand ETT Come Transact	61450
Time 9811 Programmable Resistance	£600
Toellner 7720 - Programmable 10MHz Function Gen (AS NEW)	£700
Time 9811 Programmable Resistance Time 9814 Voltage Calibrator Toelliner 7720 – Programmable 10MHz Function Gen (AS NEW) Valhalia Scientific – 2724 Programmable Resistance Standard	EP.O.A.
Mondal 9 Californiana DC884 (- antique)	T3330
wanter a Gottermann PCM4 (+ options)	£600
	£275
	£275 £850
Wayne Kerr 4210 - LCR Meter. Wayne Kerr 4225 - LCR Bridge Wayne Kerr 6425 - Precision Component Analyser Wayne Kerr 6905 - Precision LCR Meter. Wayne Kerr 8905 - Precision LCR Meter.	£850 £850
Wayne Kerr 4210 - LCR Meter. Wayne Kerr 4225 - LCR Bridge Wayne Kerr 6425 - Precision Component Analyser Wayne Kerr 6905 - Precision LCR Meter. Wayne Kerr 8905 - Precision LCR Meter.	£275 £850 £250 £P.O.A.
Wandel & Goltermann PCM& (* options) Wayne Kerr 4210 – LCR Meter Wayne Kerr 4225 – LCR Bridge Wayne Kerr 6425 – Precision Component Analyser Wayne Kerr 9805 – Precision LCR Meter Wavetek 171 – Synthetised Function Generator Wavetek 172 – Synthetised Function Generator Wavetek 184 – Sweep Generator – 5MHz Wavetek 3810 – 11-GHz Signal Generator Wiltron 65205 – Programmable Sig Source (0.001Hz – 13MHz) Wiltron 65205 – Programmable Sweep Generator (3.6 – 6.5GMz)	£250

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Higher Education in electronics

SPECIAL

Postgraduate Studies and Research

Students considering an advanced Degree in Electronics or Computing need toplevel qualifications, but have a wide choice of research departments to apply to.

n our last issue, this feature had a look at some of the courses for students of electronics engineering and computer science up to Degree level. It's well know that the number of students taking college courses has risen over the last couple of decades as formal qualifications have become important to employers. Some people blame this effect on lower academic standards (on one hand) and insufficient onthe-job training by industry (especially the lack of apprenticeships) on the other hand. A revival of apprenticeships, and the arrival of NVQs (National Vocational Qualifications) in recent years are beginning to address the latter criticism. But the trend to more higher education has seen a sharp rise in the number of postgraduate students people with degrees or equivalent higher qualifications opting to continue or return to their studies at an even higher level - in the last five years.

One reason for this has been the difficult employment situation in the UK over the economic recession period of the early 90s - which has by no means entirely passed away. Many already well-qualified and/or experienced people found themselves in a situation where their best option was to return to higher education for one, two or three years in the hope of making themselves more competitive in a difficult job market, or simply of using their time constructively. Many students emerging from first degree courses were in the same position, and so more of those with good results (and access to some financial resources) decided to continue their studies and research.

UK graduates can generally obtain a grant for one or two years of further accredited vocational training, but it is less easy to get financial help for Masters or Research studies. Some colleges have a limited number of scholarships or bursaries to help students in some subjects, usually those with strong industrial links, but in general postgraduates doing a Masters or Research degree will have to find most or all of their own finance. More higher degree students, for instance, take

regular paid work throughout their degrees, in the American style, than do undergraduate students, who are still often actively discouraged from working while studying, for obvious reasons. Once at Postgraduate level, not only is it acknowledged that the student is likely to be beyond resources often available to younger people (like financial help from parents), but the older student is expected to have sufficient experience of study be ready to balance research with some other obligations. Many working people are also studying, but on a part time basis. Full time research does not mix with heavy work obligations, so the postgraduate student must be ready to take our further student loans, or have other resources.

Anyone thinking of studying for a further degree is advised to look carefully at where their studies could apply to industry, and what industrial and commercial support there is for their field within the colleges they are applying to. Strong links with the "outside world" are valuable to everyone concerned, both during study and research, and when the student enters a commercial environment full-time.

Imperial College, London

Imperial College of Science, Technology and Medicine is one of the best-known specialist colleges in the United Kingdom. The reputation - and the staff - of the college is international, and Imperial has had strong links both with industry and government from its earliest days in the late 19th and early 20th century. Imperial is part of London University. Like Oxford and Cambridge, the individual colleges of the University are known simply by their "college" names, but unlike Oxford and Cambridge, where the colleges are mainly accommodation and tutoring establishments, sharing many



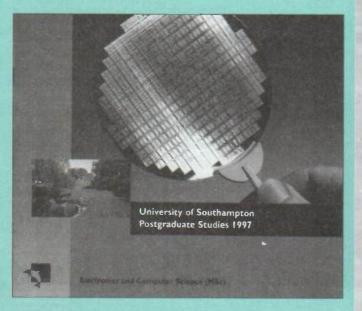
academic resources and departments, London colleges are complete establishments each with its own departments or faculties.

Imperial College's Department of Electrical and Electronic Engineering offers postgraduate education through both taught courses - leading to an MSc degree of the University of London and the Diploma of the Imperial College (DIC) - and research programmes, leading to the DIC, MPhil (Master of Philosophy) or PhD (Doctor of Philosophy). The MSc courses on Communications and Signal Processing, and Control Systems, are both currently accepted by the Engineering and Physical Sciences Research Council as suitable for tenure of its Advanced Course Studentships. The course on Physical Sciences and Engineering in Medicine is run with the Centre of Biological and Medical Systems, and is taught by their staff; the course in Semiconductor Science and Technology is run by the department jointly with the Departments of Materials and of Physics. There are two new MSc courses, in Analogue and Digital Integrated Circuit Design, and in Power Engineering: Control and Optimisation. These are all 12-month full-time courses requiring a first or good second class Honours degree In Electrical Englneering, or an equivalent to qualify for entry.

Research In the Department is conducted through a number of sections. In outline, the current sections include Analogue and digital circuit design; Biomedical systems engineering; Control and instrumentation; Digital communication; Energy and electromagnetics; Information engineering; Intelligent communications systems; Neural systems engineering; Optical and semiconductor devices, Signal processing; Solid state electronics and Thin films. Full details of the current programmes can be obtained from the Department, which produces a detailed Research Report. The normal qualification for research training is a First or Upper Second Class Honours Degree in Electrical Engineering.

The Department of Computing offers two full-time courses, both leading to an MSc degree of the University of London and/or the Diploma of the Imperial College. These are Computing Science (primarily a conversion course for graduates without computing) and Advanced Computing, training in IT research which also acts as an introduction to research degree study in the Department. Research in the Department is based in two centres, IC Parc, and Imperial College/Fujitsu Parallel Computing European Research Centre.

The research sections are Advanced Language and Architectures; Applied Systems and Decision Support;



Distributed Software Engineering; Logic and Automated Reasoning; Logic Programming; and Theory and Formal Method. The booklet Postgraduate Study In Computing describes the options in more detail and can be obtained from the Assistant Registrar (Admissions).

The University of Southampton

The Department of Electronics at the University of Southampton was established in 1947, the first in any UK university, and combined with the Department of Computer Studies in 1986 to form the Department of Electronics and Computer Science as part of a major expansion in Information Technology - at a time when IT was beginning to gain public notice - within the University. It is the largest department of its kind in the UK, with over 50 academic staff and teaching and research covering every aspect of electronics and computer science, including the fabrication of integrated circuits, parallel processing and computer systems, signal processing, program and algorithm design. Alone among universities, Southampton was chosen by the Engineering and Physical Sciences Research Council (EPSRC) to host a microelectronics fabrication facility, that also provides advice and fabrication facilities to industry and other academic institutions.

Southampton also hosts the Institute of Transducer Technology, a Transputer Support Centre and an Interdisciplinary Research Centre (IRC) concerned with optoelectronic technology. An unusual feature of the Department of Electronics and Computer Science is that it is a department within the Faculty of Engineering and Applied Science. They are separate from the Department of Electrical Engineering and the Faculty of Mathematical Studies, which in many universities are the location for studying electronics and computer science respectively. Here, electronics is taught as a discipline in its own right, and Computer Science is taught from the standpoint of software engineering with a strong emphasis on working software systems. There is a greater degree of specialisation available. The Department also has close links with industry, with a number of engineers from industry providing specialist teaching to support courses from a commercial viewpoint.

The Department runs advanced MSc courses in four areas: Microelectronics Systems Design, Radio Frequency Communication Systems, Instrumentation and Transducers, and Optical Fibre Communications. The basic courses take one year to cover, and may be studied full-time or part-time (over two years). The normal entry requirement is a Second Class Honours Degree in an appropriate subject (normally physics, maths, engineering (including electronic) or computer science, although the University will look at applicants with other applicable qualifications (for instance, the Engineering Council Part 2 examination) and/or experience. The department issues pre-course reading lists to applicants to make sure that they are up to speed if they are accepted for a course, and specifically warns against expecting to undertake part-time employment to support themselves while attending a full-time course. The college, like most institutions of higher education, will also advise on possible sources of grants and other funding, but prospective students must remember that grants and scholarships for higher studies are the exception, rather than the rule, for students who do not already have industrial sponsorship.

Research at Southampton is organised into research groups usually consisting of academic staff, fully time research staff (usually with industrial experience) and research students. As

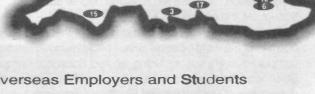
Interested Electronic Engineering **Leaving School? Unemployed and Job Seeking? Changing Career? Looking for Promotion?** Resident outside of the **United Kingdom?** If this is you, then one of the listed

colleges may be able to help

Several sources have indicated that there is a national shortage of highly skilled Electronics and Telecommunications Engineers in Britain so if Electronics is an interest or hobby, why not make it your career?

A wide variety of training programmes are available covering many branches of electronics such as:

- Computer & Office **Equipment Servicing**
- Electrical Engineering
- Electrical Installations
- Electronic Engineering
- Marine Radar, Navigation & **Electronics**
- Marine Radar & GMDSS GOC
- Mobile Radio & Radio Engineering
- Microprocessor Programming and Interfacing
- Optoelectronics
- **Telecommunications** Engineering
- Television & Video Servicing



19

Training Programmes are available for:

School Leavers, Job Seekers, Employed, Employers, Overseas Employers and Students

Member Electronics Colleges are all over Britain

- Aberdeen College, Aberdeen, AB25 1BN. BIII Thow. Tel: (01224) 612000 Fax: 612001
- Blackburn College, Blackburn, BB2 1LH. Peter Smith. Tel: (01254) 292348 Fax: 681755
- Bournemouth & Poole College of FE, Poole, BH14 OLS. John Gosting. Tel: (01202) 205654 Fax: 205313
- City College Norwich, Norwich, NR2 2LJ. David Warner. Tel: (01603) 773320 Fax: 773016
- City of Liverpool Community College, Liverpool L19 3QR. David Jones. Tel: (0151) 2524749 Fax: 4279179
- 6 Ealing Tertlary College, London, W3 8UX. Denis Thomson. Tel: (0181) 2316332 Fax: 9932725
- Glasgow College of Nautical Studies, Glasgow, G5 9XB. John Hercus. Tel: (0141) 5652660 Fax: 5652599
- Grimsby College, Grimsby, DN34 5BQ. Richard Summerfield. Tei: (01472) 315540 Fax: 879924
- Hull College, Hull, HU1 3DG. Steve Brett. Tel: (01482) 598806/329943 Fax: 598733

Post code

Jewel & Esk Valley College, Edinburgh, EH15 2PP. Derek Landells. Tel: (0131) 6577288 Fax: 6572276

- Lancaster & Morecambe College, Lancaster, LA1 2TY. Gary Wilkinson. Tel: (01524) 66215 Fax:843078
- London Electronics College, London, SW5 9SU. M.D. Spaiding Tel: (0171) 3738721 Fax: 2448733
- Matthew Bolton College, Birmingham, 85 7DB. Clive Hill. Tel: (0121) 4464545 Fax: 4463105
- Newbury College, Newbury, RG14 1PQ. Martin Rice. Tel: (01635) 37000 Fax: 41812
- Plymouth College of FE, Plymouth, PL1 5QB. Mr D J Turner. Tel: (01752) 385398 Fax: 385399
- South Tyneside College, South Shields, NE34 6ET. David Johnson. Tel: (0191) 4273500 Fax: 4273535
- Southampton Institute, Southampton, SO14 0YN. Roger Forster. Tel: (01703) 319333 Fax: 334441
- Stoke on Trent College, Burslem, ST6 1JJ. Ken Burgess. Tel: (01782) 208208 Fax: 603103
- Tresham Institute (Northants) of F & HE, Corby, NN17 1QA. John Dixon. Tel: (01536) 413307/402252 Fax: 402252

To find out exactly what the college of your choice can offer, please telephone directly or use the no obligation Enquiry Coupon below for a brochure.

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Please send details of your electronics courses to:

Send this coupon to any of the colleges listed for the latest details of courses and programmes available. School leavers under 18 years of age are recommended to contact the nearest college to their home address.

Tel No. and area code ...



Fax number and area code

Age (if under 18)

Preferred type of course?

they succinctly describe it: "The normal method of progress is for a supervisor in the first year to tell you what to do, in the second year you discuss the project together, while in the third eyar to tell your supervisor what he or she is doing!". Initially research students are registered for a Master of Philosophy degree. After 12 to 18 months and a successful thesis, this can be re-registered as a PhD (Doctor of Philosophy, the highest standard degree qualification level) which is normally expected to take three years to complete.

The current Research Groups in the department include Communications (digital cellular mobile systems), Design Automation (simulation, synthesis and testing techniques underlying CAD systems); Concurrent Computation (parallel processing) and Image Speech and Intelligent Systems (ISIS). These are only a sample of the research groups currently in progress.

De Montfort University, Leicester

De Montfort University offers MPhil (1-2 years full-time, 2-4 years part-time) and PhD (2-3 years full-time, 3-5 years part-time) qualifications, with the option to register for MPhil with the possibility of transfer to PhD after 18 months. Research at the University is conducted in partnership with, or on behalf of, large and small industrial and commercial concerns, as well as public sector organisations. The development of European and international links allows research to be carried on with organisations outside the UK through European initiatives such as BRITE/EURAM.

The University's recently opened Science and Engineering Research Centre (SERCentre) currently supports the work of several research units, and assists the carrying-on of multi-disciplinary and cross-disciplinary research among the teams. Separately, a Research Unit exists to carry out the administration for registered research degree students throughout the university.

There are a number of research schools that may have courses of interest to people with a background in electronics, computing or physics, including Applied Sciences, Computing Sciences, Engineering and Manufacture (which includes the department of Electronics and Electrical Engineering) and the Science and Engineering Research Centre (SERCentre). The School of Applied Sciences, for instance, has the Solid State Research Centre, dealing with cross-disciplinary Physical and Materials Science Research. Computing Sciences supports research programmes in computing, information systems, mathematical sciences and medical statistics. The Department of Electronics and Electrical Engineering, in collaboration with engineering giant Lucas, the Rutherford Appleton Laboratory, and Daresbury (part of the Engineering and Phusical Sciences Research Council) is one of the leading institutions in microengineering. A number of the research activities are interdisciplinary and the research groups have a commitment to applied and strategic research.

The Science and Engineering Research Centre includes research groups in Emerging Technologies (microelectronics), water software systems, computer imaging, communication networks and systems engineering. There are also a number of MSc programmes offered in the University.

What research?

Students moving on from first degrees to higher degrees will need to think carefully about whether they want to go more deeply (that you are going in deeply goes without saying) into the area they are interested in, or whether they are more Below: An electrodynamic shaker is shown being used to identify mechanical resonances in a disk drive. (Neville Miles LRPS)



interested in pursuing interdisciplinary aspects. Some colleges and research groups concentrate more on one than the other. It is normally safe to assume that all research groups have

contacts with industry and interests outside the academic sphere, but you may want to know more about which industries and bodies are involved with the area you are interested in.

Nearly all research activity is sponsored by industry or government bodies, except in the rarer cases where the college is its own sponsor. There is not a completely open choice of areas in which to research. Certain research programmes are taking place at any time, and it is for these that the institutions are considering research applicants. A positive aspect of this is that your research should per se have practical relevance to a career path.

Some colleges routinely consider applicants with first degrees only, while others prefer PhD applicants to have obtained an MSc first, but all will consider applicants with any high-level qualifications even if these do not exactly fit their normal entrance requirements. All colleges will refer applicants to bodies who may offer grants, but the number of grants is limited. Also, the most demanding full time courses do not in practice allow for financing by working, and it would be unwise to plan on doing so.

Resources

Some of the useful books that we mentioned last month are listed below, but students already at college will find more information in their college careers advice office. The Registration departments of the colleges in question often produced more detailed literature about higher degrees for prospective students and, (where available), subject booklets provided by specific departments

The Times Good University Guide edited by John O'Leary (Times Books)

The Big Official UCAS Guide to University and College Entrance (Letts Study Guides with The Independent)

Which Degree 1997 - Volume 2: Engineering, Technology and Geography and Volume 3: Science, Medicine and Mathematics CRAC Student Guide (Hobson's PublishIng)

To obtain prospectuses from the colleges above, contact The Assistant Registrar (Admissions), Imperial College of Science, Technology and Medicine, London SW7 2AZ. Tel 0171 589 511 Fax 0171 594 8004.

(Imperial College also publishes Postgraduate Study in Electrical and Electronic Engineering, Research Report '95-'96' and Postgraduate Study in Computing.)

The Prospectus Enquiries Office, University of Southampton, SO17 1BJ. Tel 01793 592379 Fax 01703 593037.

De Montfort University Leicester, The Gateway, Leicester LE1 9BH. Tel 0116 255 1551 FAx 0116 255 0307.



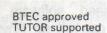
MSc/Diploma in Information Engineering

This postgraduate course offers a set of integrated courses with a common theme in information acquisition and processing, systems and control with specialisations in Electronic Digital Systems and Communications, Control Engineering, Measurement and Instrumentation, Biomedical Computing, Instrumentation and Informatics.

The course is taken 12 months full-time, or over 24 months part-time, principally on a day release basis. A major feature of the MSc is an extended (5 months) project period.

See http://www-eeie.city.ac.uk or telephone 0171 477 8135 for further details. Department of Electrical, Electronic and Information Engineering.

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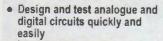
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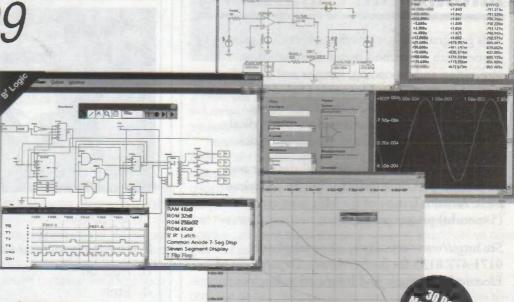
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Fully integrated and interactive

Build the circuit on the screen and set up the simulations by choosing options from menus and dialogues. Then run the simulation and

Flexible Visualisationof Results

In B2 Spice results can be displayed in graphs, tables or directly in voltmeters and ammeters. Change from typical to worst case analysis and include the effectsof temperature on components. You can customise everything, right down to the colour of an individual trace you see just what you need. B Spice and B2 Logic let you export data to other

Versatility

A plethora of components include resistors capacitors, inductors, mutual inductors / transformers, controlled sources, bipolar junction transistors, zener diodes, power MESFETs, JFETs, MOSFETs, voltage regulators, operational amplifiers, opto couplers, voltage comparators, quartz crystats. IBIS I/O buffers and switching matrix connectors and much more All devices and model parameters can be edited to suit your needs. Implement hierarchical circuits in your designs quickly and easily

With B2Spice and 8 Logic there is no limit on the number of components in the circuit.

There are literally thousands of them... The complete Berkeley SPICE model library as well as commercial libraries from manufacturers such as, Motorola, Texas Instruments, Burr-Brown, Maxim, National Semi, APEX Comlinear, AMP, Elantec, Linear Tech, and many more. Included with BSpice is a full model and symbol editing package so you can create, import and edit custom models.

B² Spice supports AC frequency sweep. DC operating point, transient analysis, fast fourier Noise, sensitivity distortion, Tf small signal

Simulation Options

Added facility for sub-circuits (macro-models). You can set all simulation options.

Allows you to set initial conditions at all nodes. Allows you to set initial guess at nodes for simulation

Allows "not given" state for all values.

Total Control

B2 Spice gives full access to Berkeley SPICE simulation control options. For example you can set global defaults for transistor channel lengths and widths! Plus much more

Waveform Analysis

Display and compare multiple response curves in a single graph at the same time. BSpice simulation results can be selectively displayed and analysed graphically and in numerical format as well as exported to other applications. All of B'Spice and B' Logic's display capabilities are completely flexible

Devices & Stimulus for Simulation

In B² Spice sinusoidal, constant, periodic pulse, exponential, single frequencyFM, AM, DC voltage, AC voltage, VCO, Vcc, piecewise linear exponential, polynomial /arbitrary source, voltage-controlled voltage, voltage controlled current, current-controlled voltage, current-controlled current, Lossy and ideal transmission line, MESFET uniform RC, current and voltage switches are all available

Cross Probing

Cross probing allows you to display waveform results simply by marking pins, wires and devices on the circuit drawing. Monitor results while the simulation is in progress then plot analogue results on linear or log scales.

in B² Spice analogue traces may be displayed as raw voltages and current values or further processed using arithmetic expressions functions and Fast Fourier Transforms. High quality graphs let you see just what you need to, clearly and easily You can also display multiple simulations in one graph. Multiple graphs can then be aligned and compared

Data Analysis

Position detection with mouse for data points. Import and export data to and from other industry standard SPICE programs. 8 Spice supports Polar, Smith and Nyquist charts.

Digital Options.

B² Logic is completely flexible. Set up ROM, RAM and PLA to your own requirements. Shrink a whole circuit to a block and use it as a component in a new design. Run the simulations in real time or step by step.

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Digitally Controlled Power Supply

Robert Penfold's power supply with PIC-controlled stability will not suddenly provide a high voltage on switch-on if you forget to readjust it

hen using a variable voltage bench power supply unit you need to take due care to avoid the classic mistake of connecting the supply to your latest circuit, switching on, and then discovering that your 5 volt logic circuit is being supplied with about 20 volts! Some up-market power supply units avoid this possibility by having the supply always start at its minimum output voltage at switch-on. In most cases this means that the supply always starts with zero output voltage, and therefore fails to supply any output signal at all until the user has set the required output potential.

Although this feature is normally only found on expensive digital bench power supply units, it can be included on relatively low cost power supplies. In fact it does not require the use of digital electronics and can be provided using an analogue circuit. However, it is difficult to obtain really good stability using an analogue design which has to rely on a capacitor "remembering" the set output voltage. Even using the highest quality components together with the usual precautions such as earth rings does not guarantee good results. Over a period of time the output voltage will change slightly, and over a period of hours if it is likely to change quite radically.

For a reasonably simple power supply that will start from zero and provide good stability, it is necessary to use some basic digital electronics. This power supply unit has a simple digital control circuit that is basically just a PIC microcontroller that acts if as a

form of up/down binary counter. At switch-on the counter it is set at zero which in turn sets the output potential at zero. The output voltage is controlled via four pushbutton switches, and two of these enable the output potential to be increased. The difference between the two is that one changes the voltage quite rapidly while the other gives a much slower change in the output voltage. Using the "fast" button enables the user to rapidly set an approximation of the required output potential, and the "slow" button is then used to set precisely the required voltage. The other two pushbutton switches provide the same basic function but enable the output voltage to be reduced.

The output voltage range of the supply is zero to 20 volts with a maximum output current of 1.4 amps. The output voltage can actually be set as high as 25 volts, but at output potentials of more than about 20 volts the maximum output current is less than 1.4 amps. The maximum output current is typically only about 250 milliamps with the output at 25 volts. The circuit is protected against overloads by output current limiting that prevents the output current from exceeding more than about 1.8 amps even with a short circuit on the output. When testing low current circuits the current limiting can be

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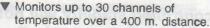
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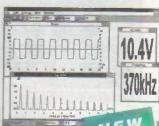
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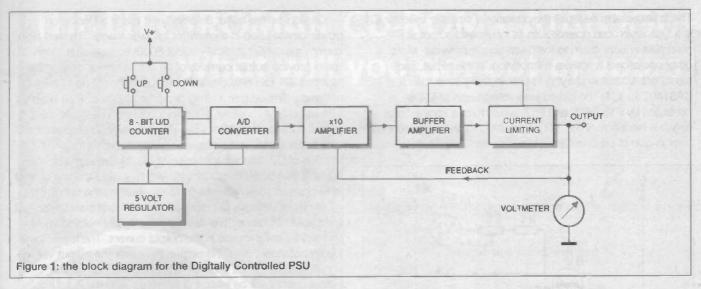
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System Operation

The hardware is greatly simplified by the use of a PIC processor to provide the basic up/down counter function. The block diagram of figure 1 shows the basic arrangement used in this power supply unit. A digital to analogue converter is fed with the eight-bit output from the PIC processor and it provides an output voltage that is equal to 10 millivolts (0.01 volts) per Isb. The maximum count from an eight-bit counter is 255 (decimal), but in this case we require the count to go no higher than 250 so the maximum output voltage is 2.5 volts. After amplification by a factor of 10, this provides an output voltage range of 0 to 25 volts with a resolution of 0.1 volts. The PIC software is used to restrict the count to no more than 250. The counter and converter stages are power from the main 30 volt supply via a 5 volt regulator, but the rest of the circuit is powered from the unregulated 30 volt supply.

A buffer amplifier at the output of the x10 amplifier enables the circuit to handle high output currents, but a standard current limiting circuit pulls the output voltage lower if an excessive output current is detected. This limits the output current to a safe level even with a short-circuit across the output terminals. The negative feedback loop to the x10 amplifier is taken from the output of the circuit so that the feedback compensates for the voltage drops through the buffer amplifier and current limiting circuit. An analogue voltmeter is used to monitor the output potential, and enables the output voltage to be set with good accuracy.

Circuit Operation

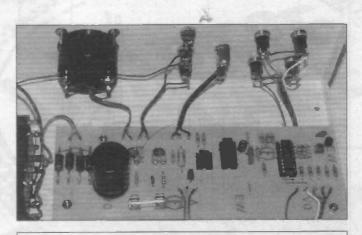
Figure 2 shows the main circuit diagram for the Digitally Controlled PSU, but the circuit for the unregulated supply generator is shown separately in figure 3. Taking figure 2 first, IC1 is the PIC processor, and this is a 16C84-04. This is the 4MHz version of the chip, and in this circuit it is used with a C - R clock generator that has R1 and C1 as the discrete timing components. These set the clock frequency at about 14 to 15kHz, but the exact clock frequency is not critical in this application. The C - R clock mode is therefore perfectly adequate.

Port A of IC1 is set as inputs and used to read the four pushbutton switches (S1 to S4). These inputs are normally taken low by pull-down resistors R2 to R5, but operating on of the pushbutton switches takes its respective input high. Port B is an eight-bit type, and it is used to drive the digital to analogue converter. There is a minor problem in that the Port B lines default to the input mode at switch-on, and there is a short delay before the program sets them as outputs. During this time they tend to drift high, producing a brief output signal at 25.5 volts. This could obviously prove fatal for any low voltage circuit connected to the

output of the unit at switch-on. Resistors R6 to R13 act as pull-down resistors on IC1's outputs, and these ensure that the outputs remain low while the program goes through its setting up routine.

The analogue to digital converter is a Ferranti ZN426E (IC2), and this has a built-in precision voltage generator. IC2 requires discrete load resistor R14 and decoupling capacitor C2, but is otherwise self-contained. IC3 is a low power monolithic voltage regulator which provides a stabilised 5 volt supply to the counter and converter circuits. Series resistor R16 is used at the Input to IC3 to reduce its input voltage. This helps to keep the dissipation in IC3 down to an acceptable level, and ensures that it is not fed with an excessive input voltage.

IC4 acts as the x10 amplifier, and this is almost a standard non-Inverting mode amplifier circuit. It only differs from the standard configuration in that no negative supply rail is used.



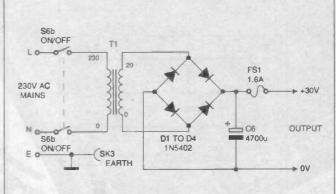


Figure 3: the circuit diagram for the rectifier and smoothing stages of the unit

This is acceptable because the operational amplifier used for IC4 is a type which can operate with its inputs and output at potentials virtually down to the 0 volt supply potential. Most other operational amplifiers will not work in this circuit, and I would not recommend trying to use anything other than a CA3140E for IC4. The closed loop voltage gain of IC4 is controlled by R15, VR1, and R17, which form a conventional negative feedback circuit. VR1 is adjusted to give a closed loop voltage gain of precisely ten.

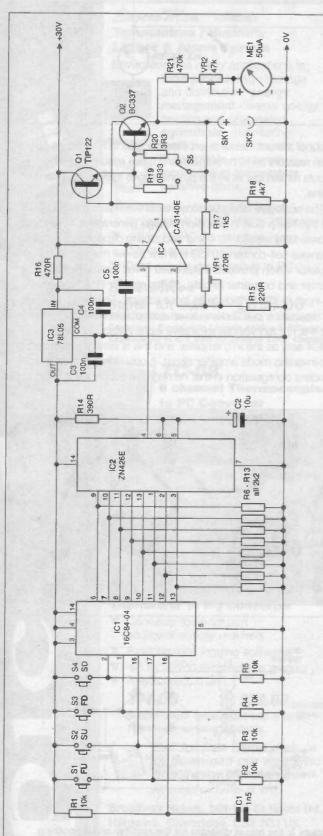


Figure 2: the main circuit diagram for the Digitally Controlled PSU

Q1 acts as the buffer amplifier, and this is a Darlington power device used in the emitter follower mode. The very high current gain of Q1 (typically about 5000) ensures that it can easily provide output currents of an amp or more despite the fact that IC4 can only provide output currents of up to a few milliamps. The current limiting circuit uses R19 or R20 in series with the output to sense the output current. The larger the output current, the higher the voltage developed across the selected resistor. This voltage is fed to the base-emitter junction of Q2, and will forward bias Q2. Voltages of less than about 0.6 volts are insufficient to switch on Q2, and the current limiting circuit then has no significant affect on the circuit.

At higher voltages Q2 starts to conduct and pulls the output voltage of IC4 lower. This also pulls the output voltage lower, and resists any increase in the output current. The lower the load resistance across the output, the lower the output voltage is pulled. Even with a short circuit across the output, the output current will be kept at a safe level, and the output potential will be reduced to virtually zero. With R19 switched into circuit the output current is limited to around 1.8 to 1.9 amps. This is low enough to ensure that the circuit is not damaged in the short term, but it is high enough to ensure that the maximum output current of 1.4 amps can be delivered without the current limiting starting to operate. The higher value of R20 gives a lower limit current of around 180 to 190 milliamps.

R18 is the output load resistor, and it ensures that the output current is always high enough to keep the output circuitry functioning properly. ME1 is used in a simple analogue voltmeter circuit that has a full scale voltage of 25 volts. VR2 is set to give the voltmeter the correct sensitivity.

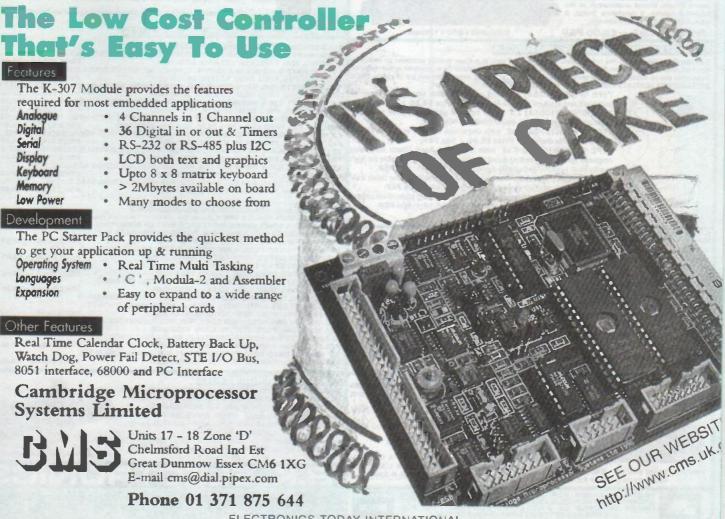
The basic 30 volt supply is provided by the circuit of figure 3, and this is a conventional full-wave circuit having bridge rectification provided by D1 to D4. C6 is the smoothing circuit, and fuse FS1 protects this circuit if a fault should occur in the control and regulator circuitry. A separate earth socket (SK3) is provided so that either output rail can be earthed, or the unit can be used as a "floating" supply with neither output rail earthed. However, for safety reasons the chassis of the unit must be permanently (and reliably) earthed to the mains earth lead.

Software

The PIC software first sefs up port B as eight outputs, and leaves port A as four inputs. A value of 0 is written to port B initially so that all eight outputs are set low, and zero volts is produced at the output of the unit. The program then goes into a loop which reads port A and tests each bit in turn. The programs loops until it detects that an input line has gone high (one of the pushbutton switches has been operated), and it then goes to the appropriate one of four subroutines.

Two of these subroutines increment the value stored in the COUNT register, and then output the new value to port B. However, a check is made first to determine whether the count has reached 0xFA (250 decimal). The subroutine is aborted if this value has been reached. Both subroutines finish with a delay loop, but one provides a much longer delay than the other. This gives the fast and slow increments of the output voltage, but note that both routines provide full resolution and differ only in their rate of change. Basically similar routines are used to decrement the output voltage, but these check to see if the count has reached zero, and abort the subroutines if it has. Again these two routines only differ in the length of the delay provided at the end of the routines.





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Construction

Details of the printed circuit board are provided in figure 4. Although this project is not particularly difficult to construct, there are a number of points that are worthy of note. One of these is simply that this is a mains powered project, and as such it is not suitable for beginners. The mains supply is potentially lethal, and only those with the appropriate experience should undertake projects that connect to the mains.

The 16C84-04 (IC1) and CA3140E (IC4) are both MOS devices, and as such they require the normal anti-static handling precautions. Use holders for both devices, but do not plug them into circuit until the unit is in other respects finished. They should be supplied in some form of protective packing, and they should be left in this until it is time for them to be fitted in the holders. Try to touch their pins as little as possible when fitting these components into the holders, and keep them well away from any known sources of static electricity. The ZN426E used for IC2 is not a MOS input device, but it is not particularly cheap either, so it is also advisable to use a holder for this component as well.

Smoothing capacitor C6 will only fit onto the printed circuit board if it is a modern type such as the Maplin "HC Series Snap-In Radial Electrolytic" used on the prototype. It is possible to use a large "can" type electrolytic, but it would have to be fixed on the case using a suitable mounting clip, and then hard wired to the circuit board. Make absolutely certain that rectifiers D1 to D4 are fitted with the correct polarity, as mistakes here could cause costly damage. They could even be dangerous.

As ever with mains projects, take particular care and seek assistance from an experienced constructor if you yourself do not have mains construction experience.

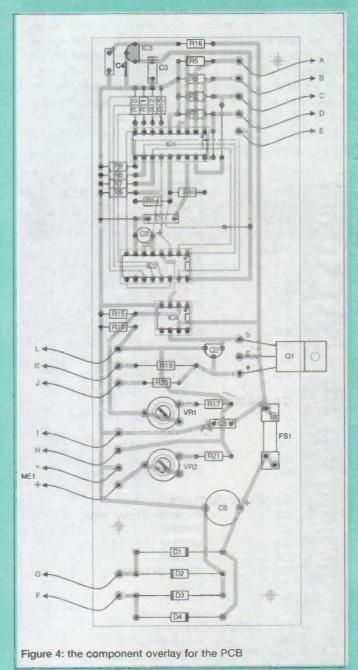
Fuse FS1 is mounted on the printed circuit board by way of two 20 millimetre fuse clips (Maplin "Type 1" or similar).

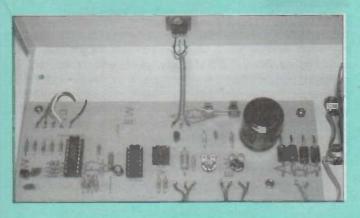
Alternatively, FS1 can be fitted in a panel mounting fuse-holder fitted on the rear panel of the case and hard wired to the printed circuit board. Do not overlook any of the link-wires. There are five of them, including one immediately above IC1, and one immediately below IC2.

Power transistor Q1 has to dissipate quite large amounts of power when the supply is used with low output voltages and high output currents. This results in the generation of a fair amount of heat, and Q1 will quickly be destroyed unless it is mounted on a substantial heatsink. The cheapest option is to mount Q1 on the metal case which then acts as a large heatsink. If the unit is likely to be used for long periods to supply low voltages and high currents, it would be advisable to fit a large heatsink having a rating of about 1.8 degrees per watt or less to the rear panel of the case. Q1 is then mounted on this, and hard wired to the circuit board via leads which pass through holes drilled in the heatsink and the rear panel of the case.

Whether Q1 is mounted on a heatsink or direct onto the case, it must be insulated from the heatsink and (or) case using a

TO220 Insulating kit. This consists of a thin mica or plastic washer which fits between Q1 and the heatsink or case, and a plastic bush which fits over the M3 or 6BA mounting bolt. The washer insulates Q1 from the case/heatsink, and the bush insulates Q1 from the mounting bolt (which is not insulated from the case). The side view of figure 5 shows how everything fits together. Make sure that the hole for the mounting bolt is properly deburred, because the thin washer is easily pierced. Once Q1 is in place,





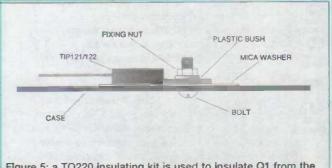


Figure 5: a TO220 insulating kit is used to insulate Q1 from the case/heatsink

use a continuity checker to ensure that it is properly insulated from the case. A break in the insulation could result in a short-circuit on the unregulated 30 volt supply, but FS1 should "blow" and prevent any major damage if this should occur.

As this project is mains powered it must be housed in a metal case, and it is essential that the case is earthed to the mains earth lead. A soldertag fitted on one of T1's mounting bolts provides a strong and reliable chassis connection point. A metal instrument case about 250 millimetres wide and at least 75 millimetres high should comfortably accommodate everything. T1 is mounted on the base panel of the case, as far to the right as possible, so that there is sufficient space for the printed circuit board to its left. The printed circuit board can be mount using plastic stand-offs, or it can be bofted to the case. If it is bolted in place, use spacers at least 6 millimetres long to hold the underside of the board well clear of the metal case. A hole for the mains lead is drilled in the rear panel of the case near to T1, and this hole must be fitted with a grommet to protect the cable.

S LIST for the Digitally Controlled PSU

Resistors (All 0.6W 1 percent metal film unless noted) R1-R5 10k /5 off)

R6-R13	2k2 (8 off)
R14	390R
R15	220R
R16	470R
R17	1k5
R18	4k7
R19	0R33 2.5W (see
Pon	202

R20 3R3 R21 470k

VR1 470R min hor preset VR2 470k min hor preset

Capacitors

C1	1n5 polyester, 7.5mm lead
	spacing
C2	10u 25V radial elect
C3,4,5	100n ceramic
C6	4700u 35V radial elect

Semiconductors

IC1	PIC16C84-04 (see text)
IC2	ZN426E
IC3	uA78L05
IC4	CA3140E
TR1	TIP121 or TIP122
TR2	BC337
D1 - D4	1N5402

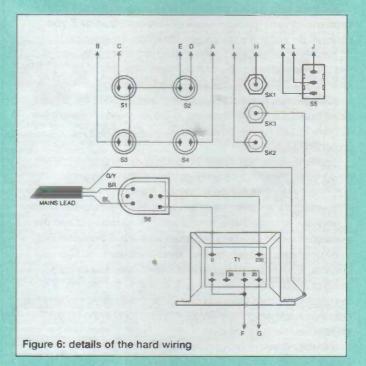
Miscellaneous

SK1	Red 4mm socket
SK2	Black 4mm socket
SK3	Green 4mm socket
S1 - S4	Non-locking push-to-make
	switches
S5	SPDT min toggle switch
S6	Rotary mains switch
T1	Standard mains primary, 20V
	2.25A secondary
ME1	50uA moving coil panel meter
FS1	1.6A 20mm "quickblow" fuse

Metal instrument case about 250 x 150 x 75mm, printed circuit board, 18-pin dil holder, 14-pin dil holder, 8-pin dil holder, pair of 20mm fuse-clips, mains lead and plug, TO220 insulating kit, wire, solder, etc.

The front panel layout is not critical, but try to use one that is reasonably neat and practical. The only minor difficulty when fitting the components on the front panel is that the meter requires a large circular cut-out. For a normal 60 x 45 millimetre component a cut-out some 38 millimetres in diameter is required. This can be cut using an Abrafile, fretsaw, coping saw, miniature round file, etc., but it is advisable to cut just inside the perimeter of the required cut-out. The hole can then be enlarged to precisely the required size using a half round file. Alternatively, most DIY superstores now stock a device which is simply called a "hole cutter" (but is also known as a "tank cutter"). This is used in a brace, and can be set to produce a wide range of hole sizes. This tool produces reasonably neat and almost instant results.

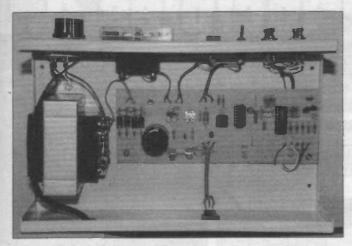
Details of the hard wiring are provided in figure 6, which is used in conjunction with figure 4. The mains transformer (T1) should have a 20 volt secondary voltage and a current rating of at least 2.25 volts (a rating of at least 50VA). The modern trend is for mains transformers to have twin secondary windings which can be wired in series or in parallel. In this case either twin 10 volt secondaries wired in series, or two 20 volt secondary windings connected in parallel are required. In practice the only option is likely to be two 20 volt secondary windings connected in parallel, and this is the method of connection shown in figure 6. Note that parallel connection of secondary windings should only be used with transformers that are designed to be used in this manner. With other transformers it could result in one winding driving a large current through the other winding.



Adjustment

With a mains powered project such as this it is essential to thoroughly check all the wiring before connecting the unit to the mains and switching on. Once you are sure everything is as it should be, set VR1 and VR2 at roughly middle settings and then switch the unit on. Initially the meter should indicate zero volts, but by operating the four pushbutton switches it should be possible to vary the output voltage. If there is any sign of a malfunction, switch off at once, disconnect the unit from the mains, and recheck the wiring, etc.

In order to give VR1 the correct setting, use a multimeter to measure the output voltage, and use the pushbutton switches to set the unit for maximum output potential. Then adjust VR1 for an



output voltage of 25 volts. Next VR2 is adjusted to give a reading of 25 volts on ME1, and the unit is then ready for use. The 0 to 50 scale on ME1 is easily converted into a voltage reading, but you might prefer to recalibrate the meter's scale. With most modern panel meters the front simply unclips, and removing two small screws then frees the scale plate. The existing numbers can then be carefully scraped off or painted over so that new figures can be added using rub-on transfers. Alternatively, the new figures can added immediately above the existing ones.

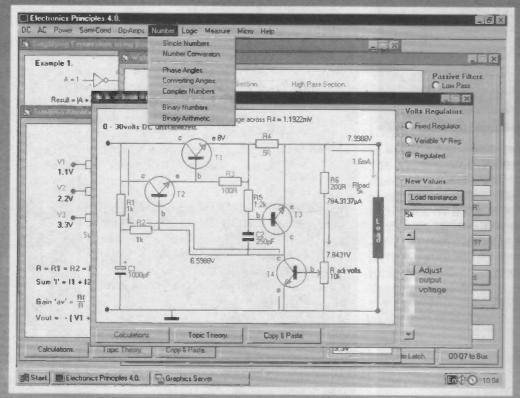
```
:Digitally Controlled PSU Program
STATUS
            EQU
                   03
            EQU
                   02
            EQU
                   06
BDIR
PORTA
            EQU
                   05
PORTB
            EQU
                   06
STORE
            EQU
                   00
            EQU
COUNT
                   OD
DELAY
            EQU
                 OF
     BSF
            STATUS,5;Select page 1
     CLRW
     MOWF BDIR
                     ;Sets Port B as outputs
            STATUS,5; Select page 0
     BCF
            PORTB ;Sets Port B to zero
     CLRF
     CLRF
            COUNT ;Sets count at zero
          PORTA,0 ;Read Port A
LOOPMOVF
     MOWNE STORE
                     :Store result
     BTFSC STORE,0 :Reads S1
     CALL
            FASTUP
     BTFSC STORE,1 ;Reads S2
     CALL SLOWUP
     BTFSC STORE,2 ;Reads S3
            FASTDOWN
     BTFSC STORE,3 :Reads $4
     CALL
            SLDOWN
            LOOP
    GOTO
            MOVF COUNT,0
FASTUP
     BCF
            STATUS,Z
     XORLW OxFA
                     ;Limits count to 250
     BTFSC STATUS,Z
     RETURN
     INCF
            COUNT,1
     MOVF COUNT,0
     MOWF PORTB
     MOVLW 50
     MOWNF DELAY
```

```
DELOOP
           NOP
    DECFSZ DELAY,1
    GOTO DELOOP
    RETURN
           MOVF COUNT,0
SLOWUP
            STATUS.Z
    BCF
    XORLW OxFA
                 :Limits count to 250
    BTFSC STATUS,Z
    RETURN
    INCF
           COUNT,1
    MOVF COUNT,0
    MOWF PORTB
    MOVLW OXFF
    MOVWF DELAY
DELOOP2
            NOP
    NOP
    NOP
    NOP
    DECFSZ DELAY,1
    GOTO DELOOP2
    RETURN
FASTDOWN BCF
                   STATUS,Z
    MOVE COUNT.0
    BTFSC STATUS, Z; Aborts if count at zero
    RETURN
     DECF
          COUNT,1
     MOVF COUNT,0
     MOVWF PORTB
    MOVLW 50
    MOVWF DELAY
DELOOP3 NOP
    DECFSZ DELAY,1
    GOTO DELOOP3
    RETURN
SLDOWN
           BCF
                   STATUS,Z
    MOVF COUNT,0
     BTFSC STATUS, Z; Aborts if count at zero
     RETURN
    DECF
            COUNT, 1
     MOVF COUNT.0
     MOWF PORTB
     MOVLW OXFF
     MOVWF DELAY
DELOOP4
            NOP
     NOP
     NOP
     NOP
     DECFSZ DELAY,1
     GOTO DELOOP4
     RETURN
END
:10000000831600018600831286018D0105088C008D
:100010000C1811208C181F200C1930208C193D2031
·1000200006280D080311FA3A031908008D0A0D0875
:10003000860050308F0000008F0B1B2808000D0831
:100040000311FA3A031908008D0A0D088600FF30E3
:100050008F0000000000000000008F0B292808001E
:1000600003110D08031908008D030D088600503098
:100070008F0000008F0B3928080003110D080319A9
:1000800008008D030D088600FF308F00000000007F
:0A009000000000008F0B4628080056
:00000001FF
```

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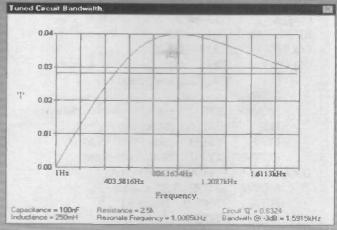
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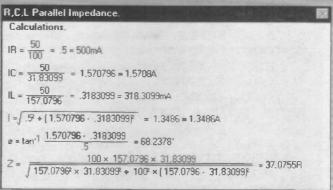
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64K Eprom EMULATOR

Following out popular ETI Eprommer in Issue 7, Keith Wardill has produced a matching eprom emulator

t is useful when developing software for a new design to be able to modify and then test the software quickly. One way to do this is with an Eprom Emulator. This is a block of random access memory (ram) which can be loaded with program data from a host computer and connected to the target circuit under development, where it acts as read only memory (rom). This allows software to be written and compiled on a host computer, downloaded to the emulator and tested in the development circuit before finally burning the software into the eprom.

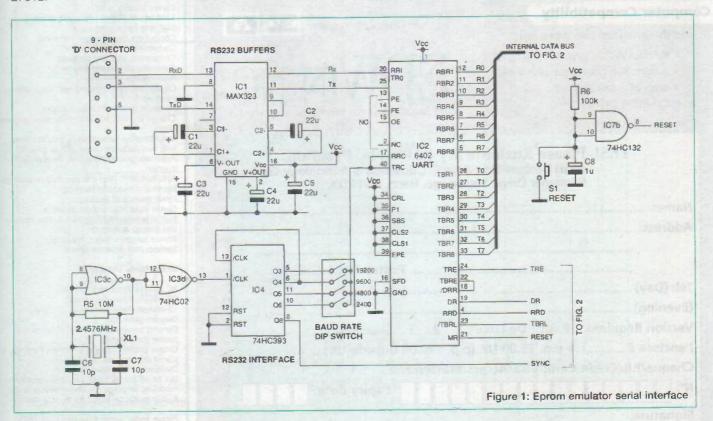
This Eprom Emulator connects to a host computer serial port from which the program data under development can be downloaded into the emulator. The emulator has a second interface to the target system, so that the development system can only read the emulator memory, and therefore 'sees' the emulator as an eprom. The emulator memory is 64bytes in size, so will emulate standard 27XX series eproms up to type 27512.

There is not room to reproduce all the relevant software here, but a disk is available (see end of article), and some constructors will be able to work on their own adaptations.

Operating protocol

1The emulator is designed so that when it receives a data byte from the host computer, it will answer with a data byte. Thus, in LOAD mode, the program data is sent to the emulator and written into the emulator memory. Then the emulator will 'echo' the same byte to the host computer, as a prompt to send the next byte. In READ mode, it is possible to read the contents of the emulator memory back to the host computer. To do this, a dummy byte is sent to the emulator to prompt it to read a byte of data from memory and transmit it to the host computer.

After a byte has been sent to the emulator, the Emulator Address Counter is incremented. If the RESET switch is pressed to zero the Address Counter before starting, then, by repeatedly sending data, the Emulator memory can be stepped



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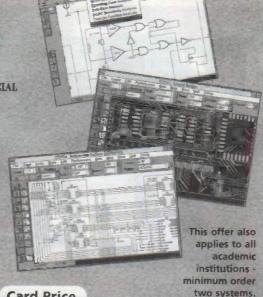
This is the first truly seamlessly integrated suite of software running in all Windows formats .. simulation, schematics and PCB design. At last allowing amateurs, teachers, students, ... in a work "individual" to take advantage of current technology, without any restriction.

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Computer Compatibility

To run the program you will need:

- Windows 3.x, Win95 or Win NT,
- a min. 386 processor (486+ rec.)
- 8mb of RAM
- CD-ROM Drive



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Maximum number of components: 32,000

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Automatic DRC with user specified parameters. Electrical connectivity checking. Linear rotation of symbols.

Gerber input read and use possibility. Built-in interface for Spectra 6.0, Max route 6.0 and Arizona Autorouter

Bitmap functions (logos, drawings, ...). Sophisticated database viewer

Mixed Mode Simulation

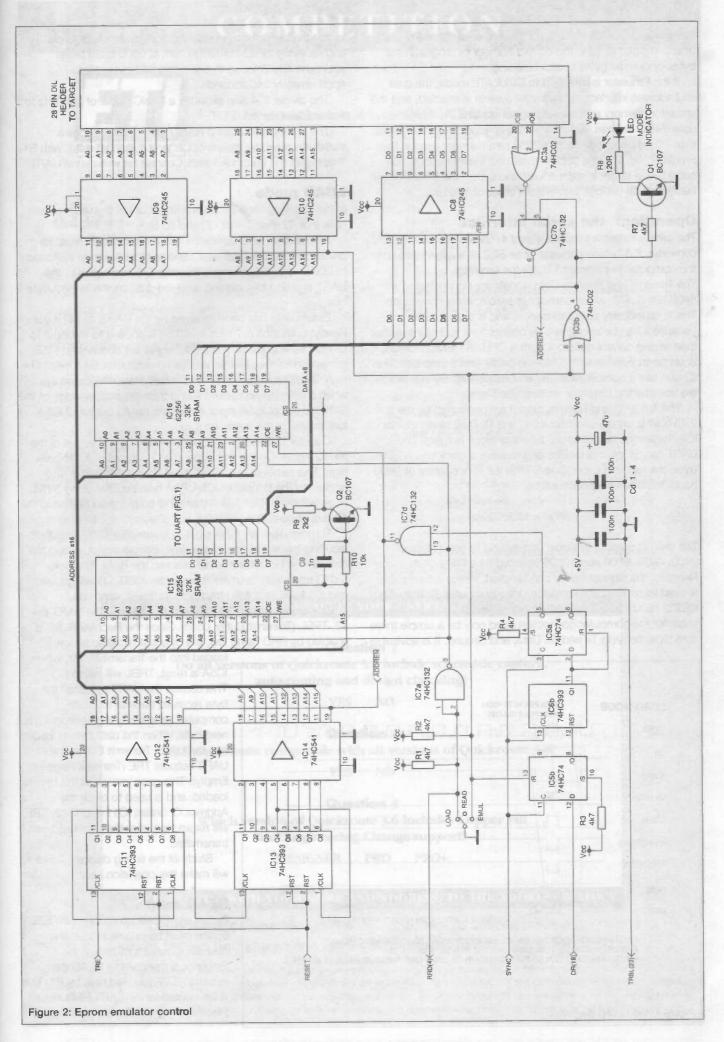
AC analysis (Frequency domain). DC analysis (Linear/non-linear). TD analysis (Time domain), Diagram generator Dynamic parameter definition of active and passive components.

Output graphs displayed on screen, hardcopy or

placed on schematic. Oscilloscope function

DLL based analog/digital simulation primitives, modelling language and library creation tools. Built-in model generator for discrete devices.

Please Note: Some of the above are ONLY provided on the De Luxe 3 Version. EdSpice and Thermal Analysis are available as bolt-on extras



through and programmed or read. It is the responsibility of the host computer to send the correct number of programming bytes or dummy bytes to allow reading.

If the Emulator is switched to EMULATE mode, the data and address interface to the target system is enabled, and the emulator memory acts as the target's eprom. The interface uses 74HC devices, which have rise times of the order of less than 20 nanoseconds. If the 62256 ram memory used in the Emulator has a 100ns access time, and the interconnection cable to the target system is kept short, then the Emulator can easily replace devices with access times of 120 ns.

Operation: the serial interface

The serial computer interface (figure 1) uses a MAX232 RS232 converter, IC1, which converts serial RS232 levels to and from the computer to standard TTL for the emulator.

The Transmit and Receive Data signals are connected to a 6402 UART, IC2, which carries out serial/parallel conversion. This is an industry standard device, and is easy to use because it can be configured by connections to its pins, rather than writing control data to it from a CPU. In this application, it is set up to operate with 8 bits, no parity, and 2 stop bits. This IC also has a number of status and handshake signals, which are used by the Emulator, as described later.

The Transmit and Receive clocks are generated by the 2.4576MHz crystal oscillator IC3C and D. Dual binary divider IC4 divides the output to produce four clock outputs. The UART has an internal divider, and requires a clock which is 16 times the required baud speed. Thus if it is to operate at 9600 baud, the clock frequency will be:

$16 \times 9600 = 153600 \text{ Hz}.$

The divided outputs available correspond to baud rates of 2400, 4800, 9600 and 19200 selected by a DIP switch. Normally, the highest speed can be used, unless a long cable is used to connect the emulator to the computer. Note that no RS232 handshakes, software or hardware are used, so the cable for the computer connection need only be a simple three core type, carrying Tx and Rx Data, and Ground. It is advisable

to use the highest speed practical, because sending one byte and receiving its acknowledgement at 9600 baud takes approximately 2ms, so to download 64 Kbytes takes approximately 140 seconds.

The divider IC4 also provides a SYNC signal of 9600 Hz to the emulator control.

The reset circuit around NAND gate IC7B provides a switch-on reset, by means of C8, and a manual reset, with S1. This resets the eprom Address Counter, as well as the UART.

LOAD mode

In the following sections on emulator control (figure 2), please note that /Q means 'not Q', and so on.

Before any LOAD operation, the RESET button must be pressed to set the Address Counters to zero. When switched to LOAD, the RRD (Receive Register Disable) signal to the UART is pulled low, placing received data on the internal data bus.

Before data has been received by the UART, its DR (Data Ready) output on pin 19 is low. This is applied to the input to D-Type flip-flop IC5B. The SYNC signal will ensure that IC5B remains in its 'reset' state with its /Q output on pin 8 high. The high Q output keeps IC6B in its 'reset' state. DR goes high when a data byte is received, and the next positive edge of the SYNC signal at IC5B clock input sets the /Q output of IC5A low, releasing the 'reset' on IC6B.

Counter IC6B can now count negative going edges of the SYNC signal. On the first edge, the Q1 output of IC6B goes high. This removes the 'reset' input from IC5A, and is also applied to the D input of IC5A. The next positive going SYNC edge will clock IC5A, such that its Q output goes high, and the /Q output goes low.

The high level on IC5A pin 5 is inverted by IC7D, and the resulting low signal provides a Write Enable signal, writing the data on the Emulator internal bus into the RAM. The next SYNC edge will increment the counter IC6B, Q1 will go low, and IC5A will be forced back into its 'reset' condition.

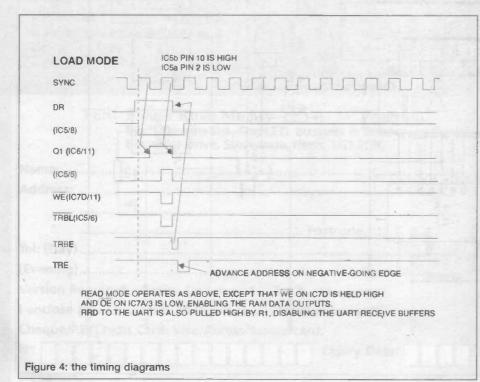
The low /Q output from IC5A is fed back to the UART pin 23, TRBL (Transmit Buffer Load). Since the Receive Buffer is enabled by RRD being low, the byte just received is now

loaded into the Transmit Buffer. When IC5A is reset, TRBL will return high. This causes the UART to 'echo' the byte received back to the host computer, which may then transmit the next byte. When the data byte is loaded into the UART Transmit Buffer, the UART outputs TRE (Transmit Register Empty). This goes low when the byte is loaded, and is used to clock the Address Counters IC11 and IC12. TRE will return high after the byte has been transmitted.

Study of the timing diagram figure 4, will make this operation clear.

READ mode.

Before any READ operation, the RESET button must be pressed to set the Address Counters to zero. If the Emulator is switched to READ the inputs to IC7A are held high by R1. This is connected to the UART RRD input (Receive Register Disable), forcing the



COMPETITION



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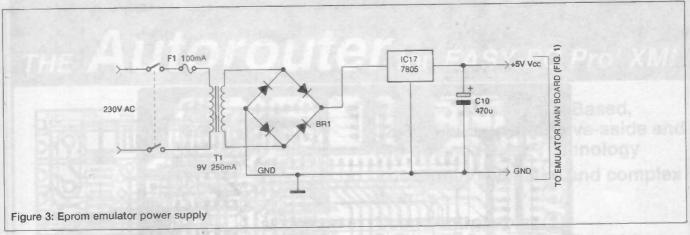
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Receive Register outputs into a high impedance state and disconnecting them from the Emulator internal data bus. The low output from IC7A is applied to the rams as Output Enable, so the ram data is placed on the internal data bus. This low output is also applied to IC7D pin 13. This forces the output of IC7D high, preventing any Write Enable signal reaching the rams.

If now a data byte is received by the UART, DR again goes high, and the same sequence takes place as for LOAD, except no data is written to the rams. However, the data echoed by the UART is the ram data, rather than the byte just received. The Address Counters are incremented by TRE as described above, so by sending 'dummy' bytes, the host computer can read the entire ram memory.

EMULATE Mode

If the emulator is switched to EMULATE mode, the output of IC3B goes high. This forces the 74HC541 Address Buffers IC12 and IC14 into high impedance state, disconnecting the internal Address Counters from the ram. The Target Address Buffers IC9 and IC10 are enabled, so now the rams are addressed from the Target circuit. Similarly, Target Data Buffer IC8 is enabled, allowing the ram data to be read by the Target when the appropriate Chip Select and Output Enable Signals are received by IC3A. The emulator is connected to the Target Circuit by means of a flat cable about 30cm long fitted with a 28 pin DIL Header. This DIL Header plugs into the socket on the target Circuit which will eventually accommodate the eprom. A led is driven by the buffer enable signal via TR1, coming on when the emulator is in EMULATE mode.

Power Supply

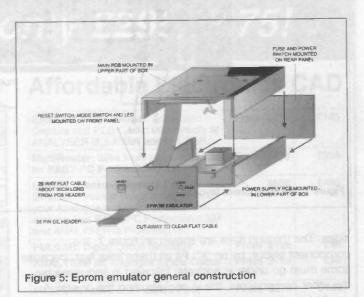
The power supply (figure 3) uses a straightforward bridge rectifier with capacitor smoothing to produce a stabilised +5V supply via IC12 for the logic. Please note that this is a mains device. Seek assistance if you have little mains experience.

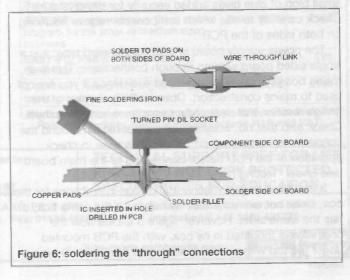
Mechanical construction

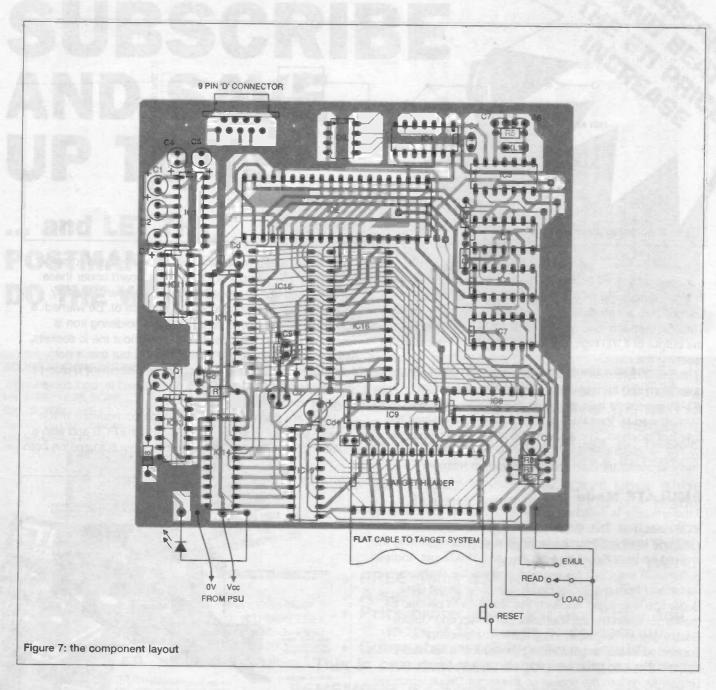
General construction is shown in figure 5. The PCB layout shown is double-sided: normally this requires through-hole plating to connect from one side of the board to the other. This is virtually impossible to do if you make the board yourself (as I do), and it is expensive if you have it made. To get round this, some of the 'through' connections are made by soldering the ic sockets on both sides of the PCB. It is strongly recommended that good quality turned-pin Ic sockets be used. These stand slightly clear of the PCB

when fitted, and it is possible with a steady hand to solder to pads on the component side of the board under these sockets, and on the 'copper' side of the board, thereby creating a through connection. (See figure 6). Be warned: a steady hand, good eye, and small, hot soldering iron is essential for this. It is possible to do without the ic sockets, and solder directly to the pins of the ics, but this is not recommended, because testing and replacement becomes extremely difficult once the chip is soldered in, and could result in mechanical damage.

There are also a number of through connections made by wire links soldered on both sides of the PCB and also a number of the resistors and capacitors are soldered on both







sides. The through links are shown on figure 7, the component layout, by an 'X'. Fit all these links first, because some must go under ic sockets, followed in sequence by the other components, the ic sockets, and the crystal. A small blob of glue gives added security for the crystal XL1. Check carefully to see which components require soldering on both sides of the PCB.

The power supply board is a straightforward type: it is a single sided board with no through connections. This is a mains board, so seek experienced assistance if you are not used to mains construction. Observe the polarities of the bridge rectifier and electrolytic capacitor when fitting them. Check also that no 'whiskers' of copper remain around the connections to cause short circuits. It is wise to check operation of the PSU before connecting to the main board (see under 'Testing').

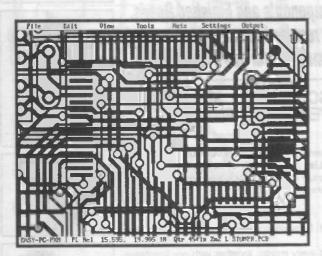
It is difficult to give mechanical design instructions for the box, since not everyone can lay hands on the same box, or has the same skills. However, figure 5 shows how the original was mounted in its box, with the PCB mounted under the top with plastic spacers and the 9 pole **D**-type

RS232 connector protruding through a hole cut in the rear panel. If you have problems finding plastic spacers, short pieces cut from a plastic ballpoint pen barrel are ideal. The interface cable between the emulator and the target consists of a piece of flat cable about 30 cm long, fitted with a 28 pin DIL Header at each end (note carefully which is pin 1). One end of this is plugged into the Target Interface socket on the main PCB, and the other is fed out of the box via a small slot cut into the edge of the front panel. It can then be plugged into the target circuit, again observing the correct orientation. The length of this cable should be kept as short as is reasonable, to prevent noise pickup affecting operation.

The Reset Switch, Mode Switch and Mode LED are mounted on the front panel, connected to the PCB with short lengths of multicore wire. I used a piece of flat cable, as this keeps things tidy. The power switch and fuse are mounted on the rear panel, and the power cable is routed through a notch cut in the edge of the rear panel.

The PSU was bolted into the base of the box, again using short M3 bolts and plastic spacers.

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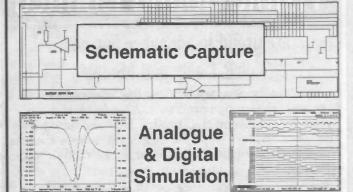
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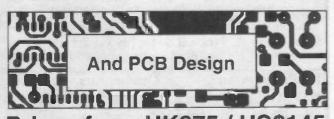
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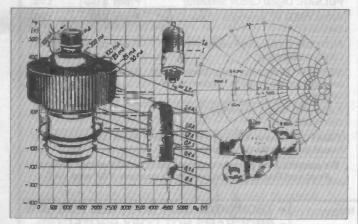
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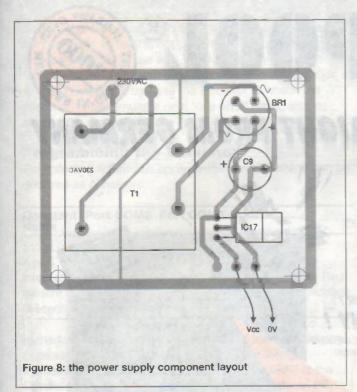
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Testing

Connect 230V ac to the PSU, and switch on, taking great care not to touch the high voltage points - 230V ac is lethal! Seek experienced assistance with building and testing if you are not used to constructing with mains power.

With a voltmeter, check that the +5V supply is available. If this appears OK, switch off, connect the main PCB without inserting the ICs, and switch back on. Check that +5V is available on the appropriate pin of any ic socket. If not, switch off and look for the probable short-circuit on the main PCB.

When all is well, insert the ics. If you have access to an oscilloscope, then the oscillator can be checked, and the baud rate divisor. If the UART is not fitted, then a link can be fitted between pins 24 and 17 of the UART socket. This connects a clock signal to the Address Counters, which can be checked for correct operation at the ram sockets.

Conclusion

The Emulator is not restricted to use on an IBM compatible PC. There is no reason why it should not be used on any machine with a serial port, providing the user can produce the appropriate software. I have tested the prototype with an Atari ST machine, as well as 386/486/Pentium-based PCs running at various speeds.

The software.

Because the Emulator responds to a byte, and returns a byte, it is not possible just to use a simple terminal program and transmit a data file to the Emulator. The software must be able to check that each byte has been received and acted on by getting a response. The original software was written in QBasic supplied with the later versions of DOS. The problem here is that it is not possible to compile an executable program, although it can be done with the stand-alone versions of QBasic. The full QBasic listing is too long to reproduce here. However, two annotated listings are given: these show how to download data to the emulator, and how to read data. These are very much 'bare-bones'

Resistors

(0.25 watt 5 percent)	
R1, R2, R3, R4, R7	4k7
R5	10M
R6	100k
R8	120R
R9	2k2
R10	10k

Capacitors

C1, C2	
C3, C4, C5	22uF/35V radial electro AT56L
C6, C7	10pF ceramic WX44X
C8	1uF/63V radial electro AT73Q
C9	1nF ceramic WX68Y
C10	470uF/25V radial electro AT51F
Cd 1-3	100nF polyester DT98G
Cd 4	47uF/16V radial electro AT39N

Eprom Emulat

Semiconal	ictors	
IC1	Max 232CPE FD92A	
IC2	6402 UART QQ04E	
IC3	74HC02 Quad 2 I/P NOR UB01B	
IC4, IC6		
IC11, IC13	74HC393 dual binary divider	
The same of the sa	AE60Q	
IC5	74HC74 dual 'D'-Type flipflop	
	UB19V	
IC7	74HC132 quad 2 I/P NAND	
TOTAL TOTAL	Schmitt LIBOOA (if difficult to	

	obtain, use 74HC00)
IC8, IC9, IC10	74HC245 octal bus transceiver UB67X
	UDUIA

74HC541 octal bus driver UB93B

IC15, IC16	62256 32K x 8 bit static ram
	UH40T
Q1, Q2	BC107 QB31Jj

L78M05CV QL28F IC17 Bridge rectifier 50V/1A, BR1 Type W005 AQ94C 2.4576 MHz crystal FY81C XL1

IC12, IC14

Miscellaneo	us
T1	Transformer 230Vac/12Vac -
	250mA YN14Q
F1	Chassis mount fuse holder
	100mA RX96E
S1 Reset switch	Momentary push to make
	FF98G
Power switch	2 pole on-off FH04E
Mode switch	1 pole/3 position FH01B
Serial connector	9 pin 'D' type plug PCB
	mounting FG66W
Baud rate switch	4-pole DIP switch JH08J
14 pin ic sockets	(7 off) Use 'turned pin' type
	FJ64U
16 pin ic socket	(1 off) FJ65U
20 nin ic sockets	(5 off) FD90X

10040
16 pin ic socket (1 off) FJ65U
20 pin ic sockets (5 off) FD90X
28 pin ic socket (1 off) FJ68Y
40 pin ic sockets (2 off) FJ69A
28 pin ic header (2 off) JP40T
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ilstings: you are encouraged to expand these and write your own software: it really is not difficult. An executable program in Visual Basic 3 for use with Windows 3.1 or Windows 95 has been developed, and a small application which sends a selected number of bytes to the emulator to check the operation of the emulator on a 'one-shot' basis, and to confirm the operation of the Address Counter

Programming the Emulator

The following addresses apply to standard PCs, and can be declared as constants.

Constant Name	Port COM2	Port COM1	Function
txport	&H2F8	&H3F8	Tx Buffer
rxport	&H2F8	&H3F8	Rx Buffer
intenreg	&H2F9	&H3F9	Interrupt Enable Reg.
intident	&H2FA	&H3FA	Interrupt Identification
linecont	&H2FB	&H3FB	Line Control

In the following description the actual program instructions generally come first, and the remarks in the listings themselves are preceded by 'a single quote. &HFF is the QBasic convention for showing hexadecimal data, in this case hex FF. The following variables must be initialised in your program:

workfile[percent]	integer containing the active
wommerporoom,	filenumber.
byte[percent]	integer containing the databyte.
workfile\$	string variable containing
	the name of a file.
counter&	long integer used as an index.
emulatorsize&	size of the EPROM in use in bytes
available = 4	interrupt bit mask for RxData

Port Setup.

Before using the emulator, your program should set up the PC port to be used to match the UART settings. This requires a divisor to be loaded to set the baud rate (in this case, 9600 baud), and the appropriate stop blts, parity and handshakes must be set:

OUT linecont, &H80	set up baud rate: enable baud rate divisors
OUT txport, &HC	baud rate division
	(1843200/baudrate x 16 = &H0C)
OUT intenreg, &H0	
OUT linecont, 3	no parity, 1 stop bit, 8 bits/char, no handshakes
OUT intenreg, 1	enable data available interrupt

Writing data to the emulator from file

This assumes that a file of data exists to be written to the emulator.

counter& = 1	er
OPEN workfile\$ FOR B	percent] 'Now
	ta is read from ulator.

do

GET workfile[percent], counter, byte[percent] 'QBASIC reads 2 bytes from file

byte[percent] = byte[percent] AND &HFF 'so reduce it to 1 byte OUT txport, byte[percent] 'then send it to the Emulator 'now wait until the Emulator echoes the data

'wait for 'data available' interrupt TIMER ON 'switch timeout watchdog on ready = INP(intident) AND available LOOP UNTIL (ready = available) TIMER OFF 'exit when data received

counter& = counter& + 1 'increment the counter loop until counter& (right arrow) emulatorsize& 'and loop if not completed

close workfile[percent] *close the file when finished

Reading data from Emulator to file

If data is received by the PC, it sets a bit in the Interrupt Identification Register. The read process enters a loop until this bit is set, then reads the waiting byte of data and writes it to file.

counter& = 1 'initialise the counter OPEN workfile\$ FOR RANDOM AS workfile[percent] LEN = 1

FIELD #workfile[percent], 1 AS databyte\$ Now loop until all data read from Emulator and written to file.

OUT txport, &HFF 'prompt Emulator to return data DO 'loop until 'data available' interrupt occurs.

TIMER ON 'switch timeout Ale

wafchdog on

ready = INP(intident) AND available

LOOP UNTIL (ready = available) TIMER OFF 'exit loop when data received rxbyte[percent] = INP(rxport) 'then read returned data LSET databyte\$ = LEFT\$(MKI\$(rxbyte[percent]), 1) 'convert number to a 1-char string

PUT #workfile[percent] 'and put it in the file counter& = counter& + 1 'increment the counter loop until counter& [right arrow] emulatorsize& 'and loop if not completed

close workfile[percent] 'close the file when finished

Any data may be sent to the emulator as a prompt in read mode (I used FF). It may also be worth noting that if the emulator fails to return a byte for any reason, this routine will stay in the loop forever waiting for data. You should include a check on the timer value, and exit after a short period, noting this is a Timeout Error. The remainder of the program is left up to you.

Software

A disk containing the Eprommer software is available from ETI advertisers Forest Electronic Developments, 10 Holmehurst Ave., Christchurch, Dorset BH23 5PQ, priced £5 inclusive of post and packing. A kit may be available in the future - please enquire at FED at the address above.

ELECTRONIC Door Chimes

An adjustable modern replacement for the traditional ding-dong. By Terry Balbirnie

hen operated, this circuit will make a sound very similar to that of a traditional set of metallic door chimes. However, there are several advantages in using this, compared with the old electromagnetic variety. This unit can be used as a replacement for a failing set of chimes, or for a new installation. The continuous standby current requirement is only 150uA so a pair of AA size cells will be sufficient to operate the circuit for at least one year in normal use.

The chimes unit is built in a small plastic box. This contains the circuit panel, batteries and a small loudspeaker. On the front is a matrix of holes for the sound to pass through. The speaker provides enough sound to be heard around the average house. However, it is possible to connect an additional "repeat" speaker so that the chimes may be heard in, say, a workshop or garage. It may also be useful to fit an additional speaker in the lounge so that the sound can be heard above that of the TV.

Hold off

With the traditional type of door chimes, the "dong" is delayed until the bell push is released. Some people, especially children, keep the button pressed to give a prolonged "ding". With this unit, two regular "ding-dongs" will be given however the bell push is used. Also, after operation the circuit will be inhibited for a preset time so that it cannot work again for up to 1 minute. This prevents multiple operations which some people like to give in an effort to attract your attention but which usually prove annoying. There is

provision, however, for those people who prefer to allow more than one operation (so that friends and family members can announce their arrival by a pre-arranged code of "ding-dongs").

A significant advantage of this circuit is that, in use, the bell push carries a current of only a few microamps. In traditional chimes, a large current - some 1A or more - flows through the bell push and its associated wiring. Any high resistance due to an excessive length of wire or poor contact will result in the chimes not working. Bell pushes are prone to moisture entering the body and causing corrosion of the contacts resulting in a significant resistance when they "make". This leads to unreliable or intermittent operation. With this unit, the low current means that operation is much less affected by the condition of the contacts or the length of interconnecting wire.

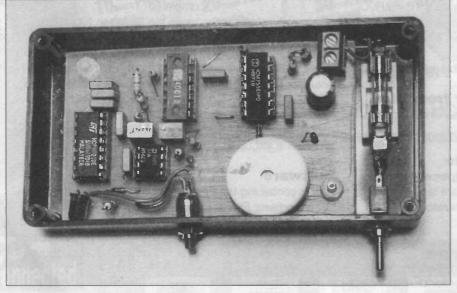
Preset controls on the circuit panel allow the unit to be customised so that it provides a quality of sound according to the user's preference. It is fun to experiment with these at the end. A further preset control sets the hold-off time between 2 and 60 seconds approximately.

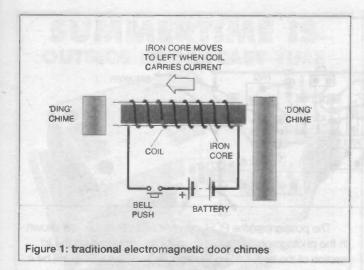
Traditional arrangements

At this point it seems appropriate to give a quick description of a conventional door chimes arrangement. The unit itself consists of a solenoid (coil of insulated copper wire) wrapped on an insulating tube and a moveable soft iron core. The core is able to slide into the tube and is held loosely in its rest position with a spring (not shown) - see figure 1. There are two metal bars attached to

rubber mountings - one on each side of the coil. These are normally kept out of contact with the core. They are tuned to emit certain sound frequencies (musical notes). When struck in turn, the chimes therefore emit the characteristic ding-dong tone associated with these units. Musicians would say that they sound in thirds. The rubber mountings prevent the sound from decaying too quickly when the bars are sounding.

When the bell push is operated, current flows from a battery or mains transformer via the bell push contacts through the coil. A magnetic field in produced by the coil and this pulls the core into the tube. However, it tends to overshoot the central position due to its inertia and the left-hand end strikes the smaller metal bar. This therefore emits the higher of the two musical notes (the





"ding"). It then centralises and takes up a position in the coil out of contact with either metal bar. When the bell push is released, the magnetic field collapses and the iron core is returned by the spring. However, as before, it tends to overshoot and strikes the lower-frequency (larger) bar (the "dong") before returning to its rest position. It makes no difference whether ac or dc is used since either a steady or afternating magnetic field will be generated in the coil and the core will be pulled inside the tube whichever is used

The chief disadvantage of such a mechanical system is the large current requirement of the solenoid. Thus, for battery operation, the cells will need to be of the heavy-duty type (eg C or D size alkaline cells) and will not give a very long period of service when used several times per day. There is a second disadvantage that, after a long period of use, the core sometimes sticks and the unit works intermittently or not at all.

How it works

The complete circuit for the Electronic Door Chimes unit is shown in figure 2. The nominal 3V supply is derived from two 1.5V cells which comprise battery B1. There is no on-off switch since there seems to be no reason why the unit should ever be switched off, IC2 is a dedicated "chimes" device, while IC1 is a timer ic configured as a monostable. This latter component provides the "hold off" aspect of the circuit. Ignore this for the moment. When

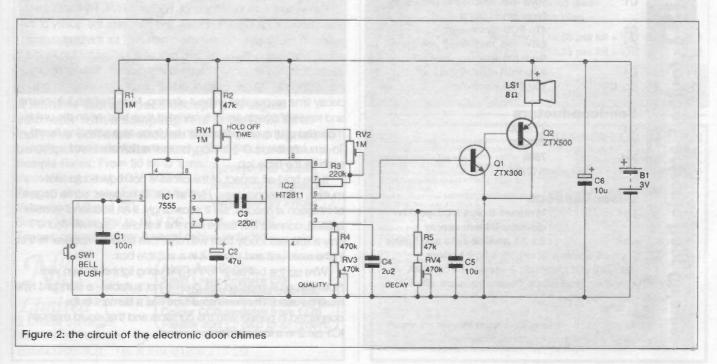
IC2 pin 1 is made high momentarity its output, pln 5, gives two "ding-dong" signals with a short space between. If pin 1 is maintained in a high condition, the signals will be repeated indefinitely. The output is of a low level so it is amplified by feeding it into the base of npn transistor Q1. This, in conjunction with pnp transistor, Q2 forms a high gain amplifier. The boosted signal then flows through loudspeaker, LS1. While pin 1 is left unconnected it assumes a low state and the significance of this will be seen presently.

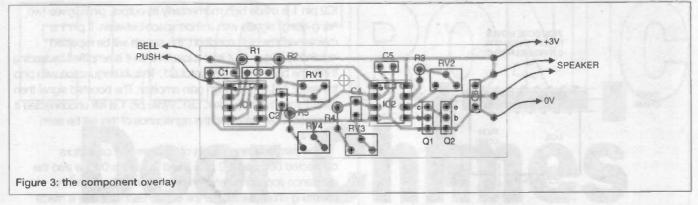
The parallel arrangements of resistors and capacitors connected between IC2 pins 2 and 3 and the 0V line also the resistance appearing between pins 6 and 7 determine the operating characteristics of the circuit. Each of these is made variable by means of the presets RV2, RV3 and RV4, and these will be adjusted at the end of construction to produce a tone which suits the user's requirements.

Monostable

The monostable circuit based on IC1 operates in the following way. When triggered by a low pulse applied to pin 2, the output (pin 3) goes high for some preset time then reverts to low. The bell push connected between pin 2 and the OV line provides the necessary low state when operated. No matter how it is pressed, the monostable will be triggered and the output will go high for the preset time. This time depends on the values of R2, RV1 and C2 and RV1 is used to adjust it as required at the end. Once triggered, a high state will be transferred momentarily via capacitor C3 to IC2 input, pin 1. The loudspeaker will therefore emit the chimes sound as described previously. However, while the monostable is active and pin 3 remains high, further operations of the bell push will have no effect. Eventually, IC1 times out and pin 3 reverts to low. Capacitor C3 then discharges very quickly since both sides of it (connected to IC1 pin 3 and IC2 pin 10) are in a low state. The circuit then becomes active again and ready to respond to further operations of the bell push. IC2 pin 2 is kept normally high through resistor R1 and this prevents false triggering. Capacitor C1 bypasses ac signals which tend to be induced in long runs of bell-push wiring from nearby mains equipment. Without this there could also be a tendency to false triggering.

The chimes ic is designed to operate from a nominal 3V dc supply such as will be provided by a pair of AA size cells. This is





also suitable for operating IC1. Do not use a higher voltage than this because the maximum allowed supply voltage for IC2 is 3.3V. Do not under any circumstances attempt to operate this circuit from a mains transformer.

Construction

The PCB component layout is shown in figure 3. Begin by drilling the fixing hole and soldering the two ic sockets in position. Follow with all capacitors and resistors (including presets RV1 to RV4). Solder the two transistors noting which is which and taking care over their orientation - the flat faces should be adjacent to one another. Solder 10cm pieces of stranded connecting wire to the two sets of pads labelled "speaker" and "bell push". Solder the positive (red) PP3-type battery connector wire to the pad labelled "+3V" and the negative one to the 0V pad (if the battery holder is of that type - otherwise solder pieces of wire for its solder tags). Adjust the wipers of RV2, RV3 and RV4 to approximately midposition and RV1 fully clockwise as viewed from the top edge of the PCB (for minimum hold-off time).

D (1884)	Desistan	THE RESERVE OF THE PARTY OF THE
	Resistors	
	R1	1M
	R2	47k
- •	R3	220k
	R4	470k
البح	RV1, RV2	1M
PARTS	RV3, RV4	470k
	Capacito	rs
110	C1	100n min. metallised polyester,
		5mm pin spacing
97	C 2	47u PCB electrolytic
- 1	C3	220n min. metallised polyester,
ш		5mm pin spacing - see text
3	C4	2.2u PCB electrolytic
	C5, C6	10u PCB electrolytic
D	Semicon	ductors
	Q1	ZTX300
-	Q2	ZTX 500
9	IC1	7555
	IC2	HT2811
for the Electronic Door Chir	Miscella	naniis
5	LS1	Miniature 8 ohm loudspeaker
	LOI	diameter 66 mm approx
8	B1	2 x AA alkaline cells and holder
5	Name and Address of the Owner, where the Owner, which is	cets (2 off), PP3-type battery
3		required); 2-section piece of 2A
n	THE RESERVE AND ADDRESS OF THE PARTY OF THE	al block. Plastic box size: 138 x 76 x
	25 mm intern	

The HT2811 ic is available from Maplin, as are all

other components for the chimes unit.

The positions of the PCB, speaker and cell holder are shown in the photograph. Note that everything is mounted on the lid section of the box. If using the specified case, the PCB will be a close fit and cannot rotate even though only one fixing hole is used. Drill this hole through the plastic but do not attach the PCB yet. Drill the hole for the bell push wires and a matrix of holes in the lid for the sound to pass out from the speaker. Drill a hole in the rear to secure the unit to the wall. Attach the speaker itself using a little quick-setting epoxy resin adhesive around the rim. When the adhesive has hardened, attach the circuit panel using a short insulating spacer on the bolt shank and solder the speaker leads in position. Secure the battery holder using adhesive fixing pads and connect it to the circuit.

Testing

Insert two AA size alkaline cells in the holder. Since capacitor C1 begins in a discharged condition, it sometimes triggers IC1 as the supply is established. There may therefore be two "ding-dongs" emitted by the speaker. Check that the circuit may be operated by touching the two "bell push" wires together for an instant. Wait for a second or two between operations to be sure that the monostable has timed out. If this only sometimes works, raise the value of C3 slightly. This should not be necessary and the specified value (220n) was found to give reliable results in the prototype unit. Check the effect of RV2 adjustment. Basically, this sets the rate at which the "ding-dong" is delivered. By varying it, the pitch and speed of the sound may be changed from a slow deep tone to a fast high-pitched one. This is rather like changing the speed of the record in an old (analogue) record player. Adjust it according to personal taste.

Experiment with the effects of RV3 and RV4. RV4 sets the time taken for the signal to decay and RV3 sets the quality of the sound. Adjust these in conjunction with RV2 for the best overall effect. The setting of RV4 is fairly critical. If it is set for too long a decay time, the sound will be suddenly clipped at the end. Once the setting of RV2 has been decided, adjust RV4 for as long a decay time as possible without clipping. Note that the full volume and range of effects are only heard at their best when the unit is mounted on the wall which then behaves as a sounding board. To simulate this during testing, press the unit on a hard surface such as the table top.

If the hold-off aspect of the circuit is not required (to allow multiple operations), leave RV1 as it is. If, however, some degree of inhibition is needed, set it accordingly. If an additional speaker is fitted, connect it in series with the first one. Each will sound only a little less loudly than with one. Use a similar speaker to that in the main unit and mount it in a suitable box.

Wire up the bell push to the unit using light-duty twin wire. Note that an illuminated bell push is not suitable - a standard type must be used. The illuminated type has a filament bulb connected in parallel with the contacts and this would maintain IC1 pin 2 in a low condition.







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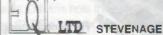
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Fast Fivers

Adaptable, affordable - handy circuits for around £5. By Owen Bishop

5. A musical booby trap

he idea for this 'Fiver' came from a Victorian port decanter which is an helrloom in our family. Instead of locking up the port and sherry in a tantalus, the decanter is left on the sideboard protected by a booby trap. Whether it was actually thought of as a booby trap in the days of Queen Victoria we do not know, but there have always been boobies and there have always been traps, even if the combined term had not then been invented. The port decanter, beautifully engraved, has its base shaped into a dome, to provide a groove to catch the lees which precipitate with age (this was before the days of lees-free supermarket port). But the dome has another purpose. Beneath it is concealed a musical box triggered by a small brass knob projecting downward from the base of the decanter. When the decanter is in its proper place on the sideboard the knob is pressed upward by the weight of the decanter and prevents the musical box from working. However, when an unwary person lifts up the decanter to sample the contents, the pressure on the knob is released and the music sounds loudly. It is no good hastily replacing the decanter for, once started, the tune is played through to its end, leaving the embarrassed booby to 'face the music'.

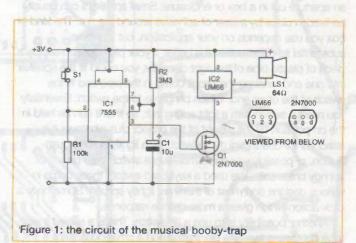
This project uses a UM66 music ic to provide the tune, and a timer ic to turn on the power for long enough to let the tune play once through. Once the circuit has been triggered, the melody plays all the way toits end, even if the pifferer replaces the booby-trapped object. The circuit is adaptable to a wide range of applications, which we leave to the reader's imagination and ingenuity.

How it works

The music ic (IC2) is one of a series available with different pre-programmed tunes ranging from 'Twinkle twinkle little star' to 'White Christmas'. The one in our prototype plays 'Love me tender' which is no longer in the catalogue, so perhaps, like the Victorian decanter, it is a collector's piece. This is connected in the drain circuit of a 2N7000 mosfet, actually a package containing a Darlington-connected pair of mosfets for greater gain. The gate of the mosfet is connected to the output of a 7555 cmos timer ic, wired as a monostable. Normally the output of the 7555 (pin 3) has a low (OV) output: Its trigger input (pin 2) is normally kept high (+3V) by the button or switch S1 being held closed. If S1 is opened, even for an instant, the voltage at pin 2 is pulled low by R1, triggering the ic into action. Its output swings sharply high and stays high for a period of time determined by the value of R2 and C1. What happens is that the voltage across C1 is detected by internal circuits connected to pin 6. In the untriggered state this is held at one third of the supply voltage (at 1V, in this circuit). Surplus current flowing through R2 is diverted into pin 7 and through the ic to ground. When the ic

is triggered and the output goes high, current is no longer allowed to flow into pin 7. It flows to C1 and the charge on C1 gradually increases. This continues until the charge on C1 reaches two-thirds of the supply (that is, 2V). This voltage is sensed by pin 6, and the output is switched low. At the same time current flows again though pin 7, rapidly reducing the charge on C1 to 1V once more. Thus the length of time that the output is high depends on how long it takes to charge the capacitor from 1V to 2V. **This** depends on the resistance R of R2 and the capacitance C of C1. The formula is:

t = 1.1RC





t is in seconds, R in ohms and C in farads. In this project we need to give the music ic time to complete its tune. Ours required 30 seconds and, with the values shown in figure 1:

$t = 1.1 \times 3.3 \times 10^6 \times 10 \times 10^{-6} = 36.3$ seconds

One of the good features of the 555 and 7555 timer ics is that the timing depends on charging from 1/3 of supply to 2/3 of supply. It is independent of the actual voltage of the supply. If the battery goes a little flat this does not affect the timing.

When it is in the resting or quiescent state (waiting to trap the unwary), the only current used is that flowing through R1 and the small amount going through the timer. These total only 60uA, so a small battery of two AAA alkaline cells lasts several months.

Construction

There are two points to consider before you begin. One is the mounting of the loudspeaker. We used one of the smallest available low-cost speakers, but a larger one can be used. Smaller types are obtainable from suppliers specialising in surface-mount devices. To obtain a reasonably loud sound it is essential that the speaker is mounted firmly on a baffle. The function of a baffle is to prevent the sound waves emitted from the back of the speaker coming round and cancelling out the sound waves emitted from the front. In practical terms, the speaker needs to be mounted in an aperture cut in a box or enclosure. Small speakers can usually be fixed in place by a layer of adhesive around the rim. The kind of box you use depends on your application, but the more substantial its construction the better. If your plumber can supply a piece of plastic pipe of the right diameter, you could fit the speaker into one end, enclose the circuit board in the pipe and fit the switch at the other end. Which brings us to the switch. Essentially, you need a switch with a light spring in it, so that it can be held in the on position by the weight of the project plus whatever it is attached to. Use a mini microswitch, or a lightly sprung pushbutton, or possibly you could make up a switch from pieces of springy brass strip. We used a keyboard-switch; these come in various designs and most of them are lightly sprung. Some have a click-action which gives a more definite response.

Another possibility is to use a tilt-switch. This is a sealed metal tube containing two wire electrodes and a small quantity of mercury. When it is the upright position the mercury rests in contact with the electrodes and completes the circuit. When the switch is tilted, even by as little as 10 degrees, contact is broken and the circuit is triggered. Another version of this is the vibration switch (though this is more expensive and will take the cost of the

Resistors

R1 100 kilohm R2 3.3 megohm

Capacitor

10uF electrolytic, 16V, axial

Semiconductors

2N7000 Fetlington mosfet

7555 CMOS timer

IC2 UM66 musical ic

Miscellaneous

S1 Switch (see text) Loudspeaker 64-ohm

8-pin dil ic socket, 40mm x 27m stripboard (10 strips x 15 holes); 1mm terminal pins; battery box

for 2 x AAA cells

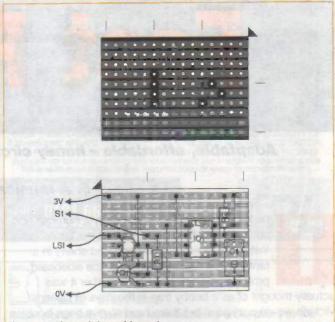
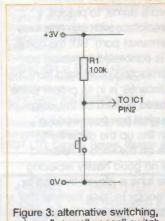


Figure 2: the stripboard layout





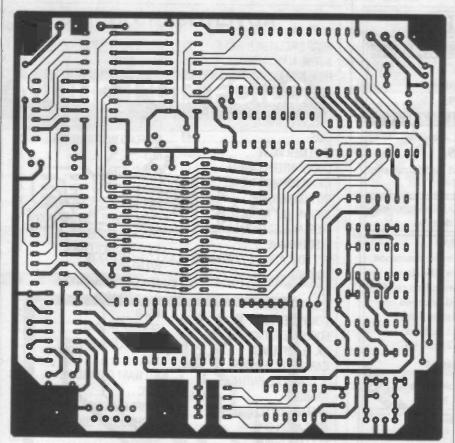
using a "normally open" switch

project above the £5 limit). This is normally open when still but closes with any vibrations. To use this you will need to alter the switching circuit as shown in figure 3. This way of connecting the switch can be used for any other kind of switch which is normally open and closes when disturbed.

The circuit is assembled on a small scrap of stripboard, stuck to the bottom of the battery box with double-sided adhesive foam strip. You could alternatively use a resin such as Araldite. The layout is compact so as to make it easier to fit the device into a restricted space. It you have any particular application in mind, you may need to adapt the layout to a differently shaped board.

First cut the strips beneath the board at E4, F3, G5 and D10-G10, as shown in figure 2. Assemble the timing circuit (IC1, R1, R2, S1), connecting the positive side of the switch to the pin at A1. Use a multimeter to test the output at pin 3, This is normally OV but rises to 3V for just over 30 seconds when triggered. Next assemble the remainder of the circuit, connecting the positive side of LS1 to the pin at A1. Note that IC2 faces the opposite way to Q1. When we assembled the prototype we put IC2 the wrong way round. It still produced a tune with the correct beat but the notes were in a strangely minor key! You might like it better than the usual rendering, but possibly the ic might burn out eventually. Assembly is now complete and the circuit should play its complete jingle every time it is triggered. If it cuts off too soon, increase the length of the timed period by increasing R2 to, say 2.7 megohms.

COMPONENT SIDE

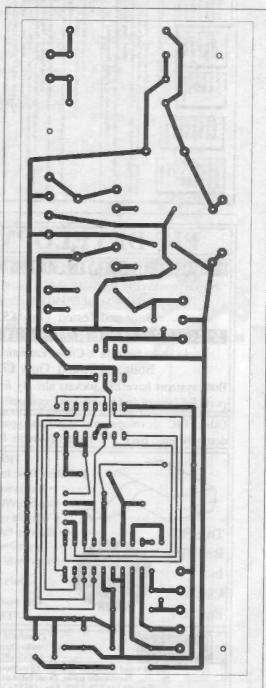


SOLDER SIDE

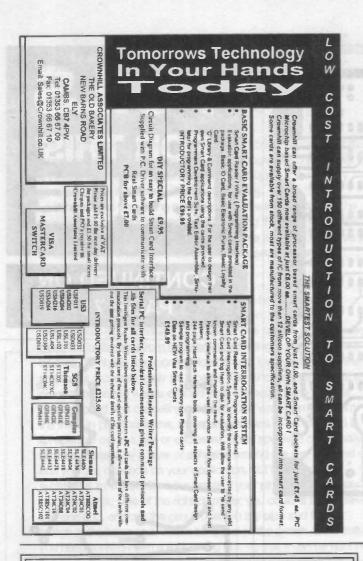
EPROM EMULATOR: COMPONENT & SOLDER SIDES

FOILS FOR THIS ISSUE

CONTINUED ON PAGE 71



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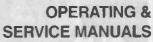


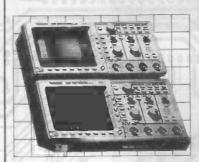






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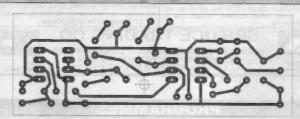
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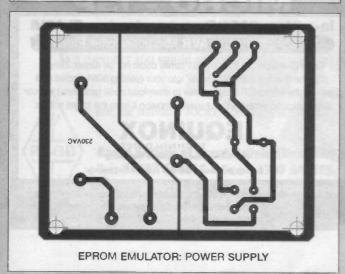
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Around the Gorner



eal improvements in electronic devices continue, though as ever they are less widely reported than the latest PC software.

For several years IGBTs (insulated gate bipolar transistors) have been heralded as a better type of switching device for switched mode applications. IGBTs, invented about 15 years ago and designed to carry serious wattages, can be viewed roughly as a combination of a power mosfet and a bipolar transistor. Conduction can be maintained without a continuous base current flowing, but at high currents they have a lower voltage drop than comparable power mosfets, as well as having a lower gate capacitance to charge and discharge.

IGBTs have not previously been relevant for switched mode power supplies of the most familiar type, because of their low switching speed. A year ago I recall seeing in a catalogue a switching frequency of 10 kHz heralded as fast for an IGBT. This is certainly useful for some things, but switched mode supplies used for desktop computers and applications of much larger loads run at frequencies of 50 kHz to 500 kHz.

Recently, International Rectifier have announced IGBTs with ratings up to 150kHz at 50A and 600V. Clearly these devices can now be considered for high power off-line switchers, and will probably soon see service in high power supplies, power factor correctors, and welders.

The problem with high-speed switching arises because the bipolar part of the device switches off by recombination of minority carriers in the base region. IR have found a way to reduce the carrier lifetime without the severe side effects, such as higher on-state voltage and, if lifetime-killing (as this process is now known) is taken too far, a negative resistance characteristic that makes the device useless.

Now, for off-line switched mode supplies operating below 150kHz, IGBTs can be used with a power saving - there are still higher switching losses than power mosfets, but with lower conduction losses, and requiring a much smaller chunk of silicon to achieve all this.

Nevertheless, while everything else gets smaller and smaller with every passing season, traditionalists will be happy to know that it may be a while before we see surface-mount versions of this kind of device.

Rob Bebbington MISTC

A few weeks ago I wrote to author Roy Bebbington with a minor query on a project he had submitted. Shortly afterwards, we received the news that Roy had died suddenly. Roy was a technical author before his retirement, and continued to design and build projects afterwards. He was not a young man, but from his project designs he was clearly young in spirit, and would have continued in his hobby for many more years had an untimely heart attack not carried him off. We at ETI extend our sympathy to Roy's family with the thought that his name, like the names of all dedicated designers, will be immortalised in the project collections of many ETI readers for a long time to come.

Sav EEEEEE!

One of our readers in the manufacturing business kindly returned a couple of comments on Controlling DC Motors (Part one) in last month's ETI. We do not know precisely what our correspondent is experimenting with, other than the fact that it involves moving quantities of toothbrushes. Perhaps he is delivering or even testing toothbrushes. Either that, or some startling developments in electric tooth-brushing techniques have yet to reach the public ear.

Next Month...

Volume 26 no. 10 of Electronics Today International will be in your newsagent on 12th September 1997 ... Tim Savage has been working on a Mk II Auto-Checker for continuity checking around the car and house ... Pei An describes a radio digital data control system which can be used for home automation applications ... At last, Terry Balbirnie's mock alarm flasher offers a very low cost safety feature for cars ... all the regulars, and more ... PLUS Buy issue 10 of ETI and save money on your PCB packs from our Beta Layout Promotion.

Contents are in preparation but are subject to space and availability.



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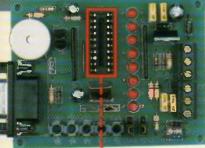
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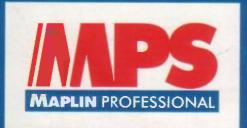


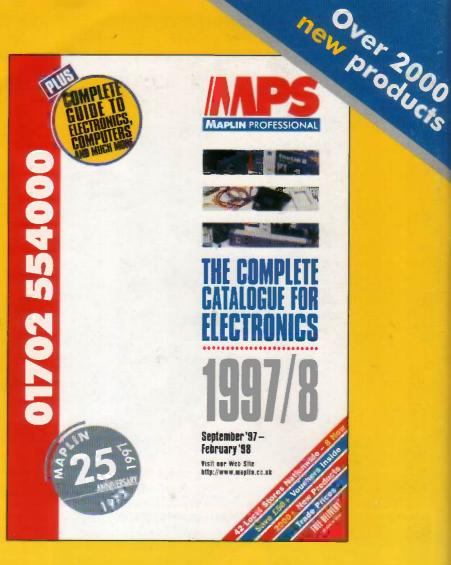


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