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FEATURES

DIGEST.....7 Some events and a few newsy components.

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STUTT-FULSE PADIATION MEASUREMENT HOW IT'S DONE

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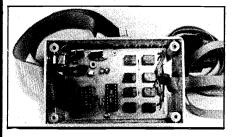
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SWITCHES TOGGLE: 2A 250V SPST35p DPDP 48p SUB-MIN TOGGLE	DIP SWITCHES (SPST) 4 way 85p; 6 way 80p; 8 way 8 10 way 125p (SPDT) 4 way 190p	P; VERO	PHOTO DECs Veroblock 480p	IDC CONNECTORS	PANEL METERS FSD 60 x 46 x 35mm	RELAYS Miniature, enclosed, PCB mount.
SPST on/off 58p SPDT c/over 64p SPDT centre off 85p SPDT biased both ways 105p	ROTARY SWITCHES (Adjustable Stop type) 1 pole/2 to 12 way, 2 pole/2 to 6 way, 3 pole/ 4 way, 4 pole/2 to 3 way	VERO BOARDS 0.1" 2½ × 1	S Dec 350p Eurobreadboard 590p Bimboard 575p Superstrip S52 1350p	with latch Header Card Pins Pins Pins Ping Edge Strt Angle Conct 10 way 65p 65p 65p 100p 16 way 75p 75p 60p –	0-50µA 0-100µA 0-500µA 0-1mA 0-5mA	SINGLE POLE Changeover RL-91 205R Coil; 12V DC, (10V5 to 19.5V), 10A at 30V DC or 250V AC 195p
DPDT 6 tags 80p DPDT centre off 88p DPDT biased both ways 145p DPDT 3 positions	ROTARY: Mains DP 250V 4 Anip on/off	3% + 5 125p 3% + 17 420p 4% + 17 550p VO Board 195p DIP Board 395p	COPPER CLAD BOARDS Fibre Single- Double-	20 way 90p 90p 95p 165p 26 way 105p 110p 115p 230p 34 way 115p 130p 135p 320p 40 way 140p 145p 150p 335p 50 way 165p 170p 175p 335p	0-10mA 0-50mA 0-100mA 0-500mA 0-1A	DOUBLE POLE Changeover, 6A 30V DC or 250V AC RL113 53R Coil, 6V DC (5V4 to 9V9) 190p RL6-111 205R Coil, 12V DC (10V7 to 19V5) 19V5) 19V5)
on/on/on 185p 4 pole 2 way 220p SLIDE 250V; DPDT 1A 14p	ROTARY: (Make-a-switch) Make a multiway switch. Shafting assen has adjustable stop. Accommodates up 6 wafers (max. 6 pole/12 way + DP swit Mechanism oniy	to Single Ended 55p h). Double ended 60p	glass sided sided 6" × 6" 100p 125p 6" × 12" 175p 225p DALO ETCH	60 way 195p 210p 225p 495p	0-2A 0-25V 0-50V 0-300V AC 520p	RL6-114 740R Coil, 24V DC (22V to 37V) 200p
DPDT 1A c/off 15p DPDT '2A 13p PUSH BUTTON 6A with 10mm Button	WAFERS: (make before break) to fit the ab switch mechanism. 1 pole/12 way, 2 pol way, 3 pole/4 way, 4 pole/3 way, 6p/2 Way	Wire Wrap 5/E. 135p Wire Wrap D/E 255p /6 VERO TOOLS Spot face cutters 150p	RESIST PEN Plus spare tip 100p FERRIC CHLORIDE	EURO CONNECTORS Gold Flashed Female Socket Male Prog Contacts Strit Angle Strit Angle Pins Pins Pins Pins Pins	CRYSTALS	ASTEC UHF MODULATORS Standard 6MHz 375p Wideband 8MHz 550p
SPDT latching 150p DPDT latching 200p DPDT moment 200p	Spacers 4p. Screen 6p. ROCKER SWITCHES	VERO WIRING PEN + Spool 380p Spare Spool 75p	1 lb bag Anhydrous 125 + 50p p&p	31 way 125p - 175p DIN41612 2x 32 A + B 200p - 175p - DIN41612 2x 32 A + C 225p - 185p210p	100KHz 400 200KHz 370 455KH 370	BUZZERS miniature.solid-state 6V; 9V & 12V 70p
Mini Non Locking Push to Make 15p Push to Break 25p DIGITAST Switch	ROCKER: 10A/250V SPDT ROCKER: 10A/250V DPDT c/off	Pen + Spool + Combs p Pen + Spool + Combs p 599p DIL SOCKETS	EDGE CONNECTORS 1".156" 2 × 6 way - 75p 2 × 12 way - 160p	DIN41612 3 × 32 A + B + C 260p 290p 295p 300p DIL PLUG (Headen)	1.008M 275	PIEZO TRANSDUCERS FB2720 70p
Assorted Colours 75p each ULTRASONIC	THUMBWHEEL Mini front mounting switcl Decade Switch Module 33 B.C.D. Switch Module 34 Mounting Cheeks (per pair) 5	ES Pin Prof Wrap Pin B 8 80 200 180	2 × 15 way - 165p 2 × 18 way 175p 160p 2 × 22 way .200 170p 2 × 23 way 150p 2 × 25 way 250p 245p	14 pin 40p 95p price per foot 16 pin 45p 100p Grey Colo 24 pin 65p 135p 28 pin 150p 200p 10 way 15p 26p	2.0MHz 225 2.4576M 200	Miniature, 0.3W-8 Ohm 2in, 2i/µin, 2i/µin, 3in 60p 2i/µin, 40Ω, 64Ω or 80Ω 60p 6" x 4" 8Ω 200p 7" x 5" 8Ω 225p
GAS/SMOKE	JUMPER LEADS (Ribbon Cable Assemb Length 14 pin 16 pin 24 pin 40	bin 22 22p 60p 39p 24 25p 68p 42p 28 28p 78p 52p	2 × 28 way 180p 2 × 30 way 280p 2 × 36 way 300p 2 × 40 way 320p 2 × 43 way 400p	40 pin 200p 255p 16 way 25p 40p 20 way 30p 50p 24 way 40p 65p 28 way 55p 60p DIL SOCKETS 34 way 60p 85p	3.6864M 300 4.0MHz 140 4.032MHz 290 4.19430M 150 4.433619M 100	8" x 5" 8၇ 250p VIDEO MONITORS
DETECTORS TGS812 or TGS813	Single ended DIP (Header Plug) Jumper 24 inches 145p 165p 240p 36 Female IDC Header Jumper Leads 36 inches lo	40 30p 89p 72p	2 × 75 way 600p -	24 pin 550p 50 way 100p 135p 28 pin 695p 64 way 120p 160p 40 pin 800p 64 way 120p 160p	4.608MHZ 200 4.80MHz 200 5.0MHz 150 5.185MHz 300	● ZENITH — 12" Green, Hi- Resolution Popular £76 ● MICROVITEC 1431. Standard
E8 each Holders for above 40p	20pin 26 pin 34 pin 40 Single ended 180p 200p 260p 300 Double ended 290p 370p 480p 521	In G-18W 620p XS P Spare tips, assorted size	117W 820p 25W 650p 100p 245p 195p 65p	'D' CONNECTORS 9 15 25 37 wsy way way way Male Solder lugs 55p 80p 120p 150p	6.0MHz 125 6.144MHz 140 6.5536MHz 225 7.0MHz 150	Res. Colour RGB input 14" incl cable £176 ● MICROVITEC 1451. 14"
TRANSF(3-0-3V; 6-0-6V; 9-0-9V;	DRMERS 1A TO22	REGULATORS Plastic Casing - ve 5p 7905	SOLDERCON PINS Ideal for making SIL or DIL Sockets 100 pins 35p	Angle pins 110p 175p 225p 300p PCB pins 100p 100p 160p 250p Female Solder lugs 90p 125p 180p 275p Angle pins 150p 200p 260p 380p	7.168MHz 175 7.68MHz 200 8.0MHx 140 8.089333M 395 8.86723M 175	Medium resolution £225 • KAGA 12". Med-res. RGB Colour. Has flicker-free charac- ters. Ideal for BBC, Apple, VIC,
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6VA: 2x6V/0.5A; 2x8 2x15V/0.2A Standard Solit Bobbin ty 6VA: 2x6V/0.5A; 2x 2x15V/0.25A	280 p 100mA TO92 Plast ype: 5V 78LO5 9V/0.4A; 2x12V/0.3A; 6V 78LO6 250 p 8V 78L08	package Op 79LO5 45p Op Op	3 x 2 x 1" 65p 4 x 2½ x 2" 100p 4 x 2½ x 2½ 103p 4 x 4 x 2" 105p	25 way 'D' CONNECTOR (RS232) Jumper Lead Cable Assembly	12.0MHz 150 12.528M 300 14.31814M 170 15.0MHz 155 16.0MHz 200	Connecting Lead for KAGA £3 Carriage £7 Securicor
12VA: 2x4.5V/1A3; 2x5V 0.5A; 2x15V·0.4A; 2x20V 24VA: 2x20V/1.5A; 2x9V/ 0.8A; 2x20V/0.6A 50VA: 2x6V/4A; 2x9V/2.5J	//0.3A 345p (35p pšp) 15V 78L15 1.2A; 2x12V/1A; 2x15V/ ICL7660 245 365p (60p pšp) RC4194 376 A; 2x12V/1A; 2x15V/ RC4195 160	Op 79L15 45p 5 TAA550 50p 5 TDA1412 150p 5 TL497A 185p	4 x 4 x 2½" 120p 5 x 4 x 1½" 99p 5 x 4 x 2½" 120p 5 x 2¼ x 1½" 90p 5 x 2¼ x 1½" 130p 5 x 2¼ x 2½" 130p	18" long, Single end, Male 475 18" long, Single end, Female 510 36" long, Double Ended, M/M 995 36" long, Double Ended, F/F £11 36" long, Double Ended, M/F 995	18.432M 150 19.968MHz 150 20.0MHz 150 24.0MHz 150	BT TELEPHONE CONNECTOR
2x20V/12A; 2x25V/1A; 2x 50VA: Outputs +5V/5 -12V at 1A 100VA: 2x12V/4A; 2 2x25V/2A; 2x30V/1.5A;	A; +12V, +25V, -5V, 620p (60p p8p) x15V/3A; 2x20V/2.5A; LM317KP 450 LM317KP 450 LM323K 450	78H12+12V/5A 695p 78HG+5V to+25V/5V 5A 550p	6 x 4 x 2" 120p 6 x 4 x 3" 150p 7 x 5 x 3" 160p 8 x 6 x 3" 210p 10 x 4 x 3" 240p	AMPHENOL CONNECTORS IDC SOLDER 24 way IEEE plug 485p 485p	24.930MHz 325 26.69M 150 27.648M 170 27.145M 160 38.6667M 240	LJU 1/4A Mini Line Master 435p LJU 1/6A Mini Line Slave 295p LJU 2/4A Line Master 370p LJU 2/6A Line Slave 250p LJU 3/4A Flush Master 370p
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Nuff Said

'What we have seen recently is a tragic shuttle failure, two Titan rocket failures, the Delta rocket explosion, the Chernobyl nuclear accident and a surgical bombing

raid on Libya that turned out not to have been so surgical. It confirms what a lot of us knew all along-technology is not perfectible,' John Pike, associate director of the Federation of American Scientists.

expensive than it is. No clarification has been offered on the definition of originality as far as circuits go. There is a problem issue, since it can take vast amounts of time and money to prove a design does more than merely exploit the well-known characteristics of its components parts. For many manufacturers in such a fastmoving field as electronics, market lead has become far more important than patent protection.

It is possible that a proposed 'unregistered design right' may apply to circuit designs although the White Paper mentions only 'semiconductor chips' in this context. The new right is intended to protect the designs of 'purely functional articles' for a ten year period. Disputes will be settled by the patent office but, unlike patents, the new right will not require the registration of a design.

It could be argued that a circuit design is the perfect 'purely fuctional article' since it doesn't even necessarily have a physical form, thereby avoiding all aesthetic or decorative aspects of its design. In fact, the White Paper recommends that the new right will come into force with 'the first expression of the design in any independent form, such as a drawing or in a computer, from which the article embodying the design can be produced' as well as from 'the first making of the article embodying the design.' We will, of course, have to wait for the White Paper to become law and for the law to be tested in court before we can safely assume that the new right will benefit circuit designers.

For the public as a whole, perhaps the most significant

recommendations in the White Paper are the ones dealing with home taping.

The expected levy on blank tape is to be introduced, but will only cover audio compact cassettes of greater than 35 minutes playing time. 'The levy,' says the Department of Trade and Industry, 'will entitle consumers to make, for private purposes, sound recordings of broadcasts or prerecorded material but not to copy computer programs.' Make what you will of the fact that videotape, reel-to-reel tape and compact cassettes of 34.9 minutes playing time are excluded from the levy. Make what else you will of the fact that private recording from TV broadcasts is to be legalised but not from pre-recorded video material. The proposals are shot through with such potential legal and moral minefields.

On the whole, the White Paper is a patch-up of the intellectual property system, which barely brings copyright and patent up to date and certainly does not simplify the intellectual property system. The idea that its recommendations will 'benefit . innovative talent' (as Paul Channon has said) is, to be kind, probably blind optimism.

One or two minor points worth noting are that the White Paper confirms the current position that computer programs are subject to copright even in non-human readable form (for example, on ROM chips) and that low power satellite broadcasts for TVRO or SMATV systems will also be subject to copyright.

Sandia's computer-controlled snapback test setup includes a 750 watt pulsed xenon laser (typically used for cutting metal and polysilicon lines on ICs, but used here to simulate the snapback triggering event), an automated probe station (typically used for obtaining a variety of electrical measurements on wafers), an infrared microscope TV camera and monitor, and appropriate voltage and current measuring instruments. The laser pulse deposits enough energy on a 10micron-square region of the IC to initiate snapback, thus permitting the minimum voltage required for sustained snapback to be measured.

INTELLECTUAL PROPERTY AND INNOVATION

CONTRACTOR FINAL REVIN

Patently A Patch-up

ritish Government proposals В meant to 'encourage innovation' have been published in a White Paper entitled 'Intellectual Property and Innovation' (Cmnd 9712, price £6.70 and available from Her Majesty's Stationery Office.)

Launching the White Paper, the Secretary of State for Trade and Industry, Paul Channon, claimed that the proposals would make the UK's intellectual property system - which covers patents, copyright and trade marks more accessible and relevant'. In fact, the major proposals seem destined to make the system intrusive more and more confusing.

In only one area are the proposals actually likely to encourage innovation — that is, the intention to allow easier public access to the Patent Office database. The patent system itself will remain almost unchanged. The Government's plan to take the Patent Office away from the Civil Service and make it a 'body corporate' rather like the BBC could be seen as a prelude to privatisation. It seems quite plausible that the Government may wish to make the Patent Office a paying business — charging for the traditional and newer services it has on offer.

Circuit designers will not find their position substantially modified as a result of any of the White Paper's recommendations. Protection of circuit designs may be afforded by the patent system although little has been suggested to make it less unwieldy and

Transistor Failure Investigated

Tests at Sandia National Laboratories are producing the first live TV pictures of silicon chip transistors as they experience a potentially catastrophic current surge. The surge is known as snapback, and if left uncorrected, can ruin the integrated circuit contained on the chip.

The pictures show a 1/100th of an inch square portion of a ¼"

IC. During snapback, microscopic transistors appear as short bright lines on an infrared TV image. The brightness is caused by high energy electrons flowing through the transistor. Transistors not in snapback do not shine because they have no high energy electrons.

Snapback attacks in-channel metal oxide semiconductor transistors. Some microelectronics experts believe that without proper planning and understanding, snapback could become a problem for the next generation of extremely small ICs, which will have individual features of 1.5 microns or less.

Snapback occurs only if two

conditions are present. First, the transistor must be operating at or above a certain voltage, called the snapback voltage. Then an initiating event (weapon-produced xrays or simple static electricity that can occur during wafer testing, for example) must trigger snapback by producing an overabundance of electrons in a transistor. The excess electrons produce the uncontrolled current.

If allowed to continue, this excessive current can melt the wires that connect an IC to its ceramic package or the wires that interconnect different devices on the IC. Snapback can also cause electron migration, which can lead to short or open circuits.

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0.22Ω to 8.2Ω E24 7p	µFd V 47 63 9p 47 100 10p	7480 7481 7482	1.75 95p	74LS125 49 74LS126 49 74LS132 65	p 4049	35p	18.95 8T26 1.09 8T28 1.09	2N3553 2N3632 19	2.95p 9.95p	BC141 BC147 BC148	15p 15p	BF457 BF458 BF459	59p 65p	ZTX320 22p ZTX330 26p	(PIV shown in brackets) 1 % amp type	LM350K 5.99p LM360N 6.24p	UPC1186 97p UPC2002 1.95p XR2206 3.95p	METRE Solid connecting
WIRE WOUND ON CERAMIC E12 SERIES	1 100 10p 1 450 39p 2.2 25 9p	7483 7484 7485	95p	74LS138 55 74LS139 55 74LS145 99	5p 4051 4052	35p 65p 55p	8T95 1.09 8T97 1.09	2N3702 2N3703 2N3704	16p	BC149 BC157 BC158	39p	BF961	69p	ZTX341 25p ZTX450 22p ZTX500 18p	WO1(100) 28p WO2(200) 34p WO4(400) 33p	LM361N 3.63p LM380N141.19p LM380N8 1.39p	XR2207 4.00p ZN409 1.49p ZN414 84p	wire MAINS SPEAKER
2 to 3W 0.22Ω to 330Ω 29p 4 to 7W 0.47Ω	2.2 63 10p 2.2 100 13p 2.2 450 43p	7486 7489	39p 1.99	74LS147 1.3 74LS148 1.3 74LS151 65	75 4053 35 4054	55p 79p 79p	81LS95 1.65 81LS97 1.65 81LS96 1.65	2N3705 2N3706 2N3707	16p 16p	BC159 BC160 BC161	44p 55p	BFR91 BFS61 BFS98	65p	ZTX501 19p ZTX502 20p ZTX503 21p	2 amp type Square	LM381N 3.50p LM382N 1.93p LM383T 2.76p	ZN1034 1.99p	Twin 1 Amp16p Twin 2 % Amp 19p
to 6K8 34p 9 to 11W 1Ω to 33K 44p	3.3 40 13p 3.3 63 14p	7490 7491	55p 65p	74LS153 65 74LS154 1.5 74LS157 55	4059 4060	3.95p 65p 85p	81LS98 1.65 6522 3.49 6522A 5.45	2N3819 2N3820	35p 69p	BC169 BC169C	19p 23p		2.75p 55p	ZTX504 22p ZTX510 22p ZTX531 26p	with hole	LM384N 2.75p LM386N 1.20p LM387N 1.89p		3 Core 2 % Amp 21p 3 Core
POTS &	4.7 35 10p 4.7 50 13p 4.7 63 14p	7492 7493 7494	65p 1 09	74LS158 69 74LS160 79	op 4066 5p 4067	39p 2.25p 25p	6532 5.25 6821 1.75	2N3903 2N3904	13p 16p	BC177 BC178 BC179	2 9p 31p	BFY50 BFY51	36p 36p	ZTX650 36p ZTX651 38p	6 amp	LM388N 1.60p LM391N601.55p	Post inc. prices	13 Amp 62p SCREENED
PRESETS ROTARY POTS LOW NOISE	4.7 100 16p 4.7 450 45p 10 25 9p	7495 7496 7497	55p 55p 1.99	74LS161 75 74LS162 75 74LS163 75	5p 4069 5p 4070	25p 25p	6840 3.75 6845 6.49 6847 6.49		15p 1.04p	BC182 BC182L BC183	15p 14p		65p 2.65p	ZTX652 42p ZTX653 45p ZTX750 37p	with hole PW01(100) 95p	LM391N801.75p LM396K 22.34p LM723CH 1.25p	All with 240V primaries 100mA	Single 16p Stereo 29p Mini single 13p
SPINDLES E3 SERIES 4k7 to 2 M LIN	10 50 14p 10 63 16p 10 100 18p	74100	1.99	74LS164 75 74LS165 1.0 74LS166 1.5	09 4072 95 4073	25p 25p 25p	8154 ASK 8155 3.30 8212 2.20	2N4037	1.06p 2.78p	BC183L BC184 BC212	16p 16p		49p 1.89p	ZTX751 41p ZTX752 45p ZTX753 47p	PW04(400) 1.30p	LM723CN 69p LM725CN 5.93p LM741CH 1.05p	6 0 6V 1.89 9 0 9V 2.19 12 0 12V 2.39	Mini Stereo 16p 4 Core 4 Screens 49p
40p 4k7 to 2 M LOG	10 450 58p 22 25 13p 22 40 16p	74104 74107 74109	15p	74LS169 99 74LS170 1.0 74LS173 99	65 4076 90 4077	25p 65p 25p	8216 1.60 8224 2.95 8226 2.95	2N4401 2N4402	27p 26p	BC213 BC213L BC214	17p 15p	BU109	2.49p 1.99p 1.94p	DIODES	PW06(600) 1.39p 25 amp type	LM741CN 28p LM741CN14 1.20p	15 0 15V 2.49 6VA 2 6V 4.30	4 core single Screen 62p 8 Core 73p
40p As above with DP Mains	22 63 18p 22 100 24p	74110	1.99	74LS174 9 74LS175 7 74LS181 1	9p 4078 5p 4081	25p 25p 55p	8255 3.50 8271 53.00 Z80ACTC 2.99	2N4871 2N5296	75p 93p	BC214L BC300	19p 59p	BU204 BU205	2.49p 1.99p	AA116 29p AA119 30p BA102 57p	Metal clad with hole	LM747CN 57p LM748CN 62p LM1035N 5.36p	2 · 9V 4.30 2 · 12V 4.30	12 Core 88p Heavy Duty Mike Guitar
Switch 99p As above stereo 1.30p	22 450 66p 47 25 16p 47 40 20p	74118 74119	99p 1.79	74LS183 1.1 74LS190 8	99 4086 5p 4089	59p 1.19p 35p	Z80ADART 7.99	2N5298 2N5401 2N5415	45p 1.67p	BC301 BC302 BC303	59p 59p	BU208 BU226	1.93p 4.45p	BA155 20p BA156 25p BA157 25p	KO2(200) 2.75p KO4(400) 3.25p	LM1037N 3.62p LM1458N 44p LM1496N 1.67p	2 15V 4.30 2 20V 4.30 50VA	Lead 33p AERIAL
PRE SETS PIHER (DUSTPROOF)	47 63 30p 47 100 32p 47 450 1.32p	74120 74121	95p 49p	74LS192 8 74LS194 7	5p 4094 5p 4095	69p 89p 89p	Z80APIO 3.19 ZN425E8 3.49 ZN426E8 7.99	2N5416 2N5447 2N5448	36p 36p	BC327 BC394 BC440	52p 35p	BU406 BU407	1.58p 1.58p	BA158 200 BA159 210 BA182 310	BYW64 35A	LM1886 5.58p LM1889 2.99p	2 / 12V 8.95 2 / 15V 8.95 2 / 20V 8.95	50Ω RG58A 29p 75Ω UHF 31p
E3 100 Ω to 10 MΩ Mini Vert 12p	100 16 16p 100 25 18p 100 50 25p	74122 74123 74125	75p	74LS197 8	9p 4098 9p 4099	79p 99p	ZN427E8 6.89 ZN428E8 4.55	2N5449 2N5450 2N5451		BC441 BC447 BC448	33p	BU409		BA317 4p BAV10 10p	OPTO Many weird &	LM2902 1.22p LM2907N 2.66p LM2907N82.66p	2 30V 8.95 2 50V 8.95	75Ω VHF 31p 300Ω Flat 16p RAINBOW
Mini Horiz 12p Standard Vert	100 63 29p 100 100 34p 100 450 1.95p	74126	75p 75p	74LS221 9 74LS240 8 74LS241 8	5p 5p 4502	55p	V REGS	2N5457 2N5458 2N5459	63p 57p 63p	BC449 BC460 BC461	38p	BUX80	6.75p 1.99p 7.32p	BAW49 21 BAX13 8	devices in stock inc var	LM2917N 2.66p LM2917N82.66p LM3301 1.16p	VERO, ETC.	RIBBON Prices per foot
14p Standard Horiz 14p	220 10 18p 220 16 19p 220 25 25p	74136 74141	69p 69p	74LS242 8 74LS243 8	5p 4503 5p 4505 9p 4507	35p 3.50p 35p	– Positive – 100mA TO92 78L05 29p	2N5460 2N5551	63p 26p	BC477 BC478 BC479	37p 38p	J300 J310	88p 88p 4.70p	BAX16 8p BB105 65p BB109 45p	of LEDS.	LM3524 2.95p LM3900 69p LM3911 2.09p	0.1" COPPER TRACKS L · W (mms)	10way 25p 16way 39p 20way 48p
CERMET 20 TURN PRECISION	220 40 29p 220 63 34p	74144	2.25	74LS245 9 74LS247 9	9p 4508 9p 4510 9p 4511	1.19p 49p 49p	78L06 47p 78L08 47p 78L09 47p	2N5684 1 2N5686 1	2.25p 5.10p 4.50p	BC546 BC547	16p 19p	MJ900 MJ901	3.21p 3.39p 1.78p	BY126 12; BY127 13; BY133 19;	G - Green Y - Yellow	LM3914 2.99p LM3915 2.99p LM4250 2.89p	50 × 100mm 49p 75 × 100mm 69p 100 × 100mm	24way 62p 30way 75p 34way 82p
PRESETS "E3 SERIES 50Q to 500K	220 100 46p 470 16 25p 470 25 32p	74148	1 20	74LS249 9 74LS251 7	9p 4512 5p 4514	49p 1.04p 1.05p	78L12 29p 78L15 29p 78L18 47p		4.60p 4.60p 60p	BC548 BC549 BC550C	16p 17p 19p	MJ1001 . MJ1800	1.86p 3.79p	BY399 27p MR754 (400V 6A) 61p	1 · 85D 100	LM13600 1.64p LM13700 1.64p MC1466L11.99p	79p 150 - 100mm 129p	40way 88p 64way 1.49p
99p 3 8" E3 SERIES 100 Ω to 100K	470 40 38p 470 63 50p 470 100 69p	74150 74151	65p	74LS257 7 74LS258 6	5p 4516 5p 4518	55p 45p 29p	78L24 29p 1 Amp TO220	2N6031 1	9.70p 10.90p 3.58p	BC557 BC559 BC560C	19p 18p 19p	MJ2501 MJ2955	2.68p 3.08p 1.40p	MR756 (600V 6A) 87 (0A91 16)	Y5D 16p Small diffused	MC1468L 8.50p MC1488 99p	160 - 100mm 135p 200 - 100mm	SOLDER ANTEX
1.19 CAPS	1000 16 34p 1000 25 43p 1000 40 53p	74155	89p 89p	74LS261 1.	19 4519 19 4520 4521 50 4522	49p 1.09p 79p	7805T 55p 7806T 85p 7808T 85p		3.86p 1.54p 1.72p	BC639 BC640 BC650	27p 27p 27p	MJ3001	2.42p 2.78p 5.90p	OA200 10 OA202 11	G3D 16p	MC3340 2.66p MC3357 2.11p	159p 500 - 100mm 389p	SOLDERING IRONS C250 (15W)
CERAMIC	1000 63 750 2200 16 530 2200 25 710	7415/	89p 1.75	74LS275 3	49 4526 49 4527	65p 65p	7812T 550 7815T 550 7818T 850	2N6212 2N6254	2.96p 2.90p	BC651 BCY70 BCY71	27p 31p 33p		75p 1.20p 2.52p	1N4001 4 1N4002 4	P RIM 21p	MC3446 3.95p MC3447 5.50p MC3470 6.95p	VQ Board 225p Track cutter	6.20p XS250 (25W) 6.50p
100 V DISC (PLATE) E12 MICRO	2200 50 80 2200 63 1.54	74160 74161	1.05 1.05 99p	74LS280 1. 74LS283 7	89 4528 9p 4529 9p 4532	65p	7824T 55p	2N6676	99p	BCY72 BD131 BD132	25p 63p	MJE29551 MJE3055 MJE30551	T 92p 2.42p	1N4004 5 1N4007 7 1N4148 3	D Large clear	MC4024 6.60p MC4044 6.60p MF10 4.18p	165p Pin inserter239p 100 pins s sided	Iron Stand 2.10p
MINI TYPIČALLY	4700 25 99r	74163 74164	99p 99p	74LS293 7 74LS295 1	9p 4534 .35 4536 .25 4538	3.49p 2.49p 75p	7805K 2.33 7808K 2.73 7812K 2.33	2SA509 2SA678	72p 77p 45p	BD135 BD136	30p 30p	MPSA05 MPSA06	17p 17p	1N5401 13 1N5404 16 1N5408 20	G5C 19p	NE529 2.25p	65p 100 pins d_sided 65p	Elements (State Iron) . 2.75p
- 5% 1pF to 4n7 6p POLYCARB 5% SIEMENS	RADIALS (Wires one end)	74165 74166	1.05 1.49	74LS323 3 74LS324 3	09 4543 25 4553 65 4555	2.39p	7815K 2.33 7824K 2.33	2SA699 2SA706	1.02p 4.99p 3.60p	BD137 BD138 BD139	38p	MPSA12 MPSA13 MPSA14	29p 25p 25p	17A 200V 125 17A 800V 158 70A 400V 230	high efficiency	NE544N 2.31p NE555 28p NE556 69p	Verobloc 495p Vero wiring pen + spool	C240 Bits No 2 (Small) 110p
7.5 mm MINIBLOC	Fd V 10 35 7 22 10 7		1.79 3.95 1.45	74LS326 2 74LS327 2	.89 4556 .89 4560	1.45p	- Negative - 100mA TO92 79L05 49	2SA1012 2SB527	1.45p 1.05p 3.95p	BD140 BD237 BD238	45p 45p	MPSA20 MPSA42 MPSA43	49p 29p 29p	70A 1.2KV 295	P R5U 53p G5U 53p	NE558 1.89p	429p Spare spool 83p Combs 8p	No 3 (Med)110p No 6 (Micro) 110p
250 V InF to 6n8 8p 10nF to 47nF9p	22 16 8 47 10 8 47 16 9	P 74175	99p 99p	74LS348 1 74LS352 1	.35 .95 4584 .19 4585	55p	79L12 49	2SB616 2SB688	2.95p 2.57p	BD239A BD239C BD240A	63p 66p 68p	MPSA55 MPSA56 MPSA92	17p 17p 27p	TRIACS	Y5U 53 Rectangular Stackable	NE566 95p NE567 1.95p	please ask for free	XS240 · 25 Bits No 50 (Small) 110p
56nF to 100nF 12p 150nF 15p	100 10 10 100 16 11	p 74180 74181	99p 3.25	74LS365 4	.19 49p 49p			2SC458 2SC710 2SC828	27p 27p 33p	BD240C BD241A BD241C	79p 66p 68p	MPSA93 MPSL01 MPSL51	27p 29p 29p	THYRISTORS 4.8 & 12 Amp			catalogue PCB	No 51 (Large) 140p SOLDER
100 V 100n, 120n 12p	220 16 13 470 10 14 1000 10 23	p 74182	1.35 1.89 1.89	74LS367 4 74LS368 4	49p 49p 95p		FOR	2SC930 2SC1061 2SC1096	45p 85p 75p	BD242A BD242C BD243A	69p 79p 76p	MPSU05 MPSU06 MPSU07	1.24p	Texas TO220 Suffix A 100	V5R 25	NE5534A 1.49p RC4194 2.60p	FERRIC	125 gms 18 swg 3.90p
150n 13p 180n, to 220n 15p	1000 16 28 2200 10 39 2200 16 51	p 74190	1.25 99p	74LS374 74LS378	95p 95p 49p	FF	REE	2SC1213 2SC1306 2SC1307	29p 1.99p 299o	BD243C BD244C BD244C	81p 76p 84p	MPSU55 MPSU56 MPSU57	1.42p 1.72p	Suffix B - 200 ' Suffix C -	AY15050 99	RC4195 2.06p RC4558 63p RC4739 1.08p	Quick dissolving. Enough to	22 swg 3.90p
270n 16p 330n 22p 390n 24p	4700 10 75 4700 16 1.09	p 74193 p 74194	99p 99p 79p	74LS390 74LS393	^{79p} .09 99p		LOGUE		35p 1.95p	BD245A BD245C	1.26p 1.30p	MRF450A TIP29A TIP29C	18.88p 35p 42p	Suffix D 400	CA3046 69 CA3048 5.54	SN76477 7.95p SN76003 4.75p	make over 1 litre 1.75p ETCH RESIST	D'Connectors
470n, 560n 27p 680n 33p 1µF (10mm) 37p	4700 63 2.99 10000 16 2.99 10000 80 4.95	p 74196 p 74197	1.19 1.19	74LS396 2 74LS398 1	.95			2SC1947 2SC1969	6.88p 2.73p	BD246C BD410	1.30p 1.46p 1.65p	TIP30A TIP30C	37p 44p	Suffix M - 600	CA3080E 69	SN76023 4.75p SN76033 4.75p	TRANSFERS 1 Thin lines	25 Way Solder Male 1.10p
2.2µF (15mm) 75r POLYESTER	10000 100 7.95 74111L	74221	2.25 99p	74LS490 1 74LS540 1	.39	OGIC CPUS	1 Amp TO220 7905T 66	2SD313	1.65p 85p	BD440 BD441	1.45p 79p 99p	TIP31A TIP31C TIP32A	56p 57p 58p	4A TIC106A 66 TIC106B 67	p CA3140E 45 p CA3140T 1.67	TA7205 1.15p TA7222 1.75p	3 Thin bends 4 Thick bends	Female 1.75p CENTRONICS
250V RADIAL 10n to 100n 8p	7400 25 7401 25	p 74LS00	24p 24p	74LS640 1	.39 650 .95 650 .95 680	2A 5.45 0 2.50	7912T 66 7915T 66	2SJ50 2SJ82	4.95p 5.29p 5.95p	BD442 BD529 BD530	99p 1.10p 1.26p	TIP32C TIP33A TIP33C	59p 1.22p 1.35p	TIC106C 68 TIC106D 69 TIC106M 75	p HA1366W 1.99 p HA1388 3.96	D TBA500 3.99p D TBA510 3.25p	5 DIL pads 6 Transistor pads	36W IDC Plug 495p IDC Socket495p
150n 10p 220n 11p 330n 15p	7403 25 7404 35	p 74LS02 p 74LS03	24p 24p		680 680 680	2 2.49 3 5.9 9	7924T 66	2SJ83 2SK55	5.95p 1.25p 4.95p	BD535 BD536 BD537	82p 82p 85p	TIP34A TIP34C TIP35A	1.27p 1.35p 1.87p	8A TIC116A 88 TIC116B 89	HA1389 2.96 HA1389R 2.96 HA1392 3.69	D TBA530 2.99p TBA540 3.25p	7 Dots & holes 8.0 1" edge connectors	Solder plug375p IC SOCKETS L. PROFILE
470n 16 680n 19 1µF 25	7405 25 7406 39 7407 39	p 74LS05	24p 24p 24p	CMOS	803	5 5.49 9 4.99	7908K 2.70 7912K 2.59	2SK135 2SK226	5.29p 5.95p 5.95p	BD538 BD651 BD652	85p 99p 99p	TIP35C TIP36A TIP36C	2.14p 1.94p 2.22p	TIC116C 93 TIC116D 97 TIC116M 99	p HA1397 4.89 p ICL7106 7.51	p TBA550 3.25p TBA560C 2.99p	9 Mixture Any sheet of above 39p	6 pin 50 p 8 pin 8 p
1.5µF 43 2.2µF 45	7408 25	p 74LS09 p 74LS10	24p 24p 24p	CIVIUS	808 -808	6 11.50 8 9.50	0 7918K 2.95 0 7924K 2.95	3N201 3SK88	1.10p 95p	BD677 BD711	89p 1.50p	TIP41A TIP41C	47p 58p	12A TIC126A 86	ICL7611 85 ICL8038 3.40	p TDA1002 4.35p p TDA1003 4.35p	GRADE ONE GLASS PCB SINGLE	14 pin 9p 16 pin 10p 18 pin 16p
	7411 25 7412 25	p 74LS12 p 74LS13	24p 32p 49p		Z80	ACPU 2.99 BCPU 7.99 EMORIES	74HC	40250 40361 40362	1.95p 75p 75p	BD712 BDT62 BDT63	1.50p 99p 98p	TIP42A TIP42C TIP49	62p 65p 1.05p	TIC126B 90 TIC126C 91 TIC126D 92	p ICM7556 1.49 p L200C 1.99	p TDA1011 2.50p p TDA1022 3.75p	SIDED 178 • 240mm	20 pin 20p 24 pin 24p 28 pin 28p
TANT BEADS 1 35V 14 22 35V 14	7414 59	p 74LS20 p 74LS21	43p 24p 24p 24p	4001	19p 211 19p 253		8	40363 40406 40408	3.99p 1.75p 1.75p	BDT65C BDV64A	1.42p 1.37p 2.34p	TIP50 TIP53 TIP54	1.15p 2.35p 2.66p	TIC126M 1.10 TRIACS	LA4400 1.99 LA4420 1.36	p TDA1151 1.95p p TDA1170S 3.10p	420 · 195mm 2.55p	40 pin 30 p
33 35∨ 14 47 35∨ 14 68 35∨ 14	7421 25	ip 74LS27	24p 24p	4006 4007	68p 256 25p 270	4 7.74 B 3.50	74HC		1.99p 4.29p 1.40p	BDV65A BDV65B	2.34p 2.55p 2.65p	TIP110 TIP112 TIP115	67p 76p 70p	Texas 400V TO220 Case TIC206D	LA4440 2.98	p TDA2003 1.95p p TDA2004 2.48p	3.75p DALO ETCH	24 pin 4.35p 28 pin 5.00p
1 35V 14 1.5 35V 14 2 2 35V 15	7425 35	p 74LS32	24p 24p 24p	4012 4013	19p 271 25p 273 25p 273	2 3.99 2A 4.50		40595	1.40p 1.49p 1.99p	BDV93 BDV94	1.05p 1.05p 3.90p	TIP117 TIP120 TIP122	78p 74p 80p	(4A) 79 TIC225D (6A) 88	Dp LA4460 2.98 LA4461 2.98	p TDA2030 1.99p p TDA2611 1.59p	spare nib 1.29p	40 pin 5.35p SWITCHES
3.3 35V 18 4 7 35V 14 6.8 35V 23	7427 39 7428 39 7430 25	p 74LS38	24p 24p 24p	4016 4017	35p 271 52p 272	4 280 2.80 28 250 2.90 56 250 6.50	AT	40871 40872	1.15p 1.15p	BDX33A BDX33C	1.22p 1.26p	TIP127 TIP130 TIP132	84p 1.06p 1.19p	TIC226D (8A) 85 TIC236D	LF351 39	p TDA4600 2.79p p TDA7000 2.49p	PHOTO SENSITIVE	TOGGLE (MIN) SPST 63p
10 16V 21 10 35V 31 15 16V 25	7432 35	p 74LS42	49p 85p 24p	4018 4019 4020	55p 411 55p 411 75p 416	6 150N 1.9	COMPET	AC127	99p 35p 35p	BDX34A BDX34C BDX42	1.22p 1.26p 78p	TIP135 TIP137	1.13p 1.20p	(12A) 1.3 TIC246D	7p LF356 1.09 LF357 1.63	p TL061 62p p TL062 99p	1st Class	SPDT 66p DPDT 79p DPDT C.OFF
15 35V 41 22 6 3V 29	7440 39 7441 39	op 74LS54 op 74LS55	24p 24p 24p 29p	4021 4022		6LP3 1.7 4 3.7		AC128 AC141K AC142K	39p 39p 39p	BDX43 BDX45	83p 85p 4.76p	TIP140 TIP142 TIP145	1.85p 2.13p 1.95p	(16A) 1.40 TIC253D (20A) 2.70	LM10CH 6.99	p TL071 39p	results than	99p 4PDT 3.79p
22 16V 32	7442 85	/415/3	230	I		<u>v 1.5</u>						•	· · ·	<u> </u>				

ETI JULY 1986

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NEWS:NEWS:NEWS:NEWS:NEWS:NEWS

20 Kilo Cycles B arry Porter, one of our regular contributors, will be taking part in a sponsored cycle ride in aid of the British Heart Foundation on Sunday June 15th.

Barry, himself a victim of heart disease, will be joining over 20,000 other people on the London to Brighton run. Among those taking part will be three heart transplant patients, a number of people who have suffered various forms of heart disease and a sprinkling of celebrities including John Peel, Kenny Lynch and Sharon Davies. The ride is expected to raise over £500,000 which will go to fund a research group at Oxford.

The ride is now in its eleventh year. It started as an informal event with just 35 participants and last year attracted over 21,000, many taking part in fancy dress and using some rather unconventional forms of transport. The £400,000 raised went to support cardiac care groups.

The riders will set out from Clapham Common in South London. Groups of about 600 will start at half-hourly intervals from around 6.00 am onwards. Barny's group will be setting off at the more civilised hour of 9.00 am.

The route chosen avoids main roads and runs through Mitcham, Carshalton, South Merstam, Smallfield, Turners Hill, Ardingley, Lindfield, Wivelsfield and Ditching. The ride is not a race, and Barry says he will be delighted to stop and accept liquid refreshment from any ETI readers who happen to be lining the route! He will be wearing an ETI T-shirt which is so new that we can't even tell you what colour it will be.

Anyone who would like to sponsor Barry (either by the mile or a fixed amount for the whole distance) should contact him after 7.00 pm on 0582-65231. The organisers are not accepting any more entrants for this year, but anyone who requires more general information should contact the British Heart Foundation, 102 Gloucester Place, London W1.

We hope to include a brief report of Barry's progress next month



Amstrad and Eric Hammond Among 1986 Award Winners Amstrad have gained the 1986 Brokers who is credited with ha

A TOBIE award for Electronics Application of the Year with their best-selling PCW8256 word processor. The Electronics Personality of the Year award went to Eric Hammond, leader of the electricians union, who aroused considerable controversy last year by allowing his members to operate Rupert Murdoch's printing plant at Wapping in place of print union staff.

The TOBIE awards (Technology Or Business Innovation in Electronics) are announced annually at the British Electronics Week Ball, held this year at the Grosvenor House Hotel, London, on April 29th. The winners are decided by postal ballot among readers of the weekly trade paper Electronics Times.

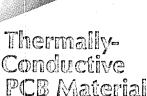
There are three nominees in each category. The other candidates for the applications award were Epson with their Taxi Software package and Plessey who have developed an Adaptive Compass which they believe will form the heart of future vehicle and aircraft navigation and weapon guidance systems. Eric Hammond's competitors for the personality award were Robb Wilmot of European Silicon Structures, a company producing low volume custom chips by direct writing using electron beams, and Peter Fraiman of Electronic

Brokers who is credited with having personally developed the concept of "second user" test equipment.

Among the other awards, the Inmos Transputer was pronounced Component of the Year and Mars Electronics gained the Product of the Year award with their Gazelle automatic circuit tester, a system said to offer the performance and facilities of products costing four times as much. The Research Achievement of the Year award went to Professor Desmond Smith of Heriot Watt University for his work towards developing the world's first optical computer.

British Telecom and Mercury Communications were both nominees in the Fibre Optic Achievement of the Year category but the award went to Cossor for their 213S portable fault locator, a device designed for ease of use and reliability. The award for Export Achievement of the Year went to Rodime, who maintained their export growth record in disc drives in spite of a market slump, and the Distributor of the Year award went to Farnell for their continued record earnings growth.

The TOBIE awards are administered by Evan Steadman Services Ltd, The Hub, Emerson Close, Saffron Walden, Essex CB10 1 HL.



A novel new PCB material from Redpoint offers a solution to some of the cooling problems associated with surface-mounting components. Unlike conventional copper-clad board, it has an aluminium core which conducts away the heat and reduces the risk of local hot spots.

The new material is known as CLEA (Copper Laminated Epoxy Aluminium). It consists of an audminuum sheet which is coated with an epoxy layer. The epoxy provides electrical insulation whilst allowing maximum heat transfer to the aluminium. A copper foil covers the epoxy layer, enabling PCB tracks to be etched using a slightly modified version of the normal PCB-etching process. The material can be cut to shape using conventional metalworking techniques.

CLEA is available in both single and double-sided versions, allowing high component packing densities to be achieved. Redpoint say they expect the material to find applications in both surfacemounting and hybrid assembly processes.

For further details and samples contact Redpoint Ltd, Cheney Manor, Swindon, Wiltshire SN2 2QN, tel 0793-37861.





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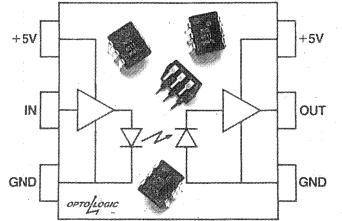
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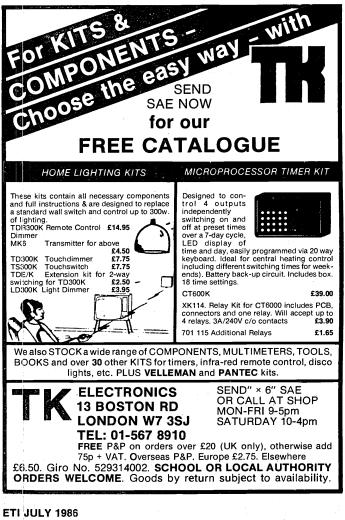
Logic-To-Logic Optoisolator

General Instrument claim their new family of optoisolators will save design engineers a lot of time and trouble. Unlike existing types, they have inputs and outputs which can be connected directly to logic circuitry.

Called Optologic, the new devices are available either as inverters or buffers and offer a choice of TL-compatible totempole outputs or an open collector arrangement for use with 4.5-15V CMOS. Both types have LS-TL-compatible inputs and feature a propagation delay of 60ns.

The new optoisolators come in standard 6-pin DIL packages and have an isolation voltage of 440V RMS continuous, 2500V RMS maximum. General Instrument expect them to be used in level shifting and for interfacing between different logic families. In addition, they could be used to drive power semiconductor devices like MOSFETS.

General Instrument, Optoelectronics Division, Times House, Station Approach, Ruislip, Middlesex HA4 8JG, tel 08956 -36522.



ETI PCB Service

W hisper it quietly, but it does begin to look as though this ill-fated service may yet survive. A new agreement has been thrashed out with our existing supplier and we hope to re-start the service next month.

All outstanding orders should now have been cleared. If anyone is still waiting for a board they have ordered, please get in touch with our Readers' Services department at Hemel Hempstead.

The new arrangement will mean that some boards currently

• Graham Nalty, whose Upgradeable Amplifier is currently running as a project in ETI, tells us that he has access to custom case-building facilities. This might prove useful to those who are still missing parts of the case for their JLLH Audio Design amplifiers. Anyone interested should contact Graham at the address given in the Buylines section on page 40.

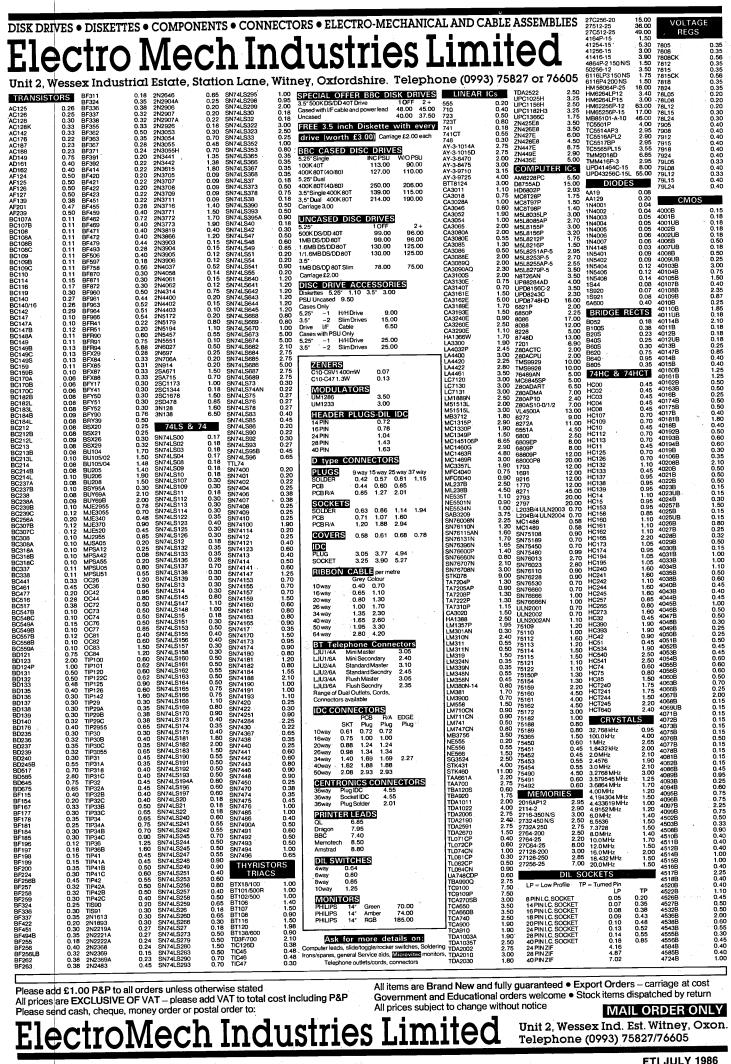
• Spectrastrip is a flat cabling system which allows power, data and telephone wiring to be laid under carpeting with the minimum of trouble. A full set of comon the list will have to be dropped because they are simply not selling in sufficiently large numbers. How ever, we will contine to make sure that boards remain available for at least a year after an article is published.

We have also revised the priceing structure to remove some of the anomalies. At present, some small boards actually cost more than some larger boards. We are adjusting prices up and down as appropriate to rectify this situation.

plementary connectors is also available and the complete system is described in a 4-page brochure which is available on request. Contact Amphenol Ltd, Thanet Way, Whitstable, Kent CT5 3JF, tel 0227 - 26441.

• Hawnt Electronics describe themselves as the UK's leading passive component supplier. Their new 272-page catalogue is now available and can be obtained from them at Firstwood Road, Garret Green, Birmingham B33 OTQ, tel 021-784 3355.

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DIARY

Advanced Infrared Detectors And Systems — June 3-5th

Institution of Electrical Engineers, London. See March '86 ETI or contact the IEE at the address below.

Hitachi Summit Conference - June 9th

Wembley Conference Centre, London. An opportunity to learn about the latest developments at Hitachi from the Japanese design engineers responsible. Areas covered include telecommunications, computing and control, and image capture, processing and display. Tickets cost £29.50 inclusive from Hitachi Electronic Components (UK) Ltd, 21 Upton Road, Watford, Hertfordshire WD1 7TB, tel 0923-46488.

Network '86 - June 10-12th

Wembley Conference Centre, London, For details see March'86 ETI or contact Online at the address below.

Power Electronics and Variable Speed Drives — June 10-12th Institution of Electrical Engineers, London. For details see June '86 ETI or contact the IEE at the address below.

The Scottish Electronics Show — June 10-12th

The Anderston Centre, Glasgow. For details see June'86 ETI or contact Network Events Ltd, Printers Mews, Market Hill, Buckingham MK18 1JX, tel 0280-815 226.

The International ISDN Conference — June 10-12th

Wembley Conference Centre, London. For details see June '86 ETI or contact Online at the address below.

Amstrad Computer Show - June 13-15th

Novatel, London. For details contact Database Exhibitions, 68 Chester Road, Hazel Grove, Stockport, Cheshire SK7 5NY, tel 061-456 8835.

Scottish Technology Week - June 17-19th

The Scottish Exhibition & Conference Centre, Glasgow. For details see June '86 ETI or contact Cahners at the address below.

Computer '86 - June 24-26th

G-Mex Exhibition Centre, Manchester. For details see June '86 ETI or contact Cahners at the address below.

Image Processing And Its Applications — June 24-26th

Imperial College of Science And Technology, London. Conference organised by the IEE at which over 60 papers from 14 countries will be presented, accompanied by technical sessions on image enhancement and restoration, interpretation, industrial and medical applications, etc. For details contact the IEE at the address below.

Association of Professional Recording Studios Exhibition --- June 25-27th

Olympia, London. For details contact the APRS, 23 Chestnut Avenue, Chorley Wood, Herfordshire WD3 4HA, tel 09278-72907.

Radio Receivers And Associated Systems — July 1-4th

University College of North Wales, Bangor. Conference organised by the IEE, the IERE and RSGB in this country in association with a variety of overseas bodies. Topics covered will include general and low-cost receiver design, digital techniques, mobile radio and ICs for telemetry and navigational systems. For details contact the Institution of Electronic & Radio Engineers, 99 Gower Street, London WC1 E 6AZ, tel 01-388 3071.

Voice Processing — July 2/3rd

Wembley Conference Centre, London. Conference. For details contact Online at the address below.

Addresses:

Cahners Exhbitions Ltd, Chatsworth House, 59 London Road, Twickenham, Middlesex TW1 3SZ, tel 01-891 5051.

Institution of Electrical Engineers, Savoy Place, London WC2 0BL, tel 01-240 1871.

Online Conferences Ltd, Pinner Green House, Ash Hill Drive, Pinner, Middlesex HA5 2AE, tel 01-868 4466. ETI

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READ/WRITE

Common Concern

Dear Sir,

I have no wish to continue the Greenham Common controversy — however, I have now read two letters in Read/Write from readers who wish to cancel their ETI subscription because of the article.

The article occupied three-quarters of one page, an informative documentary feature like mercy killing or abortion would be on BBC 2 television — if you don't like it, you turn over. Considering there are 66 other pages to choose from, the majority of which are straight-to-the-point projects without even an SDP abbreviation, I feel anyone who cancels the magazine must be out of their tiny minds. Perhaps, more to the truth, they prefer the £1.20 in their pockets.

It has taken engineers like myself many years to climb the tree of success, over 30 years since my first crystal set in Luton in 1956. Do we stand still or do we progress? Technology and defence are linked and breakthroughs in technology will always find their way into defence. In my son's lifetime Star Wars will make Greenham Common obsolete, just like Fleet Street and black and white newspapers today.

Each month ETI is a chunk of progress — the latest innovations, advances in technology and new products — an Aladdin's cave of new thought every month. If no one dares to include a comment, an article, for fear of offending someone somewhere, what a dull lot and a dull country this would surely become.

Like many readers I have learned more about the nuclear industry in the past weeks than in all my schooldays. Through all the argument, the rights, the wrongs, the facts, one thing has become apparent to me. We are more likely to have nuclear accidents than nuclear war, and in a densely populated country such accidents could have similar effects to a nuclear war. Such accidents as a fire leading to a meltdown in an old-style magnox reactor could cause an explosion which could not be contained. The result, as everyone knows from April 26 in Russia, is pollution of the air, the ground, the rain, the milk and the food chain, leading to cancer and certain death within a 30 mile radius within three days ... in a word, threads. Such an accident would not grant us 700 miles isolation and survival like Sweden. It is also apparent that such an accident whether caused by human error, component failure or terrorist attack could not offer a safe evacuation of the population in time.

The words I can hear are safe; inspected; automatic safeguards; cannot go wrong... everything made by man goes wrong sooner or later and a designer of nuclear reactors says he expects one to blow up every five years (First Tuesday — ITV — May 6).

Perhaps two readers who cancelled ETI are reading this. Yours faithfully, Keith Lawrence B.Sc., Ilkley, West Yorkshire.

Of Caves and Waves

Dear Sir,

Mike Bedford, in his article on the Troglograph cave radio (ETI May '86) has made some fundamental errors in his explanation of the underlying electro-magnetic theory. Of course, it is not necessary to understand the e-m theory in order to produce a working practical design, but I am surprised that you should print and thereby perpetuate certain misconceptions about such a fundamental subject.

Mike states that we normally mean the electric field when referring to radio waves. In fact transistor radios with their ferrite rod aerials, and radio amateurs with their frame aerials are both making use of the magnetic part of the radiated field, which does not in this case drop off under an inverse cube law as Mike states.

Mike also states that the electric field of 'normal' radio radiates in straight lines which is why direction finding (DF) is easier than using the 'eliptical loops' of the magentic field. This is not true. As I have mentioned above the magnetic field can be used for DF with no problem. (Rotate your transistor radio until the signal fades — the direction of the ferrite rod is then at right angles to the direction of the transmitter).

Thirdly, Mike states that electric fields need aerials of the order of a quarter or half a wavelength in order to radiate. Having an aerial of this size certainly helps, but it is not true in the general case and a circular loop can radiate a perfectly good electric field, just as a wire can produce a magnetic field.

I think Mike may be getting confused because what happens very close to an aerial (much less than a wavelength away) is very different to what happens at larger distances in terms of the way the fields behave. The maths is quite complicated, but in simple terms what happens is that a current oscillating in a wire or a loop causes both electric and magnetic fields to be set up in the surrounding medium. The maths can be simplified if we are either very close to or very far from the source of the fields. At large distances the electric and magnetic fields have a fixed relationship, and the magnitude of both (in certain directions at least) drops off with an inverse distance relationship. The result of this is that the power carried by the wave drops off as an inverse square relationship. This field is often called the radiation field, or the far field

At close distances the picture is different. The dominant terms in

LETTERS

the maths do not suggest any power radiation. The electric and magnetic fields behave as if they originated from static conditions and are called the electrostatic and induction fields, or together they are called the near field. For a current loop the induction field drops off as an inverse cube relation, and the electrostatic field as an inverse square. For a short wire aerial the relationships are the other way round. The magnitudes of all the field components depends on a number of factors, and a good approximation at low frequencies is to forget the radiation field altogether.

The field patterns are not quite as Mike states. It would take far too long to explain here why they are different, but it is important to realise that magnetic waves are not inferior to electric waves. It is a question of the orientation of the transmitter and whether it is the near or far field that is important. In Mike's diagram of the magnetic field lines (Fig 2, page 26) the electric field lines would be concentric circles, not straight lines as he states, and for an electric dipole (a short wire aerial) the role of the electric and magnetic fields are reversed — no straight lines anywhere!

A personal view is that Mike is wrong to think that induction loops are used because waves can be somehow 'propagated' more easily than from an electric dipole. Both vertical and horizontal wires can perform well, if not better than a loop aerial. They have disadvantages in that they are less portable, require high voltages and are not (superficially at any rate) as easy to understand as loops. For anyone who wants further information I suggest the book ELF Communications Antennas by Burrows, published by Peregrinus which Mike himself quotes as a reference. A good book on e-m theory is Introduction to

Electromagnetic Theory by Clemmow, published by the Cambridge University Press. Notwithstanding these technical errors, I am sure Mike has done a good job on producing a piece of working equipment, and that is what it is all about really isn't it? And it is pleasing to see an original project of this sort in ETI.

Yours faithfully, David Gibson, Broadstone, Dorset.

We spoke to Mike Bedford, who agreed with the points made in the letter and suggested that the confusion is substantially semantic. Throughout the articles, read electromagnetic field for electric field and induction field for magnetic field. In the context of this project, the induction field is all that need worry the constructor. — Ed.

AUNTIE STATIC'S PROBLEM CORNER

Since the fire at the Chernobyl nuclear reactor, and the consequent rise in radiation levels throughout the British Isles, we have had numerous 'phone calls from ETI readers concerned about health hazards from contaminated food, milk, water, and from the general rise in background radiation levels. The Editor has asked me to devote my page this month to describing some of the ways that radiation can be detected and measured.

It has been known since the end of the last century that some elements are radioactive — that is to say, they spontaneously disintegrate, emitting high energy radiations. The three main types of radiation involved are alpha particles (helium nuclei), beta particles (electrons) and gamma rays (short wavelength radiation, which overlaps the high frequency end of the X-ray spectrum, and continues into very short wavelengths -0.5\AA to about 0.005\AA .)

There are a number of measurements and guidelines to 'safe' exposure to radiation. The most commonly used unit of exposure is the roentgen, R, defined as the amount of radiation that

A portable ratemeter with accessories. It can be used with a Geiger-Muller tube attached to a socket on the front (as shown) or with an extension lead and socket which allows the GM tube to be some distance away from the ratemeter. The applied voltage can be varied from about 350V to 600V to suit different tubes. The remaining controls select the count range and time constant, and a socket is also provided so that a semiconductor alpha particle detector can be used instead of a GM tube.

produces in air, under specified conditions, ions carrying $\frac{1}{3} \times 10^{-9}$ coulombs of charge. Air was chosen because its mass absorption coefficient is almost the same as that for body tissue. The damage caused by radiation will depend on the absorbed dose, the exposure rate and the parts of the body exposed (hands and feet can receive comparatively high doses without permanent injury, whereas the eyes are particularly sensitive). The effects of acute radiation exposure over the whole body (ignoring carcinogenic effects and genetic damage) are: 20-50R, some changes in blood composition; 100-250R, severe illness, but recovery within six months; 400R, fatal to 50% of people exposed; 600R, always fatal. As

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READ/WRITE

far as genetic damage is concerned, there is no safe limit, but the incidence of observable effects rises rapidly for an exposure of more than about 10R over 30 years. From background radiation, the dose to be expected over that time is about 4R, so the margin is very small.

Other units of dosage you may see bandied about in the press are: the rad (radiation absorbed dose) which is 10-5J of absorbed energy per gram of absorbing material, and the rem (roentgen equivalent man) --- a unit which takes into account the fact that different types of radiation cause widely differing amounts of damage for the same absorbed energy. Table 1 gives a rough indication of the RBE (relative biological effectiveness) — a nauseating term — of various types of radiation. Alpha particles, for instance, cause 10 to 20 times as much damage for a given energy as gamma rays. The biological dose in rems is calculated by multiplying the absorbed dose in rads by the RBE for the radiation concerned.

Radiation dosage over a period of time is conveniently measured by a device called a dosimeter. A low cost quartz fibre dosimeter is available from

Radiation Type	RBE
X-ray, gamma ray	, beta ray 1
Thermal neutron	
Fast neutrons	10
Alpha particles, I	igh energy ions 10-20
Table 1 A comp biological effect different types	parison of the relative tiveness (RBE) of

Audio Electronics, 301 Edgware Road, London W2 1BN (tel: 01-724 3564) for around £7. It looks like a fat fountain pen, and clips to your pocket in a similar way, so it can be carried around at all times. At one end is a lens, giving a view of a scale reading from zero to 5R. The ionisation caused by radiation entering the tube gradually causes it to discharge, moving a pointer across the scale, from which you can read your total radiation dosage.

In the current situation, the main concern is the radioactive material which is finding its way into milk, water and food. A dosimeter is of little use in testing radiation levels directly — if you left it next to some contaminated milk for a week, it would no doubt discharge, but by then the milk would be sour anyway. An indication of the degree of contamination can be obtained by using a Geiger counter. ETI's technical wizards came up with the circuit in Fig. 1 when asked to design one on the spur of the moment.

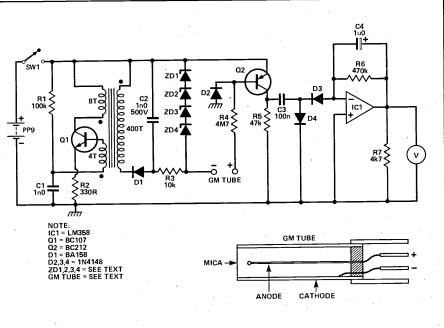


Fig. 1 Circuit diagram of a simple ratemeter.

I should point out that the circuit has not been entirely tested but it should certainly work with the minimum of attention. Strictly speaking, it is a rate meter rather than a Geiger counter, but it would be a simple matter to feed the pulses from Q2 into a counter IC instead.

The coil can be wound on a small pot core, taking a little care over the insulation of the secondary winding, since it produces about 450V. ZD1 to ZD4 are chosen to suit the particular GM tube used — the sum of their voltages should be equal to the tube's recommended operating voltage. GM tubes can be obtained from: Alrad Instruments, Turnpike Rd. Ind. Est., Newbury, Berks RG13 2NS. A general purpose gamma sensitive tube, the ZP1200, costs £45.30 inclusive of postage and VAT.

GM (Geiger Muller) tubes vary in construction, but typically consist of a tungsten anode in the centre of a tubular cathode - a metal cylinder or sometimes a layer of graphite on the inside of a glass tube. The window allows alpha and beta particles to enter with the minimum of obstruction and in some very sensitive tubes the window can be extremely fragile, and may admit ultra violet light, which can also trigger the tube. The best bet is to buy a low cost, general purpose tube, which will be quite adequate for testing your vegetables, and will give fewer spurious readings.

Inside the tube will be an inert gas, often neon, at a low pressure around 10cm. Hg. In circuit, the applied voltage should be high enough to prevent recombination of any ions formed by radiation entering the tube, but low enough not to exceed the dielectric strength of the gas. The tube will have a recommended range of operating voltages, so choose a voltage towards the centre of the range and fit the zeners accordingly.

Any radiation entering the tube will cause ionisation of the gas. Electrons from the ionisation will be accelerated towards the anode, producing more ions by collision on the way. This secondary ionisation produces more electrons, which produce yet more ions, and so it continues in geometric progression. A single charge in the tube will be enough to trigger a substantial discharge which can be detected as a sudden burst of current from the anode, amplified in our circuit by Q2.

The relatively massive positive ions drift more slowly towards the cathode, taking around 100μ S to reach it — by this time they will have considerable energy, and if left to their own devices would cause more electrons to be liberated from the cathode, giving continuous conduction. To prevent this, a quenching agent such as bromine is mixed in low concentration with the neon, to absorb the energy from the positive ions. This process has the unfortunate consequence of making the tube fairly slow to recover after a discharge — around 200 μ s is needed between discharges if the tube is to behave correctly. If the radiation is too intense, the tube will 'freeze' or 'jam' in a state of continuous conduction. This certainly will not happen in testing your milk contamination, but should be borne in mind if using the meter for other ETI purposes. - Auntie.

.FEATURE

SATELLITES -**SETTING THE WORLD ALIGHT?**

With our ever-watchful gaze on the skies, we bring you an update on the world of satellites and the satellites of the world. Keith Brindley supplies the words.

uring the Second World War considerable effort was spent on developing rocket propulsion for purposes other than communications. It became clear that manufactured Earth satellites could exist if rockets could be used to launch them high enough above the surface of the Earth. At the right altitude, a satellite could travel fast enough for its own centrifugal force to counter the tendency of gravity to bring it crashing down. Despite Werner von Braun's best efforts, it was not until 1957 that the first satellite (Sputnik 1) was launched. And it stayed only three weeks in orbit.

The world's first communications satellite (Score) was launched the following year by the USA. Like Sputnik 1, Score only lasted a short time. But satellite communication had been born, some 15 years after it had first been envisaged.

Figuratively Speaking

Since Sputnik 1 there have been an estimated 2,770 launches of satellites. Some of these have been multiple launches, so the total number of artificial earth satellites launched is nearer to 3,000. There are actually 5,560 or so objects, artificial or otherwise, in orbit. They range from the Moon right down to fragments which have broken off other satellites.

Of the 3,000 launched satellites, about 1,000 have come down again. Few of the remaining 2,000 are still transmitting — a realistic estimate of as low as 10% to 15% makes only 200 to 300 satellites functioning at this moment! This is a very difficult figure to establish beyond question because nobody wants to admit to poor design. Authorities on both sides of the Iron Curtain keep this sort of information under their hats, merely ignoring the fact that satellites have failed until everyone's forgotten about them anyway.

Very roughly, there are about 100 new satellites put into orbit each year and about 80% of these are of Soviet origin (a figure which must embarrass the USA). Thought for the day must be why the Soviets launch so many. Is it because they see World supremacy in whoever has the best satellite systems? Or is it because their standard of electronic production is so poor that their satellites have only a very short working life? Or is it because while the USA puts all its efforts into glamorous, high profile pro-jects like Moon landings and shuttles, the USSR prefers a more methodical and less publicity conscious form of space exploration and exploitation?

In any form of satellite communication one of the first problems which has to be overcome is exactly how to transmit to and receive from an orbiting satellite. The satellite must be thousands of kilometres up in space for it to orbit, so transmissions to and from it will be quite weak by the time they have traversed the distances. On Earth, a satellite receiving station (commonly called an Earth Station) must have an acceptable aerial -- or antenna - system to pick-up transmissions. The aerial must be accurately pointed at the transmitting satellite. Easy to say, but not so easy to do!

On The Right Tracks

The problem of ensuring that the aerial always points at the satellite closely and tracks it accurately is fairly complex. The aerial's horizontal and vertical position is commonly varied by steering systems, which are in turn controlled by the strength of the received signal so that, as the satellite traverses the sky, the aerial follows it and makes sure that maximum signal strength is obtained. Such aerial systems are complex, heavy and expensive; not the sort of thing to have on your roof, strapped to your chimney.

Even with this reception restraint, satellites with such orbits are used for a number of purposes. However, there is a way to overcome the tracking problem and make light work of transmission and reception.

This is the so-called geo-stationary orbit, first considered by science fiction writer Arthur C. Clarke in an article in the October, 1945 issue of 'Wireless World'. With brilliant foresight, Clarke realised that the interdependency of satellite orbit radius and period could be used to direct advantage by positioning a satellite so that its orbit radius ensured an orbital period equal to the Earth's rotational period. By putting the satellite in orbit about the Earth's equator, travelling in the same direc-tion as the turning Earth, the satellite would appear stationary to an observer on the planet's surface.

The radius of the Clarke orbit is 42,164km — that is 35,786km above the Earth's surface. Orbital speed at this height is 3,075 metres/second and period is approximately 23 hours and 56 minutes. This period is that of the sidereal day, and not the solar day which is exactly 24 hours. The orbit is, of course, circular.

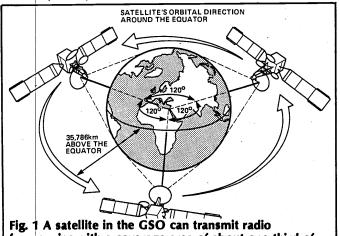
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The geo-stationary orbit (GSO) is of immense value, as earth stations with fixed aerials can be used to receive transmissions from satellites in GSO. This has meant the development of low-cost reception systems aimed at domestic consumers.

The first satellite to be launched to a geo-stationary orbit was the Syncom 3 communications satellite. It reached a geo-stationary orbit on 19 August 1964.

Near And Far

The GSO is quite a long way out in space: about 5½ times the Earth's radius from its surface, in fact. At this distance, a single satellite with a sufficiently wide angle of transmission can broadcast line-of-sight radio frequencies with a 'coverage area' (sometimes called a 'footprint') of about one third of the Earth's surface (Fig. 1). As few as three satellites can be used to cover the whole of the Earth's surface and a signal can be transmitted from one Earth station via a satellite to *any* other station on the Earth's surface. On the other hand, satellites in GSO with narrow angles of transmission can be used to broadcast to precise, fixed areas on the Earth's surface (Fig. 2). Earth stations outside a 'spot-beam' will not be able to pick up the transmissions.



frequencies with a coverage area of about one third of the earth's surface.

In practice, limits on GSO satellite transmissions are reached at latitudes north or south of the equator of about 60° because the angle of arrival of the transmission becomes low enough to be blocked by hills, buildings and other surface features close to the Earth station. Adverse weather conditions at such latitudes may also attenuate satellite signals. This is not to say that satellite transmissions can't be received by Earth stations outside the 60° latitudes, just that larger aerials must be used to pick up the weaker signals, and aerial positioning becomes more critical.

For some purposes, the GSO is not useful. Earth surveillance satellites (that is, satellites with on-board sensors and cameras — more commonly called 'remote sensing satellites') need to be close to the Earth's surface to allow sufficient resolution of details such as weather, crops, drought, pollution, mineral resources and ocean currents. At a tenth of the distance to the Moon, the GSO is just too high.

Latest surveillance satellites can resolve detail down to ten metres and their uses are as yet limitless. If you need to know anything about any aspect of the Earth's surface, a remote sensing satellite could check it out for you easily and quickly — and more often far more cheaply than sending out a team of observers would cost. Remote sensing satellites usually fly a circular polar orbit (Fig. 3) at a height of around only 900km. As the earth turns beneath them, they can survey the whole surface in a few orbital periods (Fig. 4). Earth stations which receive transmissions direct from such satellites encounter the tracking problems previously discussed. The GSO can help here by providing a geostationary data collection and transmission relay, allowing simple earth stations to receive transmissions from satellites in other orbits, without the inherent tracking problems.

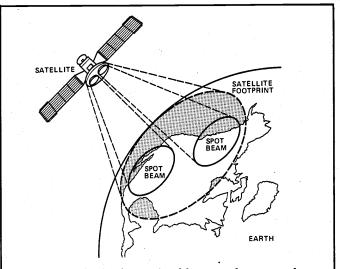
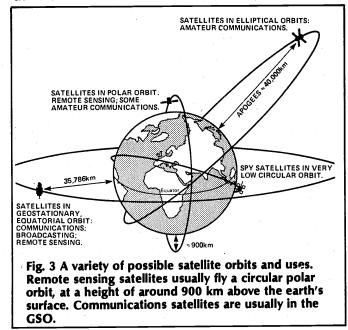


Fig. 2 A satellite in the GSO with a spot-beam can be used to transmit to selected areas of the earth's surface.

Spy satellites are really surveillance satellites in extremely low orbits so that they can resolve the finest of details, even — it is sometimes said — down to the headlines on a newspaper. As a result of such low orbits, severe atmospheric drag is common and spy satellites often re-enter the atmosphere only a few weeks after launch.



There is nothing against the GSO being used to hold a remote sensing satellite (as long as the resolution limits are acceptable), nor is there anything to prevent a circular or elliptical orbit being used to fly a communications satellite (provided that adequate tracking aerials

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are used). It really all depends on the use.

Weather Satellites

There are a number of weather satellites, in various orbits, transmitting on various frequencies. Most of them transmit their data —cloud cover pictures, temperature profiles, sea state, ice precipitation, snow cover and so on — using the automatic picture transmission (APT) system, in which each picture is transmitted in an analogue line-by-line format similar to television pictures but at a much slower rate — 120 lines per minute. A 2400Hz carrier is amplitude modulated between 5% (black) to 80% (white), separated into lines by square wave synchronization pulses. Although these pictures can't be displayed by a basic television, some simple software running on a home computer is all that's required to re-process them to a form that can be displayed in your living room.

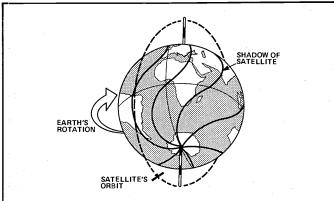


Fig. 4 A circular polar orbiting satellite will cover most of the earth's surface in a number of orbits.

The best type of orbit for weather satellites is polar, circular and at a height of about 900km above the Earth's surface. Tiros (television infra-red orbital satellites) series satellites are launched and controlled by the National Oceanic and Atmospheric Administration (NOAA). The satellites are often known by the 'NOAA' acronym, and there are currently four Tiros series satellites in orbit: NOAA-6, 7, 8, and 9, spread around the Earth. All of them have a 101 minute polar orbit, so pretty much all of the Earth's surface is covered over comparatively short time periods.

As they transmit at a relatively low frequency (137-138 MHz) reception is fairly simple, with few tracking problems as ordinary non-directional dipole aerials can be used. APT pictures consisting of two scenes side by side, one of visible light and one of infra-red conditions, are continuously transmitted by each NOAA satellite.

Similar satellites launched and controlled by the Soviets, known as Meteor satellites also transmit APT pictures in the same frequency band between 137 and 138MHz. Their transmissions are of single scenes in the visible spectrum.

Weather pictures commonly seen on television are usually taken from geostationary satellites, such as Meteosat, which is positioned directly over the Greenwich meridian at 0° latitude. There are a number of similar satellites spaced around the GSO so the whole world is effectively covered. Their data comes not only from on-board cameras, but also from hundreds of data collection platforms (DCPs) on the earth's surface. DCPs can be land-based, sea-based, or airborne. They can be automatic or semi-automatic and they can transmit only when the satellite requests information, on a predetermined weather event.

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Each satellite's correlated data are first transmitted to a receiving and processing Earth station, where they are processed to enhance picture quality and add country outlines together with latitude and longitude lines. The data are then transmitted back to the satellite for general transmission either in a basic APT format or in digital high bit rate format similar to the original format used to transmit the data Earthwards in the first place.

In either case, the pictures are of high quality due to the processing, and in the case of the digital high bit rate format very high resolution pictures can be received. Transmissions are at frequencies around 1.6 to 1.7 GHz, and are outside most amateur reception capabilities. Dish aerials, low noise converters and receivers are available from a number of sources, at prices up to about £2,000 for a system. It's probably better to buy a telly, and wait for the weathermen at those prices!

Amateur Satellites

There are a few satellites in orbit designed mainly for communications and telemetry purposes for radio amateurs and educational establishments. The two satellites of direct interest in the UK are UoSAT-2 designed and built by the Department of Electronic and Electrical Engineering at the University of Surrey (hence UoSAT).

The two UoSATs are sometimes known as OSCAR-9 and OSCAR-11 (Orbital Satellite Carrying Amateur Radio). Both are in polar orbits but at different altitudes, and so have different orbital periods.

OSCAR-9 (UOSAT-1) is at an altitude of 554km with a 95 minute period. OSCAR-11 (UOSAT2) is at an altitude of 700km, with a period of 98 minutes. Both satellites transmit telemetrical data at a frequency of 145.825MHz so reception is fairly simply, with few tracking problems, although Doppler shift takes the apparent received frequency to about 145.830MHz when the satellite approaches, and 145.810MHz as it recedes. Data is transmitted on other frequencies too, up to 10.47GHz in fact.

Another amateur satellite is OSCAR-10, like the other OSCARs but allowing two-way amateur radio communications. Separate uplink and downlink frequency bands are used for this facility, which is known as 'transponding'. The uplink frequency band is from 435.027 MHz to 435.179 MHz and the downlink band is from 145.825 MHz to 145.977 MHz. Any satellite which allows two-way communications uses this principle and has at least one transponder.

OSCAR-10 has an elliptical orbit, which means that the satellite is difficult to track as it approaches and recedes. At its apogee (the furthest point from the Earth, at about 33,500 km), however, the satellite *appears* stationary for quite long periods and simple aerial systems suffice.

There are a number of Soviet amateur satellites (the RS series, standing for *Radio Sputnik*) with transponders and telemetrical beacons in circular polar orbits with periods of around 2 hours.

Broadcast Satellites

For reasons which should be apparent, any broadcast satellite television service must use satellites in the GSO — bandwidth requirements and the number of channels desired mean that high transmission frequencies must be used, which impose tracking problems if the broadcasting satellite is in anything other than a geostationary orbit. At high transmission frequencies, dish aerials are most suitable (although some new designs are currently being investigated and the necessary tracking devices would make the aerial systems far too expensive for general public requirements. A satellite in the GSO, together with fairly small, cheap, fixed aerials are essential — particularly when it comes to direct broadcasting by satellite (DBS). DBS transmissions (to be distinguished from cable transmissions) are meant to be received by individual households. The transmissions will be more powerful than existing cable satellites produce and the recommended dish size is 0.9m.

In 1971, the International Telecommunications Union designated frequencies in the 12 GHz frequency band for use by broadcasting satellites. For frequency allocation purposes the ITU divides the world up into three regions:

Region 1 — Europe, Africa and the Soviet Union;

Region 2 — the Americas;

Region 3 — Australia, Asia including China and Japan. The three regions are shown in Fig. 5. Frequencies in each region are slightly different and have to be shared on an equal basis with other services such as terrestrial broadcasting, fixed and mobile services. The frequency allocations for region 1 is from 11.7GHz to 12.5GHz.

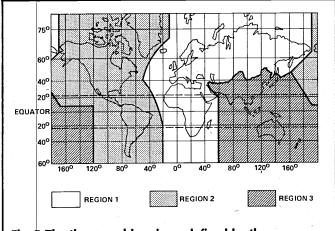


Fig. 5 The three world regions, defined by the International Telecommunications Union, for frequency allocation purposes.

This sharing of allocated frequencies with other services implied that action had to be taken to define exactly which frequencies could be used for which services, so a World Administrative Planning Conference (WARC) was set up in 1977. WARC77 defined the general guidelines which affect broadcast satellite services, such as channel allocations, channel widths, transmitter power and satellite positions in the GSO. These were only intended to be provisional and a number of minor changes to the guidelines have been made since 1977.

WARC77 confirmed that it should be possible to assign five channels to each country in the world by allowing each country a position in the GSO where its satellite could be orbited. The UK's satellite position, for example, was 31° west of the Greenwich meridian, and the five assigned channels were channels 4, 8, 12, 16, and 20, sharing the same GSO position with the Azores, the Canary Islands, the Ivory Coast, Spain, Iceland, Ireland, Liberia and Portugal.

This positioning of the satellite spot at about 30° west of the UK's longitude is typical of most countries and there is a specific reason. Figure 6a shows a non-scale illustration of the Earth, Sun and satellite in normal operation with no broadcast problems. Figure 6b shows the same set-up with a problem: the satellite is in solar eclipse, so that the earth stands between it and the sun. As the satellite circuits are solar powered, it is nonoperational and cannot broadcast.

This situation will occur for a few minutes at or around each equinox in a year (these are the only times when the sun, Earth and satellite are directly in line), at about midnight. By positioning the satellite some 30° west of the country's longtitude, however, the 'midnight' blackout occurs at the satellite's longitude, that is about two hours after the country's midnight or around two o' clock in the morning when few programmes will be broadcast. Future satellites may be battery-backed with rechargeable cells to avoid these complications.

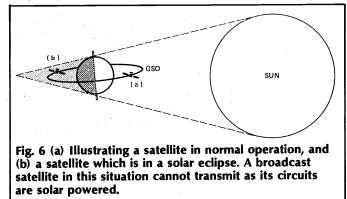
Another solar problem can occur when the satellite is directly between the Sun and the Earth, and an effect known as 'sun outage' takes place when the receiving aerial on the Earth, pointed directly at the satellite, is also pointing directly at the Sun, and so picks up extraneous noise. Again, this effect will happen at or around the two equinoxes for a few minutes at a time, around twelve hours after the eclipses — about two o'clock in the afternoon. There is no practical solution to this problem and broadcasters will simply have to schedule programmes so that no broadcasts are in progress at the time. At the time of WARC77, a number of estimates as to

At the time of WARC77, a number of estimates as to receiver technologies were made, to define transmitter power and aerial size required. Developments in receiver technology have surpassed those estimates, and lower DBS transmitter power or smaller DBS receiving aerials — or both — will be the outcome. All that DBS is now waiting for is an economically viable plan — satellite receiver and aerial technologies are well developed and the major problem is cost. Later this year, France and Germany will be launching DBS satellites — TDF-1 and TVSAT, as they will be called.

Sidestepping DBS

Satellite Master Antenna Television (SMATV) uses satellites built for communications purposes to transmit signals basically for the use of cable television operators. The operators pick up the transmitted television signals and relay them to users on the cable network.

Where television signals like these are received by organisations such as hotels, pubs or clubs, the set-up suffers a name change and becomes a Television, Receive Only (TVRO) system. Communications satellites have transponders of much lower powers than planned DBS satellites (about 20W, against 200W). The receiving SMATV or TVRO aerials need to be significantly larger than DBS aerials (often twice the size at 1.8M or so) and are, therefore, somewhat more difficult to site.



Had suitable DBS systems been developed and started as originally planned, SMATV and TVRO would have stayed exactly where they were intended — in pubs, hotels, clubs, and cable head-ends. But a market has

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grown up around SMATV and TVRO, demanding that it becomes available on a wider basis. The public, it seems, wants the choice which even existing satellite television offers, and never mind the large dish aerials and expensive receivers. For £10, the Department of trade and Industry in the UK will sell you a licence to receive SMATV signals.

To meet this demand (or help create it?) several manufacturers have developed reasonably cheap equipment (£1,000 to £2,000) to receive the transmitted signals and convert them into a form suitable for use with domestic television receivers. At least one manufacturer's equipment is currently available for rental from selected DER television rental shops in the South East of England, on a trial run as a home-reception satellite television system. If the trial is successful, it's likely that the equipment will be made more widely available. As this availability increases it may be perfectly correct to think of the SMATV and TVRO signals as 'broadcast' television — much like DBS signals, but of lower power.

Currently, some 18 television services are 'broadcast' over the UK and Europe using transmissions from two communications satellites, although some of these services double-up and use the same channels. Most of the services are in English, with four or five in Italian, French or German. The more famous of the services are problably The Children's Channel, Premiere, Mirror-Vision, Music Box, Screen Sport, and Sky Channel.

The two satellites, European Communications Satellite 1 (ECS 1 or Eutelsat 1) and Intelsat V are primarily used to trunk telephone calls over Europe and between Europe and North America. They are both in orbit in the GSO — ECS 1 at 13° east of the Greenwich meridian, and Intelsat V at 27.5° west. **ETI**

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MICRO-COMPUTER AIDED CIRCUIT DESIGN

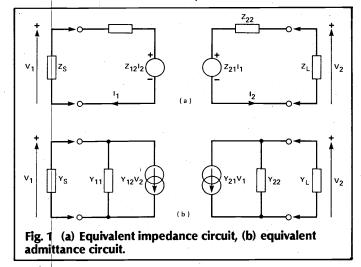
Julian Burt takes us through the first stages of circuit analysis on a home micro by way of nodal admittance and two-port networks.

here are two sides to computer aided circuit design. The main aspect of it is the actual analysis of a designed circuit — information about a circuit is fed into the computer and, after some numbercrunching, the required response is found. This aspect can be split into smaller areas: linear frequency domain analysis (AC small signal analysis); linear DC operating point analysis; and non-linear analysis. In this series we shall mainly be concerned with the first two since the third is a very complex matter hard to implement on a microcomputer.

The other aspect of CACD is the use of a computer to help with the mathematics of circuit design: solving simultaneous equations, numerical integration and so on. We will be looking at circuit transfer functions, which describe the output of circuits with respect to their inputs.

AC Analysis

Perhaps the most important area of circuit analysis is linear small signal analysis. The term 'linear' is used loosely as all components are non-linear to a degree (see Fig. 5). Luckily, they can be assumed linear over a small range of working conditions. We work on this assumption to form a method of analysis for linear circuits.



A large number of circuits are of the two port type: they accept an input of some form and produce a corresponding output, as amplifiers and filters do.

All two port networks can be represented by a simple equivalent circuit. Because the circuit will probably use reactive components, we have to use an equivalent impedance circuit as shown in Fig. 1a. The input of this is defined by I_1 and V_1 , Z_{11} being the input impedance. The output, similarly, is defined by I_2 and V_2 with Z_{22} representing the output impedance:

$$Z_{11} = V_1/I_1 \text{ with } I_2 = 0$$

$$Z_{12} = V_1/I_2 \text{ with } I_1 = 0$$

$$Z_{22} = V_2/I_2 \text{ with } I_1 = 0$$

$$Z_{21} = V_2/I_1 \text{ with } I_2 = 0$$

Standard conversion formulae allow us to convert freely between impedance and admittance parameters:

$$Z_{11} = Y_{11} / \triangle Y / Z_{12} = -Y_{12} / \triangle Y / Z_{22} = Y_{11} / \triangle Y / Z_{21} = Y_{21} / \triangle Y$$

$$Y_{11} = Z_{11} / \triangle Y / Y_{12} = -Z_{12} / \triangle Z / Y_{22} = Z_{11} / \triangle Z / Y_{21} = Z_{21} / \triangle Y$$

$$\triangle Z = Z_{11} Z_{22} - Z_{12} Z_{21} / \triangle Y = Y_{11} Y_{22} - Y_{12} Y_{21}$$

Now we can draw an admittance equivalent circuit (Fig. 1b) where:

$$Y_{11} = I_1/V_1 \text{ with } V_2 = 0$$

$$Y_{12} = I_1/V_2 \text{ with } V_1 = 0$$

$$Y_{22} = I_2/V_2 \text{ with } V_1 = 0$$

$$Y_{21} = I_2/V_1 \text{ with } V_2 = 0$$

These results can be applied to a circuit we are analysing. For example, voltage gain is defined as:

$$A_{v} = V_{2}/V_{1}$$

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where V_2 = output voltage and V_1 = input voltage.

By Ohm's law we know that V = IZ or V = I/Y, so that

$$V_2 = I_2 / Y_{out} = -Y_{21} V_1 / (Y_{22} + Y_1)$$

but A_v is V_2/V_1 . So, if we divide the equation above throughout by V_1 , we get an expression for voltage gain:

$$A_V = V_2/V_1 = -Y_{21}/(Y_{22}+Y_1).$$

The minus sign indicates that input and output currents are shown, in the equivalent circuit, as having opposite directions.

We can do a similar exercise with current gain, $A_i = I_2 / I_1.$

Looking back at the impedance circuit (Fig. 1a) we can see that:

$$-Z_{21}I_1 = (Z_{22} + Z_1)I_2.$$

By rearranging this equation, we obtain an expression for current gain in terms of impedance. By applying the conversion formulae given above, we can express current gain in terms of admittance:

 $I_2/I_1 = -Z_{21}/(Z_{22} + Z_1) = Y_{21}Y_1/(\Delta Y + Y_1Y_1).$ A similar technique will yield expressions for the input and output impedance of the circuit for which Fig. 1 represents two equivalents.

$$Z_{in} = (Y_{22} + Y_{l}) / (\Delta Y + Y_{11}Y_{l})$$

 $Z_{out} = (Y_1 + Y_s) / (\Delta Y + Y_{22}Y_s)$

 $Y_s =$ source admittance and $Y_L =$ load admittance.

From these expressions, it is clear that only values of admittance are needed. Because admittance is a complex quantity, we can also find the relative phase of each result — a subject dealt with elsewhere in this article.

Admitting Everything

We need a way to calculate admittances. One that is relatively simple and well-suited to computers (because it uses a large number of repeated calculations) is called the indefinite nodal admittance method. This involves the formulation of a set of equations which describe the currents and voltages at each node of a circuit.

The simplest way to explain this method is to consider the example shown in Fig. 2 — a basic two port passive circuit. The nodes and components have each been labelled to show their position in the circuit. A set

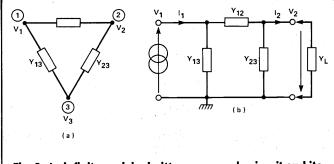


Fig. 2 Indefinite nodal admittance example circuit and its definite equivalent.

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of simultaneous equations to describe the circuit will show currents summed at each node in terms of admittances and voltages measured with respect to an external reference (for example, ground):

$$I_{1} = (V_{1} - V_{2}) Y_{12} + (V_{1} - V_{3}) Y_{13}$$
$$I_{2} = (V_{2} - V_{1}) Y_{12} + (V_{2} - V_{3}) Y_{23}$$
$$I_{3} = (V_{3} - V_{1}) Y_{13} + (V_{3} - V_{2}) Y_{23}$$

These equations can be rewritten in matrix form (Fig. 3).

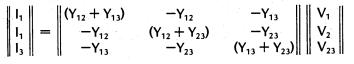


Fig. 3 Indefinite nodal matrix of sample circuit.

The 3 x 3 matrix of admittance values is the nodal admittance matrix that we require and we can observe certain properties of the elements, Y_{nm}, where 'n' is the column and 'm' the row number.

- Y_{nm} = Y_{mn} (the matrix is symmetrical);
 row and column elements sum to zero;
- Y_{nm} equals minus the admittance between **nodes** 'n' and 'm';

 Ynn equals the sum of all admittances connected to node 'n'.

Using the above rules, the formation of the nodal admittance matrix is a trivial task on a computer - and the rules are generalisable. The problem lies in properly organising the data in the first place, but before discussing that we must continue the explanation of the analysis method. A good understanding of this is imperative in order to write useful programs.

So far, we have not taken the frequency dependent component of impedance — reactance — into account. Capacitive reactance is calculated as $1/2\pi fC$ and inductive reactance as $2\pi fL$. To incorporate these elements of impedance, we could produce a nodal admittance matrix for a range of frequencies sufficient to give an accurate picture of circuit behaviour. This is easy to program and memory-efficient but the calculations involved take a long time. An alternative technique, which is quicker but memory-inefficient, is to set up a capacitive and an inductive matrix using $2\pi C$ and $1/2\pi L$ as respective admittance figures. At any given fre-quency, actual admittance is derived by multiplying each matrix (that is, each element of each matrix) by the frequency and adding the two matrices. The result is a generalised indefinite nodal admittance matrix for the purely reactive aspects of the circuit. Mathematically, this is a matrix of imaginary numbers. The corresponding real matrix represents the resistive characteristics of the circuit.

Node Trouble

A circuit with any number of nodes may be represented by an indefinite nodal admittance matrix. The next stage in the analysis is to convert this indefinite matrix into a definite one. This is done simply by assuming one in effect, by defining one node as ground, the reference point for all other nodes.

In the matrix representation, this simply involves removing the row and column corresponding to the voltage we are setting to zero. In the basic example above, we might assume V_3 to be zero with the result that the matrix would now look like this:

I ₁		(Y ₁₂ +Y ₁₃)	-Y ₁₂	V ₁
₂	-	-Y ₁₂	(Y ₁₂ +Y ₂₃)	V ₂
Fig. 4	Input-outp	out conditions.		

Note that rows and columns no longer sum to zero, but that the definite nodal admittance matrix could have been obtained from the indefinite matrix in which rows and columns did sum to zero by applying simple row and column additions. The process effectively demonstrates Kirchoff's laws, and results in a matrix — in the general case — which can easily be further reduced.

The rank order two matrix shown in Fig. 4 can be interpreted as a statement of of the input-output characteristics of the network. I_1 and I_2 are the input and output currents, previously ignored by assuming only internal currents and voltages. This type of matrix is, in general, what we are after since it expresses the equivalent circuit parameters of the original network.

The two port equivalent of more complicated networks can be derived by reducing the appropriate **definite** nodal admittance matrix. The process should be well-known to anyone familiar with the techniques for solving linear equations, and for further mathematical details readers are referred to any comprehensive text on matrices and/or linear equations.

The actual reduction method used here combines Gaussian row reduction with an application of what is known as Cramer's rule. Also involved — notionally, at least — are Kirchoff's laws which allow us the simplifying assumption that the sum of currents in all nodes of a closed circuit will be zero. This means that we can select input and output nodes as appropriate and set every other current value to zero. It helps with the manipulations if we make the input and output nodes 1 and 2 right from the start.

Let's assume we have a five node circuit which provides a definite admittance matrix of the order 4×4 with elements Y_{ij} . Thus:

 $\mathbf{I}_{i} = \mathbf{Y}_{ii} \cdot \mathbf{V}_{i}$

Cramer's rule allows us to determine a value for V_4 and thereby remove it from the system of equations. The new system of equations is described by:

 $I_{i}Y_{44} - I_{4}Y_{14} = Y_{ij}Y_{44} - Y_{i4}Y_{4j} - V_{j}.$

Since I_4 is zero this can be simplified. In general, if we are removing V_k from the system all we have to do is replace each element of the definite nodal admittance matrix, $V_{i\nu}$ by

 $Y_{ij}Y_{ik}Y_{kj}/Y_{kk}$

and then remove the k-th row and column. Assuming that nodes 1 and 2 have been chosen as input and output nodes, we can repeat this replacement in descending order of nodes until we have a simple 2 x 2 matrix which is the two port admittance matrix we are looking for.

In general, any circuit will contain reactive and resistive elements. Indeed, any real component will be both reactive and resistive to a degree — a capacitor has its equivalent series resistance, a wirewound resistor will clearly be inductive, even a length of wire will display some capacitance. Readers may be interested in pursuing this matter by reference to the series of articles by John Linsley Hood which appeared in ETI in 1985. An actual program for nodal analysis will allow the addition of a small capacitance of about 1-2pF in parallel with each resistor. This can be done by adding a notional capacitor to the reactive matrix in the same position as the resistor since we are using admittances. A resistance of about 1-10R might also be included in series with every inductor. The listings provided are not complete programs and are intended to be incorporated into programs written by readers.

Routine Treatment

Listing 1 is a routine to perform basic complex arithmetic. Complex arithmetic utilises so-called 'jnotation' (at least, it does if you're not a mathematician, for whom j-notation becomes i-notation). The central idea is that the phase changes in AC signals introduced by reactive components can be represented by use of the square root of -1. This number is purely imaginary and can only be combined with real numbers to form socalled complex numbers. A complex number is actually no more than a two-dimensional number in which the real part can be thought of as indicating a position along a single horizontal axis at the centre of which we conventionally described as magnitude (or amplitude) and of as indicating a similar position along a single vertical axis. In this way, any position on a plane can be associated with a unique complex number of the form a+jb. The co-ordinates of the position on the plane are, of course, a and b. Fundamental AC signals can also be represented as two-dimensional quantities - conventionally described as magnitude (or amplitude) and phase. These two are actually equivalent to polar coordinates rather than cartesian ones, but the principle is the same.

The square root of -1 itself can be thought of as a single unit along the positive direction of the vertical axis of our plane. A single unit in the opposite direction would be -j. These two, j and -j, represent 90° phase differences with respect to quantities in the horizonal direction. Purely reactive components produce such phase changes so that , for example, the AC current through a capacitor reaches its maximum before the voltage across it. With a resistor, voltage and current change simultaneously — or in phase. With the capacitor, current leads voltage by 90° and with an inductor, currents lags behind voltage by 90°.

100 DEF PROCcomplex(a,b,c,d,type)	
110 LOCAL outa, outb, outc, outd	
120 LOCAL denom	1
130 IF type > 2 THEN GOTO 230 ELSE GOTO 140	
140 IF type = 1 THEN GOTO 150 ELSE GOTO 190	
150REM **** addition ****	
160 outreal = a + c	
170 outimag = b + d	1
180 ENDPROC	
190REM **** subtraction ****	
200 outreal = a - c	
210 outimag = b - d	
220ENDPROC	
230 outa = a * c	2
240 outb = b * d	
250 outc = b * c	
$260 \text{outd} = \mathbf{a} * \mathbf{d}$	
270 IF type = 3 THEN GOTO 280 ELSE GOTO 320	
280REM **** multiplication ****	
290 outreal = outa - outb	1.1
300 outimag = outc + outd	
310 ENDPROC	
320REM **** division ****	
330 denom = $c^2 + d^2$	
340 outreal = (outa + outb)/denom	
350 outimag = (outc - outd)/denom	
360 ENDPROC	
Listing 1 Complex with metic	1
Listing 1 Complex arithmetic.	

We need to take account of these phase changes in determining the impedance of a network. Impedance is usually given as a single figure in ohms, but in fact it is a complex quantity whose real part represents pure resistance and whose imaginary part represents pure reac-

FEATURE: Micro-aided circuit design

Listing

tance. Only the latter is associated with phase changes and only the latter varies with signal frequency.

Very often we will need to combine two complex quantities, expressing impedance or admittance. The rules for handling such combinations follow from the graphical representation of them (or, indeed, from the fact that $j^2 = -1$). If Z = a + jb and W = c + jd, then:

Z+W=(a+c)+j(b+d)Z-W=(a-c)+j(b-d) $Z_xW=(ac-bd)+j(bd+ac)$ $Z/W=(ac+bd)/(c^2+d^2)+j(bc-ad)/(c^2+d^2)$

and the magnitude and phase of Z are, respectively, $\sqrt{(a^2+b^2)}$ and arctan (b/a). Magnitude and phase are dealt with in Listing 2.

100	DEF PROCphasemod(a,b)
110	LOCAL moda, phasea
120	$moda = (a^2 + b^2)$
130	phasea = b/a
140	modZ == SQR(moda)
150	phaseZrad = ATN(phasea)
160	phaseZdeg = DEG(phaseZrad)
170	ENDPROC

Listing 2 Complex phase and magnitude routines

Listings 3 and 4 are routines to handle the reduction of matrices. Matrices are, in effect, equivalents to arrays. The matrix element A_{ij} can be identified with the array element in a BASIC program A(i,j). Matrix manipulation is therefore relatively easy in BASIC. Listing 4 operates about twice as fast as Listing 3, but it is not structured.

Both routines operate on the complete definite nodal admittance matrix of a circuit. That is, the input matrix is assumed to have complex elements and to have already had the 'ground node' taken into account. In fact, rather than have one matrix with complex elements, the routines use two matrices - Yreal(i,j) and Yimag(i,j). The routines need to know how many nodes the matrices have (the variable is 'nodes' and each mat-rix will be of order' nodes' x' nodes'). Matrix elements are held in the relevant array elements and Listing 3, at least, calls the procedure PROC complex from the routine in Listing 1. The result of the 'condensation' process is placed in a reduced matrix with elements Yreal (or Yimag) (1,1), (1,2), (2,1) and (2,2).

	-		
	1000	DEF PROCpivot(nodes)	
	1010	FOR K%=nodes TO 3 STEP-1	
	1020	FOR IX=1 TO KX-1	
	1030	FOR J%=1 TO K%-1	
	1040	PROCnew(I%,J%,K%)	
6	1050	NEXTJ%	
	1060	NEXTIX	
	1070	NEXTKZ	
5	1080	ENDPROC	
	1100:		
	1110:		
	1120:		
	1130	DEF PROCnew(I%,J%,K%)	
	1140	PROCcomplex(Yreal(I%,K%),Yimag(I%,K%),	
	Yreal(K	%,J%),Yimag(K%,J%),3)	
	1150	PROCcomplex (outreal, outimag, Yreal(K%, K%),	
	Yimag(K	%, K%), 4)	
	1160	PROCcomplex(Yreal(I%,J%),Yimag(I%,J%),	
	outreal	,outimag,2)	
	1170	Yreal(I%,J%)≖outreal	
	1180	Yimag(I%,J%)=outimag	
	1190	ENDPROC	
	- • · •		
	Listing	3 Matrix reduction routine.	
	100	DEF PROCpivotcondense(nodes)	
	110	FOR IX=nodes TO 3 STEP -1	
	120	FOR J%=1 TO I%-1	
	130	FOR KX=1 TO IX-1	
	140	<pre>pivota = Yreal(I%,I%)^2+Yimag(I%,I%)^2</pre>	
	150	pivotb =Yreal(J%, I%)*Yreal(I%, K%)	
	-Yimag(J%, I%) *Yimaq(I%, K%)	
	160	pivotc = Yreal(I%,K%)*Yimag(J%,I%)	
	+Yimag()	[%, K%) *Yreal (J%, I%)	•
	170	Yreal(J%,K%) = Yreal(J%,K%)-(pivotb	
	* Yreal	(I%,I%)+pivotc * Yimag(I%,I%))/pivota	
	180	Yimag(J%,K%) = Yimag(J%,K%)-(pivotc	
	* Yreal	(I%, I%) - pivotb * Yimag(I%, I%))/pivota	
	190	NEXT K%:NEXT J%:NEXT I%	
	200	ENDPROC	
	Listing	4 Fast matrix reduction routine.	

m	
g 5 - calculate results	
100 DEF PROCresults	
110 PROCcomplex(Yareal(1,1),Yaimag(1,1),	
Yareal(2,2),Yaimag(2,2),3)	
120 Dreal=outreal:Dimag=outimag	
130 PROCcomplex(Yareal(1,2), Yaimag(1,2),	
Yareal(2,1),Yaimag(2,1),3)	
140 PROCcomplex (Dreal, Dimag, outreal,	
outimag, 2)	
150 Dyreal=outreal:Dyimag=outimag	
160REM ****** input impedance ******	
170 PROCcomplex(Yareal(2,2), Yaimag(2,2),	
YLreal, YLimag, 1)	
180 Areal=outreal:Aimag=outimag	
190 PROCcomplex(Yareal(1,1),Yaimag(1,1),	
YLreal,YLimag,3)	
200 Breal=outreal:Bimag=outimag	
210 PROCcomplex (Dyreal, Dyimag, Breal,	
Bimag, 1)	
220 Creal=outreal:Cimag≖outimag 230 PROCcomplex(Areal,Aimag,Creal,	•
Cimag,4) 240 Zinreal=outreal:Zinimag=outimag	
240 Zinreal=outreal:Zinimag=out1mag 250REM ****** output impedance ******	
260 PROCcomplex (Yareal (1,1), Yaimag (1,1),	
YSreal. YSimag. 1)	
270 Areal=outreal:Aimag=outimag	
280 PROCcomplex (Yareal (2, 2), Yaimag (2, 2),	
YSreal, YSimag, 3)	
285 PROCcomplex (Dyreal, Dyimag, outreal,	
outimag, 1)	
290 PROCcomplex (Areal, Aimag, outreal,	
outimag. 4)	
300 Zoutreal=outreal:Zoutimag=outimag	
310REM ****** voltage gain ******	
320 PROCcomplex(Yareal(2,2), Yaimag(2,2),	
YLreal, YLimag, 1)	
330 PROCcomplex(-1*Yareal(2,1),	
-1*Yaimag(2,1), outreal, outimag, 4)	
340 Avreal=outreal:Avimag=outimag	
350REM ****** current gain ******	
360 PROCcomplex(Yareal(2,1),Yaimag(2,1),	
YLreal, YLimag, 3)	
370 Areal=outreal:Aimag=outimag	
380 PROCcomplex(Yareal(1,1),Yaimag(1,1),	

380 PRUCComplex (Yareal (1,1), Yalma(1,1) YLreal, YLimag, 3) 400 PROCcomplex (Dyreal, Dyimag, outreal, outimag, 1) 405 PROCcomplex (Areal, Aimag, outreal, outimag, 4) 410 Aireal=outreal: Aiimag=outimag

410 Aireal=outreal:Aiimag=outimag 420 ENDPRDC

Listing 5 Impedance and gain calculations.

Listing 5 makes use of PROCcomplex to calculate input and output impedances and voltage and current gain of the circuit under examination. The routine requires two arrays - Yareal (2,2) and Yaimag (2,2) which contain the two port admittance parameters of the circuit derived in Listings 3 and 4. The output impedance is given by 'Zoutreal' and 'Zoutimag' which give the resistive and reactive parts. Input impedance is given by 'Zinreal' and 'Zinimag'. Voltage gain is given by 'Avreal' and 'Avimag' and current gain by 'Aireal' and 'Aiimag'. Magnitudes and phases can, of course, be found by further manipulating these results with the routine in Listing 2.

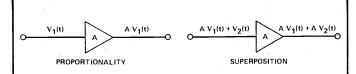


Fig. 5 Superposition and proportionality define linearity.

Next month we will look at equivalent circuits for active components including bipolar transistors, ideal transformers, transmission lines, op-amps and FETs. Following from that, we'll look at methods of writing your own programs and coping with the lack of memory of most home micros — a discussion which will cover such topics as editors, data files and program overlay. ETI

JOBS FOR A CHANGE?

Exchange Resources is an employment agency for people in electronics and computing. It was launched at the British Electronics Week exhibition by the group Electronics for Peace. Tony Wilson, their coordinator, explains how the agency came into existence and what makes it different from existing employment agencies.

S ince Electronics for Peace was founded in 1982, people have been writing in and asking for advice on how to avoid working on military equipment. Many have sent their CVs in the hope that we might find them suitable work. Similarly, response to articles in ETI and other media has demonstrated that there are many people already working in or about to enter the electronics and computing industries who are concerned about the effects their work might have on other people, but who find it very difficult to obtain suitable work without military or other undesirable content. This is particularly obvious when I visit universities, polytechnics and sixth forms.

The picture is no better on the company side of the fence. One company I know has twice developed high tech servo mechanisms for the entertainments industry only to have the MoD take over. Rather than work with the MoD, this company gave their patented rights away. In another case, artificial limb development was delayed for years through lack of funds until the all-powerful MoD took an interest.

One of the effects of this is that people become alienated from their work and end up cynical, inefficient and dissatisfied. Companies find it hard to get venture capital for commercial products, and cannot patent anything without the very serious risk that the MoD will intervene "in the national interest".

I'm not arguing for no defence. I believe that this country needs a strong and effective defence indeed, I work as a consultant to defence companies. I believe I have the right to work in the defence industry and to criticise and try to change it. Our industry is rapidly going down the pan because of the enormous amounts of government money devoted to military research and development. It is well known that military investment is the least productive in terms of jobs, sales and growth, yet the trend continues and our imports increase as our exports decrease.

We don't need so much defence. If we spent one tenth as much as we do, not only would it be more effective than the shambles we have now (useless Nimrod, unuseable Trident, fiasco Falklands, et al), but there would be an extra $\pm 17,000,000,000$ per year in R & D money alone to boost our education, indus-

try and other essentials. That is the only way this country can be strong —not through military posturing but by real economic and social strength.

One of the aims of the new agency is to raise awareness in our industry of these issues. In my work in the defence industry I see people afraid to discuss the issues openly, not only at work but outside too. I want to see people talking openly to decide what kind of future we all want. Government needs a lead from us.

In Good Company

Exchange Resources already has a large number of companies willing to use our services. We are now busy talking to them so that they can be classified on our database according to the type of work they have and their employment policies. People who approach us for work will be asked to describe the sorts of things they are not prepared to do as well as what they hope to do. We will then try to match people with companies. There are some existing agencies who are prepared to work with us in helping to place people, and this is going to be extremely useful in our early days when resources will be stretched. It should mean that we will be at least as effective as the average agency, and perhaps more so. For those who want it, there is a counselling service to help clarify issues and develop confidence in taking decisions (it is not an advice service, although advice will be available).

Existing agencies charge from 10 to 20% of the first year's salary for anyone they place — this can range from less than £1,000 to over £10,000 and would typically be around £2,000. Exchange Resources in its first year of operation will charge only a registration fee of from £50 to £2,000 depending on the size of company and the type and quantity of staff required. So, if we place just one person with a company, they pay no more in registration fees than they would otherwise pay, and after that they effectively get any other staff placements free.

There are several good reasons for us doing this. Firstly, we don't need to advertise in the same costly way as do the other agencies. There are magazines and newspapers which are willing to give us editorial space and once people know we exist they will contact us. Secondly, we regard current fees as extortionate and a burden on British industry. We also want to make it cost effective for people to care about their work and its effects on the world. And thirdly, we're not yet sure of the market, how easy it is to become established, and our effects on it. During this first year of operation, we will be appraising all aspects of our business so that we can set more appropriate rates in our second year. In this we will be aided by a post

graduate research worker whose thesis will be based on us. He will be closely involved from the beginning and will give us regular feedback on our activities.

People who normally work as sub-contractors will receive the usual agency service — they will work via their limited companies and the agency will take a (moderate) slice off the top of their earnings. In addition -will offer we the alternative of working directly for the client, with the agency taking a once and for all fee. There are pros and cons involved for both contractor and client, and these will be spelled out before any agreement is reached.

Of prime importance to the smaller client companies, co-operatives and sole trader partnerships is our business-to-business service. Any organisation or individual registered with us, provided they have paid their annual

fee, will be able to contact us (telephone, telex, electronic mail or facsimile) for assistance in finding a business service. For example, a small software house might need a specific programming skill for a short job, or an electronics company might need inspection and rectification on a large number of PCBs immediately. We will search our database, contact likely people and put them in touch — very quickly. There might be a small fee for this, say ± 5 to ± 20 , depending on the amount of time used and the urgency. This service will be developed into a major way of working for us and our clients — thus building up working relationships between companies and people who share a concern for the way they work and, of course, helping to keep costs down. Very soon this service will be extended to include help with starting new ventures.

There are a number of independent consultants in organisational and personal change associated with the agency, throughout the country. They will work with us to improve efficiency in those client companies who reqire help, and to help staff clarify ethical and inter-personal issues for themselves.

Finally, I should mention that the agency is intended to be in profit in its second year and that all profits generated will go towards furthering the aims of Electronics for Peace and the agency.

In addition to finding staff and positions for client companies and individuals, Electronics for Peace is also trying to find people to run several of its own projects.

The first project is the design and development of a three-waveband (SW/MW/FM) radio that can be

assembled and maintained in Eritrea using the resources available locally. There is a pressing need for each village to have its own radio for news and education. The design arrived at must be battery operated with very low power consumption or use a combination of rechargeable batteries and solar power. Provision for an add-on tape module would be useful. The case should be made from PVC and will be produced in Eritrea using a forming tool which will also have to be designed. Those producing the. radio design will also have to provide full instructions on assembly and test and on maintenance once in use.

If anyone feels they have skills which would be of use on this project and have time available to work on it, they should contact the agency at the address given below. It is hoped that at least one person will be able to go to Eritrea when the project is complete to see

Coming up against the military machine?

the radios being assembled and used.

The second project is the formation of a full-time monitoring team to build up a database on nonionizing radiation emissions in the UK and their effects on people. EfP hope to assemble the core of a team and then raise funding for the work. It is intended that the monitoring should start as soon as possible and preferably within six months. Again, anyone interested should contact the agency at the address below.

Electronics for Peace would also be interested to hear from anyone who has an idea for a project which uses technology in a socially beneficial way.

Tony Wilson and Exchange Resources can be contacted at Townsend House, Green Lane, Marshfield, Nr. Chippenham, Wiltshire SN14 8BR, tel 0225 - 891710.

Discussions have taken place between ETI and Exchange Resources with a view to our publishing a short, regular feature on jobs available through the agency. We would be interested to hear your views on this proposal. ETI



THE ETI DIRECT-ION

Paul Chappell reveals how you can be a magnet for ions and, amid a welter of puns, produces possibly the smallest ionizer ever.

Any claims have been made for the benefits of air ionization. Supposedly, it can cure hay fever and bronchitis, and speed up the healing of burns. Ionization is said to improve concentration, reduce susceptibility to colds and flu and turn insomniacs into deep sleepers. Some of the effects are genuine and well documented; others should probably be taken with a middling to large pinch of salt.

Air ionization is a natural process, and both positively and negatively charged oxygen molecules are abundant in areas far removed from the main centres of population. Mountain tops are the romantic locations used as an example by the health magazines, but similar concentrations can be found in the middle of Dartmoor — or the Sahara desert, for that matter.

The lack of ions in man-made surroundings is easily measurable — it is often found to be less than the natural concentration by a factor of 100 or more. This depletion of oxygen ions is associated with pollution, modern buildings and the use of synthetic fabrics and furnishing materials. It has been blamed for everything from listlessness, depression and absenteeism to violence and murder (See The Ion Effect by F. Soyka for the gruesome details).

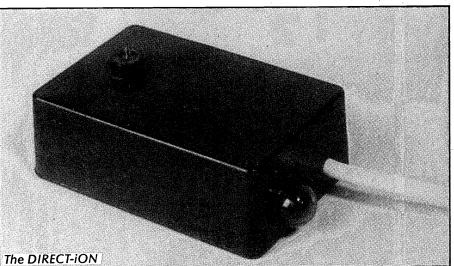
Restoring The Balance

Experiments in restoring the natural ion balance have led to the conclusion that negative ions are the essential 'vitamins of the air'. Positive ions in high concentration have an initial stimulating effect which is followed, after a few hours, by fatigue and possibly headaches. Negative ions have been found to give more profound benefits without any side effects, so air ionizers aim to produce these.

The method used is to apply a high negative voltage, several kV in fact, to a sharply pointed emitter. The negatively charged air is repelled from the emitter resulting in an 'ion breeze' you will feel if you put your hand close to the ionizing point.

There have been several published designs for air ionizers over the past few years, but we achieved by running the oscillator at a fairly high frequency.

In the present design, the ladder runs directly from the mains. This cuts out the first two stages of the usual ionizer circuit. Larger capacitors are needed — 10nF as opposed to 100pF or so but they are not much bigger in physical size. As the circuit uses no active components at all, it is almost certain to work first time, testing is easy, and the final circuit can be run continuously for years without problems.



make no apologies for offering another.

Previous circuits have, almost without exception, been split into three stages. First of all, the main voltage is reduced to 12V or so, then rectified and smoothed. Next there is an oscillator driving a transformer to step the voltage back up to a few hundred volts again. Finally, there will be a ladder of rectifiers and capacitors to step the voltage up to a few kV. The justification for this cumbersome process appears to be little more than the reduction in size of the ladder capacitors,

Rectifiers

Another advantage of running the circuit at 50Hz instead of 50kHz or so is that 1N4007 rectifiers can be used. Ordinary rectifiers are not too keen to work at high frequencies, the main reason being their painfully slow reverse recovery time, which begins to degrade circuit performance at frequencies as low as 2kHz in the case of a 1N4007. Readers may be interested in the results of applying a sine wave of increasing frequency to the circuit shown in Fig. 1.

_PROJECT: Ionizer

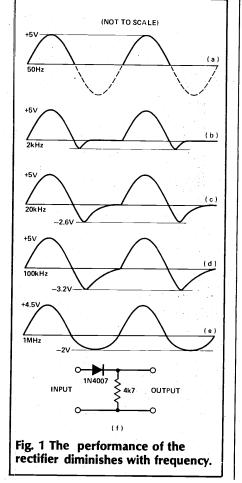
At low frequencies, the output is a half-wave rectified sine wave, as you would expect.

At 2kHz, a small reverse conduction spike is already visible, and by 10kHz conduction does not stop until the input is at about -1.8V. From then on, the situation gets progressively worse. By 1MHz, whatever the 1N4007 is doing, it certainly isn't rectifying!

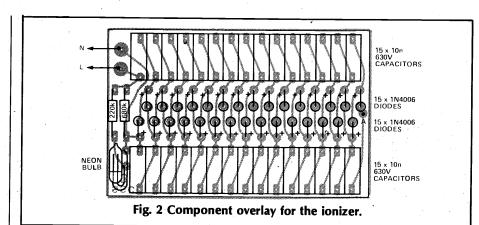
Construction

The 1N4007 rectifiers are mounted vertically on the board in the four central rows of holes and the capacitors are mounted in the outer rows as shown in the component overlay (Fig. 2). Figure 3a is an end-on view of the board with the first rectifier and capacitor in each row shown. All the rectifiers are mounted the same way around.

When putting in the capacitors, you will find it easier to put them all in place at once and hold them with a rubber band while soldering, rather than trying to solder them in one at a time. If any capacitor seems a tight fit, just turn it around — sometimes the leads are not quite central.



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After soldering in the resistors, insulate about 1/2" of each lead of the neon bulb. Solder the bulb in place and, if you are using the recommended case, bend the leads so that it lies roughly parallel to the board as shown in Fig. 3b. If you are using a larger case, you may prefer to let the neon poke through, or to use a complete neon lamp assembly.

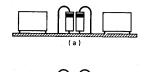
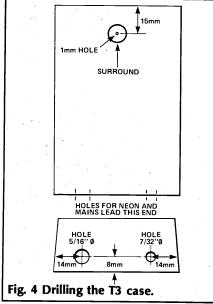




Fig. 3 End views of components on PCB.

Assembling The Case

Drill two holes in one end of the case (fig. 4). If you are using the recommended case, follow the instructions with regard to positioning. If you use a larger



case, you can easily determine your own hole positions. To position the holes

accurately, you will find it easier to drill a small hole first, then widen it out with a larger drill to the correct size. A small hole is also needed at the top of the case, half way between the two sides and 15mm from the undrilled end (Fig. 4). This hole can be made with a 1 mm drill if you have one, otherwise you can hold a soldering iron to the lead of one of the 2M7 resistors and use the hot wire to melt the hole. The hole needn't be perfectly tidy as it can be covered up later, but don't let the soldering iron touch the case.

The hole is meant for the actual emitter — a length of pointed wire. You may, of course, use an emitter of your own devising. As long as it includes sharp points and is conductive, practically anything will do.

The neon cap is glued in place over the left-hand hole of Fig. 4a. A surround may be glued around the 1mm hole in the top of the case to obscure the emitter. Use quick

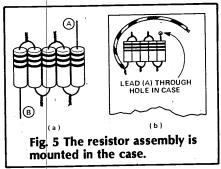
PARTS LIST

FAKIS LISI						
RESISTORS (%)	W, 5% unless stated)					
R1	680k					
R2	220k					
R3-R7	2M7 ½W					
CAPACITORS	10nF 630V					
C1-C30	polyester					
SEMICONDUC	TORS					
D1-D30	1N4007					
MISCELLANEOUS Neon bulb and suitable cap; emitter surround; length of mains lead; insulated connecting wire; insulation; 10nF 100V capacitor and neon bulb for tester; (71x46x22mm); epoxyresin; mains plugs; optional emitter (for example, PB22 phosphor bronze rifle cleaning brush available from gunshops).						

31

setting epoxy resin for all the gluing operations.

Join the five 2M7 resistors together by twisting the leads soldering and then trimming (Fig. 5a). Glue the resistor assembly to the inside of the top of the case with lead A protruding through the 1mm hole. This lead will form the emitter, or it may be soldered to a needle or other suitable object for example, a phosphor bronze rifle cleaning brush makes an attractive and efficient emitter. Solder about 3" of insulated wire to lead B, and trim the lead (Fig. 5b). Cover the resistor assembly and exposed leads with epoxy for insulation.



Final Assembly

First, check the board carefully. Make sure that all the rectifiers are the right way around and all the components are in the correct position. Check the track side of the board for solder bridges, and also for excess flux residue, which could provide a leakage path clean it off if necessary. Solder the free end of lead B — the wire jointed to the 2M7 resistors — to point X in Fig. 2.

Remove about ³/⁴ of the white outer sheath of the mains cable and about 3/16" of insulation from the conductors and solder the live (brown) wire to point L and the neutral (blue) wire to point N on the overlay. Be sure to get the mains lead connections the right way around, and don't remove more of the outer sheath than is absolutely necessary.

There is no room for mounting screws in the recommended case, so the PCB has been made for a tight friction-fit into that case. First of all, cut the corners of the PCB as shown by the dotted lines on the overlay. Feed the mains cable through the right-hand hole (Fig. 4a) and gently ease the board into the case with the component side facing outwards. Push down on the board gently until it is held in place by the pillars at each corner of the case. Adjust the position of the neon bulb so that it is behind the neon cap, but don't try to push it through the hole. Screw the base of the case in position, attach a plug with a 1A fuse to the mains lead, and straighten the emitter wire so that it points vertically upwards. Using a pair of side cutters with the flat face upwards, cut a small amount of wire from the end of the emitter. Using the cutters 'upside down' should leave the emitter wire with a sharp point at the end.

A larger case would be easier, if less elegant. The board can be fixed in a larger case with three or four blobs of epoxy, after testing.

Testing

A simple tester can be made from a 10n capacitor and a neon bulb (see Fig. 7). Plug in the ionizer, and, if all is well, its own neon lamp will glow and a few seconds later you will hear a soft hiss from the emitter. If you hold one lead of the tester and move it towards the tip of the emitter, the neon bulb on the tester will begin to flash when its free lead is about 1/2" from the emitter, and will flash faster as it is brought closer. The flashing is not very bright, so it won't show up in direct sunlight.

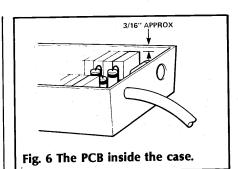
If the ionizer's neon does not light, unplug the unit at once and touch the mains plug to the emitter to remove any residual charge on the capacitors. Check the mains connections, plug fuse and wiring of the 220k resistor and neon lamp.

If the ionizer's neon glows, but the emitter does not hiss and the tester's neon does not flash, retrim the emitter wire and try to get a sharper point on it. If there is no sign of activity at the emitter at all, check again for a solder bridge between tracks, one of the diodes back to front, a short circuit in one of the capacitors, or a dry solder joint.

Finishing Touches

After testing, discharge the ionizer and remove the base. Take out the PCB and spray a few coats of anti-corona compound onto the back. If you do not have any anticorona spray, a smear of epoxy resin will do instead.

Place the board in its case. Now is the time to glue it in position. If you're using the recommended case, press the PCB down until the tops of the capacitors are about 3/16" below the rim of the case (Fig. 6). Run



some glue onto the corners of the board and put some around the mains lead where it enters the case. For the sake of safety, the mains lead must be held firmly in place, so use a strain relief grommet, too, if you have room.

If you've got plenty of epoxy, fill the neon cap with it and cover the neon bulb. This gives a pleasant, diffused light when the neon glows.

Now, screw the case together. Fill the emitter surround with epoxy (this is not essential, but it improves the appearance) and trim the emitter wire to about %" above the top of the surround (making sure to get a fine point on it).

Life With The lons

Although some of the benefits of air ionization may be exaggerated, the action itself is real enough. One consequence of using an ionizer is that the ions attach themselves to any dust, dirt or pollen particles that happen to be floating around. The dust then becomes charged and will be attracted to the nearest netural surface, which by sod's law will be your expensive new wallpaper!

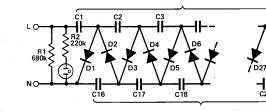
If you intend to run the ionizer within a foot or so of a wall, cover the wallpaper in some way. otherwise, try to position the ionizer towards the centre of the room where any charge dust will end up on the floor and be swept up during normal household cleaning.

The action of precipitating particles from the air is of immediate benefit to asthma and hay fever sufferers, no matter how beneficial the ions themselves may be. If you live in the country and think the air is pure and clean anyway, just try running the ionizer close to a sheet of white paper for a few days!

Improvements in health due to ionization occur over several days, rather than at the first breath of ionized air. The best position for an ionizer is by the bedside, where

PROJECT: Ionizer

The circuit is a standard Cockroft-Walton ladder network which steps up the mains voltage to -10kV or so (open-circuit voltage) (Fig. 7). The operation of this type of circuit was described in detail in 'Designer's Notebook 1' in ETI, April 1983. The idea is that charge is transferred backwards and forwards from one row of capacitors to the other on each mains cycle, but always moving further up the chain because of the action of the rectifiers. When all the capacitors are fully charged, there will be a voltage across each of them equal, in theory, to the peak-to-peak voltage of the mains.



vou will breathe in the ions for eight hours or so at a time. This design is easily portable and you can move the device into the living room, or take it to work with you during the day.

By the time you are bouncing with health and energy, you'll probably be wondering what else you can do with an ionizer. Here are a few suggestions.

Ion ImPlants

Connect a length of wire from the ionizer emitter to the soil in the pot of a houseplant — one with sharp pointy leaves is best. Stand the plant pot on an insulating mat or polythene bag. The plant is now the emitter. If you hold a finger close to one of the

leaves, it will be drawn towards you. In the dark you may be able to see a faint blue glow around the leaf tips — this works better with some plants than with others, so try several different types. The plants don't mind at all, by the way, and often seem to thrive on it!

Got My Ion You

D28

╢

D29

Carefully remove about 6" of insulation from some stranded wire, being careful not to break any of the strands. You will probably find it easier to remove the insulation in short lengths rather than all at once. Cut the wire 1" below the end of the insulation so that you are left with 7" of wire - 6" bare and 1'

pin diameter of 0.8mm, spacing of 10mm

and length of 6mm. The key point to remember if using a replacement is to

ensure that discharge cannot occur

between capacitors. The recommended

from Farnell, through Trilogic, 29 Holm

Lane, Bradford BD4 0QA (tel: 0274

684289). The Verobox 301 is slightly

larger and is widely available as are any

number of ABS boxes capable of holding

the circuit. The box must be made entirely

of plastic (with the exception of the fixing

screws). Neon bulbs are stocked by

Maplin and complete neon indicators are widely available. A complete kit of

parts can be obtained from Specialist

Semiconductors, Founders House, Red-

brook, Monmouth, Gwent for £9.50+60p

postage and packing. The PCB alone will

become available from our PCB service

as soon as normal service is resumed.

case is available from Greenweld -

BUYLINES

This project is simple enough to cause few problems to the constructor wishing to do everything themselves. The biggest problem is undoubtedly the 10n 630V capacitors which are intended to be Plessey/Arcotronics Minibox polyester types. These are probably unobtainable for the constructor but can be replaced, especially if you do not intend to use the existing PCB. Suitable replacements include 10n 600V polyesters from Greenweld, 443 Millbrook Road, Southampton SO1 0HX (tel: 0703 772501/783740) and almost physically identical metallised polycarbs from RS Components available through Crewe-Allen, Scrutton Street, London EC1. The order code for the 10n 630V version (supplied in packs of five) is 113-673. A variety of axial capacitors are available which could be used, but for reference the dimensions of the recommended capacitors are 13x9x4mm (LxHxW) with

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HOW IT WORKS

In practice, the regulation of this type of circuit is very poor, and the full voltage will never be measured because of leakage, corona discharge, and so on. The tiny ionizing current is quite enough to drop the output voltage to about -4kV, which by a strange coincidence is the ideal output voltage for an air ionizer. Voltages above this level tend to produce ozone rather than ions, whereas voltages much lower will not ionize the air efficiently. Touching the emitter directly is enough to reduce the voltage to almost nothing.

Having said that, a word of warning is in order. Although the circuit as a whole

D30

R3 2M7 R4 2M7 IONISING EMITTER

R5 R6 R7 2M7 2M7 2M

has a very high output resistance, individual capacitors don't, so please take care when you are testing the circuit. The capacitors will retain their charge for some time after the ionizer is unplugged, and a painful shock can be received from the back of the PCB if you are careless. The shock will be harmless to the average healthy ETI reader, but please don't take any risks if you have a weak heart or a pacemaker. The ionizer can be discharged by touching the mains plug to the ionizing point for a few seconds, and just to be certain you can run the mains plug down the line of diode connections on the PCB.

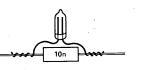


Fig. 7 Abbreviated circuit of ionizer and a simple tester.

insulated. Use this as the emitter. When the ionizer is turned on, the emitter seems almost alive and will leap about when you put your

Ion Brew

hand close to it.

Fill a glass jar with cigarette smoke, then carefully invert it over the ionizer so that the emitter is inside the jar. Within seconds, the smoke will disappear! This is one of the best demonstrations of ionizer action and with a large jar the effect is quite dramatic. The smoke hasn't disappeared, of course, it is still sticking to the jar but the air inside the jar is perfectly clear.

Ion A Feeling

Wear rubber soled shoes. Touch the ionizer emitter for a few seconds until your body is thoroughly charged up. When your hair stands on end, that's just about enough. Then give everyone you meet a jolly electric handshake. Just think, you could lose all your friends in a single evening! (A meaner trick still is to charge up a glass of water or a pint of beer ... Even your family won't speak to you after that!)

Electrostatics Experiments

The ionizer can be used as an EHT generator for electrostatics experiments. To generate a positive voltage rather than a negative one, just reverse all the rectifiers. ETI

MOTOR INTERFACE

soldered into nine holes (labelled D0 to D7 and GROUND) on the circuit board.

If the power for the interface is to be obtained from the auxiliary socket of the BBC micro (the one used for disc drives), then the leads and connectors, shown in Fig. 3, must be made up. This allows the interface to obtain 12V

This simple circuit and software, developed by Peter Timothy and Jonathan Rabet provides a painless but versatile first step in robotics.

his interface was developed with the help of the Microelectronics Education Programme, Wales, and provides a means of controlling up to four low-voltage DC motors from a Centronics-type printer port. The motor supply is variable between one and ten volts, so the interface may be used with most common types of model motors, including those made by Lego, FischerTechnik and Robotix.

The interface is fitted with connections for the BBC microcomputer range and can be powered from any suitable 12V supply, including the BBC micro's auxiliary output. Although software for the BBC is included in this article, it would be an easy matter to adapt the programs so that the interface could run off any computer with an 8-bit data output. Using a printer port would allow handshaking to be incorporated as an elementary form of simple feedback.

We'll get down to building the interface without further ado.

Construction

It is best to start by constructing the motor connector and cable, since this requires the use of potting compound. It can be set aside to harden while the rest of the circuit is being completed. The method of construction is shown in Fig. 1. The leads from the base of the PCBtype screw terminals pass through holes drilled in the potting box and will be firmly anchored by the potting compound once it sets.

The completed assembly is small enough to mount on almost any model or robot. At least one métre of ribbon cable should be used since this will allow the cable to stretch from the interface box, normally situated alongside the computer on a bench or table, down to floor level. The screw terminals provide an easy method of connecting up most types of motor and they will accept unmodified Robotix motor lead plugs. The PCB end of the cable will eventually be soldered into eight holes (labelled A to H) on the circuit board.

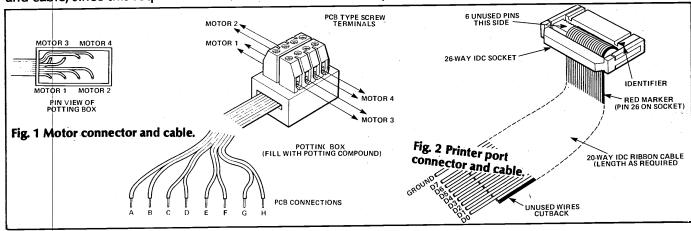
The printer port cable assembly is constructed as shown in Fig. 2 Only nine of the 20 wires are actually used, but the fitting of the IDC socket is made easier by leaving all the 20 wires intact at that end. The other end of the cable must be dressed as shown. This end will eventually be from the computer without disconnecting other devices. If the interface is to be powered from any other supply, it should be capable of delivering at least 2V more than the motor voltage at about 1A DC.

The main circuit components are mounted on a single-sided printed circuit board (Fig. 4). Solder the 11 wire links into position on the compound side of the board. Next insert the IC sockets (if you are using them), making sure that all the pins pass through the holes in the board. Insert the diode, D1, and capacitors C3, C4 and C5, making sure that each is the right way round. Solder these components in place.

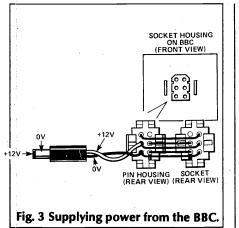
Insert and solder resistor R1 and capacitors C1 and C2. Next insert and solder the variable resistor RV1, noting that the hole nearest to R1 is for the centre pin. Solder IC1 and 2 into position,

Solder IC1 and 2 into position, taking care that the flat, all metal surfaces are nearest the top edge of the PCB. Now insert and solder the eight relays. These are all identical and can only be fitted one way round.

Solder the wires of the printer port cable to the PCB, then the motor connector wires and the



PROJECT



power leads from the 2.1mm socket.

Before inserting IC3 and IC4 into their sockets, apply 12 volts DC to the board and check that the voltage regulators are working correctly. IC1 may be checked by measuring the voltage between the links marked 'X' and 'Y' on Fig. 4. This should be 5V. IC2 may be checked by measuring the voltage between link 'X' and point 'Z' on the overlay. By adjusting RV1, this voltage should be variable from about 1.25V to 9.75V. Now adjust RV1 until the voltage matches the rating of the motors to be used with the interface. Once satisfied with the operation of the voltage regulators, remove the power and insert IC3 and IC4 into their sockets.

Software

The programs included here have been written in BBC BASIC, a dialect of BBC BASIC called MEP **CONTROL BASIC, BBC 6502** Assembler and Logotron LOGO. They will enable the interface to be used with almost any BBC micro. The BBC BASIC program is menu driven and allows motor sequences to be recorded and repeated (Listing 1). The three programs in other languages are simpler but can be incorporated into more complex structures to enable sophisticated control of up to four motors.

In general, motor control is achieved by writing to the printer port of the computer to which the interface is attached. The idea of using the printer port is to allow an existing user port to provide digital *inputs* — for example, in case an elaborate robotics system is being contemplated.

On the BBC, the eight lines of the parallel printer port are written to via location FE61H. Two bits are needed for forward and reverse

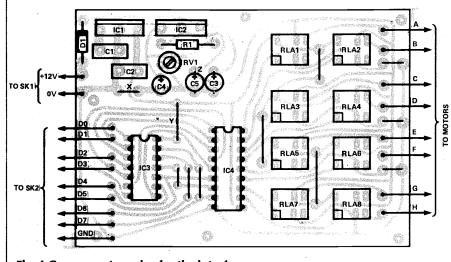


Fig. 4 Component overlay for the interface.

PARTS LIST.

RESISTORS		MISCELLANEOUS
R1	240R 1/2W	RLA1,2,3,4,5,6,7,8 microminiature
RV1	2k0 cermet	relays 5-6V coil, SPST 24V 1A con-
CAPACITORS		tacts; 26-way IDC socket (BS9525)*; 6-
C1	330n ceramic	way Multicon plug and socket
C2	100n ceramic	housings and plug and socket pins to
C3, 5	4µ7 16V tant	order*; 2.1mm panel-mounting power
C4	22µ 16V elect	socket; 2.1mm power plug; 4-way PCB
SEMICONDUCT	ORS	mounting screw terminal; small potting
IC1	7805	box; 14-way and 18-way IC sockets;
IC2	LM317MP	20-way ribbon cable; 8-way ribbon
IC3	7486	cable; potting compound; PCB.
IC4	ULN2803A	(*Optional, for use with BBC and BBC-
D1	1N4001	type computers).

and are decoded as shown in Fig. 7. The more significant of the eight bits repeat the Fig. 7 pattern for motors 2, 3 and 4.

BBC Basic

On running the program (Listing 1), the user is presented with a menu consisting of three options: PROGRAM, RUN or MANUAL (lines 70 to 290).

Selecting the MANUAL option results in direction to PROCmanual. The screen displays the message: MANUAL CONTROL. The red function keys are used to control up to four motors connected to the interface, each key switching one relay when pressed and f9 returning program control to the main menu.

Using INKEY with a negative argument (lines 790-870) tests for specific key presses and allows simultanteous control of more than one motor.

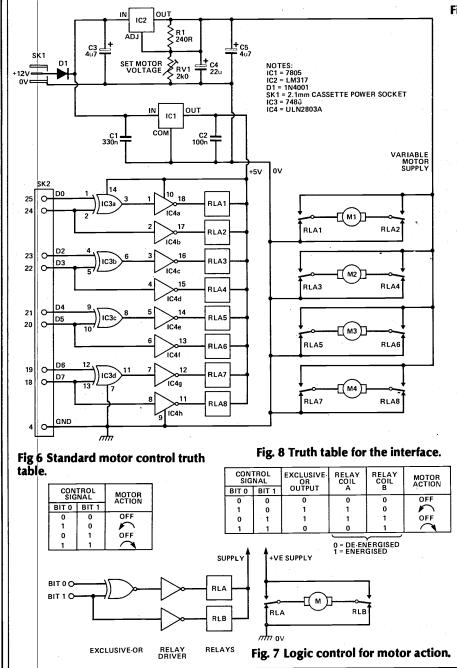
If the PROGRAM option is selected from the menu, PROCprogram is jumped to. This also detects presees of the function keys and writes to FE61H to drive the motors. This procedure differs from PROCmanual in that the length of time a particular combination of keys is pressed is stored in the array, steptime%(X). The contents of FE61H at that time are stored in the array, step%(X).

Selecting RUN from the main menu results in a jump to PROCrunmenu. This procedure puts up a secondary menu allowing selection of one of two run options or a return to the main menu. Option one runs the programmed sequence of operations as they were entered by the user — including any pauses when FE61H was at zero and no motor was on. The second option filters out these 'wait states', producing a smoother execution of the pre-programmed sequence. Both of these options invoke PROCrun, which loops through the array, step%(X), and uses the associated variable, steptime%(X), to provide a delay. If the parameter passed to PROCrun is FALSE, all occurences of step%(X) = are skipped over.

Control Basic

This is an example of a program written using a BBC Basic

HOW IT WORKS.



extension (Listing 2). This extension allows programs to be simply written which access the Printer and user ports of the BBC microcomputer.

The example program controls a single motor using function keys f0 and f1. The Control Basic keyword, BIT, permits each bit of the printer port to be controlled individually.

Machine Code Motor Driver

The assembler program (Listing 3) will produce a machine code driver for the interface. It can be assembled at any suitable address and occupies 42H bytes. Once assembled, it can be used by POKEing the motor number (1, 2, 3 or 4) into location 70H and the motor action (0=off, 1=forward, 2=back) into 71H, then calling the driver at the address at which it was assembled using CALL.

The program as listed contains a BASIC routine to test the machine code driver. If this program is run, the assembled driver can be saved to disc or tape with

*SAVE DRIVER 3000+42 then load back with *LOAD DRIVER and called with CALL&3000.

Fig. 5 Circuit diagram of the interface.

Two voltage regulators are used in the interface (Fig. 5). IC1 takes the incoming 12 volts and series regulates it down to 5 volts. This fixed 5 volts is then used to power the logic (IC3), the relay driver (IC4) and the relay coils. IC2 is another series regulator, but this time it is a variable output device. Its output is determined by the voltage applied to the ADJUST Pin, this voltage being derived from a potential divider, formed by R1 and RV1, connected across the output. The output voltage is adjusted to suit the motors being used. All five capacitors are for decoupling. Diode D1 protects the regulators should a reverse polarity supply be accidently connected to the interface.

The computer generated signals to control the motors are passed from the computer to the interface via the printer port. Eight data lines (D0 to D7) are available at this port and, since each motor used requires a two bit control signal, a maximum of four motors can be controlled simultaneously without using multiplexing techniques. In common with a large number of industrial motor control systems, bit 0 of the signal determines if the motor is on or off, while bit 1 determines the direction of rotation of the motor. The truth table for this scheme is shown in Fig. 6.

The chosen method of implementing the control scheme is shown, for one motor, in Fig. 7 and its new truth table is shown in Fig. 8.

IC3 is a quad 2-input XOR-gate. IC4 is an octal Darlington driver used to energize the eight relay coils. This device acts as a buffer between the computer's data lines and the relay coils. A buffer is required because each coil takes approximately 40mA when energized — enough to overload a data line or an XOR output.

The relays are ultra-miniature, singlepole change-over, with contacts rated at 24V, 1A DC. Most model electric motors take much less than 1A even on start-up under load.

By specifying a hex address at the end of the *LOAD command, the driver can be reloaded to any suitable location and run by a CALL to that same address.

BUYLINES SOFTWARE

MEP Control Basic is available from MEP (South Yorkshire and Humberside), Exeter Road, Off Coventry Grove, Doncaster DN2 4PY. Logotron Logo is available from Logotron Ltd., Ryman House, 59 Markham Street, London SW3 4WD.

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PROJECT: Motor Interface

Listing 1 BBC Basic.

10REM*****BBC BASIC MOTOR CONTROL***** 200N ERROR GOTO 70:REM RETURN TO MENU ON ESCAPE 50 70REPEAT 80 80 90REM*****DISPLAY MAIN MENU***** 100MODE 7 110PRINTTAB(16,2) "MAIN MENU" 120PRINTTAB(16,3) "********* 130PRINTTAB(16,10) "1...FROGRAM" 140PRINTTAB(16,12) "2...RUN" 150PRINTTAB(16,14) "3...MANUAL" 160 170REM*****ENTER MENU SELECTION***** 1801NFUT TAB(16,22)"CHOICE :"q% 190IF q%<1 OR q%>3 GOTO 100 200 210REM*****DISPLAY OPTION SELECTIUN***** 220REM*****AND BRANCH TO PROGRAMING***** 230REM****MANUAL OR RUN MODE*********** 250IF q%=1 PROCprogram 260IF q%=2 PROCrunmenu 270IF q%=3 PROCmanual 280 290UNTIL FALSE 300END 310 ्रत्व 35Ø 36Ø 370DEFPROCprogram 380REM********** 380RLEM*********** 390LCCA a%,olda% 400olda%=0 410count%=1 420TIME=0 430CLS:PRINTTAE(15,10)"PROGRAMMING" 440a%=0 450 IF INKEY(-33) AND NOT (INKEY-114) a%=a % OR 1 460 IF INKEY(-114) AND NOT (INKEY-33) a%=a % ()R 470 IF INKEY(-115) AND NOT (INKEY-116) a%= IF INKEY(-116) AND NOT (INKEY-115) a%= a% OR 12 IF INKEY(-21) AND NOT (INKEY-117) a%≠a 490 % OR 16 500 IF INKEY(-117) AND NOT (INKEY-21) a%=a 7 OR 48 510 IF INKEY(-118) AND NOT (INKEY-23) a%=a 2 0 520 IF INKEY(-23) AND NDT (INKEY-118) a%≠a % 0R 192 530 IF INKEY(-120) ENDPROC:REM RETURN TO M ENU ON f9 S40IF a%<>olda% ?%FE61=a%:steptime%(count%) =TIME:step%(count%)=olda%:olda%=a%:TIME=0:c ount%≈count%+1 55060T0 440 560 570 590DEFPROCrun (a%) 590REM********** 650REPEAT 050KFEA1 6607%FE61=step%(i%) 670UNTIL TIME>=steptime%(i%) 680REXTi% 6907%FE61=0 700ENDFRDC 710 720 720 730DEFFROCmanual 76001da%=0 770CLS: PRINTTAB (13,10) "MANUAL CONTROL" 780a7=0 790 IF INKEY(-33) AND NOT (INKEY-114) a‰≈a 790 IF INKEY(-114) AND NOT (INKEY-33) a%=a 800 IF INKEY(-115) AND NOT (INKEY-34) a%=a 2 DR 3 a% DR 4 022 IF INKEY(-116) AND NOT (INKEY-115) a%= a% DR 12 830 IF INKEY(-21) AND NOT (INKEY-117) a%=a % DR 16 820 IF INKEY(-116) AND NOT (INKEY-115) a%= OR 16 840 IF INKEY(-117) AND NOT (INKEY-21) a%=a OR 48 850 IF INKEY(-118) AND NOT (INKEY-23) a%=a y. % OR 64 850 IF INKEY(-23) AND NOT (INKEY-118) a%=a 192 DIF INKEY(-120) ENDPROC:REM RETURN TO ME 2 08 8701F 900FF aX<≥olda% ?%FE61=a%:olda%=a% 8900FC 780 900ENDFROC

910 920 930DEFPROCrunmenu 940REM********* 990PRINTTAB(16,3)"******** 980PRINTTAB(16,3)"******** 990PRINTTAB(10,10)"1...RUN WITH WAIT STATE 1000PRINTTAB(10,12)"2...RUN WITHOUT WAIT ST ATES 1010PRINTTAB(10,14) "3...RETURN TO MAIN MENU 10201NPUT TAB(10,22)"CHOICE :"q% 1030IF q%=1 PROCrunscreen:PROCrun(TRUE) 1040IF q%=2 PROCrunscreen:PROCrun(FALSE) 1050UNTL q%=3 1060ENDFROC 1070 1080 1090DEFPROCrunscreen 1100REM*********** 1100REM* 1120PRINTTAB(12,10)"EXECUTING PROGRAM" 130ENDPROC

Listing 2 Motor Control Basic.

10REM CAN ONLY BE USED WITH MEP SYH Contr Basic 20MAKE PRINTER DUTFUT 30REPEAT SUREPENT 40IF INKEY(-33)=TRUE AND INKEY(-114)=FALS E PROCmotorileft 50IF INKEY(-114)=TRUE AND INKEY(-33)=FALS PROCmotoriright 60UNTIL FALSE 80DEFPROCmotor1left YUREPEAT 100MAKE BIT 0, HIGH 110MAKE BIT 1, LOW 120UNTIL INKEY(-33)=FALSE 130MAKE BIT 0,1, LOW 140ENDFROC 150 150 160DEFPROCmotor1right 170REPEAT

170KEPEAT 180 MAKE BIT 0, HIGH 190 MAKE BIT 1, LOW 200UNTIL INKEY(-114)=FALSE 210MAKE BIT 0,1, LOW 220ENDPROC

BUYLINES

There should be no problems with the components, especially since nothing is critical. Connectors are widely available. Maplin, for example, can supply them all (PO Box 3, Rayleigh, Essex SS6 8LR - tel: 0702 552911) and they can also supply the microminiature relays. A ready-built unit (including software can be supplied by Gwentec Electronic Instruments, 26 Churchwarden Drive, Newport, Gwent NP9 0SB (tel: 0633 280526) for £42.50 inclusive. Gwentec will also supply a kit of parts for £29.25 and a PCB for £5.50. Please phone first to check on availability. A slightly modified PCB will be available from our PCB service as soon as it becomes operational again.

Logo Motor Control

Two procedures in Logotron Logo are provided (Listing 4) to show how the machine code driver can be used in a language other than Basic. The procedure, PATCH, loads the driver at 2 FC0H. The second procedure has two parameters, motor number followed by motor action. The CALL in the MOTOR procedure is to the decimal equivalent of

Listing 3 Program to assemble machine code motor driver. 10REM ASSEMBLER MOTOR DRIVER 20FOR pass% 0 10.3 SIEP 3

30P%=&3000	
001 / 00000	- 10 3 STEP 3
40LOOT	
40[OPT pass	
50LDA &71	\ load A with "direction" (
0-2)	
60BEQ store	\ if "direction" zero then
0.K.	an accessing zero chen
70CMP #3	
80BCS end	
	\ if "direction" 3 or more
then exit	
900RA #&1	\ "OR" with 1 to convert 2
to 3 leaving 1	as 1
100.store	
110STA &71 120LDX &70	\ store 0,1 or 3 in &71
1201 DV 9.70	
(0-4)	\ load X with motor number
1300EX	\ convert to Ø-3
140CPX #4	
150BCS end	\ if 4 or greater then exit
160STX & 70	\ store new motor number in
8/70	i weble new motor number in
170	
180LDY #&FC	
	\ load mask
190STY &72	\ store mask in &72
200LDX &70	\ load X with motor number
210BEQ_jump1	\ if motor number 0 no need
to shift mask	left
7205EC	
230 .loop1	
740001 070	
240ROL 872	
250ROL &72	∖rotate mask left two bits
260DEX	
270 BNE 100p1	\ repeat loop according to
motor number	
280.jump1	
290LDA %FE61	
290LDA &FE61 300AND &72	\ and contents of printer p
ort with mask	V and contents of printer p
310	
320LDY &71	X X X X X X
ion"	\ load Y with motor "direct
3305TY &72	\ store motor "direction"
in \$72 as second	j mask
340LDX &70 350BED jump2	\ load X with motor number
്ട⊎BED jump2	\ if motor number is zero o
	the motor number is zero n
o need to shift	\ if motor number is zero n mask left
o need to shift 360CLC	mask left
o need to shift 360CLC 370.loop2	mask left
o need to shift 360CLC 370.loop2 280RDL %72	mask left
o need to shift 360CLC 370.loop2 280RDL %72	mask left
0 need to shift 360CLC 370.100p2 280ROL &72 390ROL &72	N shift mask left twice
0 need to shift 360CLC 370.100p2 280RGL &72 290RGL &72 400DEX	mask left ∖ shift mask left twice
o need to shift 360CLC 370.100p2 280ROL &72 390ROL &72 400DEX 410ENE 100p2	mask left
0 need to shift 360CLC 370.100p2 280KDL &72 390KDL &72 400DEX 410BNE 100p2- motor number	mask left ∖ shift mask left twice
<pre>o need to shift 360CLC 370.1cop2 B80K0L &72 S90R0L &72 400DEX 410ENE loop2- motor number 420.ium2</pre>	mask left \ shift mask left twice \ repeat loop according to
<pre>o need to shift 360CLC 370.1cop2 B80K0L &72 S90R0L &72 400DEX 410ENE loop2- motor number 420.ium2</pre>	mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask
<pre>o need to shift 360CLC 370.1cop2 B80K0L &72 S90R0L &72 400DEX 410ENE loop2- motor number 420.ium2</pre>	mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask
 need to shift 360LU 370.100p2 B0860L&72 40960L &72 4000EX 41080E1 0002 motor number 420.jump2 4300RA %72 4408TA %FE61 4508TA %72 4408TA %72 4408TA %72 4508TA %7 4508TA %72 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
 need to shift 360LU 370.100p2 B0860L&72 40960L &72 4000EX 41080E1 0002 motor number 420.jump2 4300RA %72 4408TA %FE61 4508TA %72 4408TA %72 4408TA %72 4508TA %7 4508TA %72 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7 4508TA %7	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
o need to shift 360CLC 370.10002 290K0L &72 400DEX 410ENE 10002 motor number 420.jump2 4300KA &72 440STA &FE61 450STA &72 400.end	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
 need to shift 360CLC 370.loop2 860CL & 72 370.loop2 370.loop2 400EX 410EX 400EX 400EX 400EX 420.jump2 420CA & 72 440STA & 72 440STA & 72 460.end 470RTS 	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
<pre>o need to shift</pre>	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
0 need to shift 360CLC 370.loop2 880KDL &72 400DEX 410ENE loop2 motor number 420.jump2 4300RA &72 4405RA &FE61 450STA &72 460.end 470RTS 480] 490NEXTpass%	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
<pre>o need to shift TaOCLU 370.loop2 BOKOL %72 590ROL %72 400DEX 410BNE loop2- motor number 4200.jump2 4300RA %72 440STA %72 440STA %72 440STA %72 440STA %72 400.end 470RTS 480] 490NEXTpass% 500</pre>	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
o need to shift 360LC 370.10002 200KOL &72 400DEX 410BKE 10002 motor number 420.jump2 4300KA &72 440STA &FE61 450STA &FE61 450STA &72 460.end 470RTS 4801 490NEXTpass% 510	<pre>mask left shift mask left twice repeat loop according to "OR" A with second mask send byte to printer port</pre>
o need to shift 360CLC 370.loop2 880KDL %72 400DEX 410BEX 410BEX 400DEX 410BEX 100P2 4200CA %72 4200CA %72 4300CA %72 4300CA %72 440STA %72 450STA %72 450STA %72 450STA %72 450STA %72 450STA %72 500 510 520	mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72
o need to shift 360CLC 370.loop2 880KDL %72 400DEX 410BEX 410BEX 400DEX 410BEX 100P2 4200CA %72 4200CA %72 4300CA %72 4300CA %72 440STA %72 450STA %72 450STA %72 450STA %72 450STA %72 450STA %72 500 510 520	mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72
<pre>o need to shift TaOCLC 370.loop2 BORGL &72 400DEX 410DEX 410DEX 420.jump2 4300CA %72 4300CA %72 4300CA %72 4300CA %72 440STA %72 440STA %72 450STA %72 450STA %72 450NEXTpass% 500 510 520 530REM****TES</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask \ send byte to printer port \ store byte in &72 </pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask \ send byte to printer port \ store byte in &72 </pre>
<pre>o need to shift TaOCLU 370.loop2 BORGL %72 400DEX 410BNE loop2 400DEX 410BNE loop2 400DEA %72 440STA %72 440STA %72 440STA %72 440STA %72 440STA %72 450STA %72 400LA %72 400LA %72 500REM****TES 5402%FE61=0:7% 550REFEAT</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask \ send byte to printer port \ store byte in &72 I***** 72=0</pre>
<pre>o need to shift TaOCLU 370.loop2 BORGL %72 400DEX 410BNE loop2 400DEX 410BNE loop2 400DEA %72 440STA %72 440STA %72 440STA %72 440STA %72 440STA %72 450STA %72 400LA %72 400LA %72 500REM****TES 5402%FE61=0:7% 550REFEAT</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "DR" A with second mask \ send byte to printer port \ store byte in &72 I***** 72=0</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CIUON (0,1,2) : "d%</pre>
<pre>o need to shift TaGCLU 370.loop2 B0RCL %72 400DEX 410DEL %72 400DEX 410DEL loop2- motor number 420.jump2 4300RA %72 4300RA %72 440STA %F261 450STA %72 460.end 470RTS 490NEXTpass% 500REMAT 530REMA****TES 5407%FE61=017% 550REFEAT 5401NPUT "MOTOF 570INPUT "DIREC 5807870=m%:?%77</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CIUON (0,1,2) : "d%</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CTUON (0,1,2) : "d%</pre>
<pre>o need to shift TaGCLU 370.loop2 B08KDL &72 400DEX 410BNE loop2 motor number 420.jump2 4300RA %72 440STA %72 440STA %72 440STA %72 440STA %72 440STA %72 450STA %72 450STA %72 450STA %72 500REMT 520 530REM****TES 5407%FE61=0:7% 550REFEAT 550REFEAT 560INPUT "DIREC 570INPUT "DIREC 570INPUT "DIREC 570CALL %3000</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CTUON (0,1,2) : "d%</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CIUON (0,1,2) : "d%</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72 I***** 72=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% l=d%</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72 I***** 72=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% l=d%</pre>
o need to shift 360CLC 370,100p2 280K0L &72 400DEX 410ENE 100p2 4200EX 410ENE 100p2 4200EX 4200EX 4200EX 4200EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 44	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** Z2=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% tron Logo procedures</pre>
<pre>o need to shift</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72 T***** Z2=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% led% tron Logo procedures</pre>
o need to shift 360CLC 370.100p2 80KOL %72 593KOL %72 400DEX 410BNE 100p2 4300RA %72 4300RA %72 4300RA %72 4300RA %72 4300RA %72 4300RA %72 4300RA %72 440STA %72 400.end 470RTS 4901 470RTS 4901 520 530REM****TES 5407%FE61=0:7% 550REFEAT 5601NPUT "DIRE(5807&70=m%:%77 590CALL %3000 600PRINTC7%72 610UNTIL FALSE Listing 4 Logoo utilising driver	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72 T***** Z2=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% led% tron Logo procedures</pre>
o need to shift 360CLC 370,100p2 880KL &72 400DEX 410ENE 100p2 4200EX 410ENE 100p2 4200EX 4200EX 4200EX 4200EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 440	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in %72 T***** 72=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% tron Logo procedures .</pre>
<pre>o need to shift Ta@CLC 370.100p2 080K0L %72 400DEX 410DEX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 4200EX 520 520 520 520 520 520 520 520 520 520</pre>	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% tron Logo procedures .</pre>
o need to shift 360CLC 370,100p2 880KL &72 400DEX 410ENE 100p2 4200EX 410ENE 100p2 4200EX 4200EX 4200EX 4200EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 4400EX 440	<pre>mask left \ shift mask left twice \ repeat loop according to \ "OR" A with second mask \ send byte to printer port \ store byte in &72 T***** 72=0 R (1,2,3,4) : "m% CTION (0,1,2) : "d% tron Logo procedures .</pre>

TO MOTOR :motornumber :motordirection .DEPOSIT 112 :motornumber .DEFOSIT 113 :motordirection .CALL 12224 END

2FC0H. The primitive .DEPOSIT is the equivalent of the BASIC command, POKE

Before the MOTOR procedure can be used, PATCH must be called once.

The machine code driver could, of course, be used from within any suitable language running on any 6502 system with only minor modifications.

UPGRADEABLE AMPLIFIER

Graham Nalty begins his amplifier at the beginning with an upgradeable moving coil input stage.

he prime requirement of an amplifier for moving coil cartridges is that it should have very low noise. This would not be too much of a problem were it not for the fact that moving coil cartridges have very low impedances, typically around 2–3 ohms. Since it is a requirement of low noise design that the base resistance of the input transistor is as low as the resistance of the cartridge, this calls for a great deal of care both in the choice of transistors and in the configuration of the input stage.

Transistor Choice

The noise figure usually given in transistor data sheets is arrived at from the formula:

total output noise power

N = 10 log noise generated dB by source only

A noise figure of 3dB, therefore, indicates that the source noise and the transistor noise are equal in magnitude. A higher noise figure indicates that the transistor is contributing more noise whilst a lower noise figure indicates that the source is contributing most of the noise.

The specification sheet for a typical low noise transistor, the ZTX 384, shows a noise figure of 4dB, but this alone does not tell us how suitable it is for our particular application. We need more information. Looking again at the noise figures, we see that they apply over a frequency range from 30Hz to 15kHz when Vce = 5V, I_c = 200 microamps and R_s = 2k ohms.

At first glance it appears that 4dB relative to a source impedance of 2k0 is far too high for a low impedance, low output moving coil cartridge. However, if we are prepared to | resistance. Medium current

However, if we are prepared to accept a noise figure of 2dB as reasonable, we see from the data sheet that this can be achieved with a source impedance of around 500 ohms over a current range from 100 to 500 microamps. By using five transistors in parallel we could achieve 2dB with a 100 ohm source impedance. This approach was frequently employed when moving coil cartridges first became popular about ten years ago, and I remember one published design that used no fewer than eight transistors in parallel.

Another approach is to use medium power transistors, as did Stan Curtis in his System A preamp described in ETI in July 1981. Transistors designed for higher currents have a lower base resistance. Medium current transistors such as the 2N4401 and 2N4403 have a base resistance which is higher than that of most moving coil cartridges but considerably lower than the base resistance of most small signal transistors.

For practical purposes there is no reason why we cannot combine the two approaches, without going to the extreme of paralleling five or more transistors. Space is provided on the PCB for two transistors in parallel, and a pair of 2N4401s in this position will give a perfectly satisfactory performance at reasonable cost. This arrangement is recommended for the economy version of the preamplifier.

There are several alternatives when it comes to upgrading this

NOISE VOITAGE (in V_{1}/Hz) at:

	10Hz		100Hz		1kHz	
	Тур.	Max.	Тур.	Max.	Тур,	Max.
2SB737, 2SD786	0.55		0.4		0.4 1.1	
ZN459 2N4867A J230–232 OP37A OP37C	8 8 3.5 3.8	10 30 5.5 8			2 2 3.0 3.2 3.5*	5 3.8 4.5
NE5534 OP71					18	
*Frequency not stated.						

 Table 1 A comparison of the noise performance of selected low-noise FETs and ICs.

PROJECT

stage. One is to use the same arrangement but choose better quality medium power transistors which offer lower noise. The 2SC2385 transistors suggested in the parts list are ideal for this application and prove extremely quiet in practice.

The other approach is to use a transistor which has been designed specificially for moving coil input stages and which has a low enough base resistance to allow direct matching to the cartridge output. Several transistors of this type have been developed in recent years, and a good choice here is the 2SD786 which has a base resistance of just 4 ohms. These are, inevitably quite expensive and not terribly easy to come by, but they do seem to offer the lowest noise figure of any type currently available. A comparison of the noise performance of these and other low-noise transistors and ICs is given in Table 1.

The Circuit

Like the other stages in this preamplifier, the moving coil stage is based around a simple but very effective discrete operational amplifier. This allows the stage gain to be determined by the values of two external resistors. As a result, the gains of the two channels can be matched to within very close limits without using preset potentiometers, thus avoiding the inconvenience, cost and sound degradation this would involve.

The circuit of the moving coil amplifier in its basic form is shown in Fig. 1. Transistors Q2 and Q3 form a differential pair in which approximately equal currents flow. The total combined current of Q2 and Q3 is kept almost constant by R3. Transistor Q5 is biased on by the voltage across R2.

With no signal input, the bases of Q2 and Q3 are close to 0V. If a positive voltage is applied at Q2 with respect to Q3, more current will flow in Q2 and less current in Q3. The voltage across R2 will increase, and this will cause an increase in current in Q5. This will raise the voltage across R7 and give a positive output signal.

The effect of the negative feedback network comprising R5 and R6 is to feed some of the output back to the base of Q3. Provided the base of Q2 is at a higher voltage than Q3 the output will remain positive. If the open

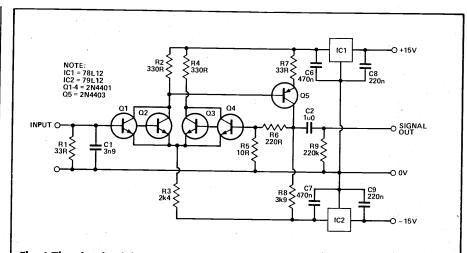


Fig. 1 The circuit of the moving coil amplifier in its standard or 'economy' form.

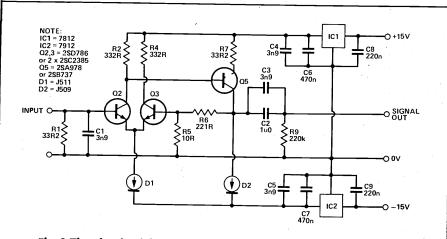


Fig. 2 The circuit of the moving coil amplifier in its upgraded form.

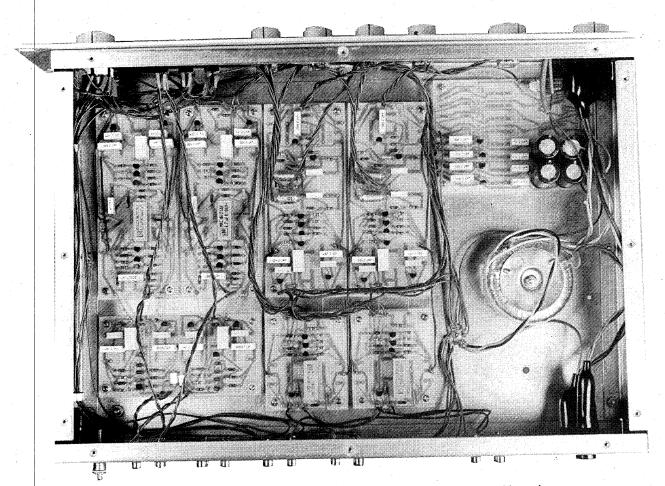
loop gain is much greater than the ratio of the resistors R5 and R6, the voltage difference between the bases of Q2 and Q3 will remain small and the gain will be defined by the values of the resistors.

The use of a differential input is not ideal in terms of achieving the lowest possible noise, but it does remove the need for an input capacitor which would otherwise. degrade the sound as well as being physically large and expensive. This means that a small current of the order of 10 microamps flows through the cartridge, but I have not seen any reliable reports which say that this is harmful. have not observed any ill effects even at higher levels of current, but if you are at all worried by DC flowing through the cartridge, replace R1 by a 10k resistor, connect a $4\mu7$ plastic film capacitor towards the input and place the original R1 across the input socket. Plastic film capacitors will pick up noise from the mains transformer, and if this is

troublesome you will either have to remove the transformer to a separate box or replace the capacitor with a tantalum type (negative end to transistor) with some degradation at high frequencies.

The input loading resistor on a moving coil cartridge is an important part of the design. A change in the value of this component will change the sound quality of the cartridge, and many people like to experiment to find the optimum loading for their system. As a rough rule, lowering the impedance reduces the high frequency output. A cartridge with a low output impedance will obviously require lower impedance matching from the amplifier and this should be considered when fitting the loading resistor in the circuit. As a practical compromise, a value of 33 ohms is suggested as a standard or a starting point for experimentation.

The performance of the standard version can be further



Internal view of the complete pre-amplifier. The moving-coil stage is the bottom, left-hand board.

improved, mainly by the use of higher quality components. The circuit of the upgraded version is shown in Fig. 2. The upgrading measures are:

1) The use of ultra low noise transistors type 2SD786 for the input stage.

2) The use of Holco H8 (% watt) precision resistors manufactured by Holsworthy Electronics. These offer better sound quality than standard grade metal film types (Ref. 1).

3) The replacement of polyester capacitors with polycarbonate types. Polycarbonate capacitors have a lower dielectric loss (Ref. 1, 2).

4) The by-passing of all medium value capacitors with polystyrene capacitors of around $3\mu 9 \mu$ to 10 μ . This improves the sound at high frequencies.

5) The use of transistor pads for all transistors. This holds them firmly to the board and reduces the possibility of distortion due to vibration. Anyone who has heard the improvement in sound quality which results when a Mission Isoplat is placed under an amplifier will know what I mean.

6) The use of high power (metal tag) regulators. This should improve the sound quality by reducing the Temperature Generated Distortion (TGD).

7) The use of constant current diodes in place of R3 and R8. The high impedance (Ref. 3) reduces

BUYLINES

A complete kit of components and PCB for the moving coil stage can be obtained from the author at 6 Mill Close, Borrowash, Derby DE7 3GU. The cost is £12.50 for the standard version and £25.00 for the upgraded version. Both prices are fully inclusive. The PCB and parts are also available individually — ask for a price list.

The case for the complete preamplifier (see pictures above and overleaf) costs £49.00 including VAT and carriage. An alternative front panel is available for those who do not want tone controls — please specify when ordering. the power supply ripple voltage feedback to the input from the negative supply and greatly improves sound quality.

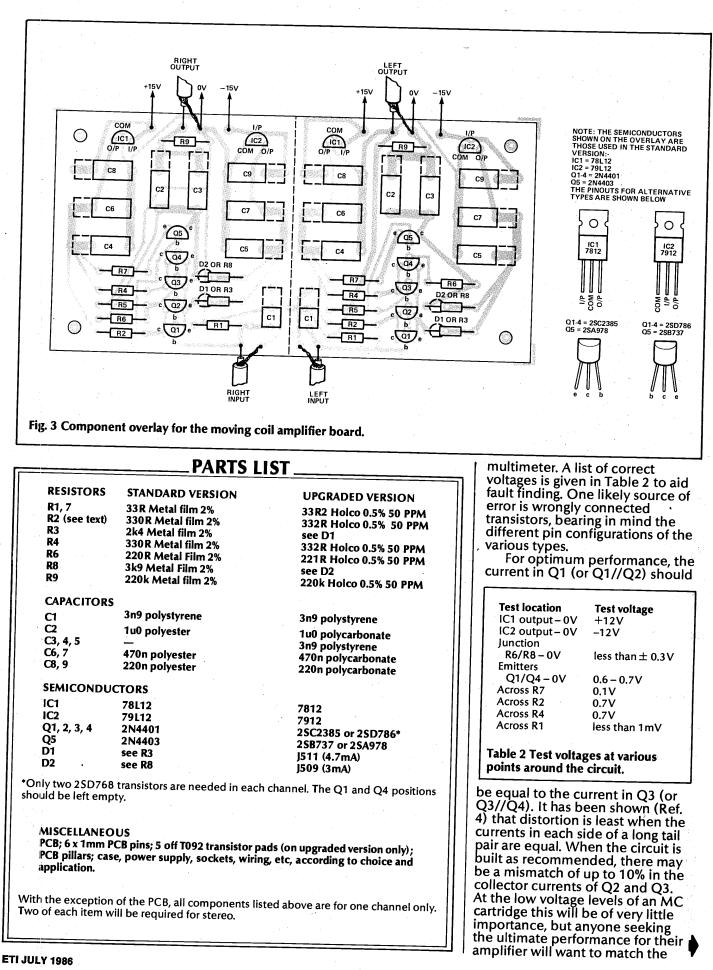
Construction

Refer to the components layout, Fig. 3, and start by soldering the resistors into place. Note that there are three holes to accommodate R3 and R8. The holes that are 0.5" apart are for the resistors and the holes that are 0.2" apart are for the FET constant current diodes (D1 and D2) used in the upgraded version. The flat faces of the FETs face the input side of the board and it is a good idea to mount them on transistor pads.

Next install the connecting pins (6 in each channel) and insert and solder the IC regulators. Now insert the transistors using the diagrams in Fig. 3 as a guide. The board is completed by soldering the capacitors into position. Feed a supply of $\pm 15V$ to

 $\pm 25V$ to the board (up to $\pm 35V$ if you are using 1A regulators) and check all voltages with a DVM or

PROJECT: MC Stage

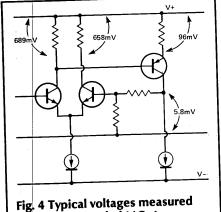


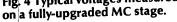
41

PROJECT: MC Stage

input currents as closely as possible.

There are two ways to do this: the first is by calculation using a DVM. Fig. 4 shows the voltages measured on a fully-upgraded moving coil stage. Current through Q2 is 2mA and through Q3 is 2.09 mA, giving a mismatch of 4.5%. In view of the variations in the





specifications of constant current diodes ($\pm 10\%$) larger mismatches may be expected through R2 and R4.

Current = 4.09 mA

Base Current through Q5

= 0.01 mA (Hfe = 300)

Total Current = 4.1 mA

Current per transistor = 2.05 mA

Voltage required across R2

Optimum value of R2 = 6892.05

= 336 ohm

The nearest E96 value is 332R or 340R.

The collector currents could alternatively be matched by either changing the value of R7 or by changing D2 (to a J508) or adjusting R8 as appropriate. I will leave the readers to make their own calculations if they wish to pursue these alternative options.

The other method of balancing the currents is by observing a null point with a multimeter or scope. The circuit is set up as shown in Fig. 5, with R2 and R4 replaced by resistors of 270R (0.5%), 39R, 22R, 10R, 4R7, and 2R2. Initially the 10R, 4R7 and 2R2 resistors are shorted out on both sides and a

meter is connected from A to B. If current flows from A to B, the

total resistance value is too high and needs to be reduced. If currrent flows from B to A, the total resistance value is too low and needs to be increased. The resistance on both sides should be adjusted by equal amounts by restoring the shorted resistances. This procedure should be repeated until a point is found where the current is lowest. That value is recorded and about 1 ohm added to allow for Q5's base current. The value of R2 is the nearest preferred value to this. It is important that the resistance values are exactly the same whilst measurements are being taken.

Whilst the final value of R2 is critical (to set the base voltage of

Negative supply	R3	R8
volts 1.2V single nicad	120R	390R
5V	1 k0	1 k6
9V	1k8	3k0
12V	2 k4	3 k9
15V	3k3	5 k1
18V	3 k6	6k2

Table 3 Values of R3 and R8 foruse with alternative negativesupply voltages.

Q5), the value of R4 after the measurements are taken will have no real effect on the circuit. Either 330R (or 332R in E96 values) or a resistance equal to the final value of R2 would be appropriate.

Applications

Although designed for use with the upgradeable amplifier, the moving coil stage is also well suited for use in other equipment or as a stand-alone unit. Its \pm 12V regulators allow it to be hooked into almost any preamplifier which has \pm 15 V regulated supplies, and it would make an ideal headamp for John Linsley Hood's Audio Design Preamp and similar amplifiers. In such instances the use of two separate stages of regulation will guarantee excellent sound quality. It would also work well as an isolated head amp with a suitable power supply.

You can also use the moving coil stage without its regulators if you have a high performance discrete regulator or a battery supply. The low noise upgraded version in particular will be extremely quiet on battery power supplies. It will also give

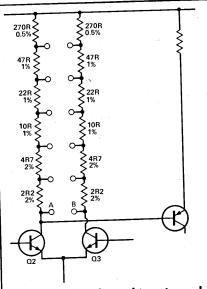


Fig. 5 Test circuit used to set equal collector currents in the input transistor pair.

outstanding sound quality when powered from high capacity batteries, but in this case it is wise to limit the short circuit current which might flow were a semiconductor to fail. The last thing you want is to have the short circuit current of a large rechargeable battery fed to the terminals of an expensive MC cartridge.

To prevent this, connect resistors of at least 10R (or 100R with a 12 V car battery) in series with D1 and D2. I don't think short circuit failure is common amongst FET diodes but I would rather be safe!

Whatever supply you use, it is essential that the correct currents flow from the negative rail. Table 3 shows suitable values of R3 and R8 for some of the more popular negative supply voltages. Constant current diodes are ideal for higher voltage working, but their impedance falls at low voltages. At 4.2 volts the current via the J511 is 10% below the current at 25 volts.

References

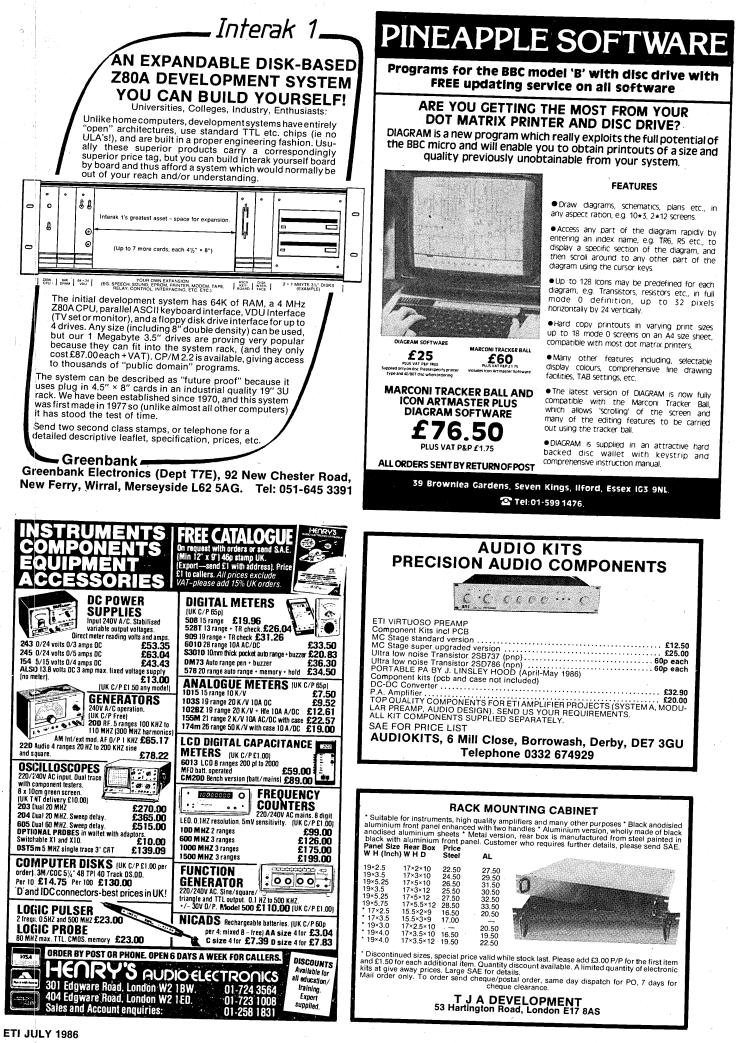
1) Colloms, M. A passive role. Hifi News Oct 1985

2) Colloms, M. Capacity to Change. Hifi News Dec. 1985 3) Nalty, R.G. Constant Care.

ETI April 1986 Taylor, E.F. Distortion in low

noise amplifiers. Wireless World Aug/Sept 1977

The next article in this series will describe the construction of the moving magnet input stage. **ETI**



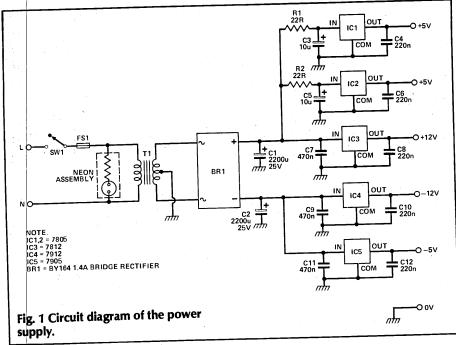
DIGITAL SOUND SAMPLER

At the risk of repeating ourselves, Paul Chappell puts the sampler into action.

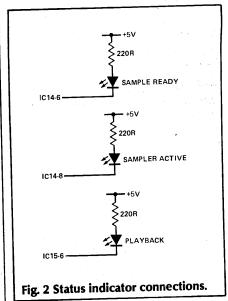
he circuit of the sampler's power supply is shown in Fig. 1, and is assembled according to the component overlay in Fig. 5. The heat sinks mount horizontally on the board with a gap of only a few mms between them, so it would be a wise precaution to use insulating washers wherever a 79-series regulator is next to a 78-series one, as their tabs will be at different voltages. Three of the four wire links on the board run very close to the heat sinks. To avoid the possibility of shorts, these should be insulated. photograph. You can arrange the front panel layout to suit your own taste, since all the controls are on flying leads.

It would be as well to keep the mains connections as far to the right of the case as possible, to minimize any hum pick up, but apart from that you have complete freedom.

We decided to put the input level control on the front panel, rather than use a preset on the PCB as originally specified. This allows a very wide range of input levels to be accommodated anything from a microphone input



We used a 19" rack-mounting case for the final assembly — there is no need to seek out exactly the same type, but you will need one of a similar size to allow room for the three boards. They fit neatly into the space available in a 19" case, as you can see from the to several volts from a signal generator. The group of three LEDs is for status indication: they show SAMPLE READY (when sampling is to be triggered by the presence of sound), SAMPLER ACTIVE, and PLAYBACK. These indicators are not essential, as the information is displayed on the TV screen, but if you'd like to include them, Fig. 2 shows how to do it.

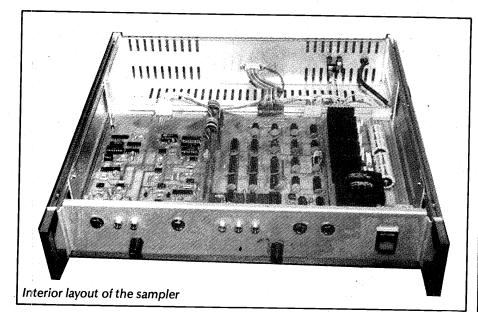


Connection to the Spectrum was made via a 15-way Dconnector at the rear of the case going to a standard, 0.1" edge connector. The ground connection was taken via the shell of the Dconnector and the appropriate links to the Spectrum expansion port are shown in Fig. 3.

A single pole toggle switch was inserted in the interrupt line when the switch is off, it allows the Spectrum to be used for other purposes; when on, the sampler's interrupts jam the Spectrum's own, and the sampler takes control.

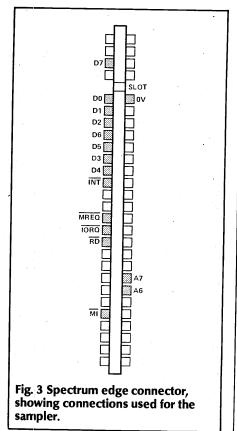
If the idea of having to operate a switch is too low-tech for you, the circuit of Fig. 4 will turn on the sampler automatically as soon as the tape has loaded — we didn't include it in the prototype, so it hasn't been allowed for the on the PCB, but it shouldn't be any great problem to add it.

PROJECT



Using The Sampler

The Sampler and computer must both be turned on and connected together before the program is loaded. If you try to load the tape without the sampler connected, the program will fail immediately, as one of the first things the program does when



loaded is to change the Z80's interrupt mode, and it requires an address from the sampler for the correct service routine.

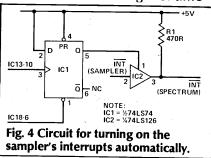
ETI JULY 1986

To load the program, type LOAD "" CODE, followed by ENTER, and then switch on the cassette recorder, and also the switch in the interrupt line if you've plumped for the manual method. As the tape loads, you should see a colourful 'SOUND SAMPLER' logo displayed on the TV. As soon as the loading is complete, this will be replaced by the help screen, as shown in the photograph, which lists the various commands you can use.

HOU	. 8 8 8 8	FULL DISPLAY	
TNOODL	# # # # # #	RIGHT CURSOR LEFT	
p X	# #	PLAYBACK SOUND OFF	
 MLRT		MODIFY SAMPLE LOOPING POINT RECORD SAMPLE SAMPLE FROM TAPE	

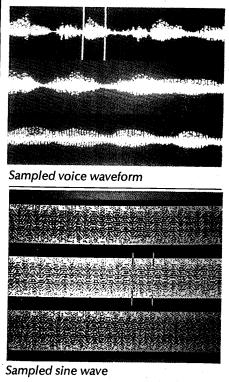
The option menu

To sample a sound, press 'I' on the keyboard (which stands for 'input'), and you will then be given a choice of sampling rates. The higher the sampling rate, the better the sound quality, but the shorter will be the length of time



you can sample for. Having chosen your sample rate, you will then be given the choice of initiating the sampling by the presence of a sound, or starting it immediately by pressing a key on the computer. A list of options is given on the TV screen at each stage, so there is no need to refer to this article or the instructions.

As soon as the sampling is complete, the computer will display the sampled waveform on the screen, in three rows, as shown in the photograph. Unless the waveform is at a very low frequency, all you will see at this stage is the envelope, so a constant amplitude wave will just appear as three bands across the screen, as you can see for the sine wave.



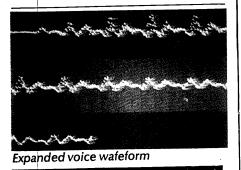
To inspect the waveform in detail, there are two cursors which can be moved to frame the part of the sample you are interested in. Pressing 'E' (for 'expand') on the keyboard will give a detailed picture of the portion of the sampler between the cursors Photographs of the expanded voice waveform and sine wave show the result of doing this. Both the samples were taken at a low rate, so the individual sample points making up the waveforms can be seen. The samples in memory are in companded form. but the program calculates the correct position for each point so that it appears on the screen as if

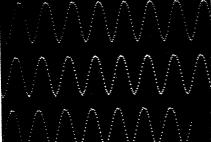
the sampling had been linear.

The sample in memory can be edited — you can cut out parts that are not required, by moving the cursors, and you can also alter any sample to modify the waveform, or even to create your own waveforms.

To hear the sound, press 'P' (for 'playback') on the computer. You will then be given the choice of having the sample played back once each time a key is pressed (which you will want if you have sampled a piano note, for instance), or of continuously looping around the sample to give a sustained note. On playback, only the portion of the sample between the cursors will be heard, so to play the entire sample you must move the cursors back to their original positions at either end of the sample. When you are looping around the sample to form a continuous note, there will be a click every few seconds because of the sudden jump from the last sample value to the first. You can eliminate this by choosing your loop beginning and end points (with the cursors) so that the last part of the sample leads smoothly into the first, or if you like the computer will choose the looping points for you automatically. Just press 'L' on the keyboard.

press 'L' on the keyboard. The sampler accepts a control signal from a standard 1V per octave keyboard, with a +5V gate signal. If you prefer to use a MIDI keyboard, you may like to consider building last month's MIDI to CV converter, which will allow any MIDI based instrument to interface with the sampler.



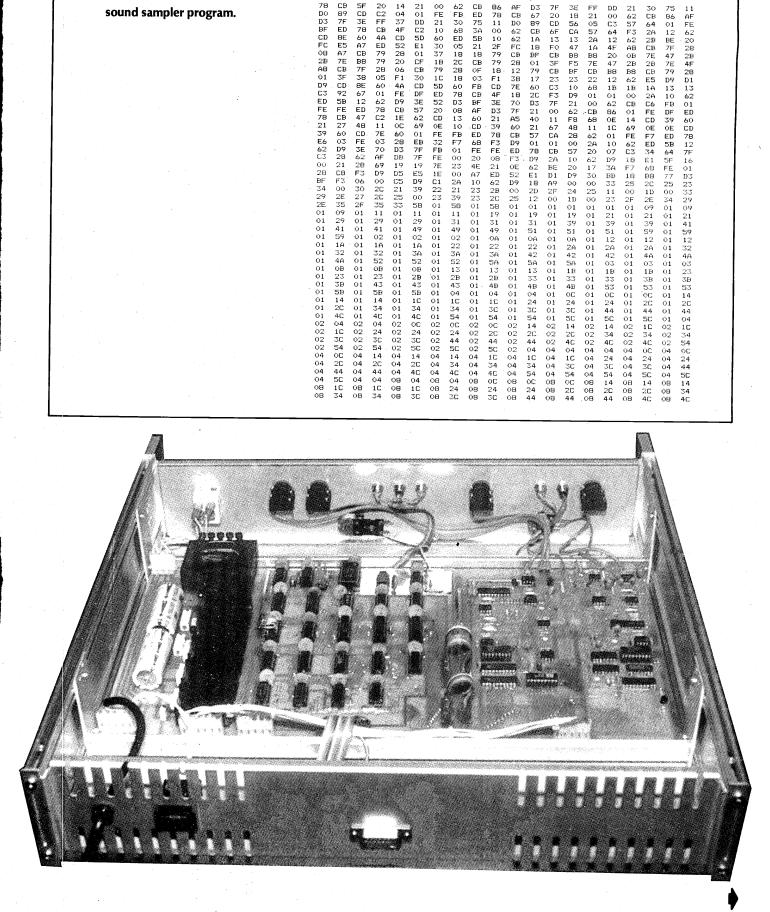


46 Expanded sine wave

20322230004D90035000051000429BDA3370EB8609417008E1150DE30198D2F1E00F1033078E1782228A8445778FE6A1798D06DB8888F022F71806D 00200001D0303227335200133ED5DD5078F78ED08061778E070E943300E11660DE38191FEF13060F1706DF2284100206206206277EF7000E18F4355BD8062178E00508897720021002062778E7000E18F4355BD08061778E7000E18F4355BB812220E 28D5112469F1D00354488C4CE597001865A2D8703A08EF87D51490E150DE2000419C58100C02776230C36EC7002F0FC10600E81660F32CFE597765797D51450DE2000419C58100E537862F00D22DBA30E0E821029011922211600F5197D5 0030000194703F9D1170A555169AEE800905C8B9F804688580D00E88191F12190DE1800D218B5C57D02228BD10F8FF220E87CF8074FD2282200000194703F9D1170A555169AEE8097504558282E8074FD22822000 24022229032120200032238156E69EF29FED2622EBD7F46EBA6000DEB0198F156B0DE7D6DE76EBE8F839E126A3FF16195810292A310467D23002FFF2E1166B 33533000F300321002253D11003210924888995820447FE9944D882FF930194F150D45EC0405FC04040D480C486420340055A4F125811827E025C820DA48880429FF03CD 25400C222F6CD0004050013C6BBD763B623A177B08871F8E9C3E150DEE019739F1730FEB270DD5C3CE10801120002662DF23660118E70487FC81760887 5300144790433392615039616088D00916228FEC2979638D0018E150DEA010899FD39CBB21750FC20570FD0638760867F01F6A88A2C248EB06610867F72098 20032422348CF5D40FD28003991C718837045286870E20F90E1500690E21988A19780682470316F2371600682F5D1703E100C3E61E12C400F07750AC10000C2 3902243323073042001011439006091601768800E203084927838507231511800DE30648100A48173801235288228D02700FF18D122336250076F0008800E0026223A662 5294E0008730F1E42235081200008948F33CE104868808C4F52200EDE88198F15076DE800E2727F2104CE72FE13087285F70A04675373266D088811507EEA2400427305145076DE80225088730F12E4223508122000873066675373266D088841507EEA2400427

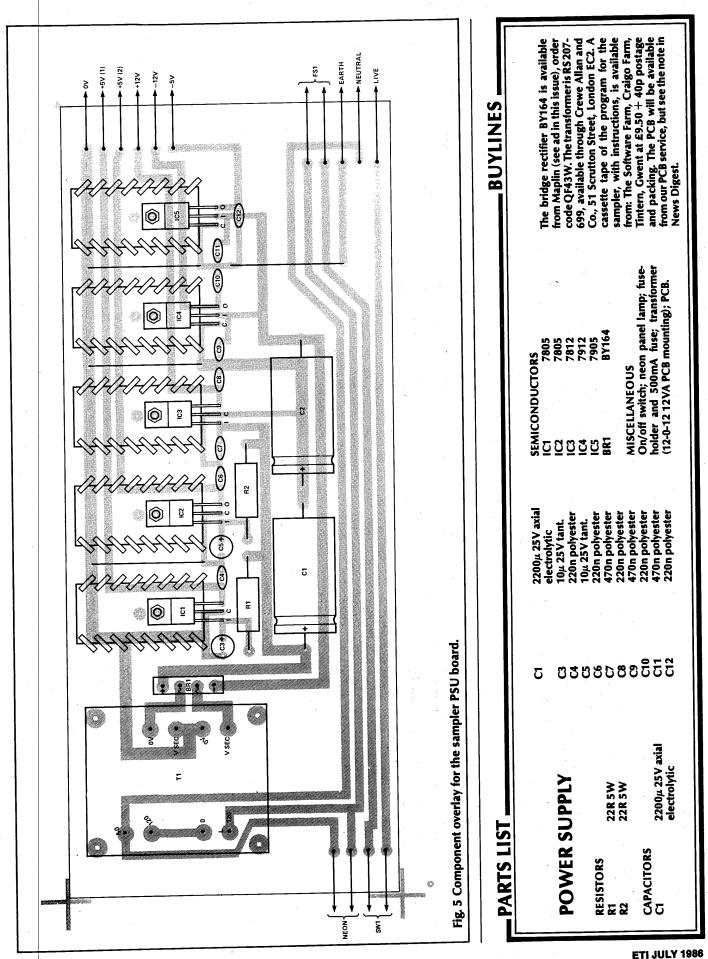
Listing 1 Hex dump of the

sound sampler program.

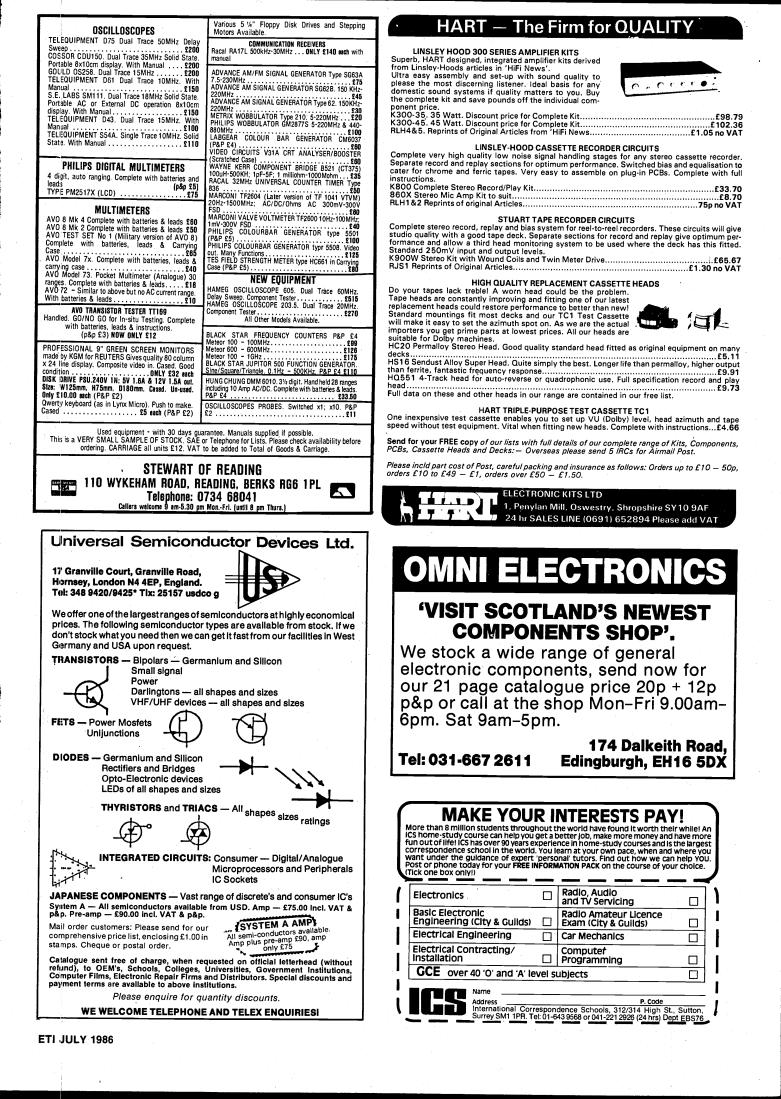


PROJECT: Sound Sampler

ETI



48



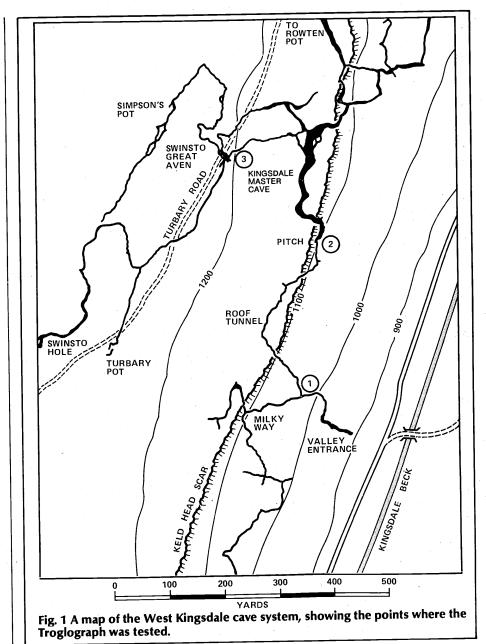
THE ETI TROGLOGRAPH

Not so much a field test, more a sort of soak test. Mike Bedford describes the results obtained with his prototype underground communications system and offers some hints on using and modifying it.

nitial tests with the Troglograph were carried out over a distance of about 200 yards with a reasonable received signal strength, but signals started to tail off significantly beyond this. These ranges were very encouraging as they compare favourably with the depths of most British caves and potholes. However, the Troglograph was not developed for surface communication — the acid test was how well it would work in the environment for which it was intended.

The field tests were carried out in a windswept valley in North Yorkshire on a Saturday in January, the surrounding hills still showing the remnants of recent snowfalls. The cave chosen for the tests was the West Kingsdale System, located about 3 miles north of Ingleton. There is an entrance in the valley bottom and the route then passes almost horizontally under the surrounding hills, providing a comparatively easy means of getting the equipment to a quite respectable depth. There is only one vertical pitch on the route chosen yet the final point is in excess of 300 feet below the hill top.

The party consisted of the author, his wife Margaret, Ivan Wuljahorodsky and James Fathers, of which the author and James made up the subterranean contingent. Figure 1 shows the West Kingsdale System, and consists of a survey of the cave passages superimposed upon a map of the surface features. It was used to pin-point corresponding positions above and below ground at which tests would be



.PROJECT



The prototype Troglograph undergoing field tests.

conducted. Although the System has a number of entrances, the one used in this expedition was the one referred to as Valley Entrance and the three points selected for tests were those marked 1, 2 and 3 on the map. Assuming that the cave passages are approximately horizontal, it is clear from the contours that these points must be about 100, 200 and 300 feet underground.

The first rendezvous point was reached in a matter of five or ten minutes, the entrance section being for the most part a walking or stooping passage with a couple of deep pools. One of these filled the passage to within a foot or so of the roof, serving to prove that the equipment was indeed waterproof. This first test proved encouraging with reasonably strong signals being received.

The equipment was packed away and we made our way to the second scheduled point, arriving there with a good five minutes to spare. As James rigged the pitch for our descent, a comparatively dry spot was found to set up the Troglograph. We switched it to

ETI JULY 1986

receive and were greeted with silence. Our spirits fell. At last, after two minutes of frantic tuning, the signal was heard. We later discovered that it had taken the surface party longer than anticipated to reach their destination — clearly there are times when it is quicker to travel underground! The signal strength was not particularly good, however, and the noise of the stream pouring over the nearby pitch as a waterfall made listening difficult.

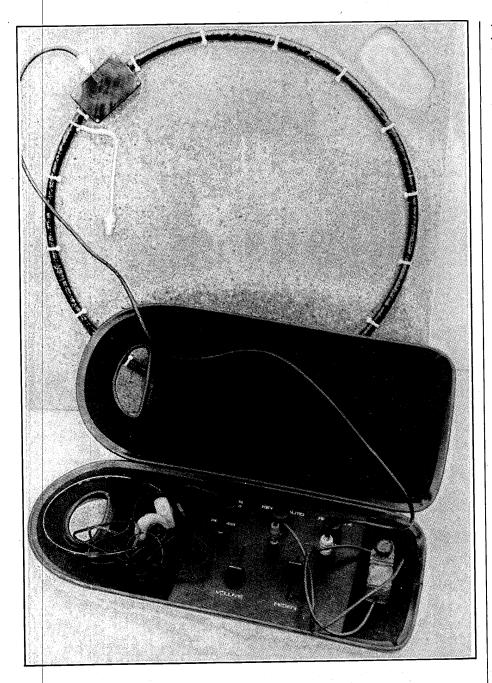
With the second test completed, we went down the 25 foot pitch, the passage enlarging considerably to form Kingsdale Master Cave, the hub of the whole system. The recent snow and rain falls had left the cave much wetter than on previous trips, making progress against the flow of water somewhat slower than expected. At the end of the master cave the route to the third rendezvous point, Swinsto Great Aven, continued as a low crawl with occasional flat-out sections. It soon became clear from the deposits on the roof that this area

had quite recently been flooded completely. In view of the unsettled weather conditions, good sense prevailed and we retraced our steps, leaving a test at 300 feet to another day. Nevertheless, we counted the expedition a success, signals having been received through 200 feet of solid rock and the ergonomic features of the design proven.

Further Developments

It is very unlikely that the first attempt at a project such as this will result in a piece of equipment which can't be improved upon. Indeed, although the field trip was a success, it served to identify a number of areas for future development. The author will probably continue to experiment in these areas, and it would be nice to think that this article will inspire others not only to build the Troglograph but to use it as an experimental project too.

Signal strength at 200 feet below ground was not as good as expected. Means of improving this



Following the field tests, the original Troglograph was re-housed by James Fathers in the case shown here. Not only is it easier to carry than the original, it also looks a lot better both inside and out (see external photo opposite). The new arrangement also dispenses with the rather cramped control panel of the original version, making operation easier. Note the use of slider potentiometers for the volume and regeneration controls.

have already been mentioned, but to recap slightly they involve increasing the number of turns and/or the coil area (especially of the surface coil) without increasing its resistance.

Audio volume proved something of a problem in locations with high background noise (for example, the waterfall). The LM380 certainly doesn't give its full power output into a 32 ohm load, so a possible solution here would be to find miniature 8 ohm phones of sufficient power handling capacity. On the subject of phones, it was found that miniature stereo headphones do not fit over the hard hats used for caving. Perhaps it would be possible to fit phones into a hard hat?

Dragging a loop aerial and an ammo box in addition to ladders and ropes along a low passage is somewhat inconvenient. A form of folding loop which could fit into the ammo box would be a marked improvement.

Perhaps the most ambitious next stage would be to develop speech communications, a facility which would bring it close to the Molephone described in the first part of this article. Initial calculations show that the most significant problem would be obtaining the required 3kHz bandwidth. The traditional solution involves reducing the Qfactor of the loop considerably, which could only be achieved practically by reducing the number of turns. Clearly this reduction in turns would have to be compensated for by a corresponding increase in the coil current. More recent designs such as the Molephone overcome bandwidth limitations by operating at a higher frequency, typically 100kHz.

Licencing

As far as international frequency spectrum agreements are concerned the portion from 0-9 kHz is un-allocated and no regulations restrict its use. Furthermore, since the Troglograph does not use radio in the normal sense of the word but uses very short range magnetic waves, it would be reasonable to assume that a transmitting licence is not required. In many countries this is the case and the Troglograph could be used without any sort of official authorisation. In Britain, however, a licence issued by the Department of Trade and Industry is required for all use of the electromagnetic spectrum

Fortunately, a suitable licence is readily available, this being the one issued for deaf aid inductive loop systems in public halls. The cost of the licence is £12.00 for a period of five years and the application form (Form BR12) may be obtained from the following address: Department of Trade and Industry, Radio Regulatory Division, Waterloo Bridge House, Waterloo Road, London SE1 8UA.

Unless mention is made in the application of the specific requirements, the licence obtained will restrict the loop current to one ampere turn. Although quite suitable for hearing aid systems this is clearly of no use for the Troglograph. Accordingly it should be requested that a limit of 100uV per metre at 100 yards

PROJECT: Troglograph

OPERATION

The Troglograph has a minimum of controls and is simple to operate. The equipment only has a transmit/receive switch, a switch to select automatic keying or morse, a volume control and a regenerating control.

This latter control is the only one which may need a word of explanation. In very strong signal conditions it really doesn't matter how the regeneration control is set — it will only be necessary to adjust the volume control to a comfortable level. When signals are weak, the regeneration should be advanced as far as possible without it self oscillating, then

from the aerial be applied. It would be wise to apply for this licence in good time as applications are currently taking about two months to process.

A Caution!

It isn't the intention of this article to encourage irresponsible underground expeditions. Caves and potholes are potentially dangerous places, hazards including pitches (deep vertical shafts), rising water in unsettled weather, route finding and collapses. Whereas all of these hazards can be minimised by taking the proper precuations, the inexperienced caver should not be tempted to go underground alone. Anyone wanting to experiment with cave communications is advised to seek some experienced the volume can be adjusted.

It should also be remembered that the transmit/receive switch has a third 'S' position. Forgetting this could result in the Troglograph being in its standby mode when it is meant to be transmitting or receiving. In practice it is unlikely that the operator will be unaware which mode the equipment is in. Although no 'sidetone' feature as such is included, the receiver continues to operate (with no aerial) when the Troglograph is transmitting, and enough signal is received to enable the or rator to hear what is being sent.

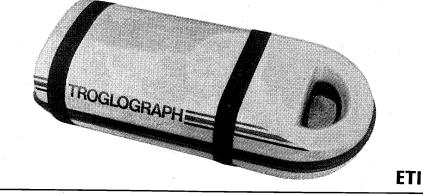
potholers as companions or preferably to join a club. Numerous caving clubs are scattered around the country, especially in the Yorkshire Dales, Peak District, Mendips and South Wales. With the increasing interest in the Molephone in potholing circles, an electronics enthusiast might well be especially welcome in caving clubs — particularly when one considers the high price of commercial equipment.

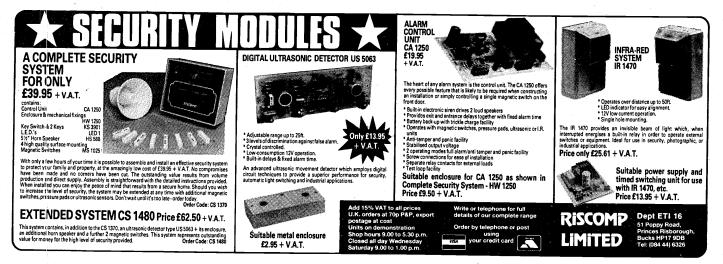
One other potential hazard is perhaps not quite so obvious. The loop aerial is tuned to resonance, which means that in common with all tuned circuits, the voltage across the coil could be much greater than the +24V applied to the final amplifier. It doesn't require much imagination to realise how unpleasant it could be to use a poorly insulated aerial whilst standing up to ones waist in a pool of water.

Acknowledgements

The author would like to thank Steven Newhouse who coined the name "Troglograph" and his wife Margaret, Ivan Wuljahorodsky and James Fathers for their assistance in testing it in less than ideal conditions!

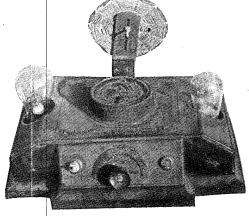
We would also like to point out that the Molefone (on which the Troglograph is based) is available from University of Lancaster Engineering Services, Department of Engineering, Bailrigg, Lancaster LA1 4TY, tel 0524 – 65201. Molefone is a registered trade mark.





THE RETURN OF THE VALVE

AN ARGUS SPECIALIST PUBLICATION



TERNATIONAL L

AUGUST ISSUE ON SALE 4th JULY

Contrary to appearances, this is not an ETI project. But we're delighted to announce:

The ETI Valve Pre-amp

The battle between valves and semiconductor devices is far from won. Many audio enthusiasts swear by valves — and not just because they're supposed to give a 'warmer' sound. Jeff Macaulay has designed a stereo pre-amp to exploit the known advantages of valves — linearity, simplicity, low noise and high overload margins. 'The sound,' he says, 'is a revelation.' But that's only the beginning, because pretty soon Jeff is swapping his triodes for transistors with surprising results.

Intelligent Call Meter

This ROM-based design is for a telephone call-meter as easy to use as a digital clock. With battery back-up, it features a 24 hour clock, automatic or manual switch-on and the ability to memorize and display individual and total call costs for up to 135 calls along with the numbers dialled. The unit can handle all STD calls whether inland or overseas. The ROM-based program makes use of unit-cost data stored in RAM which can be updated by the user. Perfect to check your bills or keep track of your telephone use.

Plus:

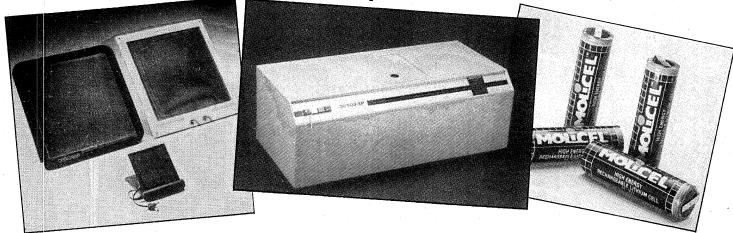
RGB-to-Composite Video Encoder, Health Hazards in The Electronics Industry, Tech Tips, your letters, reviews, news, opinions and lots more.



FEATURE

THE MAIN EVENT

... and, in the event, AT&T came and so did ETI — neither of us, we're pleased to say, having been blown up. Andrew Armstrong sends in a report from the British Electronics Week exhibition and picks out one or two salient points.



here is a theory going round that less people are

taking up electronics as a hobby than in days of yore. This is as maybe, but there is plenty of expansion in the professional area. At this year's British Electronics Week there were said to be about 1000 exhibitors. Certainly there were too many to glance at properly in a day, a far cry from the time when the show occupied a couple of rooms at the Grosvenor House Hotel.

Nominally the show was divided into four sections: The All Electronics Show, Electronic Product Design, Circuit Technology, and Fibre Optics. The first three of these were at Olympia, the last at Earls Court, with a coach shuttle service between the two venues.

Due to lack of time and stamina I gave the Earls Court part a miss. The Olympia part of the show was very popular, and it was sometimes difficult to move through the crowds.

If the show is a pointer to general trends, then I would say that the use of surface mount technology is steadily increasing, and is no longer quite so much of a black art. It is, of course, almost impossible to use a soldering iron on components on a surface mount board, so the ever-obliging manufacturers of assembly equipment will sell you a rework station for a cost just less than the national debt. This uses a very narrow stream of hot air to melt and reflow solder to enable individual components to be fixed or removed. It is the surface mount equivalent of the soldering iron. Custom and semi-custom ICs were also much in

evidence, with the inevitable accompaniement of a large range of computer aided design facilities.

Another sign of the increasing complexity of silicon technology was the profusion of static control products. Nowadays ICs with very small conductor areas can be damaged by an internal static discharge caused by an external static charge, and yet continue to work for some time before the inevitable failure

occurs. What may happen, for example, is that a hole is punched in an insulating layer, and some conductor material is transferred into it. In use, ion migration either reinforces the plated hole into a short circuit, or breaks the damaged conductor.

A company called Hartley Measurements has brought out a machine called the Autozap, which simulates the effect of a static discharge caused by the human body. This should permit reproducible evaluation of the resistance of devices to static.

There were many other items of interest. There were some very bright but tiny surface mount LEDs, and some seven segment LED displays with a surface mount driver chip on the back of the packages (see last month's News Digest). At a quick glance it looks just like an ordinary seven segment display. Allbatteries had a couple of interesting products -

Allbatteries had a couple of interesting products — excellent solutions on the prowl for problems. The first is a range of solar power panels, the largest of which is reputed to supply 14.8V at over an amp in bright sunlight, and perhaps 200mA on an ordinary British day! At £297 it is an expensive way to charge up a car battery in a week or two, but it produces enough power to be useful and may find applications in remote locations in remote locations.

I think that they will sell far more of the new rechargeable lithium batteries. These have double the energy density of nickel cadmium cells, and have the traditional lithium feature of low self discharge. They are very new, and 'handmade', so they cost around £10 each. When automatic machinery is in operation and the price drops, I think NiCads will find it hard to compete. The sale of primary cells may also be hit by the effect of this and the environmental concern over the disposal of used alkaline manganese cells. Altogether the show was a 'don't miss' event,

though these few words cannot begin to do it justice.

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Where a project has apparently been constructed correctly but does not work, we will need a description of its behaviour and some sensible test readings and drawings of oscillograms if appropriate. With a bit of luck, by taking these measurements you'll discover what's wrong yourself. Please do not send us any hardware (except as a giftl);

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OOPS!

Corrections to projects are listed below and normally appear for several months. Large corrections are published just once, after which a note will be inserted to say that a correction exists and that copies can be obtained by sending in an SAE.

Infra-red Intruder Alarm (August, 1984)

Q11, VN10LK, should be either a VN10LM or a VN10KM. R19 and R20 on Fig.5, p.59, should be, R18 and R19. R20 is missing and should be soldered between Q6 collector and a suitable point on the +ve rail, preferably on the underside of the board. There are two C18s — the one on the upper left should be marked C17. C12 is missing. It should be connected just below L2, from Q5 collector to the track connected to R15, C8 and R13. Pads are available. The +ve on C8 is not marked: it should be incorrectly marked C11.

Noise About Noise (July 1985)

In Fig. 5 on page 24, no connection should be shown between the cathode of the diode and the negative side of the 470u capacitor.

The ETI Activator (January, 1986)

Several items were mssing from the parts list (p.34). Resistors R33, 93 and 94 are all 100k; R,7, 45, 46 and 47 are all 1M0; R40 is 47k; R80 is 100k. The list includes two R68's --- the second (15k) should be R88. There is no R79 anywhere — for some reason, the number was overlooked. Capacitor C17 is missing — it should be 4μ 7 40V minelect. SW1 is missing. It should be a DPDT push-button switch. On the circuit diagram (p.32), the resistor between C5 and pin 10 of IC2c is R8, 10k. R82 should be marked 2k7, R20 should be 39k, R21 should be 1k0 and the component between C10 and R27 is a 10k resistor, R25 (not C25). Some of the IC numberings do not correspond with the PCB overlay. IC8d and IC8c should be exchanged and the pin numbers on v the new IC8d and on IC1d and 2d should read, clockwise from the output, 14, 12 and 13. The new IC8c now has pin numbers 8, 10 and 9 (clockwise from the output). IC8a and 8b should be exchanged and the new IC8a should have pins numbered 1, 3 and 2 (clockwise from the output.). Likewise, IC8b should have pins numbered 7, 5 and 6. R52 appears twice on the overlay (p.34). The one connected to pin 6 and IC2 should be R11. The parts list and circuit diagram includes two resistors (R27 and 67) specified at 14k each. 15k is the nearest preferred value, although you might prefer to use a 12k fixed and 4k7 trimmer in series to adjust for optimum setting.

date of

Digibaro (February 1986)

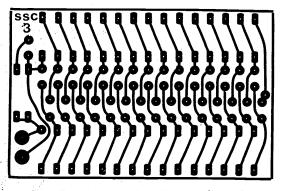
Capacitors C1, C3, C5 and C7 should be 470u 25V types as shown on the circuit diagram, not 47u 25V types as stated in the parts list. We have also been told that one of the companies mentioned in Buylines, Hawke Electronics, no longer supply the MPX100a pressure transducer. The other company recommended, Macro Marketing, should still be able to help.

LED5 on Fig. 7, page 28, the component overlay, is shown as having 16 pins. It should have 18 pins and be extended rightwards to the two pads shown. In the author's prototype the LED displays used were both MAN6710 2-digit types, LED4 having pins 16, 17 and 18 removed.

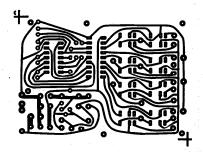
RS232-Centronics Converter (March, 1986)

On the circuit diagram (Fig. 2, p. 53), pin 11 of IC2d should be marked pin 13. Pin 10 of IC3 is missing and should be shown connected to ground. Pin 9 of IC7f becomes pin 8 and vice versa. Also, in Table 1, the figure '8' in column SW1b should be a zero and the '8' belongs in the 'DATA BITS' column. The specification of 74LS121 and 74LS07 is wrong, since LS types do not exist for these devices. They should be replaced with standard TTL Finally, some confusion seems to have been generated over XTAL1. Although not mentioned in the text, a simple calculation should demonstrate that XTAL1 needs to be 6.144 MHz to produce the baud rates shown.

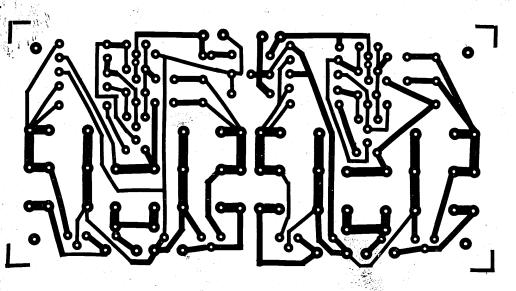
PCB FOIL PATTERNS



The foil pattern for the Direct-Ion board.

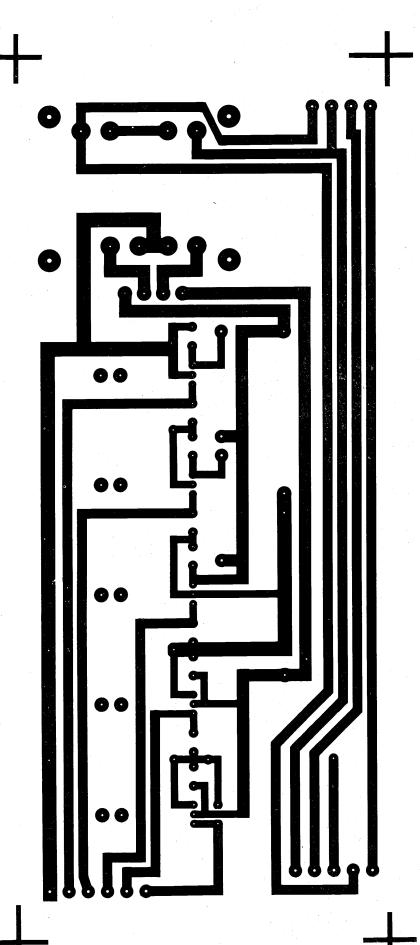


The BBC motor controller foil pattern.



The foil pattern for the Moving coil stage of the Upgradeable Amplifier.

PCB FOIL PATTERNS



The foil pattern for the Digital Sound Sampler PSU.

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ETI JULY 1986

OPEN CHANNEL

For many months' now I have refrained from making comment about GEC's proposed takeover of Plessey. Both management and employees of Plessey are totally opposed to the takeover, and have referred it to the Monopolies Commission in an attempt to prevent it. The main argument against making comment is that whatever I write takes so long to come into print that it would be out-of-date before the reader sees it. This is a very real problem with something as volatile as the GEC/Plessey affair. However, it has been dragging on for so long that I can refrain no more.

Plessey's argument against the takeover relies on the fact that the company's acquisition will give GEC a monopolistic control of the market. But, as I am sure the Commission already knows, it won't. There are other companies, albeit much smaller, which design, manufacture and market similar telecommunications products - all of which are in direct competition with the

likes of GEC and Plessey. So the argument against a monopoly (which is why the bid was referred to the Commission in the first place) falls down almost immediately.

The Trades Unions stance against the takeover seems, in my view, a much more logical argument, on the other hand. The EETPU suggests that the takeover cannot give any increase in organisational efficiency and that the proposed single organisation will be so large that it will actually be less effficient. The Union is also, understandably, against the takeover on the grounds that a large number of job cuts will probably result.

However, the main function of the Monopolies Commission in this argument is to decide whether GEC would become a monopoly-holding company if the takeover comes about. As I said earlier, it won't - therefore the Commission has no alternative but to overrule Plessey. Or does the Commission know different?

Band III

While the topic is on GEC, I should mention the new Band III national trunked radio network which is due to be brought into service in about seven or eight months' time. There are only two operators for the network: GEC and a consortium including Pye, Racal and Securicor. Detailed planning is not yet complete and no-one knows how it will work. but already there would seem to be a problem regarding the amount of radio spectrum allocated for the service. The operators, led by GEC, want more, but the Government won't let them have it.

But be fair. There is only a limited amount of radio spectrum, and it's not everyday that a significant portion of it becomes available as this portion did (the old VHF television frequencies). And other users want their bits, too. GEC will just have to make do with Plessey instead (that's if I'm right).

Data Day

In just over a year's time the BBC will start to transmit data signals along with regular broadcasts. These signals will not interfere with your pleasure when tuned to Radio 4's Afternoon Play and, in fact, may help to improve your reception! The signals will include information about the frequencies being used for broadcast purposes, station identification, and what's being broadcast on other stations.

All of this information is not a lot of good for your average tranny, of course, and it will only be a new generation of 'smart' radios which will be capable of receiving and using it. The idea is that, by decoding and using the information, the smart radio will be able to stay tuned to a particular station by itself, receiving the strongest transmission and hence giving the best sound output. This, I hear you say, is not a lot of good to your average tranny which. under most circumstances, sits in one place tuned only to one station anyway.

On the other hand, and this is the clever bit, radios in cars will certainly benefit. As a car moves around, so does the frequency at which best reception of any particular station will occur. A smart car radio will be able, by decoding the data signals, to follow the strongest transmission and give the best reception. Also, the user will be able to choose the type of programme received: the radio will pick out and automatically tune to, for example, weather announcements on other stations.

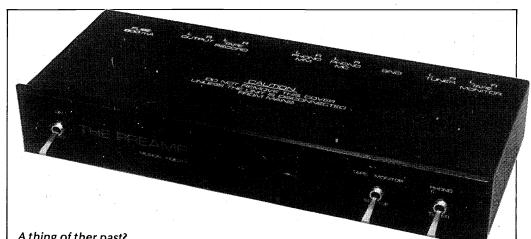
Wonderful stuff, this technology. **Keith Brindley**

PLAYBACK

For many years, preamplifiers have been designed to accept flat' line' inputs from tuners and tape decks at around 100-200mV. After the volume and balance controls, further amplification raises the signal to about 1 volt, enough to drive the power amplifier. With disc as the main sound source and the cartridge needing amplification from a very low level, the need to drive a power amp directly from a tape deck or tuner never arose.

Then along comes CD, its audio output level of around 2V being more than enough to drive power amps directly. To avoid losses in sound quality from active preamps, the passive preamp is created. As many modern power amps have extra sensitivity, and tuners and tape decks extra gain to cover matching errors it has proved possible to connect a whole system (without a turntable) using only a passive pre-– just volume control. amp switches and input and output sockets.

So why do CDs have such a high output? Presumably a high output enables much better signal to noise and distortion figures.



A thing of ther past?

The use of digital circuitry in audio applications is more likely to increase than disappear, and manufacturers are not likely to change their output levels if this degrades performance or if D to A convertors work at that level. We cannot really accept two completely different line input levels to amplifiers with the pre-topower level roughly halfway in between.

It is now time to standardise. Modern hi-fi systems do not require tone controls so it makes sense to arrange all high level outputs so that they can drive a power

amp directly using only a volume control and switch. Where tone control is needed, a graphic equaliser will do a better job. Avoiding an additional active gain stage saves on preamp power supply costs and gives better sound quality.

Why not go one stage further? Why not include all the parts of a passive preamp in the power amplifier. The savings in preamp case and cable could either be passed on to the purchaser or used to improve sound quality.

But what of the analogue disc user? A preamp will always be

needed to provide RIAA equalisation and to raise the signal to 'line' level. The opportunity is there to provide specialised disc preamps to go with the turntable. Or why not inside the turntable where the use of balanced input amplifiers would provide such benefits as low radio interference, etc. And one less wellfinished box is required!

These developments seem logical, and would improve performance and save money. Whether or not they take place remains to be seen.

Graham Nalty

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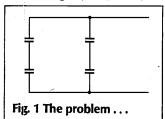
ETI JULY 1986

ALF'S PUZZLE

The story so far:

Alfand Auntie Static are locked in mortal combat for control of the puzzle page. The key to supreme power lies in the legendary capacitor puzzle which was featured in ETI, May 1986. To restate it:-

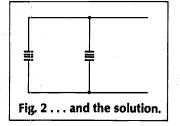
Four 2μ F capacitors are connected as shown in Fig. 1. The arrangement should be equivalent to a single 2μ F capacitor, and



if the network is charged to 10V, the energy of 25μ J per capacitor adds up to a total of 100μ J, as we would expect for a single 2μ F capacitor charged to 10V. The charge on each capacitor in the network, however, is 10μ C, so the total charge on all four capacitors will be 40μ C — twice as much as for a single 2μ F capacitor. Does this mean that it would take twice as much current for a given time to charge the network as it would for a single 2μ F capacitor?

Alf, being completely out of his depth, solicited the help of ETI readers to solve the puzzle, offering a prize of £10 for the correct answer that was easiest to understand. Now read on ...

Alf's choice, from the many correct replies, was difficult to make, but eventually he selected Bernard Chalk's explanation as being the most likely to be understood by a ten-year-old (and therefore by Alf himself):



The solution to the puzzle can be explained by drawing the series/parallel arrangement of capacitors with the inner connecting leads between the series capacitors reduced, as shown in Fig. 2. The charge on the inside plates is brought together and cancels, leaving only the charge of 10μ C on the series capacitors, and therefore providing a total charge of 20μ C on the arrangement. The argument in the problem did not distinguish between charge provided by the power supply (only 20μ C) and charge that was merely rearranged between the plates (also 20μ C).

Bernard Chalk, Southend on Sea,

Essex.'

Two other solutions, which Alf thinks worthy of a special mention for their simplicity, were sent in by Patrick Gaffney of Montenotte Park, Cork, and by Noel Moore of Belfast. Mr. Gaffney points out that if a resistor is connected across the charged network, only 20µC will flow through it, the rest of the charge being simply redistributed between the capacitors, so externally the network behaves like a single 2μ F capacitor. Mr. Moore says that Alf could have used the same spurious argument to claim that a series of capacitors, each with a charge Q, would require nQ coulombs from the supply, rather than the Q coulombs actually neeeded.

Auntie Static claims that the problem is just as absurd as adding together the currents through all the components in a circuit, and then claiming that this must be the total current provided by the supply. (Just think of two resistors in series, for instance.) Adding the charges in a circuit is equally meaningless, but since we are more used to thinking in terms of currents, the flaw in the argument is not so easy to spot.

As the power struggle between Alf and Auntie has yet to be resolved, we have decided that neither of them can provide the puzzle for this month. Instead, we have an offering, sent in by reader Henry Earle, which — we hope — will finish with capacitors once and for all.

'Insert a resistor R between two 2μ F capacitors, then charge up to 10V. When the charging is complete, the charge on the capacitors, and the energy stored, will be just the same as if there had been no resistor there at all. The resistor should be 'invisible' to the power supply, since the only current flowing through it is from the charge redistribution between the capacitors, yet it still dissipates an amount of energy given by $W = \frac{5}{5}$ I²R dt = 50 μ J. Connect an AC supply and you have a free room heater!

Henry Earle,

Nottingham.'

Well, there it is. Any comments? Is Mr. Earle's room heater really free? If not, how does the supply'know' that there is a resistor between the capacitors.

In the straightforward case of simply charging a capacitor to 10V, all the energy in the capaci-

tor can be recovered - none has been dissipated. With a resistor as in Mr. Earle's puzzle — the same amount of charge seems to be transferred from the power supply at the same voltage, involving the same amount of energy. Yet some energy will be dissipated by the resistor. The energy stored by the capacitors is equal in both cases, and should be qually recoverable (in the second case, we can short out the resistor as soon as the charging is complete.) So where does the extra energy which the resistor dissipates come from?

Mr. Earle goes on to point out, in his letter, that the energy dissipated is independent of the value of the resistor, and will always be equal to the amount stored by the capacitors.

Once again, a \pm 10 prize for the simplest and most elegant correct solution received.

The answer to last month's puzzle.

The circuit is a voltage to current converter. Let's suppose the input is at 1V, floating with respect to the op-amp's supply, with the 'resistor' side of the input more positive than the 'op-amp' side. If the output is not loaded at all, the non-inverting input of the opamp will be at +1V with respect to the inverting input, and as a result the output will rise to its most positive voltage.

With a load on the output of the circuit, the op-amp's output (and therefore the 'left hand side' of the resistor R, by virtue of the input voltage) will rise until the current to the load, via R, is enough to drop 1V across the resistor, at which point the noninverting and inverting inputs of the op-amp will be at the same voltage. The current into the load will be 1/R, or for a general input voltage Vit will be V/R, regardless of the resistance of the load.

Thus the circuit acts as a voltage to current converter, delivering a current of V/R to the load (which would normally be connected to 0V, and not V- as shown, although the circuit will work in this configuration for opamps which allow the negative supply as a common mode input).

The circuit is not often seen, since the requirement that the input should be floating with respect to the supply, and that the entire output current should flow through the input, make it impractical in many applications.

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----- 3) Japanese printer manufacturer (5).

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- 7) Underwater object detector using sound waves (5). Parity (4). 8)
- A square wave has a leading 9) one and a trailing one (4).
- 11) capacitor, used to reduce the negative feedback effect of an emitter resistor to AC signals (6).
- 14) Two pole, two way switch (1,1,1,1).
- When 13 down affects 9 15) across, this becomes longer (4, 4).
- 17) Used in both televisions and 'scopes (1,1,1).
- 18) The maximum a meter can show (1,1,1). The Y-axis of an oscillo-
 - scope (8).
 - 21) Wound component (4). 22) Sofware package to create
 - and alter text files (6). A BASIC command (4).
- Cable_ , a collection 27) of discrete wires tied or wrapped together (4). Modulator/demodulator 28)
 - (short form) (5).
 - 29) To wipe magnetic tape clean (5).

DOWN

1) Major Japanese domestic

electronic appliance manufacturer (4).

- 2) Basic command associated with read and restore (4).
- European domestic electronics company (3).
- The old name for a stylus 5) (6).
- 6) Magnetic recording tape transducers (5).
- To convert an analogue value into a binary number 10) (8).
- 11) The colour which signifies zero (5).
- 12) Microprocessor input/output channel (4).
- 13) The speed at which a circuit can react to a sudden change in input, measured in V μs^{-1} (4, 4).
- 16) Digital function block, capable of simple binary sums (5).
- 18) How to describe a circuit with uniform frequency response within a specified bandwidth (4).
- 19) Amplitude (6). 20)
 - Mechanical man or industrial tool (5).
- Tool for melting solder (4). 23) 24) No connection (4).
- Abbreviation for the mathe-26) matical expression used to calculate the average power available from a wave form (1.1.1).

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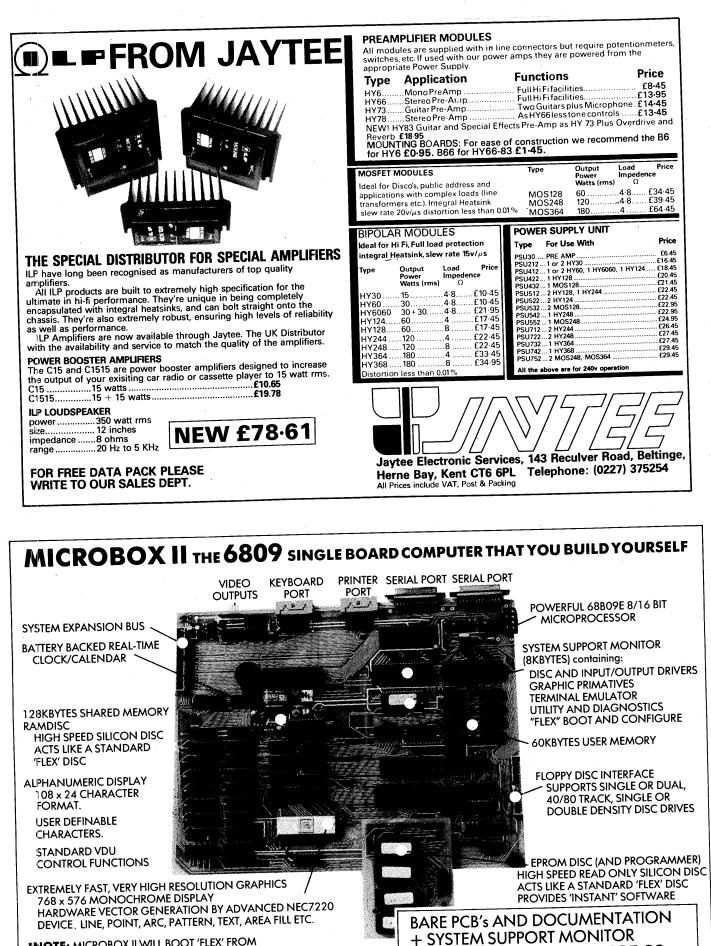
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