

## VIO....ROBOTICS







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	MAIL ORDER, CALLERS WELCOME Tel. Watford (0923) 37774/40588 Telex. 8956	095	AC128/7 35 BC3 AC141/2 35 BC4 AC176 35 BC4 AC187 35 BC5 AC188 35 BC5 AC188 35 BC5 ACY19/21 75 BC5	37/8 15 BF43 41/61 34 BF43 77 40 BF53 16/7 40 BFR 47/8 12 BFR 49C 15 BFR	51 40 94/5 40 94/5 30 39/40 30 41/79 25 80/81 25	MPSU56 60 OC26 170 OC28 220 OC35 50 OC36/41 75 OC42/75 75	ZTX302 16 ZTX303 25 ZTX304 17 ZTX326 30 ZTX451 23 ZTX500 14	2N3906 17 2N4037 60 2N4058 15 2N4061/2 15 2N4264 30 2N4266 25	2SJ83         225           2SJ85         225           3N128         115           3N140         115           40315         90           40316         95
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1	ELECTROLYTIC CAPACITORS: (Values in uP) 500v; 10uf 52; 47 78p; 63V; 0,47, 1,0, 1,5, 2,2, 15, 22 12p; 33 15p; 47 12p; 68 20p; 100 19p; 220 26p; 1000 70p; 2200 99p; 50V; 68 20p; 40V; 22 p; 33 12p; 330, 470 32p; 1000 48p; 2200 50p; 2300 75v; 47, 10, 22, 47, 8p; 100 11p; 330 22p; 470 25p; 680, 1000 34p; 1500 42p; 2200 50p; 3300 76p; 4700 92p; 16V; 47, 88, 10 16p; 470 02p; 680, 300, 500, 31p; 500, 31p; 2200 500; 3300 76p; 4700 72p; 470 25p; 680, 1000 27p; 500, 31p; 500, 31p; 2200 500; 3300 76p; 4700 72p; 470 25p; 470, 25p; 47	3.3, 4.7 8p 10 10p; 100 17p; 220 24p; 150 12p; 220 15p; 0 9p; 125 12p; 330	BC108B 14 BD1 BC108C 14 BD1 BC109 12 BD1 BC109B 14 BD1 BC109C 14 BD1 BC114/5 30 BD2 BC117/6 25 BD2	36/37 40 85Y 38/39 40 85Y 40 40 8U1 44/45 198 8U2 58 68 8U2 05/6 110 8U2	26 35 95 35 05 180 05 180 06 200 08 200 69C 225	TIP32C         45           TIP33A         70           TIP33C         75           TIP34A         85           TIP34A         105           TIP35A         120           TIP35C         130	2N2190 325 2N2219A 28 2N2220A 28 2N2221A 25 2N2222A 25 2N2222A 25 2N2368 25 2N2368 18	2N5485 36 2N5777 45 2N5879 180 2N6027 32 2SA671 250 2SA671 250 2SA715 75 2SC495 85	74 HC HC00 55 HC02 55 HC08 55 HC10 55 HC20 55 HC20 55
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	POLYESTER RADIAL LEAD CAPACITORS: 250V         FEED THROUGH         21           10n, 15n, 22n, 27n 6p; 33, 47n, 68n, 100n 8p; 1550, 220         FEED THROUGH         21           10p; 330n, 470n 15p; 680n 10p; 1u5 40p; 2u2 48p.         1000pF/450V         10p	50V nF, 1n5, 2n2, n3, 4n7, 6n8, 0n, 15n 7p	BC149C 15 BF1 BC182L 10 BF1 BC183L 10 BF1 BC184L 10 BF1 BC184L 10 BF1	67 35 MJE 73 35 MJE 77 35 MJE 78 35 MJE 79 40 MPF	521 96 2955 98 3055 70 102 40 103 30	TIP141/2 120 TIP147 120 TIP2955 70 TIP3055 70 TIS43 50	2N3055 50 2N3252 46 2N3441/2140 2N3614/5180 2N3663 20	2SC1676 140 2SC1679 180 2SC1923 65 2SC1945 225 2SC1963 80	HC160 35 HC161 135 HC164 135 HC165 270 HC173 120
	TANTALUM BEAD CAPACITORS         POTENTIOMETERS: Carbon Track.           35V: 0.1uF 0.22. 0.33 15p 0.47, 0.68.         Rotary 0.25W Log & LIN Values.           10. 1.5 16p; 2.2. 0.3 18p; 47, 0.8 22p         470R; 1K & 2K (Linear only)           10 28p; 16V: 2.2. 3.3 18p; 47, 6.8. 10         Single Gang         35p           16p; 15, 52; 24.59; 33, 47 50p; 100         5K - 2M Single Gang Log & Lin         35p           96p; 10V: 15, 22, 26p; 33, 47 50p; 100         5K - 2M Single Gang Log & Lin         35p           90rt 6V: 100 85n         5K - 2M Single Gang DP Switch         95p	80, 220, 270, 30, 390, 470, 8 p 90, 560, 12 p 20, 1000, 11 p 000V 000, 1200, 10 p 500, 1800, 12 p	BC186/7 28 BF1 BC188 6/7 28 BF1 BC12 10 BF1 BC212L 12 BF2 BC213L 10 BF2 BC213L 10 BF2 BC214L 10 BF2 BC214L 12 BF2	84/5 38 MPP 94/5 12 MPP 98/9 18 MPP 98/9 18 MPP 200 80 MPS 2448 40 MPS 248 40 MPS 5564 45 MPS	104         30           105         30           106         40           A05         30           A08         30           A12         32           A55         30           A56         30	TIS44/5 45 TIS88A 50 TIS90/91 30 UC734 99 VK1010 99 VK1010 99 VN10KM 70 VN46AF 95 VN66AF 110	2N3702/3 10 2N3704/5 10 2N3706/7 10 2N3708/9 10 2N3708/9 10 2N3713 140 2N3771 179 2N3772 195 2N3773 210	25C1957 80 25C1959 160 25C2028 85 25C2029 200 25C2078 170 25C2091 85 25C2091 85 25C2314 86 25C2166 105	HC174 120 HC175 120 HC240 195 HC249 195 HC241 195 HC242 135 HC244 195 HC245 195
	Subjective         Subject	20n, 270n, 15p 30n, 300n, 20p 70n, 560n, 26p 80n, 30p uF 34p, 2u2, 50p	BC237/8         15         BF2           BC256B         35         BF2           BC307B         15         BF2           BC308         16         BF2           BC316         80         BF3           BC327         15         BF3	566         50         MPS           557/8         32         MPS           559         40         MPS           175         55         MPS           136/7         35         MPS           194         40         MPS	5470 40 5002 58 5005 60 5006 60 5052 65 5055 60	VN88AF 120 VN89AF 120 ZTX107/8 12 ZTX109 12 ZTX212 28 ZTX300 13	2N3819 35 2N3820 60 2N3822/3 60 2N3866 90 2N3903/4 18 2N3905/6 15	2SC2335 200 2SC2547 40 2SC2612 200 2SD234 74 2SK45 80 2SK288 225	HC259 270 HC373 225 HC374 225 HC393 225 HC640 240 HC645 240
	CERAMIC CAPACITORS 50V:         PRESET POTENTIOMETERS           Range: 0.5pF to 10nF 4p. 15nF, 22nF         0.1W Miniature Vertical or           33nF: 47nF 5p. 100nF/300V 7p.         Honzontal. 100R to 4M7         8p           200nF/8V 8p.         0.25W Larget 100R to 3M3 Horz         12p	ACCESS Orders ust phone your orders through. We do the rest	CA3035 255 M CA3036 270 M CA3043 278 M CA3045 385 M CA3046 385 M	1706B1 150 151513L 230 151515L 320 151516L 475 151516L 475	TDA1022 TDA1024 TDA1034 TDA1490 TDA1490	499 7451 115 7453 350 7454 350 7460 7470	30 74283 1 30 74284 4 30 74285 3 30 74285 3 30 74286 5 50 74286 1	00 5470 325 40 5471 600 00 5472 400 80 5474 400 20 5475 425	LS181 190 LS183 190 LS190 85 LS191 85 LS191 85
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	SILVER MICA (Values in pF)         COMPUTEB         6123         160           2,33,47,56,85,21,01,21,55,18,         Hitachi         2564         8150         800           25,27,33,39,47,50,56,68,75,82,         B150         800         8154         750           200,220,250,270,300,330,360,         E12         1100         120         160         8154         750           390,470,800,800,820         21 p teach         2102L         160         8156         400           100,1220,1800,1800,2200         30 p each         2764         2147         2306         811,596         178           300,4700,DF         800p         250ns         2532         350         811,896         178           3000,4700,DF         220,220         30p each         274,8         2513         306         8198         178           2540F         27654         800F         2532         350         811,896         178           2540F         27128         25132         450         8202         225         2564         8002         225         2564         8002         225         2564         8002         225         2564         8002         225         2564         8002         225	MC6845         625           MC6846         625           MK3866-2M         £7           MM5280D         696           MM5307         1275           MM5307         275           MM5817A         865           MM5217A         875           MM74C922         420           RO-3:2513L         700           SAA5050         875	CA3081 180 M CA3085 180 M CA3085 80 M CA3085 200 M CA3080AC 375 M CA3123E 165 M CA3124 165 M CA3140 45 M CA3161 90 M CA3161 90 M CA3161 2525 M CA3189 275 M	CC1303         95           CC1304P         260           CC1310P         150           CC1445         250           CC145106         695           CC1455         50           CC1455         300           CC1469         300           CC1494         695           CC1495         350           CC1495         350           CC1495         350           CC1495         225	TDA2020 TDA2030 TDB0791 TL170 TL430C TL497A TL507 TL509 TL061CP TL062CP TL062CP TL064CN TL071CP	320 7476 190 7480 420 7481 90 7482 90 7483 185 7483 110 7486 110 7486 110 7486 40 7491 95 7491 95 7492 40 7493	45         74351           60         74365           175         74365           100         74367           100         74367           100         74368           105         74376           100         74380           40         74380           200         74428           55         74490           70         74428           100         74362	80         1500         25           70         LS01         25           70         LS01         25           20         LS03         25           20         LS03         25           20         LS04         26           200         LS05         28           201         LS04         25           202         LS03         25           203         LS05         28           204         LS09         25           205         LS10         25           205         LS12         28           205         LS12         28           205         LS12         28	LS196 85 LS197 85 LS21 85 LS240 80 LS243 85 LS243 95 LS243 95 LS244 80 LS244 80 LS244 80 LS244 80 LS245 120 LS247 105 LS248 105 LS248 105 LS249 105 LS247 75 LS249 105
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	ÖA81         10         IOA/600V         266         75322         140         652/0714         175         AF3-1013         300           OA85         10         254/200V         240         5522         6522A         545         COA72/4         150           OA90         8         254/600V         396         75325         00         6522A         340         COM8017         275           OA91         8         BY164         56         75365         00         6532RPiOT 650         D8748         6330         AF3         COA8116         700         04913         275         06         64587C         090         D8748         C33         064987C         6511         COM8116         700         063487C         06         64587C         090         D8748         233         04131         275         06         64587C         090         D87303         450         04202         8         75451/2         52         6511/CIA         650         DP8303         4350         04904         450         14914         4         ZENERS         75454         70         6592         00         DP8304         350         14914         4         50         553647         600 <td>280 SIO-1 850 280A SIO £9 280ASID-2 £9 555Cmos 95 702 75</td> <td>LM318N 150 S LM319 180 S LM324A 45 S LM334Z 150 S LM337Z 155 S LM337 275 S LM339 40 S LM348 60 S</td> <td>AB3271 485 AB4209 295 G3402 295 L490 350 L6270CD 150 N76227N 95 N76477 470 N76486 525</td> <td>7400 7401 7402 7403 7404 7405 7406 7406</td> <td>74163 74164 74165 25 74165 25 74167 25 74167 25 74170 30 74172 30 74173 40 74175</td> <td>100 5114 3 110 5124 3 130 5132 130 5133 250 5135 1 400 5138 1 130 5139 1 100 5140 100 5151 1</td> <td>20         LS96         90           00         LS107         40           90         LS109         43           50         LS112         43           60         LS113         40           10         LS114         40           70         LS122         70           80         LS123         100           80         LS124         123</td> <td>LS373 100 LS374 100 5 LS375 70 5 LS377 130 D LS378 95 D LS379 130 D LS378 95 0 LS379 130 D LS360 310 5 LS384 460</td>	280 SIO-1 850 280A SIO £9 280ASID-2 £9 555Cmos 95 702 75	LM318N 150 S LM319 180 S LM324A 45 S LM334Z 150 S LM337Z 155 S LM337 275 S LM339 40 S LM348 60 S	AB3271 485 AB4209 295 G3402 295 L490 350 L6270CD 150 N76227N 95 N76477 470 N76486 525	7400 7401 7402 7403 7404 7405 7406 7406	74163 74164 74165 25 74165 25 74167 25 74167 25 74170 30 74172 30 74173 40 74175	100 5114 3 110 5124 3 130 5132 130 5133 250 5135 1 400 5138 1 130 5139 1 100 5140 100 5151 1	20         LS96         90           00         LS107         40           90         LS109         43           50         LS112         43           60         LS113         40           10         LS114         40           70         LS122         70           80         LS123         100           80         LS124         123	LS373 100 LS374 100 5 LS375 70 5 LS377 130 D LS378 95 D LS379 130 D LS378 95 0 LS379 130 D LS360 310 5 LS384 460
	NA000 1/2         COUV         2/20         US361/H         LSCR           1N4006.4/5         6         Range 2V7 to 33V 400mW         SCR         6602         2420         US363/H         LS           1N4006.4/5         6         33V 400mW         THY RISTORS         6602         2450         DS8620         110           1N4006.4/5         6         33V 400mW         THY RISTORS         6603         6800         8600 <t< td=""><td>710 50 7418 pin 15 747C 14 pin 70 748C 8 pin 18 810 186 9400CJ 375 ADC6808 1000 AV-1-1320 228</td><td>LM358 50 S LM377 210 S LM377 495 T LM380 115 T LM381N 175 T LM382 200 T LM384 225 T LM386 90 T LM386 90 T LM387 200 T</td><td>P8629 350 P0256AL 425 A1002 485 A7120 140 A7204 150 A7205 90 A7222 150 A7210 125 AA661A 190 AA700 275</td><td>7408 7409 7410 7411 7412 7413 7414 7416 7417 7420 7421</td><td>25 74176 74177 25 74179 25 74180 25 74180 25 74180 25 74180 35 74182 35 74184 35 74184 35 74184 35 74184 35 74184 35 74184</td><td>5153         1           110         5157         2           130         5158         1           130         5162         3           300         5163         3           120         5174         3           170         5175         3           170         5188         3           120         5194         3</td><td>40         LS126         51           100         LS132         81           90         LS133         51           100         LS136         21           100         LS136         41           100         LS138         61           100         LS145         91           125         LS145         91           126         LS148         131           100         LS148         131</td><td>LS386         50           LS396         60           LS393         100           B         LS395           LS396         300           D         LS396           LS396         300           LS396         105           LS396         105           LS396         105           LS396         105           LS396         105           LS396         105           LS396         1305           LS445         125           LS445         125           LS445         140</td></t<>	710 50 7418 pin 15 747C 14 pin 70 748C 8 pin 18 810 186 9400CJ 375 ADC6808 1000 AV-1-1320 228	LM358 50 S LM377 210 S LM377 495 T LM380 115 T LM381N 175 T LM382 200 T LM384 225 T LM386 90 T LM386 90 T LM387 200 T	P8629 350 P0256AL 425 A1002 485 A7120 140 A7204 150 A7205 90 A7222 150 A7210 125 AA661A 190 AA700 275	7408 7409 7410 7411 7412 7413 7414 7416 7417 7420 7421	25 74176 74177 25 74179 25 74180 25 74180 25 74180 25 74180 35 74182 35 74184 35 74184 35 74184 35 74184 35 74184 35 74184	5153         1           110         5157         2           130         5158         1           130         5162         3           300         5163         3           120         5174         3           170         5175         3           170         5188         3           120         5194         3	40         LS126         51           100         LS132         81           90         LS133         51           100         LS136         21           100         LS136         41           100         LS138         61           100         LS145         91           125         LS145         91           126         LS148         131           100         LS148         131	LS386         50           LS396         60           LS393         100           B         LS395           LS396         300           D         LS396           LS396         300           LS396         105           LS396         105           LS396         105           LS396         105           LS396         105           LS396         105           LS396         1305           LS445         125           LS445         125           LS445         140
	15921         9         TRIACS         84600V         90         68821         200         FD1791         622           6A/100V         40         50         71791         623         800         75         FD1795         628           6A/200V         60         6833         800         H026501         75           6A/200V         60         6843         800         H026501         75           6A/200V         60         6843         800         H0315         250           8A/200V         56         81116         160         6847         650         H05301         225           8A/100V         56         1116         180         6847         650         H05301         225           8A/100V         56         7104         24         6852         250         IM6402         380           6A/200V         69         TIC45         24         6852         250         IM6605N         130	AY-1-5050 98 AY-1-5051 150 AY-1-6720 210 AY-3-1270 730 AY-3-1350 350 AY-3-8910 390 Booklet for AY-3-8910 150 AY-3-8912 500	LM393 85 T LM394CH 380 T LM558 170 T LM725CN 300 T LM725CN 300 T LM7458 35 T LM1458 35 T LM1871 300 T LM1889 400 T	AA900 395 AB1042 110 AD100 159 BA120S 70 BA900 395 AB1042 110 AD100 159 BA120S 70 BA120S 70	7422 7423 7425 7426 7427 7428 7430 7432 7433 7433	40 74193 36 74193 35 74194 35 74195 35 74195 35 74197 35 74198 30 74221 30 74221 30 74246	120 S201 100 S225 75 S226 120 S240 100 S241 200 S244 200 S244 200 S244 200 S258	LS151 7 LS153 7 LS154 15 LS154 15 LS155 7 T75 LS155 7 T75 LS157 6 LS157 6 LS158 6 LS158 6 LS158 7 LS158 7 LS157 7 LS	LS490 150 LS540 140 5 LS541 140 LS624 155 0 LS624 155 0 LS624 200 0 LS641 150 0 LS641 150 0 LS645 195 0 LS645 90
No. of Lot, House, Hous	BABOOV         115         TIC47         36         68854         750         MC1488         100           12A100V         78         2N5062         32         6859         64         MC1489         100           12A400V         82         N5064         36         6875         500         MC14411         675           12A400V         82         N5064         36         6875         500         MC14411         675           16A100V         105         2N3444         130         68000         530         MC3442         232         540           16A400V         105         6805         350         MC3445         205         MC3426         250         MC3452         250	AY-5-1317A 630 AY-5-1350 366 CA3011 130 CA3012 175 CA3014 275 CA3014 275 CA3018 85 CA3019 A0	LM2917 325 T LM3900 70 T LM3909N 85 T LM3911 195 T LM3914 300 T LM3915 346 T	BA990Q         350           CA220         350           CA270Q         350           CA270Q         350           CA280A         220           CA940         175           CA965         180	7438 7440 7441 7442 7443 7443 7445 7445	40 74248 30 74249 90 74251 100 74253 100 74263 110 74263 110 74265	145 S260 175 S262 1 90 S287 150 S288 75 S289 60 S301 180 S365	70 LS163 7 10 LS164 7 200 LS165 11 180 LS166 15 200 LS166 15 200 LS168 14 150 LS169 10 150 LS169 10	D LS670 170 LS673 890 D LS673 890 C LS674 800 C LS678 275 O LS682 250 C LS684 350
1	BB105B         40         25/500V         220         8085         £6         MC3468         225           BB106         40         25/800V         296         8085         8085         90         MC3467         225           BB109B         45         72800         125         512         25         8088         £15         1M6402         350	CA3020 210 CA3023 210 CA3028A 110	LM3916 300 T LM13600 150 T LS7220 260 T	DA1004 350 DA1008 310 DA1010 235	7447 7448 7450	95 /4276 110 74278 30 74279	130 5373 160 5374 80 5412	575 LS173 10 1575 LS174 7 LS175 7	50

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SWITCHES TOGGLE: 2A 250V SPST35/PDP40 SUB-MIN TOGGLE SPST of PDP40 SUB-MIN TOGGLE SPST of PDP40 SUB-MIN TOGGLE SPST of PDP40 SPST of PDP40 SUB-MIN SPST of PDP40 SUB-COMMENT SPST of PDP5 SUB-COMMENT	DIP SW (SPST) 4 way 85p; 6 10 way 125p (SPDT) 4 (Adjustable 10 way 125p (SPDT) 4 10 ole; 12 way 2 bo 1 pole; 21 01 2 way 2 bo 4 way, 4 pole; 21 0 3 wa ROTARY: Mains DP 25 ROTARY: Mains DP 25 ROTARY: Make-awit Make a multiway swit has adjustable stop. 6 wafes Imas 6 pole; Mechanism only WAFERS: (make befor switch mechanism. 1 way 3 pole; 4 way, 4 pole; Mining DP 44. Switch to Seacers 4 p. Screen 69 ROCKER: 10A/250V SP ROCKER: 10A/250V SP ROCKER: 10A/250V JD THUMBWHEEL Mini fr Decade Switch Module BCD. Switch Module	ATCHES           way 80p; 8 way 85p;           way 190p           ebreakl 10 fit the above poler 12 way 2 pole/8 /2 way 65p           way 50p2 Way 65p           way 65p 240p 38p           pDT 38p           pDT 38p           way 65p 240p 38p           way 65p 300p 465p           230p 480p 525p           way 65p 300p 465p           320p 480p 525p           way 65p 300p 480p 525p           way 60p 300p           370p 480p 525p           150 7185 43p           1202 7812 50p	VEROBOARD         0.1in           2% x 3%         95p           3% x 5         110p           3% x 5         125p           3% x 17         240p           4% x 17         59p           3% x 17         59p           5% x 170p         380p           5% x 170p         380p           5% x 100p         380p           5% x 100p         380p           5% x 10p         5%p           6% x 10p         7%p           COPPER CLA         Fibre           6% x 10p         17           DIL SOCKETS         Low Write           8 pim         50p           16 pim         10p         35p           16 pim         10p         35p           16 pim         10p         32p           16 pim         10p         32p           16 pim         10p         32p           16 pim         10p         32p           16 pim         30p         90p           20 pim <td< th=""><th>VC Board         195p           DIP Board         395p           Vero Sing         95p           Peroto DECe         Woro Sing           Vero Sing         95p           Eurobrick         480p           Size         395p           Eurobrick         95p           Eurobrick         95p           Superating         55p           DALO ETCH         RESIST PEN           Pus spare tip         100p           ULTRASONIC         40KHz           40KHz         475 pr           D BOAROS         90p           gle         Double           constration         17 196"           212 way         160p           215 way         160p           215 way         160p           215 way         160p           213 way 1200 =         243a way 100p           2430 way 200p         243a way 200p           2434 way 200p         243a way 200p           2434 way 200p         243a way 200p           2434 way 200p         243a way 50p           2434 way 200p         243a way 50p           2435 way 2500 pe         243a way 50p           2435 way 2500 pe</th><th>IDC CONNECTORS           PCB         Pings Fins         Fins         Pins         &lt;</th><th>PANEL           MeTERS           FSD           60 # 46 ± 35mm           C-500,A           C-100,A           C-500,A           C24           C25V           O,50V AC           S2758KHz           100KHz&lt;545           200KHz&lt;545           200KHz&lt;545           128MHz&lt;450           128MHz&lt;265           100KHz&lt;545           24576M           100KHz&lt;545           36864M&lt;300           4022MHz&lt;200           509           1000           524           36864M&lt;100           524           36864M&lt;300           4022MHz&lt;200</th><th>PELAYS         Ministure, enclosed, PCB mount.         SINGLE POLE Changeover File 3205 R Col. 12V DC. (10V516) File 320 ABC and 12V DC. (10V516) File 320 ABC and 12V DC. (10V716) File 320 ABC and 12V DC. (12V716) File 320 ABC an</th></td<>	VC Board         195p           DIP Board         395p           Vero Sing         95p           Peroto DECe         Woro Sing           Vero Sing         95p           Eurobrick         480p           Size         395p           Eurobrick         95p           Eurobrick         95p           Superating         55p           DALO ETCH         RESIST PEN           Pus spare tip         100p           ULTRASONIC         40KHz           40KHz         475 pr           D BOAROS         90p           gle         Double           constration         17 196"           212 way         160p           215 way         160p           215 way         160p           215 way         160p           213 way 1200 =         243a way 100p           2430 way 200p         243a way 200p           2434 way 200p         243a way 200p           2434 way 200p         243a way 200p           2434 way 200p         243a way 50p           2434 way 200p         243a way 50p           2435 way 2500 pe         243a way 50p           2435 way 2500 pe	IDC CONNECTORS           PCB         Pings Fins         Fins         Pins         <	PANEL           MeTERS           FSD           60 # 46 ± 35mm           C-500,A           C-100,A           C-500,A           C24           C25V           O,50V AC           S2758KHz           100KHz<545           200KHz<545           200KHz<545           128MHz<450           128MHz<265           100KHz<545           24576M           100KHz<545           36864M<300           4022MHz<200           509           1000           524           36864M<100           524           36864M<300           4022MHz<200	PELAYS         Ministure, enclosed, PCB mount.         SINGLE POLE Changeover File 3205 R Col. 12V DC. (10V516) File 320 ABC and 12V DC. (10V516) File 320 ABC and 12V DC. (10V716) File 320 ABC and 12V DC. (12V716) File 320 ABC an
474. 210/03.4. 219 2115/0.24 2115/0.24 2115/0.24 2115/0.25 21	280 280 280 280 280 280 280 280 280 280	100mA TO92 Pilastic p SV 78LOS 30p 8V 78LOS 30p 8V 78LOS 30p 8V 78LOS 30p 12V 78L12 30p 12V 78L12 30p 12V 78L12 30p 12V 78L12 30p 13SP 14009K 13SP LM317K 250p LM317K 250p LM317K 250p LM337 175p LM323 V 30p 78S40 225p 50	ackage 78L05 SOP  79L12 SOP 78L15 GOP 78L15 GOP 78L15 SOP 78L12 SOP 78L12 SOP 78L12 SOP 78L12 SOP 78L12 SOP 78L12 SOP 78L12 SOP 78L05 S	A 12 41 85 4 x 2 4 x 2 4 x 2 4 x 2 4 x 2 4 x 2 5 x 4 x 1 2 5 x 4 x 1 2 5 x 4 x 2 5 x 4 x 2 5 x 4 x 2 5 x 4 x 2 1 205 5 x 4 x 2 1 205 5 x 4 x 2 1 205 5 x 4 x 2 1 205 6 x 4 x 2 1 205 6 x 4 x 2 1 205 6 x 4 x 2 1 205 8 x 4 x 1 1 205 8 x 4 x 2 1 205 1 2 x 4 x 2 1 2 05 1 2 x 4 x 3 1 2 x 5 x 3	IDC 25 wey D' Plug 385p: Socket 450 25 wey 'D' CONNECTOR (RS233 Jumper Lead Cable Assembly 18' Iong Single end, Female 36 Iong Double Ended, M/M 936 Iong Double Ended, M/F 936 Iong Double Ended, M/F 937 Iong Double Ended, M/F 938 Iong Double Ended, M/F 938 Iong Double Ended, M/F 938 Iong Double Ended, M/F 939 Iong Double Ended, M/F 939 Iong Double Ended, M/F 939 Iong Double Ended, M/F 930 Iong Double Ende	p         12.5/MH2         130           p         12.5/84M         100           14.31814M         170         14.31814M           15.0/MH2         200         16.0/MH2           16.0/MH2         200         16.0/MH2           15.0/MH2         200         16.0/MH2           16.0/MH2         16.0/MH2         150           95.9         24.0/MH2         100           26.60/MH2         100         27.1.45M           18.0/MH2         240         19.0           27.1.45M         180         36.667M           19.9         10.0/MH2         240           19.9         10.0/MH2         300	25 Carrlage £7 Securicor <b>"SPECIAL</b> <b>OFFER"</b> 2764-250n5 350 325 27129-250n5 725 700 6116(JF-120n5 208 250 6264LP-150n5 750 725
4073           4000         20           4001         25         4076           4002         25         4077           4006         70         4076           4007         25         4082           4008         60         4082           4009         4086         4012           4011         25         4031           4012         25         4033           4013         35         4096           4014         60         4096           4015         60         4096           4016         4016         4096           4017         35         4098           4018         60         4162           4020         80         4162           4021         80         4162           4022         70         4163           4023         60         4162           4024         54         4410           4025         54         4194           4033         130         4415           4034         4446         4330           4035         70         4463           4034<	25 4538 25 4539 70 4541 25 4543 25 4549 25 4549 25 4553 25 4549 4 4556 4 4556 26 4554 27 4556 27 4556 27 4559 27 4559 28 4561 28 4569 29 4562 29 4562 29 4563 10 4562 29 4563 10 4566 10 5 4563 10 4566 10 5 4563 10 5 4582 10 5 4582 10 5 4583 10 5 4584 10 5 4584 10 5 4582 10 5 4583 10 5 40085 10 5 40085 10 5 40085 10 5 40085 10 40098 10 400098 10 400098 10 400098 10 400098 10 400098 10 400098 10 400098 10 400098 10 400098 10	B0         OPTO           80         ELECTRONIC           91         ELECTRONIC           92         ELECTRONIC           93         TIL210           94         TIL211 GRN           95         TIL212 Vel           93         TIL2202 "Red           94         TIL212 Vel           95         Red/Green           95         Beclolur           95         Red/Green           96         Green/Yellow           97         Vel           98         Red/Green or           99         Green/Yellow           90         Green/Yellow           90         Green/Yellow           90         HenghthreasRed           90         Green or Yellow           90         Green or Yellow           90         Green or Yellow           91         Green or Yellow           93         Frad Schabb           94         LD271 Intra Red           95         Red           96         Green or yellow           97         TIL32 Intra Red           90         Green or yellow           91         TIL32 Intra Red	S         PIN Low Professor           10         Professor           14         8 pin           15         16 pin           16         16 pin           17         18 pin           18         19 pin           19         20 pin           22         24 pin           23         24 pin           24 pin         24 pin           25         EPSON RX           26         EPSON RX           27         EPSON FX           28         CCC           29         Cable for all           20         EXAGATAX           23         SPARE UV           24         SWA SWT           25         GSCurifort Catl           26         EVARS WT           27         SWA SWT	BT ( B AT BT ( B BT ( B B B B B B B B B B C B B C B C C C C C C C C C C C C C	CONNECTOR           Mini Line Master         435           Mini Line Slave         295p           Line Master         370p           Line Slave         250p           A Dual Splitter         550p           BT Plug         65p           R CORNER         6209           C219         6316           C33e         6329           C209         6329           C339         6329           C339         6329           C30         6329           C30         639           C30         639           Stration Any of The ABOVE         640           Dates 1         67           To 25 Eproms Has a builtin         640           C30         659           Stration On Any of The ABOVE         640           Dates 1         57           Stration On Any of The ABOVE         000           Dates 27         57	A complete wordprok bisconter, bisconter, bi	A spectrum to full 48K with our. Very simple to fit. Fitting ed. 222 CMICRO COCCESSING CCACESSING C
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# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS

DIGEST

### TI Announce E-Beam Semi-Custom IC Service

E lectronic Equipment manufacturers will soon be able to have small quantities of semicustom ICs produced to their specific requirements at low cost and in about a third of the usual time. The service will be operated by Texas Instruments from their Bedford headquarters starting in the autumn and will use electron beam lithography, a technology which not previously been available commercially anywhere in the world.

TI will be supplying semicustom chips based upon 3micron single level HCMOS gate arrays. Customers will be able to design ICs on their own mini and micro computers using low-cost CAD packages supplied by Texas. Tested prototype ICs should be ready for delivery about two weeks after the design has been completed, a considerable improvement when compared with the six week period usually required by traditional procedures.

The electron beam system is faster because the circuit is literally carved out of a slice of silicon by a computer-guided beam. This takes substantially longer than the usual photographic method, but the time and expense saved through not having to make an elaborate photographic mask more than makes up for this and there is the added advantage that even single chips can be produced economically. This will enable the facility to be used for prototyping as well as for low-volume production, and chips developed using the system can be transferred to standard fabrication plants for later mass production.

TI say that the introduction of this service will enable European electronics manufacturers to produce specialised equipment for low-volume markets and still make a profit. They hope that this will help to insulate the European industry from the problems of fluctuating demand and fierce price cutting which currently characterise the high-volume markets dominated by Japan and the USA.

Texa Instruments Ltd, Manton Lane, Bedford Mk41 7PA, tel 0234 - 63211.



## Liquid Crystal Shutter

E pson have developed a liquid crystal display which is normally opaque but becomes transparent when activated. With a light placed behind it the unit combines all the advantages of LCDs with high brightness, and the manufacturers also expect it to find applications as a shutter in specialist cameras.

The display is known as the Black Shutter and uses a black dye which has very high light absorption qualities. This removes the need for the two polarisers found in conventional twisted nematic LCDs. The display has a contrast ratio of up to 25 to 1 and is available in two versions, one designed for use in cars which has a permitted temperature range of -30° Cto  $+80^{\circ}$ C and the other designed for use in consumer goods and rated for use over the range  $-10^{\circ}$ C to  $+60^{\circ}$ C.

Conventional LCD displays offer low consumption but emit no light and have a restricted viewing angle. The back-lit Black shutter not only offers high brightness which allows it to be seen under any lighting conditions but also has a wide viewing angle and still uses little power. Epson anticipate applications in a wide range of consumer goods and in large indoor and outdoor display units such as those found at railway stations, airports, sports grounds, etc. The Black shutter is already being used both in the UK and in Japan for motor car instrumentation.

Epson (UK) Ltd, Dorland House, 388 High Road, Wembley, Middlesex HA9 6UH, tel 01-902 8892.



## **Enclosed Rotary Switches**

Dean Electronics have introduced a range of rotary selector switches which are fully enclosed and can be sealed to provide protection from water, contaminants and most solvents. They are available in three sizes in a wide variety of contact arrangements and the manufacturers claim that they all meet or exceed the relevant military specifications.

The 3600 series switches are the largest in the new range at 1" diameter and are available with from one to six poles and from four to twelve ways. The indexing varies with the number of positions, being either  $30^\circ$ ,  $36^\circ$ ,  $45^\circ$ ,  $60^\circ$  or  $90^\circ$ . The positions are thus spaced evenly around the rotation of the switch and an adjustable end-stop is not necessary.

The 1800 series switches are 0.5" in diameter and are single pole with from two to sixteen ways. Three different indexing angles are available and they can be supplied with or without adjustable end-stops.

The smallest switches in the range are the 1500 series at 0.32" in diameter. They are available with two different indexing angles and have fixed end-stops. A number of switching arrangements can be accomodated and a printed circuit disc in the base of the switch allows BCD and other coding systems to be implemented. All three sizes in the range have self-cleaning roller bearing contacts for high current carrying capacity and pure silver contacts to give low resistance. Hardened steel sprockets and ball bearing detent mechanisms ensure a positive detent action and the rotors and stators are moulded from diallyl pthalate thermosetting plastic to give excellent electrical and mechanical properties. The manufacturers claim a mechanical life in excess of 100,000 cycles.

The switches meet or exceed the applicable M11-S-3768, Style SR20 requirements and can be supplied with an internal seal to provide full protection in harsh environments. They are expected to find applications in military equipment, in aircraft and in commercial fields such as industrial controls and medical electronics.

Dean Electronics Ltd, Glendale Park, Fernbank Road, Ascot, Berkshire SL5 8JB, tel 0344 -885 661.

Rapid Electronics	MAIL ORDERS: Unit 1, Hill Farm Industrial Estate, Boxted, Colchester, Essex CO4 5RD. Tel. Orders: Colchester (0206) 36412. Telex: 987756.
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# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS

### Modular Audio Processing System

T anrak is a 19" subrack assembly which accepts a range of plug-in audio processing and effects modules. It is aimed at musicians, smaller studios and others on a tight budget, allowing a system to be built up gradually as finances allow, and to reduce costs still further the rack and modules are available in kit form.

Eight modules are currently available, a compressor/limiter, a parametric equaliser, a multidelay unit which can be used for chorus and other effects, a noise gate, a dynamic filter, a modulation osciallator and input and output modules. Other modules are promsied including an infinite flanger and a microphone preamplifier. The modules slot in to a 4U (7") high 19" subrack which can house up to eleven of them plus a DC power supply.

The system is designed so that signals enter the system via a front panel jack on the input module, are processed by switching in and adjusting other modules as required, and then leave via another front panel jack on the output module which also has a headphone monitor socket. When used in this way the system can

handle mono signals only, but each module has a series of jack sockets which protrude through the rear of the subrack and can be used as a patchbay to permitstereo and other arrangements in which modules handle different signals. The noise gate, noise filter and compressor limiter are stereo modules so only one of each would be needed for stereo operation. Other units would have to be used in pairs.

The modules all have a high specification and have been optimised for operation at -10dBv, although all will work quite happily at 0DBm. The power supply provides  $\pm 12$ V DC at 500mA which is also available via a DIN socket on the rear panel to drive other equipment or a slave rack should eleven modules not be sufficient. The socket can also be used to plug in a supply from an external power unit.

The modules have black anodised front panels with orange lettering and matching knobs and switches. The aluminium subrack has a textured stove finish and can be supplied with a matching set of metal cover plates if it is to be used free standing. Blanking panels are available to cover unused module positions.

The subrack costs £47.95 ready built (backplane assembled but supplied flat ready for screwing together) or £33.95 in kit form. The power unit costs £42.95 ready built or £33.95 in kit form and the other modules range in price from £46.95 ready built for the input and output models (£32.95 in kit form) up to £110.95 for the multi-delay unit (£79.95 in kit form). All prices include VAT and postage. For those whoare little worried about tackling kit construction, the manufacturers also offer a getitgoing service which will repair any faults on a completed kit for a standard charge of 20% of the kit price.

For more information and detailed specifications of the various modules, contact Tantek Services, Enterprise House, Elder Way, Stevenage, Hertfordshire SG1 1TL, tel 10438 - 726155.



## **Budget Sound Level Meter**

T esting for compliance with noise legislation requires expensive Type 1 sound level meters, but there are many applications where the sophistication of such instruments is unnecessary. With this in mind, Castle Associates have introduced a general purpose noise meter which is designed to offer accurate, repeatable noise measurements at low cost.

The GA301 Noise Survey Meter is a Type 3 instrument which covers the range 35-130 dB(A) in 10dB steps. The result is displayed on an analogue meter and a switch selects either fast or slow response. The specification exceeds the requirements of British Standard 5969 for Type 3 sound level meters and also meets or exceeds the requirements of the equivalent foreign standards.

Suggested applications for the GA301 include the balancing of noise levels in different areas covered by large public address systems and demonstrations of the physics of noise in schools and other educational institutions. The meter could also be used to make initial surveys of noisy environments and for routine checking in factories, etc, allowing expensive Type 1 meters to be used only where there are definite grounds for suspecting a breach of the regulations. Any occupied area providing a reading within 3dB of the accepted limit (usually 90 dB(A) in this country) should be considered suspect and checked with a Type 1 instrument.

The GA301 is housed in a steel case which measures  $175 \times 54 \times$ 60mm. A calibration unit is also available. As a special introductory offer, Castle Associates are offering a kit which consists of the GA301, the calibrator and accessories in a small attache for £135.00, the usual cost of the meter and the calibrator alone.

Castle Associates Ltd, Salter Road, Cayton Low Road Industrial Estate, Scarborough, North Yorkshire YO11 3UZ, tel 0723 -584250.

# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS

### Thermally Compensated Infra-Red Detector

**E** ltec Instruments have developed a pyroelectric infrared detector with a unique crystal arrangement which compensates for unwanted signals generated by fluctuations in the ambient temperature of the detector housing itself.

The Model 407 consists of two lithium tantalate sensing elements and a JFET source follower housed in a standard TO-5 transistor package with optical filter. One centrally located active sensing element is exposed to infra-red radiation entering through the detector optical filter window while the second compensating element is shielded from outside radiation. The elements are connected electrically in a parallel opposed configuration which produces cancellation of signals received by both as a result of



thermal changes in the detector housing.

The 407 has an optical bandwidth of 1.5 to 1000um and an operating voltage range of 3-15V. The recommended operating temperature range is  $-10^{\circ}$  to  $+50^{\circ}$ C. Eltec say the device will allow reliable sensing in applications where temperature fluctuations previously presented a problem, and expect the 407 to be used in industrial control systems, infra-red telescopes and robotics as well as in a number of other applications.

Eltec Instruments Sa, Neugutstrasse 4, 8304 Wallisellen, Zurich, Switzerland.

## **Instant Thermal Joints**

C harcroft Electronics are distributing a thermal jointing film which can be used instead of liquid thermal compound when assembling power semiconductors onto heatsinks. The film is coated on each side with a compound which remains solid at room temperature but turns to a liquid when heated, thus wetting the thermal joint each time the equipment is operated.

Charcroft claim that Crayotherm offers a high electrical resistance combined with a high thermal conductivity and that it avoids the mess and contamina-

• Electrovalue have issued the June 1985 edition of their mailorder catalogue which remains valid until the end of September. The new catalogue has 48 A5 pages, four more than the previous issue, and includes an expanded range of test gear as well as other new lines and all the usual items. Electrovalue Ltd, 28 St. Judes Road, Englefield Green, Egham, Surrey TW20 OHB, tel 0784 - 33603. tion associated with liquid compounds. Unlike conventional elastometric insulators, Crayotherm will not harden with time and component failure caused by reduced heat dissipation across the joint is eliminated.

The film can be supplied in roll or sheet form or pre-cut to fit popular semiconductor packages such as T03, T036, T066 and D04. Charcroft say they will gladly supply free samples to readers.

Charcroft Electronics Ltd, Charcoft House, Sturner, Haverhill, Suffolk CB9 7XR, tel 0440 -705700.

• Impectrom are distributing a full-colour, 14-page, A4 catalogue from Sharp which describes their range of LEDs. The catalogue provides full technical information on over 500 LED indicators, arrays, backlights and alphanumeric and symbolic displays, and copies are available free-of-charge from Impectron Ltd, Foundry Lane, Horsham, West Susex RH13 5PX, tel 0403 - 50111.

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### Aid For People With Speech Impairments

C laudivs Converse is a speech synthesiser which can produce words, phrases and even sentences at the touch of a button. It has been developed by British Telecom to help people who cannot speak, and can be used both on its own and in conjunction with a telephone.

Claudivs stands for Calling Line Announcements Using Digitally Integrated Voice Synthesis, but is also named after the Roman emperor who had a speech defect. Up to 64 phrases selected by the user are recorded and then stored in digital form in the machine. This allows it to reproduce both male and female speech and to cope with any language. A built-in lodspeaker allows Claudivs to be used on its own for normal conversations but it can also be directly connected to a telephone line. When so connected, four red buttons provide instant dialling of ambulance and other emergency service numbers and a message including the caller's home address is automatically sent.

About 120 of the units are already in use by BT customers and more are on the way at a cost of about £250.00 each. One of the first users of the device is Beattie Brooks of Littlehampton, Sussex, who lost her voice after a throat operation two years ago. In spite of her disability she is the local secretary for the RSPCA and raises money for them by making soft toys. Many of the phrases recorded on her machine were chosen in conjunction with a speech therapist to help her in this work.

British Telecom PLC, 81 Newgate Street, London EC1A7AJ, tel 01-356 6591.

• Does not having your own mains adaptor make you blue, are you red with rage because your adaptor doesn't work or just green with envy because your friend's does? Then despair no longer, for First Castle Components have launched a range of low voltage mains adaptors in colours to suit your every mood. Whether they will also match your every equipment we do not know because the press release doesn't contain much technical information, but if you write to First Castle Components Ltd, 263 Church Road, Thundersley, Essex SS7 4QN, they'll probably give it to you in black and white.

KITS	17.40	OPTO	LED CLIPS	1	SH	71 21	ML 927	1.80
1000KB* Cock/Timer 6000* † Programmable	17.40	3mm red 9 3mm green 12	Smm red Smm green	12	556	40	ML928	1.80
imer	39.00	3mm yellow 12	Smm yellow	12	741 748	Z2 30	ML929 MM74C911	1.00
(101 Electronic Lock	11.50	triangular Mat Tace	10 Vall		AD590 AY38910	3.30 3.90	MM74C915 MM74C922	96 4.90
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Copies up to eight eproms at a time and accepts all single rail eproms up to 27256. Can reduce pro-gramming time by 80% by using manufacturer's suggested algorithms. Fixed Vpp of 21 & 25 volts and variable Vpp factory set at 12.5 volts LCD display with alpha moving message £395(b).

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SOFTY II This low cost intelligent eprom programmer can program 2716, 2516, 2532, 2732, and with an	DCO	DNN lo of 9	ECTO Ways	)RS
adaptor, 2564 and 2764. Displays 512 byte page on TV — has a serial and parallel // Oroutines. Can be used as an emulator, cassette interface. Softy il	MALE: Ang Pins Soider IDC FEMALE St Pin	120 60 175	180 85 275	23
UV ERASERS	Ang pins Solder	160 90	210 130 325	27
All erasers with built in safety switch and mains Indicator. UV1 B erases up to 6 eproms at a time £47(c)	St Hood Screw Lock	90 130	95 150	10

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These are fully cases and wired drives with slim line mechanisms of high quality, Shuggart A400 standard Interface. Drives supplied with cables manuals and formatting disc suitable for the BBC computer. All 80 track drives are supplied with 40/80 track switching as standard. All drives can operate in single or dual density format.

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PS400 with psu	) Plinth Version:
	2 x 100K 40T SS TD200P
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Above monitors are now available in plastic or met	alcases
please specify your regulrement.	.u. 04000,
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All monitors are supplied with leads suitable for	the BBC
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12	6264LP-15	£7.00

### (a) £8 (b) £2.50 (c) £1.50 (d) £1.00 **CONNECTOR SYSTEMS**

I.D. CONNECTORS           (Speedblock Type)           0         Reder Recep         Edge           ays         Pitg         Tacle         Conn.           20         90p         85p         120p           20         145p         125p         245p           26         175p         150p         240p           24         160p         160p         32op           20         160p         23op         140p           50         230p         200p         39op	Construction         0.1         0.156           2 · : 0-way         150g         300p           2 · : 10-way         150g         350p           2 × 12-way         150g         140p           2 × 23-way         225p         220p           2 × 25-way         225p         220p           2 × 25-way         225p         220p	AMPHENOL CONNECTORS           Solder ZOC           36 way plug         500 p         475p           36 way skt         550p         500p           24 way plug         IEEE         475p           16 way skt         500p         500p           24 way skt         1100         1100           11         1100         1100	TELEPHONE CONNECTORS           4 way plug         110p           6 way plug         150p           6 way rtang skt         160p           Flexible cable         4 way           6 way         50p/m           6 way         72p/m
D CONNECTORS No of Ways 9 15 25 37 LE: Pins 120 180 230 350	2 x 35 way         250p            1 x 43 way         260p            2 x 22 way         190p            2 x 43 way         395p            1 x 77 way         400p         500p           2 x 50 way(\$100conn)         600p	PCB Mtg Skt Ang Pin 24 way 700p 36way 750p GENDER CHANGERS 25 way D type	RIBBON   CABLE           (grey/metre)           10-way         40p           34-way         160p           16-way         60p           20-way         85p           20-way         85p           20-way         20p
der 60 85 125 170 175 275 325 - MALE:	EURO CONNECTORS	Male to Male£10 Male to Female£10 Female to Female£10	DIL HEADERS Solder IDC
2 n 100 140 210 380 plns 160 210 275 440 der 90 130 195 290 195 325 375 - 100d 90 95 100 120 ew 130 150 175 - ik	DIN 41612         Pug         Socket           2 × 32 way St Pin         230p         275p           3 × 32 way Ang Pin 275p         320p           3 × 32 way St Pin         260p         300p           3 × 32 way Ang Pin 375p         400p           IDC Skt A + B         400p           IDC Skt A + C         400p	RS 232 JUMPERS           (25 way D)         (25 way D)           24" Single end Male         £5.00           24" Single end Female         £10.00           24" Male Male         £9.50           24" Male Female         £9.50           24" Male Female         £9.50	14 pin 40p 100p 16 pin 50p 110p 18 pin 60p · 20 pin 75p · 24 pin 100p 150p 28 pin 160p 200p 40 pin 200p 225p
TEXTOOL ZIF           CKETS         24-pin £7.50           pin £8.00         40-pin £12	For 2 $\times$ 32 way please specify spacing (A + B, A + C).	DIL SWITCHES 4-way 90p 6-way 105p 8-way 120p 10-way 150p	MISC CONNS 21 pin Scart Connector 200 p 8 pin Video Connector 200 p
			ETI AUGUST 1985

	74279	90p	741.5230	1800	4008	600		LIN	EAR	1Cs		1	C	омі	UTE	RCC	OMPO	ONE	NT		-	
7400 30p 4701 30p 2402 30p	74283 74285 74290 74293	105p 320p 80p	74L S283 74L S290 74L S292 74L S292	80p 80p 80p	4009 4010 4011 4012	450 AD 240 AD 250 AD	7581 C0808 17910DC	E15 LM 1900 LN E25 LN	4709 4710 4711	188	BA820M BA950 BA950	730 2000 228p	CPL B02CE	650p	8755A TM\$9943 TM\$99.1	£16 £25 £18	2532-30 2564 2708	550p 800p 400p	MC1/ ULF		4/	1
7403 30p 7404 36p 7405 30p	74298 74351 74365A	180p 200p 80p	74LS296 74LS297 74LS298	140p 90p 100p	4013 4014 4015	360 AY 600 AY 700 AY	-1-5050 -3-1350 -3-6010 -3-6010	100p LN 190p LN 480p LN 400p LN	4725CN 4733 4741 4747	45p 22p 7%	TCA210 TCA220 TCA270 TCA940	150p 150p 360p	1650A 16692 16502A	1050p 350p 550p	TMS99 4 280PIO 280APID	612 2405 2505	2716+15v 2716-35 2732 2732A-2	350p 550p 450p 900p	ULN2068 ULN2802	1905 IN.		4
7406 40p 7407 40p 7406 30p 7409 30p	74366A 7436A 74367A 74368A	80p 80p 80p	74LS299 74LS321 74LS323 74LS323	220p 370p 500p	4016 4017 4018 4019	360 AV 550 CA 600 CA 600 CA	-3-8912 3018A -3026A -3046	100p Lik 100p Lik 10p Lik 70p Lik	4748 41011 41014 41801	30p 480p 150p 188p	TDA1010 TDA1022 TDA1024 TDA11205	2259 400p 1189 3889	5502B 5800 5802	800p 250p 300p	ZBORTC ZBOACTC ZBOBCTC	250:0 275p 500p	2732A-35 2764-25 27C64-25	550p 350p £10	ULN2804 1 75107 75108	90p /	W 5 36.	
7410 30p 7411 30p 7412 30p	74376 74390 74393	160p 110p 112p	74L S348 74L S352	350p 200p 120p	4020 4021 4022	1000 CA 6000 CA 7000 CA	3080 3080 3086 3086	50p LA 78p LA 80p LA 510p LA	41871 41872 41666 41669	300p 388p 580p 450p	TDA2003 TDA2004 TDA2006 TDA2020	190p 140p 130p 230p	5809 5809E 58B09 58B09E	E12 E12 E12 E16	ZBODAFT ZBOADART TMS4500 TAISDOUT	850p 700p £18	27128-25 27128-30 TMS2716	800p 800p 500p	75110 75112 75113	90p 60p 20p	BAUD RAT	
7413 505 7414 705 7418 365 7417 405	74490 7443A 74LS S	100p	74L\$353 74L\$356 74L\$363 74L\$363	210p 180p 180p	4025 4025 4025	48p CA 24p CA 80p CA	U3080AQ U3130E U3130T U3140E U3140T	2759 LA 900 LA "389 LA 480 LA 000 LA	42917 43302 43800 43809 43811	800 BB 1800	TDA2583 TDA2583 TDA3810 TDA7900 TEA1002	290p 500p 790p 380p 700p	58000-LB 8035 8039	036 360p 420p	TMS9982 Z80DMa Z80ADMA	500p 700p 750p	CRT CONTROL	LER E16	75114 75115 75121 75122	140p 140p	OMB116 7028	850p 750p
7420 305 7421 805 7422 365 7423 365	74LS00 74LS01 74LS02	24p 24p 24p	74LS366 74LS366 74LS367 74LS367	50p 50p 50p	4027 4028 4029 4030	40p CI 60p CI 75p CI 35p CI	43160E 43161E 43162E 43160E	10p Li 200p Li 100p Li 170p Li	M3914 M3915 M3915 M13600	340p 340p 340p 150p	TL081CP TL082 TL084 TL071	2882	8085A 8086 8088	300p E22 1750p	280A 5H3-0/ /9	1/2 700p	CRT5037 CRT6545 EF9364	£12 £9 £8	75150P 75154 75159	20p	AV-3-1015P AV-5-1013P	300p
7425 400 7426 400 7427 400	74LS03 74LS04 74LS05 74LS05	24p 24p 24p 24p	74LS373 74LS374 74LS375	80p 80p 75p	4031 4032 4033	125p C4 100p D7 250p D4	43280G 1002 MC1408-8 AC0800	700 M	151818L 163712 161310P 161413	480p 200p 190p 75p	TL074 TL081 TL082 TL083	110p 38p 53p 7%p	6748 TMS1601 TMS9980 TMS9995	£16 £12 £12 £12	2016-150 ° 2101	400p	EF9366 EF9367 MC6845	C25 C36 650p	75161 75162 75172	350p 400p 300p	COM8017 M6402 UHF	300p 480p
7428 439 7430 309 7432 369 7433 309	74L509 74L510 74L511	24p 24p 24p	74LS377 74LS378 74LS379 74LS381	140p 140p 140p 450p	4034 4035 4036 4037	70p 00 70p 00 70p 10 110p 10	AC0808 3308 12106 12108 12708	100p M 100p M 180p M 475p M 85o M	IC1458 IC14951 IC1495 IC3340P IC3401	43p 300p 79p 200p 70p	TL084 TL084 TL970 TL430C UAA1003-3	100p 200p 30p 120p 838p	WD55 280 280A 280A	£14.50 280p 325p 700p	2102 21078 2111A-15	250p 500p 400p	MC68455P MC6847 SFF96364	850p 650p £8	75182 75188 75189 75451	90p 60p 60p 70p	MODULAT	375p 450p
7437 30 7438 40 7439 40 7440 40	74LS13 74LS14 74LS15	34p 50p 24p	74LS390 74LS393 74LS396A 74LS396A	60p 110p 100p 140p	4038 4039 4040 4041	100p IC 250p IC 60p IC 55p IC	L7650 L7650 L8038 M72168 M7217	-00p M 388p M -00p M 222 M	IC3403 IF 10CN IK50240 IK50398 IL 920	960 300p 900p 798p 500p	UA758 UA2240 UAA170 UCN4801A 18 N2003A	130p 130p 170p 590p 71e	280 SUPPO DEVIC	750p RT ES	2147 4118-15 4116-20	400p 200p 150p	TMS9918 TMS9928 TMS9929	£15 £10 £10	75452 75453 75454 75480	78p 70p 70p	12MHz CRYST	E12 ALS
7441 00 7442A 70 7443A 100	74LS20 74LS21 74LS22 74LS24	24p 24p 24p 50p	74LS445 74LS465 74LS467	190p 120p 120p	4042 4043 4044	50p IC 60p IC 60p L0	M7585 M7556 C7120 C7130	80p M 68p M 100p N 100p N	41.922 HAR6221A RE531 RE544	400p 300p 130p 190p	ULN2004A ULN2066 ULN2802 ULN2803	75p 290p 100p 180p	265 1 3242 3245 6520	E12 800p 450p 300p	4118-3 4164-15 41256-30 4164-20	300p E13 300p	INTERF	ACE	75491 75492 8T26	65p 65p 120p	32.768 KM2 100 KHz 1 00MHz	100p 400p 270p
7445 100 7445A 100 7447A 100	74LS26 74LS27 70LS28 74LS30	24p 24p 24p 24p	74L5540 74L5541 74L5608	100p 100p 700p	4046 4047 4048	50p U 50p U 55p U	F347 F361 F363 F365	130p N 60p N 80p N 80p N	E556 E564 E565	60p 400p 138p 190p	UPC575 UPC582H UPC1156H UPC1185H	2710 200p 300p 500p	6522 6522A 6532	350p 550p 480p	4418-18 4532-20 4816AF-3 5101	400p 250p 200p 370p	AD558CJ AD561J AD7581	775p E20 E15	819f 819f 8196 8197	120p 120p 120p 120p	Freq in MHz 1.8432 2.00 2.45780(1.)	225p 255p 200p
7448 120 7450 36 7451 36 7453 38	74L532 74L533 74L537	24p 24p 24p	74LS610 74LS612 74LS624 74LS626	1900p 1900p 350p 225p	4049 4050 4051 4052	35p U 35p U 65p U 60p U	F3684 F367 M10C M301A M307	100p N 100p N 190p N 30p N 45p N	4E570 4E571 4E592 4E5532P	400p 300p 80p 150p	XR210 XR2208 XR2207 XR2211 XR2211	400p 400p 375p 575p 675p	6821 68B21 6829	150p 220p £12.50	5514 5516 6116P-3	450p 550p 350p	AM25510 AM25L5252	350p 350p	8798 81L595 81L595 81L595 81L597	120p 140p 220p 140p	2.45760(S) 2.5 2.662	250p 250p 250p
7454 38 7460 55 7470 50 7472 55	74L538 74L540 74L542 74L543	24p 24p 50p 150p	74LS628 74LS629 74LS640 74LS640	225p 140p 300p	4053 4504 4055 4056	60p L1 60p L1 80p L1 80p L1	M308CN M310 M311 M318 M318	719 N 8250 N 800 H 8500 C	465533P 4655334P 465534AP 0P-076P 51.02A	180p 120p 130p 500p 500p	XR2240 2N409 2N414 2N419P 7N423E	120p 190p 80p 175p 130p	6840 68840 6850 68850	375p 600p 160p 250p	6264-18 6810 745189	7000 1600 2250	AM25LS253 AM26LS31	350p 120p	81L S98 88L S120 9602	220p 300p 300p	3.5795 4.00 4.194	200p 150p 200p
7473 55 7474 50 7475 80	P 74L547 74L548 74L551 74L554	80p 90p 24p 24p	74L5641 74L5642-1	300p 200p	4059 4060 4063	400p U 70p U 85p U	M324 M334Z M335Z M336	45p F 111p F 130p F 100p S	1C4138 1C4151 1C4558 55668	34p 200p 34p 220p	20424E 20425E8 20426E 20427E	138p 380p 300p 800p	6852 6854 68854 6875	250p 650p 800p	745201 745289 93415 931,422	3500 2250 6000 9500	AM26L532	920p £6	9637AP 9638 ZN425E8	160p 200p 350p	4,43 4,608 4,9152 5,000	100p 250p 250p 150p
7476 45 7480 65 7481 100 7483A 105	74LS55 74LS73 74LS73	24p A 30p A 35p	74L5643 74L5643-1	300p 250p 300p	4066 4067 4068 4089	40p U 230p U 25p U 24p U	M330 M348 M368P M377 M300H-8	40p 5 80p 5 90p 5 100p 5	544 1000 5F F 96364 5L 490 5N 78033N 5N 76489	800p 300p 300p 400p	214428E 214429E8 214447E 214449E 214460E	225p 235p 28,50p 300p 750p	8154 8155 8156	850p 380p 380p	93425	c008	DM8131 DP8304	600p 350c	ZN426E8 ZN427E ZN428E8 ZN429E	350p 600p 450p 210p	5 058 6.00 6.144	175p 140p 140p
7484A 125 7485 110 7486 42 7489 210	p 74LS76 p 74LS76 p 74LS83 74LS85	A 360 A 70p 75p	74LS644 74LS645 74LS645-1	350p 200p	4070 4071 4072 4073	24p 24p 24p	M381AN M381AN M382 M383 M384	178p 5 178p 5 235 1 236 1	5P0256ALJ TA7120 TA7130 TA7204	400p 700p 138p 148p 150p	204580CP 201034E 201040E 204134J 204234E	300p 200p 680p 633 850p	8205 8212 8216 8224	225p 220p 160p 300p	ROM 8/PT	400p	DS8630 DS8631 DS8632	350p 140p 150p 150p	ZN447E ZN459CP	900p 300p	7,168 8.00 8.867	176p 150p 175p
7490A 53 7491 70 7492A 70	P 74LS80 74LS90 74LS91 74LS91 74LS93	35p 48p 80p 55p	74L 5668 74L 5669 74L 5670	90p 60p 180p	4075 4076 4077	24p 65p 25p	M386H-1 M387 M389 M381 M381	100p 1 270p 1 180p 1 180p 1	TA7205 TA7222 TA7310 TBA231	90p 190p 190p 190p	REAL	TIME	8226 8228 4. 8250	425p 450p 290p 850p	24510 185030 185A030 745188	250p 200p 200p 180p	DS8833 DS8835 DS8836 DS8838	225p 280p 150p 225p	CONTROL	LLEA	10.00 10.50 10.70 11.00	175p 250p 150p 300p
7493A 91 7494 110 7495A 60 7496 80	74L99	8 75p 90p	74LS684 74LS687 74LS688	350p 350p 350p \$50p	4081 4082 4085	24p 25p 80e	M384CH	Jabo Jabo VOLT	AGE	100 500	MM58124	AN 980p	8251A 8253C-5 8255AC-5	325p 350p	745287 745288 745387 82523	225p 180p 225p 150p	MC1488 MC1489 MC3446	60p 60p 250p	6843 8272 D765A FD1771	E8 E13 E13 E20	12.00 14.00 14.315 14.756	150p 176p 180p 250p
7497 21 74100 10 74107 5 74109 71	74LS10 74LS10 74LS1 74LS1	9 40p 2 45p 3 45p	74LS783 745 SE	E21 RIES	4086 4089 4093 4094	750 120 350 90p	1A 5V	FILED PL	450 79	re 05 50a	V TELE	TEXT DOER	8256 8257C-E 8259C-5	£18 400p 400p	825123 825129	150p 175p	MC3470 MC3480 MC3486	475p 850p 250p	FD1791 FD1793 FD1797	C222 C20 C222	15.00 16.00 17,734	200p 200p 150p
74110 7 74111 5 74116 17 74116 11	5p 74LS1 5p 74LS1 5p 74LS1 5p 74LS1	4 45p 2 70p 3 80p	74500 74502 74504 74505	50p 50p 50p	4095 4096 4097 4098	90p 90p 270p 1 75c	6V 8V 12V	7806 7808 7812 7815	80p 79 50p 79 45p 79 50p 79	06 50p 08 50p 12 50p 15 50p	SAA5020 SAA5030 SAA5041 SAA5041	800p 700p £16 800p	8271 8275 8279 8284	E29 E11 750p	2516+5v 2516-31 2532	360p 550p 480p	MC4024 MC4044 MC4044	250p 550p 550p 750p	WD2797 WD1691 WD2143	E27 E16 E8	16.432 20.00 24.00	150p 150p 150p 150p
74119 17 74120 10 74121 5	0p 74L51 0p 74L51 0p 74L51 74L51	\$29/140p 5 50p 6 50p 12 65p	74508 74510 74511	50p 50p 75p	4099 4501 4502	90p 36p 55p	18V 24V 5V 100m 6V 100m	7818 7824 A 75L06 A 78L06	50p 79 50p 79 30p 79 30p 79L	18 50p 24 50p .06 45p	LOW P	DFILE	8288D 8 pin 14 pin	£11 39p 10p	22 pin 24 pin 28 pin	22p 24p 26p	WIRE T	WRAP	8 pin 14 pin 16 pin	30p 30p 42p	22 pin 24 pin 28 pin	78p 78p 100p
74123 0 74125 6 74126 5	0p 74LS1 5p 74LS1 5p 74LS1 5p 74LS1	13 50p 16 45p 18 55c	74520 74522 74530 74532	100p 50p 60p	4503 4504 4505 4506	90p	12V 100m 15V 100m	A 76L 12 A 78L 15 OTH	30p 79L 30p 79L	12 50p 15 50p	SOCK	-S BY	18 pm 20 pm	16p 18p	40 pin	30p	24 pm	550	18 pin 20 pin 2816-30 2	50p 66p KX8	40 pm	130p DMs ts
74128 5 74132 7 74136 7 74141 8	Sp 74LS1 Op 74LS1 Op 74LS1	15 95¢ 17 175¢ 18 140¢	74S37 74S38 74S40 74S51	60p 75p 50p 45p	4507/4030 4508 4510 4511	35p 120p 55p	taed Regu M309L M323L		ATORS	140p 350p	Profile	Sockel	8 pin 14 pin 16 pin	25p 30p 35p	20 pin 22 pin 1930a	4thp 50p	28 pin 40 pin 2N2180	85p 90p 360p	1 25C1306	E16	(16X16) 2A 100V	£4.50 36p
74142 25 74143 27 74144 27 74145 11	Op 74LS1 Op 74LS1 Op 74LS1 Op 74LS1	52 2005 53 655 54 1605	74564 74574 74585 74586	45p 75p 300p	4512 4513 4514	55p 7 150p 7 110p V	BH05KC BH12 BP05 ariable Re	5/ 5/ 10 Inguiators	A 5V A 12V DA 5V	575p 840p 900p	AD161/	45p	BFX29	DRS	TIP30C TIP31A TIP31C TIP32A	40p 40p 45p	2N2219A 2N2222A 2N2369A 2N2369A	30p 30p 30p 30p	2SC1307 2SC1957 2SC1969 2SC2028	150p 90p 150p 80p	2A 400V 3A 200V 3A 600V 4A 100V	45p 60p 72p 85p
74147 17 74148 14 74150 17 741514 7	0p 74LS1 0p 74LS1 5p 74LS1 0p 74LS1	55 859 56 859 57 501 58 859	74S112 74S113 74S114	150p 120p 120p	4515 4516 4517 4518	1100 L 550 L 2200 L 480 L	M305AH M317T M317K M337T M350T		0-220 03	150p 240p 225p 400p	BC109C BC109C BC169C BC172	18p 20p 18p 18p	BFX84/S BFX86/ BFX88	30p 30p 30p	TIP32C TIP33A TIP33C TIP34A	43p 73p 83p	2N2646 2N2904/ 2N2906A 2N2906A	50p 5 30p 30p	2SC2029 2SC2078 2SC2335 2SC2335 2SC2612	200p 160p 200p 200p	4A 400V 6A 50V 6A 100V 6A 400V	100p 80p 100p 120p
74153 0 74154 14 74155 0	0p 74L51 0p 74L51 0p 74L51	50A 75 51A 75 52A 75 53A 75	745132 745133 745138	100p 60p 180p	4519 4520 4521 4522	32p L 60p L 115p 7 80p 7	M396K M723N BHGKC 9HGKC	10 54 54	RAV+AC	£15 50p 850 875p	BC177/ BC179 BC182/ BC164	30p 30p 15p	BFY50 BFY51/3 BFY56 BFY90	2 30p 33p 90p	TIP35A TIP35A TIP35C	120p 120p 140p	2N2926 2N3053 2N3054	12p 36p 60p	3N128 3N140 3N141 3N141	200p 200p 200p	10A 400V 25A 400V	200p 400p
74150 10 74157 1 74159 11 74160 11	0p 74L51 9p 74L51 0p 74L51	54 75 55A 110 55A 150	745140 745151 745153 745157	150p 150p 210p	4526 4527 4528 4529	70p 7 80p 9 65p 10	9GUIC 9GUIC Writching I CL7660 63524	1/ 1/ Regulators	A+VAR	250p 250p 300p	8C187 8C212/ 8C214 8C214	30p 3 16p 18p	BRV39 BSX19/ BU104 BU105	45p 20 30p 225p 190p	TIP36A TIP36C TIP41A TIP41C	150p 50p 58p	2N3442 2N3553 2N3584	140p 240p 250p	3N204 40290 40361/2	200p 250p 75p	TRIAC	S
74161 0 74162 11 74163 11 74163 11 74164 11	0p 74LS 0p 74LS 0p 74LS 0p 74LS	69 100 70 140 73A 100	745158 745163 745169 745174	200p 400p 700p 300p	4531 4532 4534	75p T 65p T 380p 7	L494 L497 8540	10. EL 6	стро	300p 300p 250p	BC327 BC337 BC338	16; 16; 18;	BU108 BU109 BU126 BU205	250; 225; 150; 200;	TIP424 TIP420 TIP54 TIP55	60p 65p 160p 160p	2N3643/ 2N3702/ 2N3704/ 2N3706/	4 48p 3 25p 5 25p 7 25p	40595 40673 40871/2	90p 100p	6A400V 6A500V 8A400V	70p 64p 75p
74165 11 74166 14 74167 40 74167 9	0p 74LS 0p 74LS 0p 74LS 10p 74LS	74 75 75 75 81 200 83 190	745175 745188 745189 745189	320p 190p 225p	4538 4539 4541	750 750 90p	DL 707 Red FND357 FND800 TIL	140p 100p 730 100p	MAN4640 MAN6610 NSB5861	200p 200p 878p	BC477/ BC516/ BC5478	8 36 7 50 20	8U208 BU406 BUX80	2005 1456 8005	TIP120 TIP120 TIP120 TIP120	75p 75p 60p	2N3708 2N3773 2N3819 2N3823	25p 200p 40p 30p	01001	120	8A500V 12A400V 12A500V 18A400V	850 850 1050 2200
74172 40 74173 14 74174 1	10p 74LS 10p 74LS 10p 74LS 10p 74LS	90 75 91 75 92 80 93 80	74\$195 74\$196 74\$200	300p 350p 450p	4543 4551 4553 4555	70p 100p 240p 36p	MAN74DL7 MAN78/DL7 MAN3640 71L32	04 100p 07 100p 178p 35p	TH 729 TH 730 MAN8910	100p 100p 120p	BC5490 BC5590 BC5590	10	E310 MJ413 MJ802	50 250 400	TIP125 TIP142 TIP142	80p 120p 120p	2N3866 2N3904 2N3906 2N4036	90p 22p 22p	8YX36300 0A47 0A90/91	20p 10p 9p	16A500V T2600D TIC206D TIC226D	130p 130p 60p 750
74176 1 74178 1 74179 1	00p 74LS1 00p 74LS1 50p 74LS1	94A 75 95A 75 96 80 97 80	745201 745225 745240 745241	820p 400 400p	4558 4557 4560 4586	50p 240p 140p	TIL 31A TIL 100	170p PTO-IS	TIL70 TIL81 OLATO	540 1280	BCY70 BCY71 BD131 BD132	30 36 75 80	MJ296 MJ296 MJ300 MJ450	1 225 5 90 1 225 2 400	TIP3055 TIS93 VN10KN	70p 30p 50p	2N4037 2N4123 2N4125	6 27p	OA200 OA202 1N914	9p 10p 4p	TIC246D	110p
74180 1 74161 3 74182 1 74184 1	000 74LS 000 74LS 000 74LS	21 100 40 80 41 80	74S244 74S251 74S257 74S258	\$00p 250p 250p 250p	4568 4569 4572	240p	LQ74 MCT26 MCS2400 MOC3020	- 90p - 90p - 90p - 50p	TIL 111 TIL 112 TIL 113 TIL 116	70p 70p 70p 70p	BD135 BD139 BD140 BD189	6 40 40 40	p MJE34 p MJE29 p MJE30 p MPF10	0 60 65 150 65 120 2 40	P VN66AF P VN86AF P ZTX146 P ZTX340	50p E1 16p 18p	2N4401. 2N4427 2N4871 2N5087	/3 250 900 500 27p	1N916 1N4148 1N4001/2 1N4003/4	7p 4p 5p 6p	3A400¥ 6A600¥ 12A400¥	18p 180p 160p
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# **READ/WRITE**

### Newrad

### **Dear Sir**

ETI in conjunction with John Linsley Hood produced what appeared to be an excellent amplifier design. I would imagine that the design appealed to many electronics hobbyists and possibly like myself many of them were delighted that Newrad Instrument Cases was to supply a complete kit of parts. You will appreciate the major problems of electronics as a hobby: namely the supply of components of a suitable quality and the frequent inability to obtain an adequate professional enclosure for the finished item. I am always pleased when manufacturers such as Newrad Instrument Cases produce the parts in one complete package.

As an electronics hobbyist I could easily understand the difficulties experienced by Newrad Instrument Cases in obtaining parts, effecting changes in circuit design, and also anticipating the number of kits they would need. Even the greatest degree of understanding and patience wears a little thin after a year. I have telephoned Newrad Instrument Cases on numerous occasions and certainly over the last few months have been met by an Ansaphone machine during normal business hours although I admit that my letters have been answered promptly. It would also be worth noting that Mr. Phillips sounds a very reasonable person on the telephone but has had difficulty in fulfilling his promises particularly in respect of time, and has needed reminding on occasions.

The final promise for completion of this kit in respect of the item that Newrad should have found the most easy to produce, namely the case, promised for three weeks delivery some two months ago has finally worn my patience out. I wrote to Newrad Instrument Cases two weeks ago by recorded delivery letter threatening legal action if my money had not been returned by Monday, 3 June. It would appear that Newrad Instrument Cases are

uninfluenced by the right and proper use of the legal system and before proceeding further I felt I should inform you. I appreciate that you have no direct legal responsibility in the respect of the supply of the components by mail order but I am sure that you will accept a large moral responsibility for recommending Newrad Instrument Cases as the sole supplier for a product initiated by John Linsley Hood and Electronics Today International. I would imagine that a large number of your readers have ordered such kits from Newrad and, like myself some years ago, are probably not in a position to take on the companies who dishonour their public agreements. I am in a better position today to meet such people head-on and although I have no desire to be a martyr I am fully prepared to take this case to its limit and demonstrate that large numbers of people cannot be treated so shabbily.

I have spoken with my solicitor but am prepared to withold instructions until the next publication of ETI in order that you may publish this letter in part or in whole and give me a much better idea of the scale of the problem that Newrad have created. I have no idea how many people are involved but, expressed in round figures, £100 or £250 units tens of time over, invested for the period of about a year, would come to an enormous sum of money. I would certainly welcome your own comments re the above and should you decide to publish the letter I would like to hear from anybody else in a similar situation to assess the full nature of the best.

Yours faithfully Dr. P.A. Joiner Caithness.

We have decided to print Dr Joiner's letter in full because, despite assurances from Newrad that the problems with the Linsley Hood MOSFET amplifier would be cleared up by now, we are still receiving complaints. We accept that we have a moral responsibility to our readers who may be being ill-served by Newrad despite having received an implicit

recommendation from us to deal with the company. In fairness, I should point out that the level of complaints has dropped off since Chris Phillips of Newrad gave his assurances. I should also point out that we can continue to recommend Newrad's products (including the JLH amplifier) without reservation. They are undoubtedly of high quality and good value. However, such recommendation is undermined if the products are unavailable, for whatever reason. Like Dr. Joiner, we urge any other readers still awaiting delivery of the JLH amplifier kit (in whole or part) to write to us (please don't phone) so we can gauge the extent of the problem, if problem there be. Armed with this information, we will be able to approach Newrad, if necessary, from a strong position in order to discharge our moral responsibility. Regrettably, the Mail **Order Protection scheme** - under the terms of which it is possible to claim compensation for goods paid for but not received — only applies in the case of a mail order supplier ceasing to trade. To the best of our knowledge, Newrad are not in this position. — Ed.

### **Seeing The Light**

### Dear Sir,

In my original article ('Large Digit Scoreboard', ETI, May 1985), I mentioned the trouble I experienced with lamps blowing and taking their Triacs with them. I have since found a solution, which is applicable to any Triac controlled lamp project.

There are a number of situations where a Triac is used to control mains power to a bank of lamps, eg. the scoreboard, disco light shows, etc. In such situations, if a lamp blows it can form a momentary short circuit and the resulting current surge destroys the Triac. Typically, the Triac will go short circuit, and the other lamps in the bank will be permanently on.

The solution to this involves putting a current limiting resistance in series with the lamps, but for practical purposes a limiting resistor will be very wasteful of power. The answer is to wire the bank of lamps in series instead of

## LETTERS

parallel. In this case, when a lamp fails, the others in the string act as a limiter and protect the Triac.

Naturally, the string of lamps acts as a potential divider, so each lamp must operate from a lower voltage than normal, much the same way as for a string of Christmas tree lights. If, for example, the bank contains five lamps, each one should be rated at 250/5 = 50V operation. A range of pygmy bulbs with ordinary bayonet bases called Sign bulbs is available from various suppliers. They are rated at 15W, with a variety of operating voltages, and selecting from these should fill most requirements.

This idea is adaptable even to applications where a Triac drives a single bulb. Providing it is not a drawback to replace the single bulb with a cluster of lower voltage bulbs, the modification can be made and the Triac duly protected.

Yours sincerely, Ken Wood, Ipswich.

Not so much a case of 'If at first you don't succeed, Triac again', more a case of a light subject treated series-ly ... Ed.



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# THE REAL COMPONENTS

### This month's topic is amplifiers in small packages, and as usual, John Linsley Hood has it all wrapped up.

n the late 1950s, it occured to one of the semiconductor manufacturers in the USA that it would be possible to put together complete pieces of simple circuitry on a slice of P-type mono-crystalline silicon. The necessary components could be formed by suitable masking and diffusion processes.

For example, a resistor could be made by diffusing in a zig-zag track of fairly lightly doped N-type material, with a connector pad at each end (Fig. 1). A capacitor could be made by oxidising a small area of the silicon and putting a layer of evaporated a luminium metallising over



the top of it, with a buried N-type layer underneath as the other plate (Fig. 2). NPN and PNP transistors could be made as shown in Figs 3 and 4. However, there are snags.

Resistors and capacitors take up a disproportionate amount of room on the chip surface, unless the values are pretty small, and it is very difficult to get the values within limits closer than  $\pm$  30%. In addition NPN transistors are not likely to be very good ones in terms of noise, current gain, or breakdown voltage because they have to be made by three separate sequential diffusions (an Ntype impurity, followed by a P-type impurity, followed by another N-type), each of which adds to the total impurity concentration within the collector, base and emitter region.

In the case of the PNP transistors shown in Fig. 4, the only one which is at all reasonable in performance is the

one which can use the substrate (usually connected to the »ve supply line) as its collector. If the circuit doesn't allow this, it is necessary to use the lateral construction in which the base region is formed by masking off a narrow strip, with the emitter and collector regions diffused into the N-type zone as close together as possible. Even so, and with the best mask technology in the world, the current gain of such a transistor may be only 5–10 and its HF response will be pretty miserable.

Within these limitations it was possible to make some useful circuit blocks, and the circuit and mask layout designers learnt from their experience. They certainly needed to, since some of the early ICs, from my painful recollections as a user, left a lot to be desired in both performance and reliability.

Modern circuit techniques and circuit designs have transformed this situation, and my honest opinion now is that, if there is an IC which will do what one needs, then it is pointless to try to do the job with discrete components other than in a few specialised applications. After all, some of the best electronic circuit designers in the world work for the IC manufacturers.

### **Operational Amplifiers**

These, normally known just as op-amps, are the most common form of IC which the user of linear circuitry will encounter. They can be regarded simply as gain blocks



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# FEATURE







of the form shown in Fig. 5, with a couple of input connections (one inverting and one non-inverting), an output pin, and a couple of leads for a dual rail supply. The supply voltage can lie anywhere between  $\pm 1.5$ V and  $\pm 18$ V depending on the op-amp specification.

The basic circuit layout employed is of the form shown in Fig.6. The input transistors, Q1 and Q2, are connected as a long-tailed pair, Q5 is an amplifier stage, and Q6 and Q7 are a push-pull output stage biased into class



AB. In modern designs there will also be an internal HF compensation capacitor, which I have shown as Cc.

Although there has been a proliferation of op-amp types in the past decade, they all have certain features in common. These are an open loop gain (that is, the gain without externally applied negative feedback) in the range of 50,000–250,00, a low static power supply consumption, usually in the range 0.5–4 mA, a high commonmode rejection ratio (by which is meant the ability of the op-amp to ignore signals which are present simultaneously on both inputs while amplifying the difference between such signals) and a high degree of rejection of any voltage variations on the supply lines.

This latter feature allows the circuit designer to ignore the need for supply line decoupling in a way



amplifier.

which would have been disastrous for the valve circuit designer, and for which I, for one, am truly thankful.

Early general purpose op-amps were uncompensated. This meant that one had to be careful when applying negative feedback between the output and the inverting input, unless two external HF compensation networks of suitable values of R and C were connected between four of the pins.

At the time, this was a necessary requirement if a respectable HF bandwith for the amplifier was to be achieved, but it was an inconvenience which one could



well do without. Subsequent 'second generation' op amps like the familiar 741 had the worst-case HF compensation components built in, without too much sacrifice in performance.

A later development was to make dual and quadruple op amps in the same package. The dual ones, in an 8pin DIL or TO5 pack, have become very popular.

### Drawbacks

Early op-amps, while they did what they were supposed to, had a lot of snags, which were seldom mentioned in the makers' catalogue descriptions.

Noise This was partly due to the fact that the whole of the electronic circuitry floated on top of a P-type silicon slice and was isolated from it by reverse biased diode junctions. As I mentioned in an earlier article, the leakage currents in reverse biased diodes are very noisy. Also, the transistors used were heavily and multiply loaded with impurities, which certainly didn't do their noise figures much good.

Output overload Again, in early types, no specific form of output overload protection was employed, so if the output became short-circuited the op-amp would burn out.

Latch-up If the output was driven hard in one direction or the other, it often just stayed there, regardless of what the signal at the input subsequently did.

Input protection No specific protection was included to prevent damage to the device if the input was taken, quite legitimately within the supply voltage range of the IC, to an ill-chosen voltage level.

Frequency response This was often quite poor when any significant output load was applied or if a wide output voltage swing was required. The slewing rate could also be poor and often differed for positive-going and negative-going output swings

Input impedance This could be quite low, and the input bias currents (those currents which must flow into the IC if it is to work) were often relatively high.

### Third Generation Op-Amps

Most of these problems were removed in the second generation designs, such as the 741, but the problem of a relatively low input impedance remained, with a consequent need for a carefully matched resistance path in both the inverting and non-inverting input circuits. The HF bandwidth and noise figure were also not nearly as good as could be achieved with discrete components.

Careful attention to the doping of the various regions of the IC, and in the choice of dopants employed and the

way in which they are introduced, has led to great improvements in the noise figure of a modern IC opamps. Improvements in circuit design have also helped.

However, the major breakthrough came when techniques were evolved for producing FETs on the same chip as bipolar transistors. This allowed the construction of Bi-FET types of op-amp such as the Texas TLO71 and the National Semiconductors LF351, which have a remarkably good performance.

These have the general circuit layout shown, in slightly simplified form, in Fig. 7. This is very similar to the 741, whose structure I illustrated in Fig. 6, except that the cascode-connected PNP input transistors (used to compensate for the poor performance of the lateral PNP types), have been replaced with P-channel junction FETs.

This gives a noise performance which is very nearly as good as that obtainable from the best of the discrete component circuit layouts, an output voltage swing very nearly equal to the difference between the +ve and -ve supply rails over the whole audio bandwidth, and a harmonic distortion, on loads of 4k7 or greater, which is



typically well below 0.01% for a stage gain of 20x or less. In addition the input impedance is of the order of a million megohms, which means that one doesn't have to worry about the input circuit resistance values being precisely matched.

The pin connections used for the single op-amp designs are shown in Fig. 8.

A feature most modern IC op-amp designs possess is an offset-null facility, to allow the output DC voltage to be set precisely to zero volts plus or minus a few millivolts. This is commonly done by connecting a small trimmer potentiometer between pins 1 and 5 and taking the slider to the-ve supply rail, as shown in Fig. 9. It is wise to check the recommended connections and circuit values if the op-amp used is not a 741, LF351 or TL071 since some types differ in their requirements.

Nulling the DC output voltage (only practicable if the input voltages are near zero) is only necessary if the amplifier is being used in a DC application, such as a DC energised strain-gauge or thermocouple amplifier.

Since this isn't a very common application, the quite popular dual ICs such as the bipolar MC or LM 1458 or the LF353/TL072 BiFET types omit this connection. Their pin connection arrangements are shown in Fig. 10. With both inputs connected to the 0V line and a gain of up to 100x, a DC offset of less than 100mV would be expected even without any HF DC nulling.

The type of circuit used with an op-amp as a general purpose AC amplifier stage is shown in Figs. 11 a and 11 b.

The non-inverting stage has a higher input impedance Rin, which can be as high as one likes, but a slightly worse distortion figure and an inferior sound in audio applications when compared with the phase-inverting circuit of Fig. 11b, in which the input impedance is equal to Ra.

This difference is due to a slight failure in the op-amp common mode characteristics. It is only worth worrying about this in 'ultimate Fi' audio applications. For all normal purposes there is no measurable difference between the two.



The third common configuration in both AC and DC usage is the unity-gain impedance converter layout of Fig. 11c. This will convert an input signal at the megohms level to a very nearly identical signal at an output impedance of less than 1k0.

In all of these applications, where negative feedback is being used to define the gain or improve the performance of the op-amp, it is prudent to include an output resistance of the order of 100-220R. This will reduce the likelihood of the amplifier becoming unstable should the output load have a particularly unfavourable reactance characteristic. If the circuit is driving something whose input impedance is purely resistive, this can be omitted.

The circuit applications in which op-amps can be used would fill a book — indeed they have already filled several books — so this is not the place to compete with this outpouring of ingenuity. The thing to remember is that the more recent designs are, inevitably, better than the earlier ones, partly as a result of competition between manufacturers and partly because new design techniques are continually being discovered which lead to better products.

Also, one should remember that there are, as the old saying has it, horses for courses. If one wants a very high input impedance indeed, for use, perhaps with an ionisation chamber, but low noise and low distortion are not terribly important, then a MOSFET input op-amp



like the RCA CA3140 or the more recent Intersil ICL 7611 DCPA, would be the best choice.

If on the other hand, very low noise indeed is required, perhaps for some audio application, but a high input impedance isn't particularly important, then the Signetics NE5534 or its dual package equivalent, the NE5532 would be a good choice. If cost is of no consideration whatever, a Precision Monolithics OP-27, currently the Rolls-Royce of bipolar op-amps, would make an enviable possession, particularly since it combines very low circuit noise with a very low DC drift and equally low distortion characteristics.

### **Other Op-Amp Types**

Additional possibilities which exist in the op-amp field are devices like the TL061 (062 dual, 064 quad) series, which, in addition to a FET input, have a typical current consumption of 0.25mA per amplifier and are ideal where economy in use of supply current is desirable. Even more frugal is the OP-220 device with a consumption of 100uA, or the OP-420 quad op-amp which lives on a beggarly 50uA per amplifier.

Alternatively, ICs like the TL091 (092 dual) offer the facility of operation from a single line supply, provided





that the input and output voltage swings are not required to go outside the supply voltage range.

Where DC amplification for strain gauges or similar low output transducers is required, the National Semiconductor LM725 or the Precision Monolithics OP-07, OP-27 or OP-37 devices would be preferable.

There are also IC op-amps aimed at very fast response, wide-bandwidth applications, such as the National Semiconductors LH0024 and LH0032 designs which have a 70MHz bandwidth, and the LH0063/HA5033 unity gain buffer ICs, which have a DC-100MHz pass-band. However, it should be remembered that such devices will require a lot of care in the layout design if stable operation is to be obtained.

If more muscle power is required there are also power op-amps, though a simple and relatively low-cost



alternative is to hang a pair of transistors on the output of a conventional op-amp as I have shown in Fig. 12. Since the output transistors are zero biased they take very little quiescent current, but the residual crossover distortion would spoil the performance for audio use. If low THD is needed, the transistors must be biased into class A or class AB as shown in Fig. 13. This produces a very good headphone amplifier design.

### **Interpreting The Specifications**

Most of these are fairly simple to understand, but there are some IC parameters which are a bit confusing. Input Offset Voltage refers to the difference in the baseemitter voltages between Q1 and Q2 in Fig. 6. This





would give rise to a spurious apparent DC input signal, to be amplified by the op-amp voltage gain. Typical offset voltages for a 741 type op-amp would be in the range 2-5mV, in the absence of an offset trim adjustment. FET input op-amps would be worse than this were it not for the fact that they are usually laser trimmed to around the 2mV level.

Input Offset Current refers to the difference in the base currents of Q1 and Q2 in Fig. 6. It is usually a good bit less (5-10x) than the input bias currents, which are the actual base currents drawn by the input transistors. It will cause an unwanted voltage offset if the resistance of the two input circuit paths are unequal.

The input bias and offset currents in FET input opamps are usually too low to be of great importance in normai circuitry.

All of these offset voltages will probably be worse at temperatures higher or lower than the 25°C figure normally specified for commercial grade ICs. Military or industrial specification units will be better in this respect.

Input offset voltage drift is specified as a function of temperature and relates to the temperature stability of the circuit. It is influenced by care in matching the input transistor chip areas and doping levels, and is a parameter which is unlikely to be specified except in relation to Military op-amps or those which are intended for use in DC instrumentation applications.

### Voltage Regulator ICs

The second class of linear ICs which I feel one should not try to do without is the three-terminal voltage regulator IC. This is an invaluable aid in ensuring that circuits work as well as one would hope by providing a



stable, precisely controlled, low ripple, low impedance and low noise DC supply line.

The type of circuit layout used is shown in Fig. 14 and the simplified internal circuitry is shown in Fig. 15. The only thing which it is necessary to remember in use is that the negative supply line regulators (usually listed as the 79 series) have a different pin configuration to the positive line (78 series) ones. The pin connections for a number of different packages are shown in Fig. 16.

In addition to the fixed output voltage types (7805, 7812, 7815, 7824 etc.) there are adjustable output voltage versions. These are good, but not quite as good in performance as their fixed voltage brothers.

At present, there is a general limitation of 40V as the maximum input voltage which may be applied. Higher



# **FEATURE: Real Components**

voltage types are beginning to appear but at present they are very costly.

Unlike zener diodes, the three-terminal IC voltage regulators employ a relatively nose-free band-gap type of voltage reference which is compensated for temperature variations. They are amplified to give higher voltages than the 1.205 V silicon band-gap potential (the voltage which a forward conducting diode would have at absolute zero temperature), and so are preferable as sources of stable DC potential. However, at least 5% of the rated output current must be drawn from the device if a good performance is to be achieved.

Typical output impedances for such a voltage regulator IC can be well below 0.1 ohms down to as low as 1 Hz. It would require a decoupling capacitor of 1.5 farads to equal this! To ensure proper operation, the output capacitor, C1 in Fig. 14, should be at least 100n. More than 10u is unnecessary.

These ICs also contain internal circuit elements to provide protection against inadvertent output shortcircuit or thermal overload due to a combination of excessive current and input-output voltage drop.

### **Other Linear ICs**

There are an enormous number of other linear ICs, and every month new ones appear. These are mainly aimed at special fields of application, such as TV sets, FM tuners, various industrial and automotive applications and audio circuitry. The competition in the TV and audio field is largely for low cost units for the relatively undiscriminating user, and the more of the circuitry which can be done with ICs, the lower the price tag will be.



Fig. 16 Pin connections for the most popular types of three terminal voltage regulators.

It is a fascinating field to explore, but I always remember the dozens of special purpose ICs which I have seen in the past and which are now no longer made. I try to stick with the rather longer lived general purpose designs, of which the op-amps and the voltage regulators seem to be the most useful.

Next month I will look at the strengths and weaknesses of the various types of digital ICs, such as standard TTL, ECL, Schottky, LS, ALS, CMOS and HCMOS.





# FROM A TO D AND BACK AGAIN

Data converters are finding their way into an ever increasing range of electronic equipment, including a number of ETI designs. Stuart Smith takes a closer look at some of the more common types.

N owadays, digital circuitry is very often used to process signals which originate in analogue form. The results of the processing may need to be reconverted into analogue form, and different conversion methods have been developed to meet the needs of different applications. These vary from the high speed/low precision requirements of a video digitiser to the slow speed/high precision of a digital voltmeter.

### **Back To Front**

Digital to analogue converters (DACs) are the simplest and I'll describe them first.

Usually the ouput of a digital circuit is in the form of a set of words of fixed bit-length. Occasionally it takes the form of a frequency. A frequency-to-voltage converter



will serve for the latter kind of output and the general principle is shown in Fig. 1.

The node X is switched to either Vcc or 0V at a rate dependent on the input frequency. As long as the input pulses are of fixed width (which can be arranged using edge-triggered monostables) the mean output voltage at Y is proportional to the input repetition frequency. If CMOS bilateral switches, such as the DG200 or 4066 types, are used for S1 and S2 their own ON resistance can provide the R component. The maximum input frequency is limited by the switching speeds of S1 and S2 and the minimum frequency by the value of C. If C is too high, the output will take a long time to respond to changes in frequency. If C is too small, the output will ripple as each input pulse arrives.



Most digital circuits do not produce variablefrequency outputs — they give parallel digital data. To decode such inputs, the circuit of Fig. 1 can be effectively extended as in Fig. 2, although it is rather too slow for some applications. Instead of a variable rate, fixed width input, the technique uses variable width pulses at a fixed rate.

For higher speeds, the method of switching weighted resistors (Fig. 3) can be used. The illustration shows an 8-bit DAC with the weighted resistors so numbered that  $Rx = R.2^{8 \cdot x+1}$ . In general, an n-bit DAC can be constructed with  $Rx = R.2^{n \cdot x+1}$ . In such an op-amp configuration, the inverting input is a virtual earth point and the current flowing in one of the resistors when it is switched in is Vref/Rx. The output voltage due to that current will be -Vref.(R/Rx) which simplifies to -Vref.(R/ (R.2<sup>8-x+1</sup>)) or -Vref/2<sup>8-x+1</sup>. For any particular input number, several resistors will be switched in and the output

# FEATURE



will be summed. For example, if the input is binary 11001 (decimal 25), the output will be Vref. (1/256 + 1/32 + 1/16) or Vref. (25/256). A little calculation will show that the output of our 8-bit DAC will be Vref. (A/256), where A is the number input in binary form. For an n-bit DAC, the output will be Vref.  $(A/2^n)$ .

In a practical circuit, the switches S1-S8 would have finite resistance and so the correct weighting of each input would be difficult to achieve. Apart from this problem, the type of converter shown in Fig. 3 requires a very large range of resistance values for even a modest ten bits. As the number of bits increase, the resistor tolerance constraints become tighter. Take the example of an input code change from 0111...to 1000.... If the MSB resistor is one part in 2<sup>10</sup> out then the analogue out-



put of a ten-bit DAC may actually decrease on this major carry. In fact, accuracy should be to within a half of the LSB weighing or, in the case of a ten-bit DAC, to 0.05%.

### **Divide And Rule**

Tolerances of this precision and beyond are difficult to maintain over a wide range of resistances and temperatures. It would, then, be useful to have a circuit which did not require such a large range of resistor values.

Such a circuit is the R-2R ladder network, a series of potential dividers which forms the basis of many DACs in use today (Fig. 4). It is widely used as part of the reference DAC in successive-approximation ADCs (see later).

At each node (W, X, Y, Z) current entering from the right 'sees' a resistance of 2R to the left and 2R down-ETI AUGUST 1985 wards, so it divides equally down each arm. Thus binary weighting of currents is achieved in each vertical arm, and consequently, a binary weighting of voltages across the 2R resistors. By suitable adjustment of the resistors, a logarithmic weighting can be achieved.

The R-2R ladder is very often used in the configuration of Fig. 5, which shows a 5-bit DAC. The reference voltage, Vref, is converted to a current Iref = Vref/Rin, flowing in the reference transistor Q1. The currents flowing in the other transistors can be switched to either the output or ground, depending on the setting of the bit switches. The switch resistance is unimportant because of the use of constant current sources T2 to T6.

In some commercial DACs the individual outputs may be switched to a true or complement current output; the sum of these two output currents is always equal.

The output of any DAC is proportional to the product of the voltage reference and input code. Some converters are designed so that the voltage reference may



\_\_\_\_\_

vary widely during operation — these types are designated 'multiplying' converters. A high quality multiplying DAC may be used as a digitally controlled attenuator in an audio system; the Analogue Devices AD7110 is an example of a DAC specifically tailored for this application — it attenuates input signals in 1.5dB steps according to input code.



The output signal is often wanted as a voltage, rather than a current. Of course, this can be achieved with a resistor, but the output voltage will always be negative (Fig. 6). Also the output voltage swing is limited by the 'compliance' of the DAC outputs, which is the range of output voltages over which constant output current will be delivered. Some are not capable of maintaining constant output current unless the output voltage stays close



u zero. Finally, the output voltage will change if it is loaded by another resistance.

It is, then, quite usual to add an op-amp to provide gain, buffering and the correct polarity of output voltage. Some proprietary DACs incorporate an output op-amp to provide voltage output. Figures 7 and 8 show two methods of connecting a current-output DAC as a voltage-output device.



Another DAC structure in common use incorporates a ladder of equal-value resistors — 2° of them for an n-bit converter. The reference voltage is applied across the ladder and a fixed fraction of it is tapped off by a series of switches. (Fig. 9).

This structure is unwieldy for many bits — at eight bits, an eight-line to 256-line decoder with 256 control lines is required. An alternative arrangement uses more switches but incorporates decoding in the wiring and



requires only 2.n control lines (Fig. 10). This method is known as tree decoding, and, since voltages are being switched, is probably best suited to MOS implementation, because MOS switches introduce no offset voltage.

### Flash, Bang, Wallop ...

The analogue-to-digital converter is a rather more complicated device than the DAC. ADCs often incorporate a DAC as a feedback element, to improve the performance of the ADC.

The most direct implementation of the A to D function is probably the parallel or'flash' converter, so called



# FEATURE: Data Conversion



because of its speed (up to 100 million conversions/sec) (Fig. 11).

A voltage reference is divided by an equal value resistor string into 2<sup>n</sup> equal steps for n-bit conversion. These voltages are applied to the reference inputs of 2<sup>n</sup>comparators. The input voltage is applied to all the comparators simultaneously. A priority encoder gives an n-bit digital output indicating where the comparator outputs change from low to high.

The flash converter is rather expensive — for ten bits no less than 1024 resistors and comparators are required, as well as a correspondingly large logic circuit to encode the output. An interesting variation on the flash converter, which sacrifices only a little of its speed whilst considerably reducing the complexity, is the two-stage converter, an example of which appears in Fig. 12.

During stage I, the analogue input is decoded with seven bit precision. A fast, accurate seven-bit DAC returns a value equivalent to the ADC output. The ADC output is stered to the most significant bits of the output register. The DAC output is stored for use in stage II. In



stage II, the previous DAC output is subtracted from the analogue input, leaving a remainder which is some fraction of the converter's most significant bit. This fraction is amplified by 128 ( $=2^7$ ) and converted by the flash converter to provide the seven least significant bits.

### **Successful Approximation**

Another important ADC method is successive approximation, which is of medium to high speed (up to 50,000 10-bit conversions/sec). Successive approximation is probably the most popular ADC method today.



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A block diagram of a successive approximation converter is shown in Fig. 13. A register is filled with ones in one bit position at a time. The register output is applied to a DAC and the resulting analogue output compared with the ADC input. On the basis of this comparison a decision is made on whether to retain the bit just tested or discard it. The total conversion time is approximately n times the DAC setting time.

The basic hardware implementation of a sucessiveapproximation (SA) register, using S-R flip-flops, is shown in Fig. 14. The top line of flip-flops is the sequencer, which puts a one in successive code register flip-flops, MSB first. The bottom line of flip-flops is the code register,



which holds estimates of the digital code representing the input signal.

On receipt of a START CONVERT pulse, the sequencer is set to 1000, which sets the code register to 100. This code is applied to a DAC, and the resultant output is compared with the analogue input. If the estimated code is too high, the comparator output goes high. When a CLOCK pulse arrives, the sequencer output goes to 0100 and the R input of F2 will be 1, so the code register's MSB is reset to 0, and the new code is 010. At the next CK pulse F1 may be Reset, depending on the comparator state. This process repeats until all the bits have been tested.

During the SA process the analogue input must obviously remain constant. It is usual to sample and hold the input signal before applying it to the ADC.

If a high conversion speed is not required, a microprocessor (or microcomputer) can take the place of the





SA hardware. The program is very simple, and may allow a high precision, low-speed ADC to be built using just a comparator and precision DAC in addition to the processor.

### **Ramp Until Ready**

In the ramp method (Fig. 15), a ramp voltage is generated by an integrator, synchronised with a clock. The clock is applied to a counter, which is stopped when the ramp voltage equals the input voltage. At this time the counter output is a binary number proportional to the input voltage.

The dual slope integrating converter (Fig. 16) is an extension of this idea, and is a popular circuit in digital voltmeters. The integrating capacitor is charged first of all for a fixed time by a current -Vref/Rref, and then



Fig. 18 Tracking ADC - block diagram.

discharged by the unknown current, Vin/Rref, until the integrator output reaches zero. A counter times this discharge period and the number it reaches is proportional to the analogue input.

In this way the Rref and Cref need not be precision components, and the comparator specifications can be relaxed as it has only to compare with a ground reference. It is also possible to null out offsets before every conversion by introducing a third stage during which 0V is integrated and the resultant output stored.

Both these types of single and dual slope converters suffer from having to generate ramps by analogue means which are synchronised to digital circuitry. Another method uses a DAC on the counter output to generate a ramp voltage which is compared with the input voltage

# **FEATURE: Data Conversion**

(Fig. 17). When the comparator changes state, the clock is stopped. The conversion time of this type of ADC varies with word length and input voltage. It takes less time to ramp up to a small input, and if fewer bits are used, fewer clock cycles are required to reach a given fraction of full scale.

A more elaborate scheme uses two comparators and an up/down counter. This is the tracking type of converter (Fig. 18). The maximum conversion rate depends on the rate of change of the input signal, not it's absolute value. To prevent the output jittering about a code, the ½ LSB offsets shown are introduced to the comparators. If the input is within this LSB wide window the counter is held.

A final type of circuit is the non-linear converter, which operates by assigning more codes to low input voltages, giving a higher resolution. An example of a coding law for a three-bit non-linear device is shown in Fig. 19.

Some types of signal — for instance, speech — show predominantly low levels. An eight-bit non-linear converter might have effective 12-bit resolution for signals from 0 to 25% of full scale, and only four bits for signals from 75 to 100% of full scale. But if the signal spends 80% of it's time between 0 and 25% of full scale, the effective signal-to-noise ratio is similar to that obtained from a 12bit converter, but with only eight-bit complexity. These types of converters are used in telecommunications to reduce transmission bit-rates and hence bandwidth.

An example of this type of converter is the Precision Monolithics PMI DAC78, which uses the three most significant bits to select one of eight'chords' of output—



shorter for lower input codes — and the next four bits to select one of 16 levels within each chord.

Data converters are now available in many guises to make their implementation easier, multiple converters in one package, time-multiplexed converters in one package, devices with serial digital inputs or outputs, and some which accept BCD rather than straight binary inputs. Special devices, designed to be easy to interface to a microprocessor, are also on the market.

This variety, combined with the falling cost of complex digital hardware, leads the way to a future where data converters are almost as commonplace as the opamp.

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# RCL Bridge



With precision autoranging digital meters available to test just about every electrical quantity, the humble measuring bridge has been rather neglected of late. L. Boullart feels it's time we took another look.

s there still a need for a measuring bridge? As far as resistors are concerned a digital or FET multimeter will do the job just as well. We all know that digital capacitance meters can be very accurate pieces of gear, but they are quite expensive and I suspect that many hobbyists don't have one. On the other hand, a measuring bridge — even a modern high-quality one — is reasonably cheap, and it is quite possible to obtain an accuracy of 1% or even better with a good design.

But doesn't it take longer to carry out a measurement? Not

really: I have never worked on a new design without it. Perhaps I am over-cautious, but I always test the capacitors, the resistors and the inductors before soldering them to the printed circuit board. If the design doesn't work properly from the start, at least I know it is not the Rs, Cs and Ls!



# PROJECT

A measuring bridge consists of three parts:

- a generator which feeds an 1) AC voltage to the bridge
- the bridge proper with 2) switchable elements (capacitors, inductors, resistors)
- 3) a measuring amplifier to indicate the tuning of the bridge.

### The Generator

In older bridges the generator often used the 50 Hz signal from the mains, injected into the circuit by means of a suitable transformer. However, such a low frequency will seriously limit the range of capacitors and inductors that can be measured.

Consider, for example, the impedance of a 10 uH inductor at 50Hz:

- $Z = 2\pi fL$ 
  - $= 6.28 \times 0.05 \times 0.01$

which means a virtual short circuit. On the other hand, a capacitor of 10p will have an impedance:

$$Z = \frac{1}{2\pi fC}$$

$$Z = \frac{10^3}{6.28 \times 0.05 \times 10 \times 10^{-6}}$$

318 x 106Ω



Clearly, we will have to look for a compromise.

First, let us assume a scale with a range from 0.1 to 10 times the central value, a sensible compromise of range and accuracy of reading. Next, what are the smallest and the largest values of C and L we wish to measure?

For capacitors there is no point in going below 10p and above 100u. Likewise, the inductance range should be from 1uH to 10H. Below 1uH, the influence of the connecting wires will cause appreciable reading errors and so will stray capacitances below 10p.

The lowest standard values in our bridge will then be 100p and 10uH and the highest will be 10u and 1H0. A little calculation will show that a frequency of 15 kHz represents the best compromise. At this frequency, both the lowest induct (10uH) and the highest capacitance (10u) will have an impedance of approximately 1 R0.

It will be relatively easy to design an oscillator which can deliver a small voltage into a 1R0 load. The requirements for such an oscillator are:

- good frequency stability a)
- b) a modest amount of harmonic distortion, say  $\pm 1\%$
- c) constant output voltage
- d) low-power output capability symmetrical output, isolated e)
- from earth
- simple circuitry. Ð

These requirements can be met by using a standard op-amp, a Wien bridge oscillator and an external complementary output stage.

### The Bridge

The working principle of a measuring bridge is illustrated in Fig. 2. The two resistors marked R are equal in value,  $R_{cal}$  is the calibration resistor,  $R_x$  is the resistor whose value is being measured and M is an AC meter. When the voltages at A and B are equal, the bridge will be in equilibrium and the meter will read zero. This will occur when R<sub>cal</sub> is equal to R<sub>w</sub> so if a calibrated scale is provided for  $R_{cal}$  the value of  $R_x$  can be read directly from it. There are several possible

circuit arrangements for a

### **HOW IT WORKS**

The circuitry around IC1 is a Wien-bridge oscillator with an external com-plementary output stage. The stabilising element in the negative feedback path could be an RA 53 thermistor, but this is rather expensive, so a combination of silicon diodes and resistors has been used. The configuration is similar to the CA 3019 and gives excellent results as far as distortion and output stability are concerned.

Diodes D5-D8 form a bridge, which is stabilised by means of a series combination of resistor R7, diode D9 and zener ZD1. R5 and RV1 are shunted across the bridge and determine the onset of oscillation.

The oscillation, The oscillator frequency is fixed by R2, C4 and R3, C3, both networks hav-ing a period of 10.4 us. The output voltage is 3.9 V with rather better than 1% THD.

To feed the measuring bridge, 0.5 V RMS will be amply sufficient, and this supply voltage must be symmetrical to earth. An 8:1 transformer enables us to meet these requirements. The worst case loading on the secondary of this transformer is Z = 1 ohm. This impedance, when transformed into the primary, is equivalent to 64R. The output current is then 62.5mA and Port = 250mW

out = 250mvv. This is beyond the capabilities of the CA3140 so a low impedance output stage is required, the simplest solution being a complementary pair of NPN/ PNP transistors such as the BC 548B/ 558B.

The complementary pair is designed as a separate unit with biasing current through R7, R8 and D10, D11. There is no quiescent current in Q1, Q2, but the transistors are on the verge of conduction.

Oddly enough, this gives best results on heavy loads, and the arrangement provides a slightly lower harmonic distortion - 0.8% for Vout = 3.8 V RMS. The reason is that the distortions in the CA3140 and the complementary pair partly cancel.

The output of T2 feeds into the The output of T2 feeds into the bridge itself, the network consisting of R9 and 10, RV2 and the switch-selected standard capacitors, C8-13 and standard inductors, L1-6. RV2 balances the bridge and has its wiper connected to ground. Since capacitive reactance falls with frequency and inductive reactance rises with fre-quency, the X and CAL positions for these two components are the reverse of one another if the 0.1 to 10 balance scale is always to read in the right direction. For this reason, the standard capacitors are connected to one arm of the bridge and the standard inductors to the other.

Any voltage appearing when the bridge is out-of-balance will be fed via the potential divider R11/R12 into the me potential divider K11/K12 into the non-inverting input of IC2, another CA3140 op-amp. A potentiometer, RV3, has been included in the gain setting network so that the sensitivity of the measuring amplifier can be reduced during initial balancing of the bridge. Another diode bridge, this time consisting of germanium diodes, drives the meter from the output of the opthe meter from the output of the opamo

The power supply is conventional in every respect. A centre-tapped transformer, T1, feeds four rectifiers in centre-tapped a bridge arrangement to provide posia broge arrangement to provide posi-tive and negative rails, each of which is provided with a single 470u reservoir capacitor. Two further electrolytic capacitors, C16 and 17, are included to aid smoothing and decouple noise, etc. picked up on the supply rails.

		PA	RTS LIST		
RESISTORS otherwise sta	(all 0.3W 5% film unless ted)	C7, 14 C8	10u tantalum 100p 1%	L3	1mH (Toko 181LY -102)
R1	470R (not needed	<u></u>	polystyrene	L4	10mH (Toko 181LY
1	It LED1 has integral resistor)	C10	10n 1% polystyrene	L5	– 103) 100mH (Toko
R2, 3	6k8 1%	C11	100n 1% (see text and Buylines)	16	181LY – 104) 1H0 (Toko 239LY –
R5, 6	6k8	C12	1u0 1% (see text		105)
R7 R8 R9 10	3k9 4k7 4700	C13	and Buyines) 10u axial elec- trolytic (see text)	MI	100uA panel meter (or 50uA — see Rudines)
R11	820k	C15	1u0 polycarbonate	SK1 – 3	4mm binding post
R12	3M3 100R (or 220R —			SW1	SPST miniature
RV1	see Buylines) 500R horizontal	SEMICONDU	CTORS		mains toggle switch
RV2	skeleton preset 10k linear	IC1, 2 Q1	CA3140 BC548B	SW2	1 pole, 12 way rotary switch (see
D)/2	potentiometer	D1 - 4	BC558B 1N4002	T1	6-0-6V, 3VA PCB-
KV3	tiometer (or 10k —	D5 - 11 D12 - 16	1N4148 0A91		transformer
	see Buylines)	ZD1 LED1	5V1 400nW zener Panel-mounting	T2	Philips P22/13 pot core (or
CAPACITORS	;		red LED		equivalent) with 120 turn primary
C1, 2	470u 16V radial electrolytic	MISCELLANE	DUS		and 15 turn secon- dary (see text).
C3, 4	1n5 1% polystyrene	L1	10uH (Toko 144LY	PCB; case, Te similar: knobs	eko Desko TEK 363 or ; mains cable and strain
<u> </u>	electrolytic 470n talntalum	L2	– 100) 100uH (Toko 144HY – 101)	relief bush; n support PCB; wire, etc.	uts, bolts and pillars to solder pins, connecting

measuring bridge, the two main ones being illustrated in Fig. 3. The arrangement shown at (a) is the simpler method because it is not symmetrical about earth and does not therefore require a transformer to couple it to the output from the AC generator. The circuit shown in Fig 3b is symmetrical about earth and does require a transformer but it is far less likely to suffer from the effects of stray capacitance. This is the arrangement which is used in this design, and the stray capacitance in the prototype is below 2p0.

There are six capacitance ranges and six inductance ranges on the prototype. It is not difficult to extend the bridge to measure resistance as well but this makes the range switching rather complicated and pushes up the cost.

A simpler alternative is to omit one of the standard capacitors. The range switch can be set to this blank position and an external decade resistance box connected in the  $L_x$  position, between the L and common terminals. An unknown resistance can then be connected in the  $C_x$  position (between the C and common terminals) and measurements carried out in the normal way.

Since electrolytic capacitors larger than 10u are difficult to measure accurately without a polarising current, the 10u range is of limited value only. It can safely be discarded to leave a blank position, and if a high capacitance range is ever required a 10u capacitor can be connected externally.

### The Measuring Amplifier

With a 500mV input to the bridge, a sensivity of 10 mV full scale will be more than sufficient: a difference of 0.1 mV (1/100th of the full scale deflection) can still



arrangements; a) one side earthed and b) symmetrical about earth. easily be seen. This means a maximum sensivity of 1 part in 5000 (500 mV/0.1 mV). If the diameter of the calibrating scale for RV2 is 90 mm (235 mm scale length), 1 part in 5000 averages 0.047 mm. Are your eyes good enough to detect that small an increment?

The amplifier itself is another standard op-amp, here arranged as a voltage follower. A potentiometer is included to vary the gain of the stage, allowing the sensitivity to be reduced to make initial balancing of the bridge easier.

### Construction

The prototype was built into a grey ABS case with a sloping aluminium front panel. Almost all of the components, including the mains transformer, mount directly onto the printed circuit board. The remaining components mount through the front panel and the PCB is supported against the underside of the panel. This makes for a neat and compact assembly, and the only construction work required on the other half of the case is to provide a hole and strain relief assembly for the mains cable to pass through.

The pot core must be wound with 120 turns of 34 SWG enamelled coper wire to form the primary and 15 turns of 24 SWG

# PROJECT: Bridge



Fig. 4 Component overlay for the measuring bridge PCB. SK1-3 and LED1 mount through the front panel but have been shown here for completeness. Note that the potentiometers and the rotary switch are mounted on the PCB and that capacitors C8-13 are mounted vertically with their upper leads taken directly to SW2.

enamelled copper wire to form the secondary. Other pot cores could be used provided they have an inductance factor ( $A_1$ ) of 250 nH/turn<sup>2</sup> or a turns factor ( $\alpha$ ) of 62.8 turns/mH.

Before starting to assemble the components onto the PCB, line-up the board behind the case front panel and mark and drill the necessary holes. It is a good idea to start with the holes for the two potentiometers and the rotary switch, then loosely assemble these components through the board and the panel to check that they line-up correctly. The board can then be clamped to the front panel using the switch and potentiometer mounting bushes while the remaining holes are drilled.

When the drilling is complete, begin assembling the PCB by installing the solder pins, the wire links, the IC sockets, SW2, the potentiometers, T1 and T2 and then the resistors, capacitors and inductors. Note that R15 is soldered into the board at one end and onto one of the contacts of RV3 at the other end. R11 must be soldered directly to the wiping contact of SW2 and kept away from other parts in order to keep stray capacitances to a minimum.

The standard capacitors C8-13 should be mounted vertically with one lead soldered through the board and the other bent over and taken to the appropriate switch contact. If you have been unable to find 1% tolerance 100n and 1u0 capacitors for C11 and C12, leave these positions blank for the moment. Once the rest of the bridge is working, it can be used to select suitably accurate components from standard 5% and 10% stock.

If you intend to use the bridge for resistance measurements, you have the choice of either modifying the range switching arrangements or simply omitting one capacitor as discussed earlier. If you choose the latter option, simply leave out C13 and remember when you come to label the front panel that the 10u range should be marked external instead. When all of the other components have been soldered into place, the diodes and then the transistors can be added. The extended copper pads around the collectors of Q1 and Q2 are intended to help heat dissipation, and the collector leads should be trimmed to 4mm or less before soldering. The other two leads on each transistor can be made a little longer to ease installation.

The remaining components should now be mounted on the front panel, these being the meter, the LED, SW1 and the three binding post connectors. It is important that the connectors are well isolated from the panel so as to keep stray capacitances to a minimum. This will largely depend on the construction of the connectors themselves, so choose fairly 'meaty' types which have as much plastic insulation between the metal conductor and the panel as possible. If stray capacitance is a problem, try cutting a single large hole in the panel and mount the connectors on a piece of paxolin or other insulating material.

**BUYLINES** 

The resistors, the semiconductors and most of the capacitors are available from companies advertising in these pages and from the other usual sources. The inductors are all available from Cirkit, and West Hyde Developments stock the Teko case. Electrovalue can supply the pot core, a winding bobbin and a PCB mounting kit for the assembly. Their order codes are B65661 L0250 A028 for the pot core, B65662 B0000 T001 or T002 for a single or double plastic bobbin, and B65665 C0004 X000 for the mounting kit. The pot core is available while stocks last at 60p, after which it will be replaced by a similar item, B65661 N0250 A022 which will cost £1.26.

Some of the 1% tolerance capacitors are available from Maplin but the 100n and 1u0 are not. Philips manufacture a range of polystyrene capacitors

Attach the PCB to the front panel using four spacers and countersunk-head bolts. Solder a pair of leads to the points marked XX on the PCB and connect the other ends to SW1. Connect up the meter carefully observing polarity and also solder leads from the PCB to the three connectors and to the LED. Note that R1 will not be needed if you use a panelmounting LED which has an integral resistor. Insert the two ICs into their sockets, making sure that they are the right way around.

Feed the mains lead through a hole in the bottom half of the case, secure it with a strain relief bush and solder the live and neutral leads to the pads near T1 primary. Connect the earth lead to the front panel by means of a solder tag under one of the PCB support bolts. This will reduce the effects of hand capacitance when testing small capacitors.

### Calibration

Before starting the calibration procedure, the oscillator must be adjusted. This is done with the preset resistor RV1, which must be set such that the amplification factor is just a little above 3. If the gain is below 3, the oscillation stops altogether, and if it is too much above 3 the wave-form becomes very distorted. RV1 must therefore be set very carefully just a little above the point where oscillation starts.

If this point is not within the range of the preset, it will be necessary to make a slight correction to R5 (6k8). This can easily be done by shunting R5 designated C424 and C444 which includes a 100n type, and constructors who have access to trade component suppliers may be able to obtain some of these. We do not know of any manufacturer who produces 1u0 capacitors with a tolerance of 1%. Both types can be substituted by gathering together a number of wider tolerance capacitors and then using the otherwise completed bridge to select the most accurate. The procedure is described in the text.

Although a 100ua meter is specified in the parts list, the prototype used a 50uA Micronta meter from Tandy. If this or any other 50uA meter is used, R13 and RV3 should be exchanged for the values shown in brackets. Nothing else should present any problems and the PCB is available from our PCB Service.

with a high value resistor (on the copper side of the printed circuit board). In the prototype, 270k was just right.

An oscilloscope or a distortion meter will be very helpful here, but if these are not available just make sure that the oscillator output is  $3.9V \pm 2\%$ . As a final test of correct working conditions, connect a 1R0 resistor across the secondary of T2. If the oscillator stops, the gain must be slightly increased by means of RV1.

Now we turn to the calibration. First we need a knob with a dial pointer, and this can be made from a piece of clear plastic glued to the underside of an instrument knob. You may be lucky enough to find a knob with threaded holes on the underside.

Make a provisional scale and tape it to the front panel, then turn SW2 to the free position. You will need nine precision resistors of 100R and one of 1k0, preferably all of 0.5% tolerance.

Connect a 100R resistor across  $C_x$  as the reference value and another 100R resistor across  $L_x$ . Adjust the calibration knob carefully to read a minimum on the meter and make a pencil mark on the scale. This will be the '1' position.

Next, connect two of these resistors in series across  $L_x$ , adjust the potentiometer for zero reading on the meter and mark a '2' on the scale.

Repeat the procedure with 300R to give the 3 position, 400R to give the 4 position and so on up to 10. Intermediate points can be found by using two 100R resistors in parallel as one series element, giving 1.5, 2.5, etc. To calibrate the other half of the scale, connect the 1k0 resistors across  $C_x$  as the reference and use series strings of the 100R resistors to obtain 0.1, 0.2 and so on and series-parallel chains to obtain the intermediate points.

If you were unable to find either or both of the 100n and 1u0 1% standard capacitors, you can now use the bridge to choose suitable examples. One method for selecting the 100n capacitor is to set the bridge to 10n and then test a batch of 100n capacitors until you find one which gives a reading sufficiently close to 10 on the scale.

A more accurate method is to connect several 1% 10n capacitors across the  $C_x$  connections and then select a 100n capacitor which gives a reading of 0.1 or 0.2 or whatever is appropriate for the number of capacitors you have used. The more capacitors (and therefore, the nearer their



# PROJECT: Bridge



combined capacitance is to 100n) the more accurate the selection will be. Once the 100n capacitor has been selected, the process can be repeated to find a 1u0 capacitor.

The inductors specified in the parts list all have a tolerance of  $\pm 5\%$  except the 1 H0 coll which has a tolerance of  $\pm 10\%$ . These are rather low levels of accuracy but are generally regarded as acceptable for inductors. In my opinion it is not really worth spending much time and effort improving on these figures, but if you particularly want a high level of accuracy then the following method should enable you to achieve it.

Figure 5 shows the circuit to be used. C is a capacitor of  $\pm 1\%$ tolerance or better and R is adjusted according to the impedance of LC at a given frequency. The frequency generator should be adjusted for minimum reading on the AC voltmeter and the frequency then noted using the DFM. The value of the inductor can be calculated using the formula:-

Once the value of a particular inductor is known, it can be used to select other inductors by means of the procedure described for selecting capacitors.

Once the standard capacitors and inductors have all been soldered into place, the front panel can be secured to the bottom half of the case and a final calibrated scale prepared. It is a good idea to use a graduated arc and to plot a curve on millimetre scaled paper. This will reveal any serious errors arising from the calibration process and also yield a few extra intermediate points such as 1.1, 1.2, etc.

In use, the only point to remember is that there will be a residual capacitance of about 2p0 which should be taken into account when measuring small capacitances.

ETI



# ETI SORCERER STRING SYNTHESIZER

Contrary to popular belief, writes designer and author Graeme Durant, a string synthesizer is not a machine for making twine. Relax and unwind as the yarn unfolds.

he string synthesizer originated in the 1970s, and perhaps owes more to organ developments than synthesizer technology; the basic instrument being polyphonic with preset sounds, usually generated by an organ-type frequency divider, gating and filtering, followed by a chorus generator to enhance the massed string quality.

Although the string synthesizer is very commonly used in many types of music today, due to its unique ability to fill out the sound of a small band without being too forward, it is usually used as a backing instrument to other keyboards. So, with an average sort of selling price in the order of four hundred pounds for a commercial unit, the string synthesizer is often out of the reach of many amateur musicians, as a mere second instrument.

That is where the ETI Sorcerer comes in. For a mere fifty pounds outlay, the Sorcerer provides this lush backing sound albeit in monophonic form only, but with sufficient power to produce very emotive backdrops.

### **Facilities**

The ETI Sorcerer is unlike most commercial synthesizers in that it is built along the lines of a general purpose analogue synth, using similar circuit blocks such as VCOs and envelope generators. Its variable controls also mirror some of those more often found on analogue synths, providing great versatility and many sound options apart from the more usual string effects.

The basic keyboard is a three octave unit, but Sorcerer provides three switches akin to organ stops, which allow individual or simultaneous selection of three one octave spaced ranges. Selecting all three ranges allows an effect like holding down a bass note, a note an octave up and a note two octaves up. The results are extremely powerful. A glide



# **PROJECT**

control allows slewing between played notes and a fine tune control means Sorcerer can be tuned to other instruments. A vibrato effect is included, and controls are available to adjust its speed and depth. There is also an envelope contour generator with variable attack and release rates.

To simulate strings it is not necessary to go to the expense of using a full ADSR envelope generator, as on most analogue synths. An AR generator with a 'sustain on/off' switch is perfectly adequate. To produce the effect of massed instruments, a powerful chorus generator is included in the unit, which can be bypassed if desired for solo playing. The Sorcerer has a line output for an amplifier and a high impedance headphone output for use during recording.

The keyboard is a touch operated unit, chosen primarily for reasons of economy. A conventional keyboard can be used if preferred and the budget allows.

A three octave range is provided for by a PCB keyboard with full width keys. Being electronic, it has been designed to delay its response slightly to

simulate the time usually taken for key travel and to ensure reliable detection of touch. As a result, it will not respond to fast playing, but this was not considered a particular disadvantage since Sorcerer was designed for slow backing-type use anyway. The keyboard includes circuits which detect when more than one key is being pressed. These provide a sort of multiple trigger function, similar to 'two key roll-over' found on computer keyboards, so that as long as only one key is pressed, it will be the one which is sounding.

### **Block Diagram**

Sorcerer is built up on six printed circuit boards with a separate power supply board. This allows a modular construction and the possibility of adding new modules for special effects. The keyboard forms two of these boards, producing a key voltage proportional to musical pitch and two timing signals — gate and trigger.

The key voltage goes on to the VCO board. This board centres around a precision voltage-tofrequency converter which outputs a pulse train at a multiple

of the desired frequency. A low frequency oscillator provides the modulation for vibrato. The pulse train is divided in frequency to the three required pitches, at one octave spacings, and the square pulses resulting are given the required width characteristics. After passing through octave selector switches, the three signals pass on to the chorus boards. Here, the three signals are individually filtered to produce sounds closer to strings, and then mixed to form one composite signal. The signal is fed either via parallel delay lines to gain a heavy chorus effect or directly off board if the chorus mode is not switched in.

The signal now reaches the final processing board, the envelope circuits. These give the signal the desired amplitude contour for the synthesis and allow variation of the amplitude attack and release rates, as well as selection of the sustain time. Synchronization is obtained from the trigger and gate signals from the keyboard. The output is then buffered and sent to the output jack, and to a low-power amplifier suitable for driving high impedance headphones.





### Fig. 2 Circuit diagram of the VCO section.

In Sorcerer, the VCO circuits are primarily concerned with converting the keyboard pitch voltage into a proportional frequency output. They also provide the facilities for glide and vibrato, and generate three one octave spaced pulse train outputs, with the correct markspace ratios.

The keyboard pitch voltage is applied to one end of RV1, the glide control. C1, a low leakage tantalum capacitor at the slider of RV1, provides the required variable slewing between notes. The other end of RV1 goes via buffer to the rest of the circuit. The buffer, IC1, is required so that the glide control sees a ver high impedance (about 1012 ohms) otherwise the pitch of the VCO would be affected as the glide control was adjusted. The pitch voltage is then inverted by IC2 wired as a unity gain inverting amplifier. This is necessary since the voltage-tofrequency circuit used requires a negative input voltage.

The voltage-to-frequency converter circuit is based around IC3 and IC4. IC3 is either an LM331 or equivalent RC4151 which is somewhat cheaper. IC4 is another LF351, this time chosen for its low input offset current, and is wired as integrator. A simplified circuit is shown in Fig. 3.

The output voltage of IC4 goes go one input of a comparator inside IC3 at pin 7. The other input, pin 6, goes to a

circuit point at half the supply voltage. When the voltage at pin 7 is the greater, the comparator triggers the one-shot timer. This will turn on both the frequency switched current source for a time t = 1.1R.C. During the source for a source fo output time  $t = 1.1R_1C_1$ . During this time, a current i will flow out of pin 1 into the input of the integrator. The integrator output will start to ramp down. This current will have an average magnitude of  $I_{av} = itF$ , where F is the frequency of oscillation. This average current perfectly balances the current due to the input voltage,  $-V_{in}/R_{in}$ , at the integrator's virtual earth input. At the end of the one-shot timing period, the current source and the output transistor are both switched off. The integrator output will start to ramp positive again, until it exceeds the voltage at pin 6 of IC3, when the cycle will start again.

The frequency of oscillation can be determined from the balanced input current:

 $-V_{in}/R_{in}=I_{av}$ but l<sub>av</sub>=itF and

 $i=V_{ref}/R_s$  which equals 1.9/Rs, since Vref=1.9V and is internal to IC3. Also:

=1.1R<sub>t</sub>C<sub>t</sub> so that F=-( $V_{in}/2.09$ ).(R<sub>s</sub>/ R<sub>in</sub>).(R<sub>t</sub>C<sub>t</sub>)) and F proportional to -Vin.

is

In our case R, is made up from R16, RV4 and RV5. The latter two variable resistors make for fine and course tuning respectively. Instead of connecting the end of R, to ground, it is fed from a buffer IC5a, driven by a variable lowfrequency and variable amplitude triangle wave generator. This standard integrator-Schmitt trigger oscillator generates a triangle wave which is symmetrical about 0V. It slowly varies the frequency of the VCO, cyclically about its programmed pitch, by adjusting the current flowing adjusting the current flowing through R<sub>s</sub>. This provides the vibrato function.

The frequency output of IC3 at pin 3 is divided by 2, 4, 8, and

signals pass on to the filter/ chorus boards. Vref/Rs IC3 SWITCHED CURRENT SOURCE i = Vref Rs RI 0 COMPARATOR Cint 11 FREQUENCY OUTPUT ONE SHOT Vin Rin INTEGRATOR Fig. 3 Block diagram of voltage

16 at pins 12, 11, 9 and 6 and

IC6 respectively, a CMOS ripple counter. These outputs pass through IC7 and IC8c, which

convert the squarewave signals

to pulse waves with a 25% duty cycle at the 4' and 8' outputs and with a 12.5% duty cycle at

the 16' ouput. These particular pulse widths have harmonic con-

tents which much more closely approximate the sound of a

violin and cello respectively, and

are thus used for the basis of

these sounds. The outputs are

switched electronicaly using IC8a, b, and d, via front panel switches SW1, 2, and 3, to save routing signals to the front panel. The three, octave-spaced,

# PROJECT: Synth



The keyboard unit must take an input from the musician paying the keyboard, and produce information from the key operated, to tell the rest of the synthesizer what to do, and when to do it. This information is a voltage, proportional to the frequency of the required pitch, a trigger pulse produced every time a new note is operated and a gate pulse signal which is a local high for the duration of any key press. Suitable touch switches come

Suitable touch switches come in three types, with different principles of operation. The simplest is the resistive type which detects the change of resistance between two contacts bridged by the fingers. This method was used some years back in an ETI project for a miniature organ keyboard. Although simple, it has serious

drawbacks — it is disabled by moisture and does not respond to musicians with dry skin! A much better, but more complex technique is to use the principle that a human body acts just like a small value capacitor with one end grounded, using a finger as the other connection. Such designs usually employ a high frequency oscillator and moderately complex detection circuits — not really suited to being reproduced thirty seven times on a keyboard! The technique chosen for Sorcerer uses hum-detection. The human body acts like a sort of aerial, picking up mains hum which can be detected using a high input impedance amplifier.

Each key is formed by an area of copper on the keyboard PCB, is linked to the non-inverting input of a low-power op-amp and held to the supply voltage by a very large resistor. When the keys are untouched, the resistors pull the non-inverting inputs above the voltage of the inverting inputs, each tied to 0v, and so the op-amp outputs are high. When a key is touched, a 50Hz signal is injected into the non-inverting input of the appropriate op-amp. Since the op-amps are used here as large gain comparators, the output is in the form of a 50Hz pulse train between 0V and 3.5V which must be detected.

Each key is encoded with an eight-bit binary number which will eventually go for digital-toanalogue conversion. The coded numbers must be proportional to the required pitch voltages.

The relationship between the

frequency of the successive notes on a musical keyboard is exponential not linear. Each octave is a doubling in frequency and adjacent notes are multiples of the twelfth root of two in frequency! The usual method used in analogue synthezisers to achieve this musical scale is to design a keyboard which produces a linear voltage output, often a standard 1 volt/octave, passing this to a special VCO which responds in an exponential fashion. Thus, a simple keyboard is used to control a very complex and expensive VCO. The linear to exponential conversion technique is very prone to the effects of temperature change and component mismatch. If it can be avoided life becomes much simpler!

# PROJECT: Synth

### **KEYBOARD UNIT**

of stability and complexity at the price of precision. The keyboard produces an exponentially incremented pitch voltage output, using a series of exponentially related binary keycodes, and drives a linear VCO. Being digitally generated, the keyboard output is very stable, but since it is only limited to eight-bit precision, the voltages have small — but normally unnoticeable — errors referred to the exact voltage required.

The binary codes are programmed using a diode matrix. The key op-amp outputs are connected, where required, to the eight-bit data bus by reversed signal diodes D40 to D186, forming the exponential code (Table 1).

The data on the data bus is still in form of 50Hz pulse trains and must be made into steady logic levels to drive the digitalto-analogue converter. The signals on the data bus are lowpass filtered by R97 to R112 and C7 to C14 to remove the 50Hz component, and drive CMOS inverting Schmitt triggers in IC19 and IC20. If a data line is inactive, the input to the Schmitt is held high by one of R105 to R112. An active line produces a high at the Schmitt trigger output, and an inactive line a low.

The resulting steady codes are passed on to IC21, an eight bit digital-to-analogue converter, to be changed into the output pitch voltage. Diodes D195 to D202 and storage capacitors C15 to C22 ensure that data is valid even if a key is operated for less time than it takes the DAC to latch.

The gate signal is produced by using an eight input diode OR gate (D187 to D194 and R113) connected to the eight logic level data lines. Pin 3 of IC22 will go low when any key is pressed. A delay is produced on this falling edge by R122, D203 and C23, so that the output of IC22b will go high shortly after any key is pressed, giving the data lines time to settle down.

D3 to D39 and R60 to R96 feed a filter-Schmitt trigger circuit using IC19a and b, the output of which will go low if two or more keys are pressed at the same time inhibiting the trigger and latch. This signal has its rising edge delayed by R123, D204 and C24, so that the data lines can settle down when two keys are pressed simultaneously and one is then released.

IC22c, C27 and R124 produce a negative pulse when there is a change from no keys or more than one key being pressed. This pulse is used to update the digital-to-analogue converter latches, and in inverted form, via IC22d, used as the trigger signal for the envelope generator.

NOTE	EXP. COD	BINARY COD
C	31	00011111
C#	33	00100001
D	35	00100011
D#	37	00100101
E	39	00100111
F	41	00101001
F#	44	00101100
G	46	00101110
G#	49	00110001
A	52	00110100
A#	55	00110111
B	59	00111011
C	62	00111110
C#	66	01000010
D	70	01000110
D#	74	01001010
E	78	01001110
F	83	01010011
F#	88	01011000
G	93	01011101
G#	98	01100010
A	104	01101000
A#	110	01101110
В	117	01110101
C	124	01111100
G#	131	10000011
D D	139	10001011
U#	147	10010011
E r	156	10011100
	166	10100110
<b>F#</b>	175	10101111
6	186	10111010
<b>U</b> #	197	11000101
A	209	11010001
A# D	221	11011101
C	234	11101010
C	248	11111000

Usually, one of the most critical parts of a music synthesizer is the power supply unit. In an analogue design most of the synthesizer parameters are supply dependent. A professional machine must have a drift free tuning, and as a consequence the power supply

design is very complex. This is not the case in Sorcerer. All critical parts of this design have local references of their own, most based on very stable band-gap devices in the ICs used. Thus a simply regulated supply is all that is required. Sorcerer requires three power

Sorcerer requires three power rails to run, and all of these are mains derived. A dual secondary 9V transformer, rated at 8 VA is used as a source. This is conventionally rectified and

smoothed by BR1, C28 and C29. IC42 provides a +5 volt regulated output, and IC43 a -5 volt supply. The other supply required is a +10 volt output. Since it is not common to find fixed ten volt regulators, a low current 5 volt device, IC44, is stacked on top of the main +5 volt rail. D205 ensures that the IC starts up correctly when driving a capacitive load.



Since the Sorcerer is a project of considerable length, we have been obliged — for reasons of space — to split it up into a number of parts. The circuit diagrams and descriptions of the envelope shaping and chorus sections of the synthesizer will appear next month. We hope to publish the PCB foil patterns and component overlays the following month, along with constructional details, information about modifiying the circuit for use with a conventional keyboard, details of the setting-up procedure, parts list and Buylines.

# .PROJECT

# EX42 INTERFACE FOR THE BBC 'B'

In response to a flood of readers' queries, we present a project devised and typed by Philip Ashby using a BBC computer and an EX42 daisywheel typewriter, in which he tells us how he did it.

n ETI, October 1983, we featured an interface that enabled a Silver Reed EX 42 electronic typewriter to convince itself it was a computer printer. The economic arguments for using a typewriter rather than forking out for a daisy-wheel printer are still just as valid — and you get a good quality keyboard thrown in as well!

For this author, the interface in its original form proved capable of many hours of trouble-free listings when coupled to a Jupiter ACE. The home computing capacity has since been upgraded to a BBC B, and with that came the promise of more work for the EX42: not just listings but text print-outs, too, from the word-processing package. A couple of extra ICs and 180 or so bytes of machine code make for a conversion kit, to give compatibility with the Centronics port on the Beeb.

### Repeat

The basic interface remains intact (Fig. 1), so if you don't have the board go scurrying back to October 1983's ETI, p21. To recap briefly: the processor inside the





typewriter scans across an 8x8 matrix of switch contacts in the keyboard, looking to see which of the 8 horizontal lines has been connected to a vertical one. It does this by sending successive voltage pulses down each vertical and looking for them on the horizontals.

Connections run from the interface to each of the keyboard matrix lines in the typewriter. The interface 'fools' the typewriter's processor into thinking a physical contact has been made on the EX42 keyboard by mirroring the scanned pulse from an X line into the appropriate Y line.

Between them, the computer and interface have two major jobs: code translation (from output ASCII to a 6-bit code for the keyboard matrix) and overall control of data flow via the handshake lines.

### HOW IT WORKS.

IC2 in Fig. 4 forms a monostable timer with a period adjusted by R2 to approximately 2s. When triggered, it produces a positive pulse. The trigger the timer is the output to on D7 from the computer, gated with STROBE through the NANDs, ICla and 1b. The timer will trigger with D7 at logic 1 and STROBE on a negativegoing edge going low. Output of the timer then extends the the ACK pulse to 2 secs: otherwise ACK is the logical inverse of BUSY from the interface and would be high for about 100ms. The theoretical timer period is 1.1 x R<sub>3</sub> x C2. Adjust R3 if the actual period is too long or too short.

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### Shake On It

There are two handshake lines between the interface and the computer (STROBE and ACKNOWLEDGE on the BBC Micro) and, as a first job for the adaptor, their polarity needs to be inverted.

The handshake lines work like this: the parallel printer port on the Beeb will pulse STROBE down to 0V when a set of valid data (the next character) exists on the lines. The data stays the same until the return handshake on ACKNOWLEDGE. The protocol for ACK is to go negative when the printer has done its work. (For those readers expecting a line called BUSY, it exists as a contact pin on the socket of the Beeb, but it isn't used for control purposes.)

### **Number Crunching**

The translation from ASCII to keyboard code in the modified design is done by software in the micro, and data lines DO to D5 carry the translated information. D6 is used to signal a shift character to the typewriter, and D7 to indicate when a carriage return is being sent. The latter logic signal is used to trigger a 2s delay in the ACK pulse, to allow the printer head time to travel back to the left hand margin. It's strictly from the 'dirty tricks' side of electronic design: one 555 timer chip in hardware doing the job of several lines of software!

In contrast, software translation of ASCII to keyboard code was preferred because of the options available for future expansion different character sets (if, say, you save up your pennies for a new daisy wheel), or specials such as underlining. Software, like life, is never as simple as you hope, and two obstacles hit you straight away. BASIC won't suffice if you

#### PARTS LIST **RESISTORS (all 5%, ¼W) R1** 10k **R**2 1k8 **R3** 470k CAPACITORS **C1** 10n mylar or disc ceramic **C2** 4u7 **C3** 100n mylar or disc ceramic SEMICONDUCTORS IC1 74LS00 IC2 **NE555N MISCELLANEOUS** 26-way IDC connector and ribbon cable; PCB; thick gauge wire.

want to output text when in wordprocessor mode. Resident ROMs within the computer such as VIEW and WORDWISE have the rather nasy habit of doing fairly powerful rearrangement of memory to suit their own ends when storing text. The driver software has to be in machine code and, together with the translation table, be tucked away in memory out of reach of the word-processing program. They are listed here as programs in BASIC to generate the translation table and machine code routine, and then relocate them. Generation need only be done once for any one version of the character code.

The location to store the program in is page C: COOh to CB8h. This is normally used to store the fonts for extended character sets, so it's rather unlikely that it will ever clash with print demands from the EX42 unless you have a particularly exotic set of daisy-wheels!

The advantage of this location is that the area is not grabbed by

### SOFTWARE .

The software falls into three main areas: generation of the translation table; generation of the machine code translation program; and a routine to load these programmes and modify vectors used by the Operating System so that the programs are called when appropriate.

Listing 1 generates the translation table, the data for which is printed as Listing 2. The programme prompts you to enter the codes for each ASCII character, display the results, and assembles the code into an array called TABLE. The array is then stored in a file known as chars. Readers with utility programmes resident in their machines would obviously have quicker ways of locating the table in memory and storing to disc.

Lines 10 to 30 set up addresses used for temporary storage of registers and a value used later for a call to a routine in the Operating System (an OSBYTE call). Line 50 sets the compilation address of the code, and lines 40 to 70 set up the BASIC assembler for two passes. When the programme is activated, it will be entered whenever the computer wishes to store a character (held in a register) into a buffer. This is accomplished by changing the value of a vector used by the operating system, resident at 22Ah. The Operating System identifies the required buffer with a value in the X register held on entry to the routine. The value is 3 for the case of the print buffer, and this is checked by line 100, with branching on line 110.

If the print buffer is requested, the next job is to check whether it is full before attempting to fiddle the character! Lines 130 to 180 perform this task, and all being well, lines 190 to 220 make the change, using the code-table stored from OCOOh to translate the character.

The rest of the program restores various registers and assembles routines for the remaining branches. The address JUMPed to in line 260 is that of the original buffer routine.

Listing 3a is the BASIC program that assembles the translator machine-code program.

Listing 3b shows the screen display when the program is RUN and the code compiled. After compilation, type in \*SAVE "mc" OC81 OCB3 to store the machine-code program.

You now have two files, "mc" and "chars" stored on tape or disc, and Listing 4 loads both of them into memory. It also activates them by inserting the program address (OC81h) into the vector location in memory (22Ah) used by the Operating System for buffer operations. For convenience, this has been created as a 1800T file, assembled on disc, which will load and run on a SHIFT-BREAK. On completion, in this case, control is handed over to VIEW for word processing.

# **PROJECT: EX42/BBC Interface**

10 CLS: PRINT TAB(5,5) "ASCII CHAR" 20 DIM TABLE(128) 30 FOR A=0 TO 31:TABLE(A) = 16:NEXT A 40 TABLE(10)=137 50 TABLE(13)=137 00 FOR A= 32 TO 128 70 PRINT TAB(5,7) SPC(15) 80 PRINT TAB(5,7), SA;SPC(5);CHR\$(A); 90 INPUT AI INPUT AI TABLE(A) = AI 100 NEXT A C = OPENOUT "chars" FOR A= OTO 128 PRINT A;SPC(5); TABLE(A) BPUT# C,TABLE(A) 110 160 170 NEXT 180 CLOSE# C 190 END

#### Listing 1 Translation table generator.

other languages or wordprocessing ROMs, at least not in the author's experience. An alterantive location for disc users is page 8: 900h to 9B8h, an area normally used for tape buffers. The location of the program is set by lines 10, 20, 50 and 220 in Listing 3a and in Listing 4.

### **Construction and** Installation

The convertor is assembled on a small PCB which sits piggyback over the site of the interface's original EPROM, which should be removed and replaced with wire links (Fig. 2). Before fitting the convertor, connect the links from pins 2, 3, 4, 5, 6, 7 and 8



Fig. 4 Connections between boards, on original ROM socket and to IDC plug.

to pins 16, 15, 14, 13, 11, 10 and 9, respectively, on the EPROM socket. Three connections (D7, 0V and +5V) are made from the convertor to the interface, fitting pins 1, 12 and 24 in the EPROM socket. If these are made in thick gauge wire, they serve to anchor the boards together sufficiently (Fig. 2).

Connection to the micro is by means of 26-way ribbon cable. with an insulation displacement connector at the micro, which plugs into the parallel printer

socket on the underside of the computer. IDC connectors are the answer to many a constructor's prayer as a means of connecting so many wires at once: they can be bought ready assembled, but are simplicity itself to make up. Just position the cable over the plug, matching wires with contact points, fit the plastic cover plate, taking care to line it up, and tap gently with a hammer. With luck all connections are made at once and it then only remains to fit the strain relief clamp over the cable.

40 FOR opt%=0 TO 3 STEP 3

10 store = & 0 CB720 storey = &OCB8 30 OSBYTE = &FFF4

#&80 140 LDX #&FC 150 LDY

#&FF 160 JSR OSBYTE 170 CPX #&00 180 BEQ escape 190 LDA

store

270 .escape LDA store 280 LDX #&03

Listing 3a BASIC program to

assemble translation program.

200 AND #&7F 210 TAX

290 JMP exit

1 310 NEXT opt% 320 END

240

270

300

220 LDA &0C00,X 230 LDX #&03

.exit PLP 250 LDY storey 260 JMP &E4B3

50 P%=&0C81

60 C 70 OPT opt% 80 PHP 90 STY storey 100 CPX #&03 110 BNE exit 120 STA store 130 LDA

LOC	CONT	EH	1 nc	CONT	CH	LOC	CONT	CH	1.00	CONT
Ŭ	16		32	1	a	64	10	μn.		LONI
1	16	1	33	71	Ā	45	97	-	70	27
2	16		34	103	R	66	50	et b	77	20
3	16	£	35	90	Ē	67	94	5	70	27
- 4	16	\$	36	102	D.	68	86		100	22
5	16	%	37	69	F	40	79		100	22
6	16	8.	38	101	F	70	118	4	102	5.4
7	16		39	68	G	.71	85	à	103	21
8	16	(	40	100	н	72	117	9	104	5.7
9	16	)	41	67	T	73	108	1	105	44
10	137	*	42	82	Ĵ	74	84	i	106	20
11	16	. +	43	115	ĸ	75	116	L L	107	52
12	16		44	60	1	76	83	1	109	19
13	137	-	45	2	M	77	92	m	109	28
14	16		46	27	N	78	125		110	61
15	16	/	47	59	0	79	75	0	111	11
16	16	0	48	35	P	80	107	n	112	43
17	16	1	49	7	Q	81	79	0	113	15
18	16	2	50	39	R	82	110	r	114	46
19	16	3	51	6	S	83	119	5	115	55
20	16	4	52	38	Т	84	77	ť	116	13
21	16	5	53	5	U	85	76	u	117	12
22	16	6	54	37	V	86	126	V	118	62
23	16	7	55	4	ы	87	111	w	119	47
24	16	. 8	56	36	Х	88	127	×	120	63
25	16	9	57	3	Y	89	109	Y	121	45
26	16	0	58	18	Ζ	90	95	z	122	31
27	16	5	59	51	8	91	202	<	123	1
28	16	<	60	196	$\mathbf{N}^{-}$	92	1	1	124	187
29	16	No.	61	99	1	93	138	>	125	1
30	16	>	62	194	$\sim$	94	1	~	126	1
31	16	?	63	123		95	66		127	16
										- 1

Listing 2 Decimal data for translation table.

# **PROJECT: EX42/BBC Interface**

0C81				05.1	opt%	
0C81	80			PHP		
0C82	<b>8</b> C	B8	0C	STY	storey	
0C85	ΕO	03		CPX	#&03	
0C87	DO	1 B		BNE	exit	
0C89	8D	B7	00	STA	store	
0C8C	A9	80		LDA	#&80	
0C8E	A2	FC		LDX	#&FC	
0C90	A0	FF		LDY	#&FF	
0C92	20	F4	FF	JSR	OSBYTE	
0C95	EO	00		CPX	# \$ 00	
0C97	FO	12		BEQ	escape	
0C99	AD	B7	0C	LDA	store	
0C9C	29	7 F		AND	#&7F	
0C9E	AA			TAX		
0C9F	BD	00	0C	LDA	&0C00,X	
OCA2	A2	03		LDX	#&03	
0CA4	28			.exi	t PLP	
OCA5	AC	<b>B</b> 8	0C	LDY	storey	
OCA8	4C	B3	E4	JMP	&E4B3	
OCAB .	AD	B7	0C	.esc	cape LDA	store
OCAE .	A2	03		LDX	#&03	
OCBO ·	4C	A4	00	JMP	exit	

## Listing 3b Runtime display of assembly program.

At the interface end, you can use a D-type plug and socket but with a little care and patience it's perfectly possible to do without this, and solder wires from the ribbon cable directly into the PCBs.

A reminder that BUSY and STROBE are connected from

convertor to interface, while ACK and STROBE run from convertor to micro. Connection details (courtesy of the BBC handbook) are shown in Table 1 and the IDC plug pin numbering can be seen in Fig. 2.

1 3 5 7 9
3579
579
7
ġ
11
13
15
17
19
en nos.)
en nos./

### **Further Developments**

The one drawback to using D7 to signal carriage return is that the original design reserved this line to denote the alternative character set which is needed for certain ASCII characters (assuming you have that daisy-wheel fitted). This \*BUILD BOOT (return), then type: \*LOAD chars 0C00 \*LOAD mc 0C81 ?&22A=&81; ?&22B=&0C \*WORD (ESCAPE) \*OFT4,3 (return)

Listing 4 Program to assemble !BOOT file to load and run translation program from disc.

can still be done, but the penalty is a 2s delay every time such a character is printed. Further software could make use of the delay when back-spaces are output, for underlining. The keyboard code for a back-space is 25 (decimal) and this could be assigned to one of the unused ASCII codes and built into a specialised printer driver routine from the word-processor.

Further details of Operating System routines in the BBC Micro can be found in 'The Advanced User Guide For The BBC Micro', by Bray, Dickens and Holmes published by the Cambridge Microcomputer Centre at £12.95.

ETI





### **E-Bus Board For The Cortex**

Designed by Richard Roberts of Micro-Processor Engineering, this project will give the Cortex computer two digital joystick ports, a Centronics printer port and 22 bits of programmable input/output (PIO). The board was originally designed around the 74LS2001 Dynamic Memory Access Controller, a chip which is now rather difficult to obtain. To overcome this problem, the project includes details of a TTL header board which directly replaces the 74LS2001 and could also be used in other applications where this exotic IC is specified. Two further Cortex projects by the same designer are also in the pipeline, a twin RS232 board and a stereo sound board, and we hope to bring you these in the near future.

### **Flat Screen Television**

People have been trying to produce flat screen televisions for a very long time, but most of the designs which have made it to the market place so far are based upon conventional cathode ray tube technology. Keith Brindley will be looking at the alternatives, including thin film transistors, liquid crystal displays, electroluminescence, plasma panels, vacuum flourescence and electrochromic displays. He concludes that, whatever direction development takes, the television of the future will be very different from what we have now.

### **ROM Board For The Spectrum**

Promised for this month but (blush, blush!) unavoidably held over, this project enables the Spectrum owner to enjoy the advantages of having a'sideways ROM' facility similar to that found on some other micros. The ROM board disables the top 32K of RAM in the Spectrum and replaces it with between 2K x 8 and 32K x 8 of EPROM. The lower 16K of memory is left intact. Suitable for use with the popular 27 series of EPROMS (2716 to 27256), the design will give Spectrum owners some valuable flexibility.

## **Direct Injection Box**

It is often necessary to split a signal from a musical instrument so that it can be fed to both the musician's own combo or amp and cab and also to the main PA system. The device used for this purpose is a direct injection box, a battery or phantom powered buffer amplifer which has one high impedance input and either one high and one low or two low impedance outputs. We have made our design battery powered so that it can be used with any system, and provided it with a high impedance input for the instrument and two low impedance outputs which should feed without problems into just about any amplifier or mixer input. In addition, we have incorporated an optional balancing arrangement on one of the outputs so that the box can be connected directly to mixers having balanced inputs.

# 

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# **EPROM EMULATOR**

### At long last, this much-delayed project hits the pages of ETI. Design by new project editor, Paul Chappell.

The EPROM emulator described here is primarily intended to assist with the preparation of programs for the '6802 Evaluation board' (ETI, May 1985), although it can equally well be used with any other system using 2716 EPROMs. The circuit uses a bare minimum of parts and the cost and construction time should be similarly minimal.

In use, a computer is connected to port A, and port B goes to the EPROM socket of the circuit that will run the program. With S1 in the 'program' position, the program to be tested is loaded from the computer into the 6117 memory IC. SW1 is then set to the 'EPROM' position and the program is run. If the program fails to perform as intended, SW1 is returned to the 'program' position and modifications are made from the computer. The new program can then be run, and so the process continues until the program does what is required of it

### Construction

The connections on the right hand side of the board are



# **PROJECT**

arranged so that a length of 24way ribbon cable can be connected to a 24-pin DIL header to plug into the EPROM socket of the host system. If an insulation displacement type of header is used, the connections will automatically be in the right order; otherwise, wires should be soldered to alternate sides of the header, with the bottom connection to pin 24, the next to pin 1, next pin 23, then 2, and so on.

A double sided board is used, and where possible through connections have been made via component leads, so check the top (component side) of the board carefully to make sure that any pads around component leads have been soldered. Positions where a wire through-link is required are marked with a '\*' on the component overlay. The memory IC can be socketed but the less expensive ICs should be soldered directly to the board to make it easier to solder the top side of the lead where this is required.

### HOW IT WORKS.

Switches SW2 to SW6 allow the emulator to be placed at any convenient point within the computer's memory map. If they are all set to 0, the emulator memory will represent the first 2 K of the computer's memory, and each binary code increment will shift it upwards by 2K. Suppose your computer has 16K of memory and you would like the emulator board to run from 16K to 18K. You want to shift the emulator location upwards by 16K, so the switches would have to be set to 00100, because this is the binary for 8; and you want a shift to 8 x 2K. The switch codes are, in fact, compared directly with the upper 5 address lines from the computer by IC7. IC1, IC3 and IC5 allow the computer access to the memory on the emulator board when a valid address and the appropriate control signals are received. The computer then writes to or reads from the emulator memory as if it were an extension of its own memory.

are received. The computer then writes to or reads from the emulator memory as if it were an extension of its own memory. With switch SW1 in the 'EPROM' position, the computer no longer has access to the emulator's memory; instead, IC2, IC4 and IC6 are enabled by CE and OE signals from the host system, which will now have access to the emulator's memory for read operations only — as if it were an EPROM.

#### RESISTORS R1-9 1k miniature carbon film CAPACITORS 100n disc ceramic C1-10 **SEMICONDUCTORS** 74LS245 IC1 IC2 -6 74LS541 74LS684 1C7 1C8 6117 74L532 ič **MISCELLANEOUS** PCB mounting minia ture SPST toggi **SW1** toggle switch

PARTS LIST.

SW2-6 Six section, SP make DIL switch (one off) Length of 24-way ribbon cable; 24-way DIL plug; PCB; cable and socket for connection to computer.

### **BUYLINES**

The components for the prototype were all supplied by Watford, who have everything in stock. The 6117 RAM and 74LS684 comparator might prove problematic if sought

from other suppliers, but everything else should be easily available. The PCB is supplied by ETI PCB Service.



# **PRINTER BUFFER**

Following on from last month's article which covered the design and construction, Nick Sawyer describes the connection, testing and use of the buffer and presents a full listing of the EPROM contents.

It is recommended that the power supply is built and checked before adding anything else that could be damaged by a power supply fault. First solder in diodes D2 and D3, capacitors C3-8 and the 7805 voltage regulator, IC17. When wired up to the transformer and plugged into the mains, this combination should give five volts plus or minus 5% at the output of the regulator.

If the 5 volt supply is not present then check the orientation of the diodes, smoothing electrolytic and regulator as It has to be decided at this point how the connections to the unit are going to be made. The best method of making the input connection is by mounting a right angle 36 way connector on the PCB. This means that the printer lead you were already using will plug straight into the buffer. As connectors are rather expen-

sive however, it might be decided socket. The other end of the cable can be terminated in a 36 way IDC connector ready for connection to the printer. The two LEDs and the two switches are mounted on the front of the box and wired to the relevant points on the printed circuit board.

### Testing

It is a good idea at this stage to check that the regulator is still producing 5 volts. If all is well and with the input and output leads now

these are all that can be at fault. If all is well, disconnect from the transformer for ease of working and proceed with fitting the other components and sockets to the

hard-wire straight into the board using either ribbon or conventional cable. It is suggested that the output connections are made using either a 26 way ribbon cable soldered direct into the PCB or a 26 way IDC plug and

to

fitted, insert the Z80 microprocessor and the 2716 EPROM into their respective sockets. Do not insert any of the dynamic RAM chips into their sockets at this stage. Connect the buffer output cable to a printer and switch both units on. The 'buffer ready' LED should illuminate after about a second if all is

board.

# PROJECT

0300 0310 0320 0330 0340 0350 0360 0380 0380 0380 0380 0380 0380	0200 0210 0220 0230 0250 0250 0250 0280 0290 0280 0280 0280 0280 0280 028	0100 0110 0120 0130 0140 0150 0160 0160 0180 0180 0180 0180 0180 018	0000 0010 0020 0030 0040 0050 0050 0050 0080 0050 0080 008
21         CE         03         7E         FE         FH           7E         32         00         20         32         00           00         27         E6         0F         26         02           00         20         32         00         30         7E           3B         03         7E         32         00         20         32           0A         32         00         20         32         00         20         32           0A         32         00         20         32         00         20         32         00           DB         FF         E6         80         CA         76         64         03         7E         32         00         20         32         00         DB         FF         E6         80         CA         76         64         03         7E         32         00         64         03         7E         32         00         26         64         80         CA         97         76         32         00         32         00         33         7E         32         00         32         32         30	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	F3       L3       B0       60       60       60       60         00       00       00       00       00       00       60         00       00       00       00       00       00       00       00         00       00       00       00       00       00       00       00       00         00
CA 1B 03 DB 30 23 C3 03 2 6F DB FF E6 3 82 2F E6 0F 3 32 00 30 DB 2 00 30 DB FF 3 32 00 30 DB FF 3 32 1D F 03 3 03 7E 32 00 5 0F E6 0F 26 0 20 32 00 30 5 32 00 20 32 2 00 20 32 00	5         36         37         38         3''           E         FE         FF         CA         21'           10         32         00         30         2'           20         2         DB         FF         E'           4         64         0F         6F         01'           30         30         7B         0F         0'           30         7B         0F         0'         2'           31         47         10         7E         20'         3''           31         47         10         7E         20''         2''           32         3E         0D         32''         0''         5''           34         47         02''         7E         3''         0''         2''           34         40''         2C         7E         0''         0''         5''         5''           34         2U         FF         0''         0''         0''         0'''           30''         00''         00''         00''         0'''         0''''         0''''           30''         00''''         0''''''''         0'''	0 21 00 00 3E F BD C2 21 01 A 3E 01 3A 00 9 D9 B8 C2 3E 6 B0 CA 11 01 B C2 11 01 05 1 00 00 00 00 0 00 00 00 00	0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       73       C3         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00         0       00       00       00       00       00      0
FF         E6         80         CA         09           63         7B         B2         0F         0F           80         CA         27         03         7E           64         DB         FF         E6         80           7E         E6         80         CA         27           26         BF         E6         80         CA         45           7E         FE         FF         CA         B3         93           20         32         00         30         23         30           20         32         00         30         B         FF         E4           20         32         00         30         DB         FF         E4           203         DB         FF         E4         97         98         97         198           200         30         DB         FF         E4         96         98	9         41         42         43         44           D         02         DB         FF         E6         B           13         C3         15         02         74         0           6         B0         CA         37         02         7           8         FF         E6         80         CA         4           9         0.5         0.7         0.2         7         0           18         FF         E6         80         CA         4           10         22         00         30         7B         8           12         20         32         00         30         D           12         20         32         00         30         D           12         20         32         00         30         D           13         74         20         6C         6F         6           14         74         20         6C         6F         6           150         00         00         00         00         00         00           16         00         00         00         00	E         FF         32         00         20         F1           1         D9         78         D9         BC         G           0         10         02         23         03         31           E         01         01         00         40         71           1         1A         32         00         20         33           0         00         00         40         71           1         A         32         00         20         33           0         00         00         00         40         71           1         A         32         00         20         33           0         00         00         00         00         00           0         00         00         00         00         00           0         00         00         00         00         00         00           0         00         00         00         00         00         00         00           0         00         00         00         00         00         0         0           0         <	0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       01       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00       00       00       00       00       00       00       00         0       00
03         0700           0F         0710           32         0720           CA         0730           03         0740           3E         0750           03         0740           5C3         0770           80         0780           FF         0790           80         0780           CA         0780	5         46         0600           0         CA         0610           F         0F         0620           E         32         0630           9         02         0640           F         DB         0650           6         0F         0660           0         DB         0670           3         61         0640           0         00         0680           0         00         0680           0         00         0680           0         00         0680           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640           0         00         0640	B         00         0500           A         3E         0510           E         00         0520           C         E5         0530           2         00         0540           1         01         0550           0         00         0540           0         00         0540           0         00         0570           0         00         0570           0         00         0570           0         00         0570           0         00         0570           0         00         0570           0         00         0570           0         00         0570           0         00         0580           0         00         0520           0         00         0520           0         00         0520           0         00         0520           0         00         0520           0         00         0520           0         00         0520	000         0400           000         0410           000         0420           000         0430           000         0440           000         0450           000         0450           000         0450           000         0450           000         0450           000         0450           000         0450           000         0450           000         0450           002         0490           01         0480           000         0420           000         0420           000         0420           000         0420           000         0420           000         0420           000         0420           000         0420           000         0420           000         0420           000         0440           000         0440           000         0440           000         04450
7A         B3         C2         10           17         07         B4         2F           FE         00         C2         B0           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00           00         00         00         00	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	33 33 11 00 E6 80 CA 0E 04 7E FE FF 32 00 30 23 4C 04 7B FE 80 CA 4D 04 04 7B FE 7F CA 6C 04 79 4B 12 32 00 00 30 DB FF 00 05 21 E2 AB 04 7E 32 52 61 6D 20 0A 0D FF 52 61 65 74 65 64
06         21         0           C3         10         0           07         C3         A           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0           00         00         00         0	C2       0.3       05         00       00       00	84         2F         77           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00	20 21 L0 04 /E 12 CA 37 04 C3 21 04 7F 3E 33 79 12 32 CA 12 32 00 30 DB FF E6 80 C7 74 65 73 61 6D 20 6D 20 74 0A 0D FF
0       40       C3         2       23       7C         2       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00         0       00       00	5 7D FE 0 00 00 0 0 00 0 0 00 0 0 00 00 0 0 0 0	23 C3 00 00 00	04 7E 1 32 00 3 DB FF 1 D7 7B 1 CA 4C 0 00 30 3 S0 DB 1 E6 80 0 92 04 3 FF CA 0 0 30 3 74 20 5 74 20 7 74 20 0 70 72 6 5 73 00 0 0
B0         07         71           D9         B9         D           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00           00         00         00	00       C2       03         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00         00       00       00       00	B0         05         00           00         00         00	FE FF CA 30 23 C3 E6 80 CA D9 47 FE 04 3E 34 78 FE 3F 3E 3B 4F FF E6 80 CA 85 04 23 C3 45 73 74 61 73 74 63 74 20 63 00 00 00
E 2F 94 BD C4 7 C2 80 07 71 0 00 00 00 00 0 00 00 00 00 0 00 00 00	5       0.5       1.1       FF       7F         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0       0.0       0.0       0.0       0.0         0	00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00       00       00         00       00       00 <td< td=""><td>1E 04 DB FF 08 04 21 D3 27 04 7E 12 3F 3E 31 CA 4F DB FF E6 3E 36 CA 6b DB FF E6 80 CA 78 04 3E 3E 0D 12 32 32 00 30 C3 FF E6 80 CA 04 C3 00 01 72 74 65 64 6E 74 20 3A 6F 6D 70 6C 00 00 00 00</td></td<>	1E 04 DB FF 08 04 21 D3 27 04 7E 12 3F 3E 31 CA 4F DB FF E6 3E 36 CA 6b DB FF E6 80 CA 78 04 3E 3E 0D 12 32 32 00 30 C3 FF E6 80 CA 04 C3 00 01 72 74 65 64 6E 74 20 3A 6F 6D 70 6C 00 00 00 00
	F J O O O O O O O O O O O O O O O O O O		02/4///2224444

Table 1 Full listing of the software which must be programmed into the 2716 EPROM.

well with the buffer, and the 'printer ready' LED should light and extinguish as the printer is put on-line/off-line.

If all is well press the test button. The printer should produce "RAM Test Started" followed immediately by "RAM Present : 16K" and ten seconds

ş

later by "RAM Fail at Location 4000". This shows all is well, that the unit is functioning and that those expensive dynamic RAM chips can now be used. The first pair of RAMs is inserted (observing normal MOS precautions) in positions IC11 and IC14, the next pair in IC10 and IC13 and the last pair in IC9 and IC12. Take great care that these devices are the right way around in their holders. The buffer will work with either 16, 32 or 48K of RAM provided the RAM chips are installed in the right sockets.

Having installed all the RAM you think necessary, switch every-

# **PROJECT: Printer Buffer**

thing back on and repeat the test as above. The results should be the same except that the "RAM present" figure should reflect the amount of RAM that you have installed, and after ten seconds the printer should produce "Ram Test Passed".

The buffer is now ready to use. Plug in the lead from your computer and try LLIST on a fairly long program. The computer should come back READY within a few seconds, and the printer should carry on printing until it is finished — with no errors of course!

### Faultfinding

Having built the buffer, if you are unfortunate enough to find that nothing happens when you switch on, check the obvious first. Is the printer on-line? If it is then the 'printer ready' LED should be glowing, if it is not, check carefully the output lead from the buffer to the printer.

Next check the 5 volt supply from the regulator. If it has dropped from five to only one or two volts, then it is likely that there is



either a short circuit between two solder joints or that a component has been soldered in the wrong way round. If this is the case a hot component will result and can easily be tracked down by the burning smell. If the 5 volt supply is still all right, then it is more likely that you have missed a solder joint out. Check all the through board connections for continuity, and check especially the output wiring from the buffer to the printer plug.

If all still appears well but the buffer fails to function the services of an oscilloscope will ideally be required. Check first for a 2 MHz clock signal on pin 6 of the Z80, then check the operation of each address, data and control line. If they have a varying signal on them then all is well, otherwise suspect a short circuit at some point.

### In Use

The use of the printer buffer is absolutely straight forward as it is completely transparent to the user and his computer. Merely leave the unit connected to the printer, and power it up whenever the printer is switched on. Any data sent from the computer will then be buffered and passed on at a speed suitable for your printer, leaving the computer free for use.

ETI



PROJECT

# UNIVERSAL EPROM PROGRAMMER MKII

This hex dump spells a fully operational EPROM Programmer — and you can't byte that. Gordon Bennett performed the magic.

ast month, lack of space obliged us to hold over the software listings for the EPROM Programmer. This month we remedy that omission with a complete hex dump of the Universal EPROM Programmer software. Unfortunately, space reasons forbid the publication of the complete disassembled listings — which runs to some 26 pages of print-out.

The hex dump provides everything you need in order to enter the code to run the programmer. Notes on the various locations involved can be found in last month's introductory article and below. If anyone should require the disassembled listing, for the purposes of modification or out of simple interest, we can provide a photocopy at a price of £3. Please send a cheque, made out to ASP Ltd., to ETI, Dept. UEP, 1 Golden Square, London W1R 3AB, and enclose a large stamped addressed envelope. The author will provide a ready programmed EPROM plus listing for £10.00 or a tape including source and object code (for use with the TUG two pass/assembler editor) for £5.00. Please write to 187 Beaulieu Gardens, Blackwater, Camberley, Surrey GU17 OLF. Note that this address is different from the one published last month, and is the correct address. Please allow 28 days for delivery on any of these items.

The following hex dump was performed using the DUMP command in the EPROM Programmer software. The first address on a line is the absolute address in memory, the second is the offset address from the current Base selected. Then there are 16 bytes of data and the final number is the checksum computed from that line of data. With reference to

last month's notes, the screen clear routine is located at EFEC h to EFFF h, inclusive. It may be removed for the addition of extra facilities in the program. It is called twice, by the command JSR CLRSCRN (the hex bytes 20 EC EF), at locations E9CF h and EA23 h (the header and the help routines). Locations E861 h through E8C8 h contain the type numbers of the EPROMs supported by the software. Length parameters are contained in the locations E8C9 h through E8D5 h. Messages used in the programme are contained in locations E8F6 h through E9C h. The table used to set up PIO ports for the EPROMs supported are contained in locations E806 h through E860 h. These tables could be altered to support different EPROMs, but once again - close study of the disassembled listing is recommended.

The hex dump from E800 h to EFFF h.

E800 0000 4C D8 E9 4C E7 E9 30 30 30 30 30 30 34 34 34 34 0661 E810 0010 34 34 34 98 98 98 90 90 98 92 92 90 92 92 90 90 0814 E820 0020 18 18 18 08 08 18 10 10 10 10 10 10 00 88 88 88 0268 E830 0030 85 A5 88 22 C2 00 22 C2 C1 C5 01 01 01 01 01 10 0515 E840 0040 02 02 10 02 02 01 01 06 06 06 06 06 0A 06 06 0A 0058 E850 0050 06 01 01 06 06 06 01 01 01 00 01 01 00 01 01 01 0022 E860 0060 01 32 37 35 38 20 20 20 20 32 37 31 36 20 20 20 0287 E870 0070 20 32 35 32 31 36 20 20 20 20 32 37 33 20 20 20 0290 E880 0080 20 32 37 33 32 41 20 20 20 32 32 20 20 20 02BB 35 33 E890 0090 32 37 34 20 20 20 36 20 20 32 37 36 34 41 20 20 02C7 E8A0 00A0 32 35 36 34 20 20 20 20 20 32 37 31 32 38 20 20 02B5 20 32 37 31 32 38 41 20 20 32 37 32 35 36 20 20 E8B0 00B0 02EB

_	_				_					_	_					_		_		-
	000	0000	20	00	27	OF	2.1	00	20	20	20	0.2	07	07	OF	OF	0E	10	0.105	
	ESCU	0000	20	34	31	30	JI	32	20	20	20	0.3	07	07	UF	UF	0 F	11	UTDE	
	E800	0000	1F	1F	3F	3F	7F	FF	48	51	54	52	56	46	53	4E	44	42	053C	1
	FRED	OBED	23	FA	87	FR	SD.	FR	28	ED	DE	ER.	88	ED	84	FD	26	FB	0ACD	
	EOEO	0000	20	En	10				4.5		50	40	40	00			Ea	45	0705	
	E8F0	0000	38	EU	68	FR	EA	EY	45	50	52	41	4D	20	54	28	50	45	072F	t
	E900	0100	00	42	41	53	45	20	41	44	44	52	45	53	53	20	28	48	03D1	1
	5010	0110	45	50	20	00	45	50	52	4E	40	20	50	52	AF	47	52	41	0434	
	E 2 10	0110	40	00		00		00	02			20	00		4.0		22	~ -	0404	
	E920	0120	40	40	45	44	20	20	20	45	52	49	46	28	47	4E	47	ZE	0450	
	E930	0130	0.0	45	50	52	4F	4D	20	43	48	45	43	4B	20	46	49	4E	03FE	
	E940	0140	10	50	10	45	11	0.0	45	50	50	45	40	20	54	45	52	40	0444	
	2740	0140	77	00	40	70				00	22			20	20		22		0440	
	E950	0150	46	49	45	44	20	41	48	uυ	45	50	52	4F	40	20	45	52	0400	
	E930	0160	41	53	45	44	20	4F	4B	00	45	50	52	4F	4D	20	53	49	0416	
	E970	0.170	50	45	20'	45	58	43	45	45	44	45	44	0.0	20	20	20	20	0376	
	5000	0100	00	00		00	45	FO	=0	45	FO	20	40	00	0.4	00	20	45	0000	
	E280	0180	20	20	20	00	40	54	54	41	52	20	40	20	24	uυ	20	40	UZFB	
	E990	0190	45	4D	2E	3D	00	20	45	50	52	4F	4D	3D	00	20	42	41	0380	
	E940	0140	53	45	20	30	20	24	0.0	0D	20	20	20	20	20	45	50	52	02DA	
	FORG	0 1 00	45	40	20	EE	= 4	40	40	40	EA	40	AE	50	20	E/	00	25	0445	
	EABO	0 IB0	4F	40	20	22	54	47	40	47	54	47	45	03	20	20	33	ZE	0446	
	E900	0100	37	35	2D	0D	0 D	0.0	45	6E	74	65	72	3A	20	0D	0.0	20	0352	
	E900	0.100	FC	FE	A0	81	20	6B	FF	60	20	C2	FA	20	DE	FA	20	CE	0.244	
	FOFO	0150	FO	20	24	ED	20	40	EP	20	20	EA	EA	20	DE	EA	20	1.4	0701	
	EYEU	UIEU	E7	20	20	ED	20	00	ED	20	23	CH	EH	20	UF	CH	20	14	0701	
	E9F0	01F0	EB	20	1D	F8	A5	01	48	20	ÛE	F8	68	A2	00	DD	06	E8	0709	
	EA00	0200	F0	08	E8	ΕŪ	0A	DO	F6	4C	EB	E9	8A	0A	AA	BD	E0	E8	0A73	
	EA10	0210	95	50	FQ	PD	EO	FR	85	50	AD	E5	FS	48	4D	F4	ES.	48	0403	- 1
	EA10	0210	10	EC	00	00	50		00	00	50		05	20	20		~0	00	0740	
	EAZU	0220	66	ວບ	00	20	EU	EF	20	υĽ	F8	AU	75	20	OB	EF	HY	00	Q749	
	EA30	0230	85	53	A5	52	0A	0A	0A	AA	BD	61	E8	20	0E	F8	E8	E6	0791	
	EA40	0240	53	A5	53	0.9	08	90	F 1	A0	AZ.	20	6B	EF	A5	4B	20	1A	0788	
	EAEO	0.250	-0	AE	4.0	00	1.0	FO	40	0.0	DD	47		00	0.0	50	07	20	0740	
	EHUU	0200	FO	HJ	414	20	TH	FO	HZ	00	БО	01	EH	07	00	FU	07	20	0787	1
	EA60	0260	0E	F8	E8	-4C	58	EA	60	QD	0D	28	48	29	65	8C	70	0D	0500	
D	EA70	0270	28	51	29	75	69	74	0D	28	54	29	65	73	74	0D	28	52	0479	
	EAON	0.290	20	45	41	44	0D	20	54	20	45	72	40	44	70	00	20	44	0401	
	CAOO	0200	20	00	201	74	00	20	30	15	00	00	00	00	EO	20	20	70	0450	
	EAYU	0290	29	61	13	74	20	50	12	OF	01	ZE	00	28	23	27	oL	OF	U4E3	1
	EAA0	02A0	-77	-20	50	72	6F	67	2E	0 D	28	44	29	75	6D	70	0 D	28	0486	
	FABO	0280	4F	29	65	77	20	74	79	70	65	0D	28	42	29	61	73	65	050E	
	CACO	0.200	0.0	00	0	20	on	01	PC	on	00	PC.	on	27	PC	on	20	RC	0.400	
	EALU	0200	00	00	HZ	30	00	41	DU	00	20	DL.	00	21	DU.	00	20	DC A O	COMU	
	EADO	0200	A9	FF	8D	20	BC	8D	22	BC	8D	26	BC	-80	2A	BC.	6U	AY.	0867	- 4
	EAE0	02E0	04	8D	27	BC	8D	2B	BC	8D	2A	BC	A9	90	8D	26	BC	A9	07AC	
1	FAFD	0.250	24	80	21	BC	80	23	RC	40	0.0	80	25	BC	80	20	BC	8D	0717	
	ERIO	0210	07	00	201	00	00	20	50	~~	0.0	00	OF	00	00	20	00	24	0770	
	FROO	0300	22	BL	AY	FF	80	24	BC	AY	04	80	20	BU	H7	00	80	24	0700	
	EB10	0310	BC	85	48	60	20	00	F8	A9	3E	20	θE	F8	60	20	00	F8	069E	
	EB20	0.320	A9	24	20	0E	F8	60	20	00	F8	A0	00	20	6B	EF	20	14	0505	
1	5000	0.220	FD	20	CA.	EE.	20	E4	FE	02	0.0	O Z	50	00	0.1	R 1	ñΔ	00	0.952	
	CDOU	0.550	00	20	04	EF.	20	0	En o	00	00	50	40	00	51		EO	07	00002	
1	EB40	0340	20	FU	24	00	61	E8	DU	05	67	E8	40	30	EB	EO	52	HO	0730	
	E850	0350	52	0A	0A	ŪΑ	09	68	B0	04	AA	4C	38	EB	20	00	-F8	A9	063E	
	ERAD	0360	3E	20	0E	E8	40	2E	EB	60	20	00	E8	A0	0B	20	6B	EF	0673	
	6020	0.070	20	10	ED	20	04	EE	0	0.0	20	17	FO	A5	12	05	AA	45	0454	
	EB70	0370	20	10	EB	20	64	EF	AU	00	20	16	FO	HU	13	00	HH	HJ	0000	
	E880	0380	14	85	4B	20	00	F8	-60	20	00	F8	40	20	F8	A9	00	85	USIE	
	EB90	0390	53	85	4C	85	4D	A9	FF	85	4E	A6	52	BD	09	E8	E0	00	0803	
	FRAD	0340	DO	OP	40	75	20	AD	FR	49	80	85	40	49	FF	85	4F	20	0852	
	EDHO	OORO	00	00	20	0.4	05	EI	20	05	50	00	10		50	0.4	0.0	40	0704	- 1
	FRRO	0380	LL.	EU	HY	UI	80	01	20	30	EU	20	44	CL	00	00	HU	02	0784	
-	EBCO	0300	20	6B	EF	- 30	AU	8E	20	6B	EF	20	-7A	EF	A0	9F	20	68	0705	
	EBDO	03D0	EF	20	98	EF	20	A3	EF	DÜ	03	20	AA	EF	40	B6	EB	20	08E1	
	FRED	0.350	00	FC	20	CC	EC	20	50	FE	49	80	85	51	49	0.0	85	47	0839	
	EDEO	00000	00	EO	20	OF		00	0.0	EE	DO	10	20	DE	EA	AF	47	EO	0914	
	FBEO	03F0	85	- 33	20	30	ED	20	44	EE	DU	13	20	DE	CH	HD	47	-0	0014	
	EC00	0400	0.6	A0	3B	20	6B	EF	60	A0	50	20	6B	EF	60	20	1F	EC	0.980	
	EC10	0410	A9	01	85	47	20	A3	EF	DO	03	20	AA	EF	40	F2	EB	A0	087D	
	5000	0420	OF	20	4D	FF	20	70	FF	00	00	20	AP	FE	20	90	FE	0	0882	
	EUZU	0420	OL	20	00		20	OC.	EF	10	- 00	00	00	00	00	50	00	0.0	0.700	
	EC30	0430	9F	20	6B	EF	20	98	EF	00	20	77	EU	20	LL	EL	20	00	0769	
	EC40	0440	F8	A9	0.0	85	53	85	5E	85	-5F	20	32	EE	F0	25	20	85	073A	
	EC50	0450	EF	20	E6	EF	20	7A	EF	20	E6	EF	20	E6	EF	A2	00	A1	099A	

# PROJECT: Programmer

	EC60	0460	45	5 48	3 20	) 81	) E(	6	3 20	16	E E	3 20	1 8.	5 E	E	0.0.	4 2	1 22	0400	
	EC70	0470	EE	E DO	) E7	7 60	) A(	88	3 20	) 6E	R FF	A	5 50	E 20	1 12			5 55	007M	
	EC80	0480	20	16	A FS	3 40	: 3E	E	E E	53		5 53		2 10	2 AF 1 AF	1 1 (		J JE 5 EE	UQE3	
	EC90	0490	85	5 5E	A5	5.5	- 69	2 00	85	5 5E	- A0	20				1 0 2		J JE	U OEU	
	ECAC	04A0	FS	3 A5	5 13	8 85	5 40	: A5	5 14	1 85	1 40	20	1 13			5 4 4			0640	
	ECBC	0480	AS	5 14	85	4	i ne	1 4	90					с пе 5 ир		1 13	5 83	0 4E	0606	
	ECCC	0400	- 60	20	00	ES	2 49	36	2 20		- E0			, 40 		40	- 70		06A3	
	ECDO	0400	0.1	85	55	05	: ar				FC FZ	1 40 1 40			36	A	5 40	EY	07A0	
	ECEO	04E0		1 46	20	0.00					00		S AC	9 46	. 67	. U 1	85	57	05E2	
	ECEO	04E0	54	05	. 14	20					- 4A	00		85	45	A	5 4E	3 65	061A	
	EDOO	0500	00		20	0				A0	02	BL	08	) E8	80	23	8 BC	2 A9	075D	
	ED10	0510	10		20		- A7	04	80	20	BC	BD	13	EE	8 80	26	6 BC	A9	0754	
	ED20	0520	12	24	2H	BL	- OU	AD	20	BC	49	10	80	24	BC	20	4F	EE	06A1	
	EDBO	0520		49		00	00	A2	. 00	80	20	99	EC	: 20	CC	EC	20	59	075A	
	ED40	0540	20	- H7	55	00	101	A2	00	20	55	EE	20	F7	EC	20	4F	EE	07D3	
	ED50	0550	07	04		FU		20		ED	20	15	ED	A4	51	FO	10	30	0774	
	EDAD	0540	45	40	20	FU	EO ZO	40	64	ED	01	45	FU	DE	40	64	EC	81	0A33	
	ED20	0570	20	90	- OH	0.4	00	Að	52	BD	0.6	E8	80	23	BC	A9	00	8D	075D	
	EDSO	0520	00	24	H7	20	80	20	BU	BD	20	E8	80	2A	BC	BD	20	E8	0801	4
	EDOO	0500		40	50	00	A7	01	80	48	20	99	EC	20	00	EC	20	59	0730	
	ED/0	0540	20	HU SE	52	07	00	90	07	AS	48	Dü	03	, 4C	7E	EE	- A2	FF	0865	
	EDEO	0.5PO	20	- 00	EE	20	60	ED	20	21	EF	20	4F	EE	20	32	EE	FO	0792	
	EDCO	0500	10	20	01	ED	AZ	00	A1	45	80	24	BC	20	63	EE	A0	1D	071E	
	EDDO	0500	H4		LA	00	FU	88	DU	FA	20	63	EE	40	A3	ED	40	F5	0818	
	EDEO	0550		HJ 07	00	80	22	BC	BD	54	ES	FO	08	A5	53	8D	20	BC	08 <b>A8</b>	
	EDEO	OSEO	40	07	EE	- AU	20	BU	05	59	80	20	BC	A5	56	29	10	FŨ	06B5	
	EEGO	0400	08	AU DC	20	BU	09	08	80	20	BC	A5	56	29	08	FO	08	AD	05DC	
	5510	0410	20	DL	0.2	01	80	26	BC	A5	56	29	20	F0	08	AD	23	BC	0623	
	5520	0100	07	08	80	23	BL	AS	56	29	40	FO	08	AD	26	BC	09	02	0573	
	EE20	0020	80	20	BC	AD	56	-29	80	FŪ	08	AD	20	BC	09	80	8D	20	06CA	
	EE30	0630	BU	60	EO	45	00	02	E6	46	E6	55	DO	02	E6	'56	A5	56	0889	
	EE50	0450	10	EZ	80	57	A5	55	05	57	DO	04	A5	56	05	58	- 60	A0	07F0	
	FEAD	0440	10	DC	20	FU	0U	AY	00	80	25	BC	SE	24	BC	A9	04	8D	0780	
	EE ZO	0470	29		00	HO	32	BU	47	E8	89	20	BC	50	ЗA	E8	99	20	07F1	
	EE80	0420	84	50	HZ EO	PP 02	20	00	LL	20	65	ED	20	19	EF	60	A2	00	07BC	
	EE90	0490	20	AE	EE	20	00	20	32	EE	20	44	EE	FU	68	20	72	EE	0813	
	FEAD	0.620	20	50	EC	20	501	ED	AZ	00	AI	45	80	24	BC	20	08	EF	0747	
	EEBO	0.480	FE		DA	20	20H	HO	02	BU AE	09	EF	15	5A	FU	05	20	42	088B	
	FECO	0400	49	Δ4	50	20	00	DO	20	46	EE	20	01	ED	A5	5A	0A	0A	0867	
	EEDO	0300	0E	EO	05	00	20	55	50	AD	04	40	08	EE.	BD	09	EF	C9	0963	
	EEEO	0.6E0	D9	FF	05	50	00	00	20	40	55	DB	20	08	EF	A6	52	BD	0766	
	EEFO	06F0	FA	20	16	FC	40	20	20	94		20	73	20	37	EF	20	DF	096A	
	EFOO	0700	AD	1F	20	AB.	FF	40	57	FD	20	2H 20	55	27	12	80	ZA	BU	UGFE	
	EF10	0710	DO	FD	88	DO	E8	20	40		20	00	24	H4	28	AZ	90	UA	0880	
	EF20	0720	BC	A5	52	0.9	00	ED	0.9		24	RC	10	1D	27	20	80	40	0308	
	EF30	0730	AD	26	BC	<u>6</u>	14	80	24	BC	40		07.	PC 10	00	ZA OA	50	80	0708	
	EF40	0740	BC	60	A2	0.0	20	55	FF	20	E7	EC.	20	20	07	30	80	20	0804	
	EF50	0750	20	D1	ED	20	15	ED	C1	45	A0	44	50	DD	CO	20	45	EE AE	0709	
	EF60	0760	80	08	A0	72	20	AB	FF	40	CI	FE	40	DO	EZ	EO	00	46	08E0	
	EF70	0770	FO	07	20	0E	F8	08	40	AB	FE	A0	45	54	20	10	50	00	0700	
	EF80	0780	55	20	16	E8	60	A5	46	20	14	ES	45	45	20	IA	F 0	40 40	0760	
	EF90	0790	A0	00	B1	45	20	1A	F8	60	AD	0.0	AS	50	20	10	FO	20	0405	
	EFA0	07A0	00	F8	60	E6	53	A5	53	09	OE	60	20	10	FR	45	01	00	0770	
	EFB0.	0780	20	FO	0E	09	0D	DO	F3	A9	00	85	53	20	00	FO	40	40	0770	
	EFC0	0700	68	38	68	60	A2	00	20	1D	F8	E8	45	01	ro	20	ED	50	0724	
	EFD0	07D0	90	06	20	0E	F8	4C	C3	EF	30	00	0.0	00	00	00	00	0E	0420	
	EFEO	07E0	19	0F	0F	19	19	19	A9	20	20	0E	FS	60	A0	00	40	20	0420	
	EFFO	07F0	99	00	02	99	00	03	C8	DO	F7	84	0A	A9	02	85	0B	60	OSEE F	TI
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# **TECH TIPS**

## **BCD To Binary Conversion**

A.J. Holme Warrington

The following circuit converts two binary-coded decimal digits into seven-bit binary form. The circuit was originally designed to produce a binary output from two BCD encoded thumbwheel switches. The ten digit bits are labelled T0 -T3 and the units digit bits are labelled U0 - U3. The encoding into binary bits uses two 7483 4-bit binary full adders with fast carry. It may be understood by considering the following sum:

 T3
 T2
 T1T0
 0
 (2 x tens)

 T3
 T2
 T1
 T0
 0
 0
 (8 x tens)

 +
 U3
 U2
 U1
 U0
 (units)

 B6
 B5
 B4
 B3
 B2
 B1
 B0
 (binary)

### Two Utilities for ETI Spectrum/ Centronics Interface

P.H. Sheather Cranleigh Surrey

Two programmes are presented here for the Spectrum Interface (ETI, December 1984). The first produces a printed listing of any BASIC programme between designated line numbers. Anyone who has significant experience of programme development and debugging will know that it is invaluable to be able to work on a printed listing rather than the VDU.

The second is a routine intended for use with graphics packages and enables the screen contents to be reproduced on the printer pixel by pixel.

The programmes are written for a 48K SPECTRUM and an EPSON FX80 printer. All necessary interface and printer initialisation is included.

Both programmes read from the screen buffer. In the first the

For example, the BCD representation of 94 is 1001 0100 (that is, a '9' followed by a '4'). Filling in the above sum, we get:

1 +	0	1 0	0 1 0	0 0 1	1 0 0	0 0 0	
1	0	1	1	1	1	0	

which is the binary representation of 94.

The two 7483s are configured so that all the relevant bits are added and the binary sum output in the appropriately labelled position. The 74283 could be used —it is functionally identical but has a somewhat more logical pin-out.



listing is automatically presented on the screen line by line, and transferred to the printer. In the second the screen display will have already been generated by the user. Communication of commands or data to the printer is carried out by a handshaking subroutine like that of lines 9942 and 9944 of the BASIC programe, or its machine equivalent.

9900 REM BASIC LISTING PROGRAMME
***************************************
9902 GD TO 9914
9904 PRINT "BASIC LISTING"
9906 PRINT "" '
9908 PRINT "MERGE programme for
listing"
9910 PRINT "Then use RUN 9900"'
: GO TO 9999
9912 REM Select range of line nu
abers
9914 LE1 bas=65234
9916 INPUT "First line number"
AAIR INFOL "FWET TIVE DOWDER'S "
111 0000 157 bb-107 (41(084)
9720 LEI ND=IN1 (11/2007 9822 LET 15=41-284-55
7722 LE1 10=71-230=00
9974 POKE (basa) bb
9920 ( LT 1) 100057774
9930 LET bbeINT (11/256)
9932   FT thett-256+bb
9954 POFE 111 ne.1b
9936 POLE (111ne+1).hb
9938 GO 10 9948
9940 REM Handshaking subroutine
9942 IF IN 63<>0 THEN GO TO 9942
9944 OUT 123,n: RETURN
9946 REM Initialise interface
9948 OUT 255.79: PAUSE 10
9950 OUT 251,15: PAUSE 10
9952 REM Clear print buffer
9954 LET n=27: GO SUB 9942
9956 LET n=64: GU SUB 9942
7758 LET N=241 GU 508 7742
9960 RANDUNITE USK 65000
7762 CLS 1 PRINT H1 11,131 UP
9944 DEM Autoload Lostructors
9040 (1EAD 44000) PRINT AT 11.13
*"WAIT": LOAD "ob "CODE 65000; B
181 9904
Clockwise from above: the BASIC
listing many how dump of m/s
listing program, nex dump of m/c

Clockwise from above: the BASIC listing program, hex dump of m/c listing program, the screen dump routine.

FDEB	ED	4B	DZ	FE	2A	D4	FE	7C	
FDF Q	88	DA	FE.	FD	7C	<b>B8</b>	C2	00	
FDF8	FE	7D	B9	DZ	00	FE	69	00	
FEUO	3E	02	CD	01	16	CD	68	QD	
FE08	00	3E	02	FD	36	02	00	CD	
FE10	30	25	C4	01	16	DF	CD	70	
FE18	20	38	14	DF	FE	3B	28	04	
FE20	FE	20	20	06	E7	CD	82	10	
FE28	18	08	CD	E6	IC	18	03	CD	
FE30	ĎΕ	10	CD	EE	1 B	CD	99	1E	
FE38	ED	<b>4</b> B	D2	FE	78	E6	3F	67	
EE40	69	21	22	49	50	23	CD	6E	
EE4B	19	1E	01	CD	55	18	23	7E	
FESO.	32	D2	EE.	2B	7 F	32	D3	EE.	
FE58	TE.	7F	DZ	00	SE.	00	32	DA	
FEAD	FF	30	DA.	FF	AF	ŏř.	70	32	
FEAR	D4	5.6	36	00	87	07	EF.	10	
5570	07	56	47	OA.	70	30	07	EE.	
FE 70	30	D4	55	AF	00	00	05	CD	
EE OA	10	25	00	E 1	20	57	00	EE	
5500	70	20	00	70	607	EE.	EE	01	
FE00	20	74	10	70	00	70	P.E.	EE	
FE 70	20	30	10	10	~	0.0	30	76	
PE70	PE.	01	20	10	20	UH LO	26	20	
FEAU	CD	HU DO	FE.	10	F 7	18	00	20	
FEAU	2017	DB	38	FE.	00	20	FA	18	
FERU	03	18	CY.	00	78	LD	AB	PE.	
FEBB	00	SA	0/	FE	FE.	20	20	AH-	
FELO	SE	UH	CD	BH	트	18	YA	00	
FECS	SE	0A	CD	BH	112	00	03	EB	
FEDO	۴Đ	00	00	ŲΟ	••	66	ŲΦ	00	
FUEB	SE.	41	0.5	1 -	06	00	UE	04	
FDFO	CD	50	11-	312	01	03	19	16	
FDF8	00	OE	UA	LD	- 50	11	21	15	
FEOO	CD	60	PE.	SE	40	CD	00	1 E	
FLOB	3E	18	CD	60	PE	10	HO	NE	
FEIQ	10	CD	60	PE.	SE	41	CD	60	
FE18	FE	3E	08	CD	60	F.E.	-SE	0A	
FE20	CD	6D	FE	3E	18	CD	6D	FE	
FF∠8	3F	28	СD	6D	FE	-3E	05	CD	
FE 30	6D	FE	3E	ΟŌ	CD	60	FE	3E	
FF30	01	CD	6D	FE.	1E	00	26	QQ	
FE40	2E	01	7A	95	47	<b>4</b> B	D5	E5	
FE 48	CD	LE	22	CD	94	1E	E 1	DI	
FE50	CB	04	84	67	20	7D	FE	09	
FE 58	20	E8	70	CD	<b>6</b> D	FE	10	78	
FE60	FE	00	20	DA	7A	06	08	57	
1E68	FE	Ųΰ	20	A3	69	47	DB	3F	
FE70	FE	00	20	FA	78	D 2	7B	C9	
		_	_	_	_	_	_	_	_

# **TECH TIPS**

### Mr Discrete's Car Alarm Guy Mellor Macclesfield

Most designs for car alarms use IC timers such as 555s, 14528s or 74221s, but here is a design that can readily be made from discretes out of your junkbox.

On power up, C1 will pass current until fully charged, keeping Q2 turned on for about ten seconds. While Q2 is on, the gate of SCRI is low, ensuring that the SCR is off. In this period it is safe to get out of the car as any turn on signal by the courtesy light to Q1 will be ignored. Assuming the car door is now shut the state of circuit is Q1 and Q2 off. Any intruder opening the door now will turn Q1 on which will put the gate of SCRI high thus latching on. The voltage across C2 will now ramp up and the emitter of Q4 will follow it



until sufficient potential appears on the gate of SCR2 which will latch RLA1 on, disabling ignition, and the RLA2 will click on and off, sounding the horn intermittently.

None of the parts used are critical so no buying-in should be necessary, with the possible exceptions of SW1 and RLA2. SW1 is specified as a keyswitch, but any ordinary switch with 1A contacts can be used if its location is concealed. Do not conceal the switch too well, because the circuit only gives you about 9 seconds to turn the alarm off before it does the old waking the neighbours act. RLA2 is specified as a 2PCO with 20A contacts. In the prototype I used an 11 pin relay with 10A contacts, but since 11 pin relays are 3PCO, two of the NO pairs were connected in parallel for RLA2/2. RLA2/1 is still just one pair.

SCR2 is specified as a thyristor but the acual device I used was a BT139 (which is a triac). This is because I happened to have a BT139 which wanted using, and in any case, either a thyristor or a triac will do. The same design criteria apply to SCRI.

The listing programme has both BASIC and machine code parts. Type in the BASIC section first and save it with SAVE "list" LINE 9968 and verify. Do not attempt to run it without the machine code or it will almost certainly be lost. Enter the machine code with a hex loader or similar and save this on tape immediately after the BASIC with SAVE "obj" CODE 65000,240, and verify. It should now be possible to autoload the whole programme with LOAD "list". Operating instructions will be displayed on the VDU.

The BASIC programme starts at line 9900 so any programme to be listed should have a lower maximum line number than this.

The screen dump routine is quite independent of the other and may be placed in any convenient location in memory as it has no internal absolute jumps.

It should be entered with a hex loader to say 65000, and then saved with SAVE "scr" 65000.

To call it from a BASIC programme, a line of the form RAN-DOMISE USR 65000 would be used.

For those who wish, I can provide a tape with both programmes at a cost of £3. Requests should be addressed to 14 Waverleigh Road, Cranleigh, Surrey.



# PCB FOIL PATTERNS



The top and bottom foils for the EPROM Emulator.



Г



The foil pattern for the RCL Bridge PCB.



The EX42 Interface Board.

# **ETI PCB SERVICE**

In order to ensure that you get the correct board, you must quote the reference code when ordering. The code can also be used to identify the year and month in which a particular project appeared: the first two numbers are the year, the third and fourth are the month and the number after the hyphen indicates the particular project.

Note that these are all the boards that are available — if it isn't listed, we don't have it. Our terms are strictly cash with order — we do not accept official orders. However, we can provide a pro-forma invoice for you to raise a cheque against, but we must stress that the goods will not be dispatched until after we receive payment.

		-	
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	E/8109-4 Laboratory PSU	n	F/8310-3 Typewriter Interface 4 17
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	E/8110-2 Sound Bender	n	E/8311.2 Alarm Extender 3.21
	E/8111-1 Voice Over Unit 4 57	-	E/0311-2 Additional Extender
	F/8111-3 Phone Bell Shifter 3 40	-	E/0311-3 Multiswitch
n	F/8112-4 Component Tester 1 71		E/8311-4 Multiple Port
100	2		E/8311-5 DAU ADC Filter
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	E/0202-2 Allez Cat Fest Repeller 1.95	0	E/8311-/ Logic Clip
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### **REVIEWS**

### DIGITAL CONTROL SYSTEMS

<b>F</b>	

C.H. Houpis and G.B. Lamont McGrawHill Book Co. (UK) Ltd. Shoppenhangers Road Maidenhead Berkshire SL6 2QL

price:£34.95

I.

Have you ever dreamed of finding a textbook on your favourite electronics topic that was readable, iucid, stimulating and exciting? One where the author's enthusiasm for the subject shone from every page? You may have found your dream already, but - sad to say - I'll have to keep on dreaming. 'Digital Control Systems' is the intellectual equivalent of a stiff dose of castor oil - hard to swallow, and the chances are it won't do much good anyway.

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In the introduction, we are treated to some examples of control systems straight from 'The Boys' Own Book of Digital Control'. My favourite is the man on the motorbike with an American-style speed COD spying on him from behind an hoarding. 'This advertising example,' say the authors, 'illustrates the beauty of the human being operating as a digital con-trol system.' The motorbike rider, it seems, represents a multiple-input, multiple-output (MIMO) control system. A few pages later, it's suggested that we might analyse such systems using the root-locus method, 'or - if all else fails — a quick blast of the state variables might do the trick. Okay, I'm game. How

Unenlightened, we reach the next chapter, which begins with a ten-page explanation of the binary system (yawn!) followed by a potted version of 'How To Do Logic and Computers'. Then, without warning, we are into an intenselv plunged mathematical analysis of a generalised control system which assumes the reader to be conversant with Fourier and Laplace Transforms.

In my estimation, the number of people who are au fait with Compex Frequency Domain analysis yet need ten pages to come to terms with the mysteries of binary arithmetic could be counted on the fingers of one elbow.

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In fairness, this book probably contains as many facts to the square inch as any other text book. It is merely another example of the kind of sludge students have been subjected to since time immemorial. Polytechnics will love it. I don't.

**Paul Chappell** 



### MICRO COMPUTER HANDBOOK

### Book

J.A. McCrindle (ed) **Collins Professional and Technical Publishers** 8 Grafton Street London W1

price: £30

It could have been subtitled 'Everything you wanted to know

about microcomputers but were afraid to ask'. It is 622 pages long, which works out at about 5p a page, and it contains papers on everything from the origins of the microcomputer. through ICs, buses, peripherals and software to test instrumentation. The authors are drawn from the most august ranks -Marconi, Ferranti, Motorola, Tl, BT, the University of Manchester, AMD, Intel, NEC, Hitachi, Zilog, Digital Research, Shugart, QUME, GEC, Hewlett Packard and Tektronix, to name some of them. All-in-all, it should be the last word in micros, but somehow something seems to be missing.

The problem, I think, is that the field is such a rapidly moving and expanding one that any book aiming at comprehensiveness is working at a severe disadvantage. For example, I could only find a throwaway reference to battery-backed, single-package RAMs such as the Mostek 48Z02, which has been around a couple of years now, in what is an otherwise useful chapter on memory devices. The Apple Macintosh is included as an unknown new Apple model. The IBM PC and PC XT are here but not the AT. There is less than a nod in the direction of data encryption. I couldn't find anything on HCMOS technology, although CMOS, NMOS and PMS are all there. The LSI 11 and and 68020 mpus are barely mentioned. In the chapter on systems, the only manufacturers mentioned at any length are Apple, Commodore, IBM and Tandy (Radio Shack). DIL is dealt with but not IEDEC. V24/ RS232 gets some treatment but there seems to be no mention of the Centronics standard. Indeed, there is very little of any depth on interfacing as such, but a considerable amount on buses, networks, printers and operating systems. There is almost nothing on modems and while there are useful chapters on high and low level languages, the treatment of applications (word-processing, spreadsheets and so on) is cursory, to say the least.

I could go on, but the point is well-made. Any reader of this book is likely to be able to construct their very own list of missing and shallowly treated topics at least as long as mine. They will also be able to point out all the apparent discrepancies in the emphasis on the topics that are covered. The conclusion is really quite easy to draw.

In trying to cover the subject from as many angles as possible, the editor and publishers have foregone the selectivity that might have given this book a sharper focus. It doesn't take much to see that no-one involved in the production of the book is too clear as to the indentity of its target market. In the preface, editor McCrindle writes 'I hope that this handbook, by providing in one volume, information on all facets of microcomputers, will prove an invaluable reference on a technology that has come so far in such a short space of time.' Inelegance part, the hope, I'm afraid, is forlorn. 'The book.' writes McCrindle, 'should be . . . of use to scientists, engineers, consultants and managers as well as lecturers and students.' This aim, transformed into an assertion by the simple expedient of replacing the word 'should' with the word 'will', is echoed in the blurb.

Parts of the volume will clearly be of use to managers, consultants and students - ali of whom, it can be safely assumed, seek authoritative but palatable enlightment. After all, many of the contributors know what they're talking about. Scientists, and lecturers, engineers however, would be advised to keep away from this book unless, that is, they happen to be engaged in biology, bridgebuilding, English literature or other field largely some unrelated to microcomputers. Had the editor decided to go a thoroughly popular for approach, he might have been better rewarded, for there is a need for a book like this on the shelves of every informed citizen in any society under the impact of the microcomputer.

. . .

Had he decided to hit the technological nail squarely on the head, I might now be reviewing a valuable collection of technical papers. As it is, this is something of a curate's egg part buyer's guide, part history. part introductory treatment of some complex topics, part technical manual and part survey. The parts are by no means equal: some may serve as useful additions to a reference library (even given a less than adequate index), while others may be wholly unsatisfactory or merely redundant.

### PLAYBACK

A company called HHB were having a demonstration — something to do with digital audio, which the editor thought sounded interesting. It seemed worth dragging my lazy carcass down to the Smoke. While I was there, would I like to look at some new equipment that Marantz are meant to be exhibiting at the audio trade show?

After about an hour on the train going from the sticks to Euston station, and about twenty minutes travelling two stops on the tube, I arrived at HHB to find that a well organized exposition had just started. It was soon clear that HHB are a company who are firmly committed to the supply of digital audio recording equipment to professional studios.

#### **Picture This**

The equipment in question on this occasion is all Sony gear imported from Japan, and its function is to convert analogue audio signals into a digital form suitable for recording on a video cassette using an ordinary VCR. This is both more difficult and more useful than it may at first appear.

Because the video cassette machine requires a video signal to keep it in sync, the 16 bit digital audio has to be buffered to take account of the sync pulses (during which signals cannot be recorded) and then added to sync pulses to give a composite 'video' output. Once this process can be made to work reliably and economically, master tapes can be made and copied, yea unto the third and fourth generation, without degradation of quality.

#### **Hi Numbers**

The equipment which got the whole thing going properly were two processors intended for the hi-fi buff. For digital processors, these were and are very economically priced, though they cost more than the videocassette recorder they are intended to work with! The number of people willing to pay £700 for a PCM701ES, or £1,200 for a PCM F1, is clearly somewhat limited. Indeed, people who spend this much on an entire stereo system are probably an oppressed minority.

The people at HHB realised that, while this gear was a bit expensive for most hi-fi, it was very competitive as audio equipment. Its quality, in terms of frequency response and signal-tonoise ratio compared very favourably with analogue equipment. At the same time, it was becoming clear that compact disc was here to stay, and that sooner or later this medium would find mass sales. In order to make good use of the quality available from this system, digital master tapes are essential. It would be absurd, after all, for the average domestic hi-fi user to have at their disposal equipment capable of better sound reproduction than is available from the master tapes.

#### The Hit Machine

Supporting HHB's claims about the quality of digital audio was a videotaped interview with Phil Collins, who takes his work home with him in the form of a PCM F1 processor. He commented that many musicians do not that if they make good use of digital recording technology they won't have to.

### **Statistics**

This was done in part to publicise the fact that Sony have, largely due to the influence of HHB, restarted production of their economical digital encoding units. Just as HHB had made a start on renting and selling these units, Sony decided that the domestic market for these items was not big enough to support production. They dismantled the plant! HHB smartly bought up all the stocks they could get their hands on, and started trying to talk Sony into restarting production. They thought that the next item in the range, a large scale professional

estimate for 1985 is 3 million.

### CLUEdo

To assist the use of this digital equipment in recording studios, HHB have developed a computerbased editing aid, called Computer Logging Unit and Editor, which permits butt editing to the nearest frame. The only convenient means of editing with these cheap digital processors used to be by turning the signal into analogue form and back again.

Of course, editing to the nearest frame (or did they really mean field?) is not good enough to perform edits in the middle of a track on most rock music — doubly so at the PAL frame rate of 25 Hz as against the NTSC rate of 29.99



Easy lover loves his PCM 701ES and PCM 1610 digital sound processors.

'fine tune' the sound to the n-th degree because they know that by the time it gets onto the master disc no one will be able to hear it. He reckoned that in studios where digital recording is used, musicians can put in the effort to get sound quality spot on in the knowledge that people will have a chance to appreciate it. Well said. A far cry from the day when the LPs were so bad that you needed three or four tries to find a reasonable copy.

Maybe the record companies will go back to recycling the LP labels in order to lower the quality of LPs and 'encourage' people to change to the more expensive compact discs, but my guess is processor costing a cool £15,000 was too costly for a number of professional applications.

The percentage growth in the use of digital equipment is rapid. In 1982, an estimated 1% of recording studios used digital equipment. In 1983 the number rose to 15% and in 1984 reached 40%. HHB estimates (hopes) that the figure will soon be 75%. Of the studios using digital, HHB expect that 80% will be using F1 and 701 processors supplied by them.

The growth in the use of digital technology in the recording studio runs parallel with the sales of compact discs in this country. These rose from % million in 1983 to nearly 1 million in 1984. The Hz. Still, different tracks can be joined together on one tape without the addition of noise and distortion, and the the information stored on disc by CLUE enables the recording engineer to work out what edits to carry out before transferring to more expensive equipment. CLUE controls the videocassette recorders for you, and finds the marked places, so there is time for the recording engineer to drink his coffee while the computer does some of the fiddly bits.

I must admit that I am moved to wonder why, with the buffering which is required anyway in the encoding system, they could not edit to the nearest line instead of the nearest frame, and simply detect the end of the next digital word to use as the actual edit point. Maybe it's more difficult than it appears, but to me it seems like spoiling the ship for ha'p'orth of tar. Perhaps the people at HHB, CLUEd up as they are, had a severely limited timescale and had to do what they could with equipment avialable off the shelf, and only minor mods.

#### **All Gas And Gates**

By no means all the applications for PCM 701 and F1 digital processors are in recording studios. The low background noise (approximately 6dB per bit or 6°16=96 dogbiscuits) make these digital processors useful for the accurate analysis required in speech recognition. British Telecom, among others, use the equipment for this purpose.

The Gas Board use the equipment to aid analysis of mechanical resonance in detecting metal fatigue. Nobody went into a lot of details about this, but apparently with analogue recording, the subtleties which actually indicate what is going on would be lost in the noise.

All this is a far cry from the origins of HHB's business. in 1976, when they started hiring

### OPEN CHANNEL

Advanced Micro **Devices** (AMD) has recently announced its intention to sample a chip set later this year, for integrating the simultaneous transmission, in digital form, of voice and data along a single telephone line. Such a chip set brings us one step nearer to the total integration of voice and data communications into one single network, as envisaged in the Integrated Services Digital Network (ISDN)

#### **Chips With Everything**

Although still a long way off, the ISDN will eventually replace the existing telephone network and many data networks too. It will be a purely digital network, and so each telephone terminal must be capable of converting the analogue voice signals to a digital form before transmission, and reconverting received digital signals into sound. Alongside the transmitted digital sound will be the capability to transmit data, too, at a fairly high rate — out PA equipment for bands doing live performances.

#### Sansui and Sensibility

After this, the piece of trade show I visited was a slight anticlimax. The people on the Marantz stand told me that I needed to talk to the technical man — I realised that — but several people were waiting to see him already. Could I come back in half an hour? I conceded that I could and wandered off to see what else was around.

One of the first products I looked at (because it was near the door) was a Sansui digital encoder designed to record audio on a VCR. This is definitely intended for the domestic market, as it only uses 14 bits rather than the 16 used by the Sony equipment. This would give a theoretical signal to noise ratio of 84dB at best, which is still good compared with other domestic recording equipment. The encoder has the advantage that it can record successfully on video recorders running at half speed. Thus, on VCRs with this extended play mode, up to eight hours of recording are possible on one tape. I wonder how bad the dropouts are in this mode, and what effect they have on the sound?

in the region of 64,000 bits/sec. For these purposes, some quite complex electronic circuits will be required, within each telephone terminal and within the many existing exchanges throughout the land; and of course the only way of approaching the problem is to design some chips to do the job. A number of semiconductor manufacturers are currently working on the designs.

The AMD chips set comprises five chips; the first two of which are the devices needed in the user's telephone terminal, the remaining devices are for exchanges. The company is to be congratulated for taking this brave step, given that worldwide standards are not yet finalised.

It really is about time that some of the governing bodies of the telecommunications world got their act together in this field. After all, how long do we want to be saddled with inferior data communications, at least when connected via the existing telephone network? It is rather archaic that to transmit data between telephone users we have to resort to devices which convert the digital data into analogue signals for transmission - modems. They're not exactly fast and they're certainly pricey.

Much of the equipment on the Sansui display looked similar to that available in most hifi shops, but there were some novel items. One was a graphic equaliser incorporating remote control, and a bar display of settings. The remote control box slides into the front of the unit to work as the local controller if required. This unit formed part of a rack system, at the top of which was a stereo amplifier featuring what looked like a video game display. It was actually the function indicator and power level meter.

On the left, a parametric equaliser control panel looked like mission control but was well laid out and should be easy to use. The whole assembly, including tuner, dual cassette deck, and computerised turntable, is called an 'Intelligent Super Compo' but I reckon it's the operator who has to be intelligent. With two types of equaliser to set for the right frequency response it is definitely for the initiated.

#### **Round and Surround**

Back to Marantz. The technical man is tied up and could I come back a bit later? Grumble. Oh well, just one more time. The question is, if he is in that much

The latest modem to hit the streets (in mid-May) follows CCITT Recommendation V32, which enables its use over ordinary dial-up telephone lines to a speed of only 9600bits/sec, at a cost of around £3000. Chicken feed? Hardly!

True, there are digital options such as Packet Switchstream, but they're costly too.

#### Pie in the Sky

Meanwhile, back to my favourite subject — or at least it seems to be my favourite subject; I've featured it quite regularly over the last few months — satellite television.

After long hassles over costings and finance, direct broadcasting by satellite (DBS) looks as if it may still get off the ground, if a little shakily. Only days before penning this column, the news broke that Unisat, the organisation which hopes to provide the satellites to be used, has finally retendered at a much lower price.

The Club of 21 (the operating consortium) appears to have won the round, after its gamble not to commit itself. Its argument was that Unisat's price was too high. Unisat finally appears to have agreed, and has given a number of options to demand, why only one technical man?

A bit later he is still occupied and my feet are suffering from the strain so its down the road to the ETI offices for a sit down and a talk to the editor. A phone call to the Marantz offices resulted in a promise of information.

Sure enough, the man who had been in such heavy demand telephoned me a few days later to give me the lowdown on their new products. So new are they, that he had not had a chance to try them out properly himself before the show. The most interesting one is the Dolby surround sound decoder. It was news to met that rear channel information is encoded onto most prerecorded stereo videocassettes. The surround decoder allows the viewer to make use of this information, and it includes a stereo amplifier, so just add speakers and sit back.

It might be a bit unnerving to hear the spaceship roar overhead while watching 'Star Wars' on a small screen, but people who hire videocassettes of a spectacular nature may often be very interested, particularly considering the £129 price tag.

#### **Andrew Armstrong**

the Club of 21, one of which is a much lower costing of (only?) £290m over 10 years.

That option may not, of course, be the one which the Club of 21 picks, and it's unclear at the time of writing what the other options are ... watch this space ....

#### Money To Burn — But Whose?

Finally, a package has landed on my desk from the Department of Trade and Industry. Called 'The Development of the Liberalised Telecommunications Market in the United Kingdom - An Information Pack' it is an extremely glossy press and publicity gimmick which seems to have been sent out to just about anyone with anything to do with electronics, telecoms, or computers (the ETI office alone received two packs). Basically comprising issues six to fifteen of the Department's occasional news letter 'Ringing the Changes', it aspires to sum up all of the changes in the telecommunications field over the last couple of years, with regard to liberalisation. Very interesting, but how much did it all cost? Did it really do any good? Is that my income tax you're spending?

# **SERVICE SHEET**

### Enquiries

We receive a very large number of enquiries. Would prospective enquirers please note the following points:

We undertake to do our best to answer enquiries relating to difficulties with ETI projects, in particular non-working projects, difficulties in obtaining components, and errors that you think we may have made. We do not have the resources to adapt or design projects for readers (other than for publication), nor can we predict the outcome if our projects are used beyond their specifications;

Where a project has apparently been constructed correctly but does not work, we will need a description of its behaviour and some sensible test readings and drawings of oscillograms if appropriate. With a bit of luck, by taking these measurements you'll discover what's wrong yourself. Please do not send us any hardware (except as a gift!);

Other than through our letters page, Read/ Write, we will not reply to enquiries relating to other types of article in ETI. We may make some exceptions where the enquiry is very straightforward or where it is important to electronics as a whole;

We receive a large number of letters asking if we have published projects for particular items of equipment. Whilst some of these can be answered simply and quickly, others would seem to demand the compiling of a long and detailed list of past projects. To help both you and us, we have made a full index of past ETI projects and features available (see under Backnumbers, below) and we trust that, wherever possible, readers will refer to this before getting in touch with us.
 We will not reply to every the state of the sta

We will not reply to queries that are not accompanied by a stamped addressed envelope (or international reply coupon). We are not able to answer queries over the telephone. We try to answer promptly, but we receive so many enquiries that this cannot be guaranteed.

Be brief and to the point in your enquiries. Much as we enjoy reading your opinions on world affairs, the state of the electronics industry, and so on, it doesn't help our already overloaded enquiries service to have to plough through several pages to find exactly what information you want.

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We would normally expect to have ample stocks of each of the last twelve issues, but obviously, we cannot guarantee this. Where a backnumber proves to be unavailable, orwhere the issue you require appeared more than a year ago, photocopies of individual articles can be ordered instead. These cost £1.50 (UK or overseas surface mail), irrespective of article length, but note that where an article appeared in several parts each part will be charged as one article. Your request should state clearly the title of the article you require and the month and year in which it appeared. Where an article appeared in several parts you should list these individually. An index listing projects only from 1972 to September 1984 was published in the October 1984 issue and can be ordered in the same way as any other photocopy. If you are interested in features as well as projects you will have to order an index covering the period you require only. A full index for the period from 1972 to March 1977 was published in the April 1977 issue, an index for April 1977 through to the end of 1978 was published in the December 1978 issue, the index for 1979 was published in January 1980, the 1980/81 index in January 1982, the 1982 index in December 1982, the 1983 index in January 1984 and the 1984 index in January 1985. Photocopies should be ordered from: ETI Photocopies, Argus Specialist Publications Ltd, 1 Golden Square, London W1R 3AB. Cheques, postal orders, etc should be made payable to ASP Ltd.

### Write For ETI

We are always looking for new contributors to the magazine, and we pay a competitive page rate. If you have built a project or you would like to write a feature on a topic that would interest ETI readers, let us have a description of your proposal, and we'll get back to you to say whether or not we're interested and give you all the boring details. (Don't forget to give us your telephone number).

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So far as we know, all our advertisers work hard to provide a good service to our readers. However, problems can occur, and in this event you should: 1. Write to the supplier, stating your complaint and asking for a reply. Quote any reference number you may have (in the case of unsatisfactory or incomplete fulfilment of an order) and give full details of the order you sent and when you sent it.

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If you exhaust the above procedure and still do not obtain a satisfactory response from the supplier, then please drop us a line. We are not able to help directly, because basically the dispute is between you and the supplier, but a letter from us can sometimes help to get the matter sorted out. But please, don't write to us until you have taken all reasonable steps yourself to sort out the problem.

We are a member of the mail order protection scheme, and this means that, subject to certain conditions, if a supplier goes bankrupt or into liquidation between cashing your cheque and supplying the goods for which you have paid, then it may be possible for you to obtain compensation. From time to time, we publish details of the scheme near our classified ads, and you should look there for further details.

### **OOPS!**

Corrections to projects are listed below and normally appear for several months. Large corrections are published just once, after which a note will be inserted to say that a correction exists and that copies can be obtained by sending in an SAE.

Digital Delay Line (December 1984 - January 1985) In Fig. 6 on page 21 of the December issue, C19 and C20 are both 100uF. In Fig. 8 on page 62 of the January issue, C3 should be marked 33p. On the overlay diagram (Fig. 9, p.64), R37 is missing and should be connected between pin 3 of IC9 and the OV line; R20 is missing and should be located in the holes immediately to the left of R18; R50 is missing and should be connected between pins 1 & 2 of IC14. Some components on the overlay have also been wrongly numbered:- C20 should be marked C19 and C21 should be marked C20; R12 (between ICs 5 & 6) should be marked R22; R48 should be R44, R49 should be R45, R57 should be R46, R51 should be R47, R50 should be R48, and R47 should be R49. The unmarked capacitor directly above what is now C19 is an un-numbered 100n ceramic. C30 does not appear on any diagram or parts list and this is correct.

On the digital board, IC24 in Fig.10 (p.55) is shown the wrong way around and IC35 at the bottom centre of the overlay should be marked IC25. The tracks to pins 8 and 9 of IC28 are the wrong way around but this should not affect the performance. It is quite easy to cut the tracks near the IC pins and connect across using wire links soldered into the adjacent through-board holes. D6 and R31 should be swopped over.

No frequency stabilising capacitor was included in the design but it has been found that in some cases the output frequency of IC30 is affected by stray capacitance and does not remain steady at 1MHz. This affects all the system timing. To overcome this problem, a 33p capacitor should be carefully soldered directly onto pins 6 and 7 of IC30 on the underside of the board.

#### Single Board Controller (March 1985)

There were a number of errors in the parts list. RP2 is listed as a 10k SIL pack but is actually four separate resistors, and the same applies to RP3. RP4 is also listed as a SIL pack but should consist of seven commoned resistors. R13 is always required, not just when a cassette interface is used as stated.

### The Real Components (May 1985)

In Fig. 1 on page 20, the connections for the Texas L and 2N transistors are incorrectly shown. They should read B, C and E from the top.

#### Heat Pen (June 1985)

The instruction in the penultimate paragraph on page 49 should read "... adjust RV2 for 2.73V...", not 2.37V as stated.

#### Low Cost Audio Mixer (June 1985)

In Fig. 6 on page 39, the PCB foil pattern has been incorrectly shown as though from the copper side. The board is shown correctly from the copper side in the foil pattern pages. In Fig. 10 on page 40, the positive power rail at lower left should be shown connected to pin 8 of the TL072s, IC1-5).

#### Noise About Noise (July 1985)

In Fig. 5 on page 24, no connection should be shown between the cathode of the diode and the negative side of the 470u capacitor.

#### Printer Buffer (July 1985)

The case specified is actually larger than the one used for the prototype. It will, of course, work perfectly well, but if you want a compact unit use a Verocase 202-21038H ( $180 \times 120 \times 65$  mm) rather than a Verocase 202-21035. The regulator IC17 should be bolted to the back of the case to provide heatsinking or, alternatively, fitted with a TO220 heatsink.

l

## SCRATCH PAD

### by Flea-Byte

To many readers, the abbreviation 'CIA' may be best known as one of a number of similar acronyms for devices used to interface microprocessors with the real world. To others, the three letters will be redolent of unreality.

The CIA (not just any old CIA, you understand) is the Central Intelligence Agency of the United States - one of eighteen such agencies operating in the home of the brave, land of the free. Known as 'the Company' to its employees, the CIA inhabits a strange world of fantasy spy games which it plays with deadly seriousness. You may recall that it was the CIA which once devised a plan to overthrow Fidel Castro by sending a squad of trained barbers over to Cuba to shave off his beard. The clean-shaven look, they reckoned in Langley, Virginia, would absolutely ruin Castro's street credibility! Exit one thorn in America's side - or (to be geographically more accurate) one pain in its backside.

We know this at least in part because the CIA maintains a curiously high profile — reckoning, perhaps, that either it is so powerful or its schemes are so lunatic that it will be all but invincible. Compared to the National Security Agency, the Department of Army Intelligence (a contradiction in terms according to Groucho Marx) or even the game old FBI, the CIA is a publicity-crazed strumpet.

So, scanning a journal called 'Mini-Micro Systems' that recently came my way, it came as no great surprise to find the CIA advertising for electronics engineers. These days, it would seem, barbers are out.

• •

Don't all rush. There is a snag. You have to be an American citizen. It would probably help if you didn't serve on the USS Nimitz too. Oh, and you should have experience in Telecoms, Networking, Mini and Micro Systems, Logic Design or Firmware Development.

Once you've passed the interview and promised not to spy for anybody else, what delights can you expect? The Company may be crazy, but it is no joke. Among its more notable achievements has been the overthrow of legitimate governments in Iran, Guatemala, Vietnam, Cambodia and Chile. It has propped up dictatorial regimes across the world and the shadowy presence of the CIA can be detected wherever dirty tricks are done.

For your part, you could find yourself working on 'computer systems and communications efforts unique to the CIA', according to the recruitment ad. Read 'bugging, surveillance and electronic eavesdropping'. The Company promises 'excellent compensation, comprehensive benefits, the opportunity for foreign travel, and the chance to advance the state of the art while contributing to the Agency's mission'.

Or, to put it another way, you could journey to exotic little countries like Nicaragua and help to subvert genuine democracies. Alternatively, you might find yourself deep in the heart of the Soviet Union, or even listening in to overseas phone calls at Menwith Hill in North Yorkshire. And even if you do find yourself in a Russian jail serving a twenty-year term, you can console yourself with the thought of that 'excellent compensation' and those 'comprehensive benefits', like a pension when you get out and a pill to salve your conscience.

### . . .

#### ... Not Tobie

Of course, we don't have any spies in our country, except those working for the other side. And I wouldn't dream of suggesting that there's anything underhand in the way the police or security agencies deal with British dissidents. In fact, we don't even have any dissidents. Dissidents are all Russian. I mention this just to ensure that you don't get the wrong idea from the following little tale.

It seems that Tony Wilson, coordinator of the Electronics for Peace group and an electronics engineer in the pay of the government, was attending a meeting in Scotland recently with some people from the Scottish Campaign to Resist the Atomic Menace (SCRAM) and Scientists Against Nuclear Arms (SANA). Now Wilson has recently been voted **Electronics Personality of the Year** in a ballot organised by 'Electronics Times' (ETI, July 1985). It is believed that the ballot was fair and above board — it is thought that it was even postal - and

Wilson was doubtless proud of the inexpensive little trophy he was awarded. Indeed, he had his trophy (known as a TOBIE) with him in his car. The strange thing is that the car was broken into and a bag containing the TOBIE and a few odds and ends was stolen. The thieves left some expensive electronic goodies also sitting in the car completely untouched. On the indubitable assumption that the runners-up in the TOBIE vote were not bad losers, there are two possible explanations for this curious theft. Either Wilson's award has annoyed somebody or Scottish thieves are notorious collectors of unsaleable baubles. Perhaps the Special Branch or MI5 will let me know if I've missed any pertinent points.

### The Prince And The Leveller

'When His Royal Highness arrives,' said the man on the podium, 'you should all applaud but not stand up.' This was news to me — that you aren't required to rise when the Queen's consort enters a room. The room in question was the Great Hall at Westminster School and the occasion, the Young Electronics Designer of the Year awards, 1985, sponsored by Cirkit Holdings plc, the component company, and organised by Professor John Eggleston of Warwick University.

Ironically, nobody had bothered to check the PA, which was not working when I arrived and the only person to think of doing a sound-check was presenter Petula Clark, an old show-biz pro. We were introduced to Ronald and Richard Bulgin (or was it Doug and Dinsdale) of Bulgin, the company which owns Cirkit, and HRH duly arrived. Nobody stood up. On this occasion, the Prince had evidently not been engaged to speak, so he didn't say anything (at least, not so's I could hear). The tension mounted as we waited for the prizes to be announced. Eighteen finalists had been selected from 1,000 entrants (some being teams) and four finalists in both a senior and junior category would be rewarded with cash amounts of between £50 and £500 plus help with their careers and the chance to market their ideas. On top of that, Texas Instruments were offering a computer to the top school in the senior category and several calculators to the top school in the junior category.

The entries were judged for originality, construction, everyday usefulness and commercial feasibility. There seemed little doubt that the last was going to be the most important consideration and some sophisticated but no doubt expensive designs that had made it to the final stood little chance of winning. Successful electronics design is often a case of using simple ideas to perform original tasks at as little cost as possible. Brilliant design may reveal genius, but in the real world genius is often an encumbrance to profitability.

There were plenty of devices employing tried and trusted ideas in crafty disguises - the rain detectors and LDR light detectors of every elementary elec-tronics course. One LDR circuit even won first prize in the senior section - Jonathan Kempster's audible spirit level, which used the liquid in an ordinary spirit level to break a beam to an LDR. And my personal favourite design, the Heath-Robinsonian eggdipping controller gained first prize in the junior section for Daniel Rodenhurst with a very simple circuit. What pleased me most, however, was the number of designs aimed at helping the disabled. So take this column's independent awards Christopher Howard, for your toy for the severely handicapped, Russell Vowles for your infra-red remote controller for immboilised people, Andrew Burrows for your granny alarm buzzer and junior entrant Gareth Arthurs for your milk tester for the partially sighted.

As a matter of fact, the editor tells me that readers can expect to see one or two of the designs appearing in these pages. Watch out for them.

\* \* \*

### No Mean Time

Hughes Aircraft, the company founded by the reclusive and eccentric Howard Hughes, has announced the development of a hydrogen maser clock accurate to one second in 30 million years. This beats the previous record (held by caesium and rubidium gas-cell atomic clocks) by a factor of about 100. The clock, which originally weighed 500 kilos, has been miniaturised for use in satellite-based navigation systems. It now weighs around 20 kilos and is about the size of a portable television. This may well mean that nobody will have an excuse to be late for work any more.

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