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## Plotting Brother

Brother have introduced a plotter/printer which not only functions as a portable typewriter and printing calculator but will also draw graphs. The BP30 Graph Writer features three type sizes, four colours, a correction buffer with a fifteen-character LCD display and is expected to sell for just under $\mathbf{£ 1 8 0}$ inclusive.

The BP30 is essentially a plotter which uses miniature pens to 'draw' its characters. Four pens, red, green, blue and black, are
held in a circular carrier which rotates to select the desired colour. The carrier moves along two parallel bars to provide horizontal movement while the roller moves the paper up and down to provide vertical movement. It is because it draws in this way rather than stamping pre-formed characters as most typewriters do that the BP30 is able to produce three different type sizes, micro, normal and jumbo, and can even print vertically up and down the page as well as across it in the normal way.
Four buttons move the pen carrier and the paper roller up,
down, left and right to select the starting position and another button provides a choice of four different line spacings between text. An LCD display above the keyboard shows the user which line spacing, type size, print direction (horizontal or vertical) and mode have been selected and whether capital shift has been engaged or not, and also displays the last fifteen characters to be entered.

The BP30 has three text modes plus the graph mode. In Non Print mode no printing takes place and the machine can be used as a fourfunction calculator, the results appearing on the display. In Direct Print mode text is printed as it is entered on the keyboard. In Correction Print mode, the fifteencharacter buffer is filled before anything is transferred to the paper, and errors spotted in the display can be corrected before printing using a cursor and insert and delete keys.
When Graph is selected, the display flashes up mesages inviting you to choose a graph type and then enter the appropriate data. Eight graph options are offered including shaded and unshaded pie charts and bar charts and single or multiple variable sets on bar charts and line graphs, and up to twelve variables can be displayed on each. When the data has been entered, a test facility allows the user to check the position of the complete graph and if necessary alter it before any marks are made on the paper. The machine automatically calculates percentages and angles, etc from the data given and presents the
results in several colours.
ETI has had a BP30 on trial for the last month or so and has had enormous fun putting it through its paces. The most common complaint is that so potetnially versatile a machine should surely be equipped with an interface to allow it to be used as a computer printer. Brother were rather cagey about this at the press launch but hinted that an interfaceable version might be offered if there were sufficient demand. The machine is slower thian conventional tvoewriters as one might expect of a plotter, but nonetheless managed to keep up with the fastest of us in all except the largest of its three type sizes. Since the jumbo size is unlikely to be used much except for headings and the like, this should not cause problems. The BP30 cannot cope with paper of varying thickness and should not therefore be used on envelopes, but otherwise worked well on all that we tried it on and produced a pleasing end result. The only real shortcoming we found was that the pens did not write well on greasy surfaces - we had to handle paper with care before inserting it because the BP30 would often fail to print over areas which had been in contact with fingers. A little more development work on the pens themselves would not go amiss.

The BP30 measures 337 xs $266 \times$ 67 mm and weighs 2.7 kg including batteries. A mains adaptor is available as an extra. It will be available from a number of high street stores and the recommended price is $\mathbf{£ 1 7 9 . 9 5}$ including VAT.

## Banshee Siren

n our September issue we featured a versatile alarm system which we called the 'ETI BANSHEEE ALARM".
A. P. Besson of Hove, Sussex, have pointed out that they are the owners of the registered Trade Mark "BANSHEE" which they use on their range of electronic alarm sounders.

Obviously we intended no confusion by our article and have agreed to draw to our readers' attention the rights of $A$. $P$. Besson in the registered Trade Mark "BANSHEE". The company points out to us that although their product is intended mainly for use by the Fire and Security Industry, it is perfectly suitable for use by amateurs and can be pruchased directly from themselves.

If any reader is interested they should contact Miss Jane Squires at A. P. Besson, St. Josephs Close, Hove, East Sussex BN3 7EZ, tel 0273-722651.

## An Interesting Case?

f you've got a problem with moisture, petrol or some other unwelcome liquid getting at a piece of electronics, then a new range of boxes from Boss Industrial Mouldings could be the solution (sorry!). They now offer four sizes of cases, from 40 by 52 by 75 mm to 80 by 120 by 220 mm , which incorporate an oil and petrol resistant neoprene gasket seal.

Also pictured are the new cases which include internal PCB supports, but these are not waterproof. For more information and sales contact Boss Industrial Mouldings, James Carter Road, Mildenhall, Suffolk IP28 7DE, tel 0638 716101. Incidentally, Boss say that most of their products are available in small order quantities through their sister company, Bimsales; any that aren't available through Bimsales, Boss will supply themselves direct to the public.



# Tolerant Buffers 

Monolithic Memories have introduced four new eightbit buffers which are specifically designed for use where system noise is a problem. The buffers all have Schmitt-trigger inputs to improve their noise immunity and the manufacturers claim that no other buffers on the market have this feature.

The SN54/74DS310, SN54/ 75S340, SN54/74S341 and SN54/ 74S344 all possess tri-state outputs, low current PNP inputs to reduce
loading and are pin-compatible with the SN54/72S210/240/241/ 244 series. Operating on a five volt supply, the ' $\$ 310$ and ' $\$ 340$ have a maximum data to output delay of $15 n s$ and a worst-case current demand with outputs high of 80 $\mathrm{mA}, 50 \mathrm{~mA}$ typical, while the 'S340 and 'S344 have a maximum delay of 22 ns and a worst case demand of $130 \mathrm{~mA}, \mathbf{8 0} \mathbf{~ m A}$ typical. Power dissipation for the two groups of devices is $\mathbf{2 5 0 ~ m W}$ and $\mathbf{4 0 0} \mathbf{~ m W}$ respectively.

The new buffers are available in 20-pin skinnydip plastic and ceramic packages and are marketed by Microlog Ltd, 1st floor, Elizabeth House, Duke Street, Woking, Surrey GU21 5BA, tel 04862-66771.

## Stereo TV Sound

n our March issue we reported plans by the BBC to make experimental television broadcasts using digitally-encoded stereo sound. Initial experiments using the system, which employs a digitally modulated second sound carrier, had already been made using the Wenvoe transmitter in South Wales, and the next stage was to be full transmission from the Crystal Palace transmitter to test the compatibility of the system with existing monophonic television receivers. The BBC have now told us that such a transmission took place on May 24th when a pop concert was broadcast after official closedown. The programme consisted of an analogue
video recording previously used for a simulteneous television and stereo radio transmission, and the associated digital sound recording was replayed into the stereo transmission system without being converted into analogue form. The BBC believe that this was the first 'all digital' transmission of stereo television sound and say that they are now confident that a digital system is the best way to obtain stereo from terrestrial transmitters.
Consultation with the IBA, the home office and industry aimed at establishing a standard UK specification is now well under way and an early agreement is anticipated. Meanwhile, the BBC plan to further test the sysxtem by making occasional broadcasts during normal programme hours.

BBC Engineering Information Department, Broadcasting House, London W1A 1AA, tel 01-927 5432.

## Buffered Delay Lines

shcroft Components Ltd have announced the RHT series of buffered (active) delay lines. These lumped constant devices incorporate Schottky TTL logic elements (equivalent to 74SO4) in the input and output terminals.
The modules can be used as TTL elements with precisely fixed delay time. Any change of delay time due to temperature variations is minimised by using a delay line whose coefficient is complementary to that of the IC.
The seven types available cover total delay times from 20 nanoseconds to 250 nanoseconds with corresponding delay times per tap of $\mathbf{4 n s}$ to 50 ns and rise times of 3 or


4 ns. All have 5 outputs available to the user.

Significant PCB area savings may be achieved with the RHT series. Their used can considerably simplify the design of digital circuitry and provide highly accurate pulse timing. For further details contact Ashcroft Components Ltd, 28 Somerford Road, Cirencester, Gloucestershire GL7 1TW, tel 0285-67756.


## TurboLeds Are Here

eneral Instrument Lamps have been appointed sole UK and European agent for the new range of TurboLeds. These multichip solid state lamps are available in wire terminal form or with midget flange, miniature bayonet or miniature screw caps as plug in replacements for incandescent lamps. Red, Yellow and Green versions in $6 \mathrm{~V}, 12 \mathrm{~V}$ and 24 V ratings can
be supplied and as a bridge rectifier is included in the lamps, they can be operated from AC or DC supplies.
A major feature of TurboLeds is the metal shroud which acts both as a reflector and a heat radiator to avoid excessive chip temperatures which can cause the premature failure of high light output LEDs. Full details on the complete range of TurboLeds are available from General Instrument Lamps Ltd, Beetons Way, Bury St Edmunds, Suffolk IP32 6RA, tel 0284-62411.

## When Is 32 Bits 32 Bits?

0n page 23 we take a look at Motorola's 68020 32-bit microprocessor. What is beyond dispute is that this is a true 32-bit microprocessor, with 32-bit architecture and 32-bit data and address busses. On this basis, and the fact that pre-production samples were already in wide circulation with equipment manufacturers, we have accepted Motorola's claim that it is the first true 32-bit microprocessor.

However, the story doesn't end there. A number of other manufacturers have 'true 32-bit' microprocessors, and these include Nat Semi, NCR and AT\&T. Exactly who was the first and what 'true 32 bit' means will doubtless be cause for much argument amongst semi-conductor giants. And who can blame them, with a market worth an estimated $\$ 3.3$ billion at stake?

We here at ETI will be keeping a watchful eye on this squabble, and will keep our readers informed. Meanwhile, our advice to those of you involved in this trade is to keep your heads down!

Looking for some education? Then you might do worse than contact ICS Publishing Company (UK) Ltd who organise a number of professional technical courses. ICS Publishing Company (UK) Ltd, 3 Swan Court, Leatherhead, Surrey KT22 8AD, tel 0372-379211.

- Things still aren't looking too bright for the electrical industry. Business information company Dun \& Bradstreet Ltd tell us that company liquidations in the industry during the first six months of 1984 totalled 388 , a $2.9 \%$ increase over the figure for the same period last year. Bankruptcies among firms, partnerships and
individuals totalled 62, a $58.9 \%$ inrease over the figure for the first half of 1983 but no worse than the figure for the latter half of the year.

Tele-Production Tools have introduced a solution which can be used to retrieve components from potted assemblies. Called Stironol, the solution disintegrates epoxy and polyester resins but will not attack most metals in normal use and is also non-toxic and nonflammable. For details contact Tele-Production Tools Ltd, Stiron House, Electric Avenue, Westcliff-on-Sea, Essex SSO 9NW, tel 0702352719.

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# Fastest 32 x 8 Bipolar TTL Prom? 

M onolithic Memories have introduced what they claim to be the industry's fastest $32 \times 8$ bipolar, TTL programmable readonly memory (PROM) device, the 53/63S081A.
The PROM has a guaranteed access time of 15 ns and is $\mathbf{4 0 \%}$ faster than present $32 \times 8$ bipolar TTL PROMs. Power supply current of the 635081 A is 125 mA maximum and the output drive capability is

## 16 mA .

Special on-chip circuitry and TiW fuse links in the 63S081A provided for pre-programming and testing, ensuring high reliability with programming yields of greater than $\mathbf{9 8} \%$. Other features include PNP inputs for low input current, three-state outputs and full Schottky clamping. The devices are available in both plastic and ceramic 16 -pin, dual-in-line packages.

Further details on the 635018A $32 \times 8$ TTLPROM are available from Monolithic Memories Limited, Monolithic House, 1 Queens Road, Farnborough, Hants GU14 6DJ, tel: 0252-517431.

# 30V-out Function Generator 

The new Jupiter 500 Function Generator is claimed to be a rugged, mains-operated instrument offering features unique in its price range. Both amplitude and frequency are fully programmable by an external voltage and an exceptionally high output of up to 30 V peak-to-peak is available.

The frequency range of the Jupiter 500 is 0.1 Hz to 500 kHz in 7 switched decade ranges with fine frequency control. Sine, square, triangle and TTL ( 30 loads) waveforms are selectable and an adjustable DC offset up to 15 V can be applied to the output.

The Jupiter 500 is supplied with a comprehensive instruction manual and a spare fuse and sells in the UK at £110.00 (+ VAT). An illustrated colour data sheet is available from: Black Star Limited, 9A Crown Street, St Ives, Huntingdon, Cambs PE17 4EB, tel 0480-62440.

## Op-amp SOA Restrictions Eased?

Teledyne Philbrick say that they've alleviated the safe operating area restrictions and secondary breakdown problems suffered by virtually all power opamps with the introduction of their TP1463. The 1463 is the third in a series of high-speed FET input power op-amps, and incorporates a class A-B complementory VMOS output stage which is what is
claimed to make the difference.
The 1463 comes in an eight-pin TO3 package, can operate on supplies of +15 V to +40 V and can supply up to 1A (minimum). The slew rate is $80 \mathrm{~V} / \mathrm{us}$ despite the internal compensation for unity gain stability, and the gain-bandwidth product is 7.5 MHz .
Potential applications for the device include video yoke drivers, distribution amplifiers, CRT displays and gyro and oscillator drives for inductive and capacitive loads. A deatiled preliminary data sheet is available from MCP Electronics Ltd, 38 Rosemont Road, Alperton, Wembley, Middlesex HA0 4PE, tel 01-902 6146.

# Hi-fi Mains Transformers 

That every component in an audio amplifier is capable of degrading the performance is without doubt. However, it is debatable just how far one can go in the other direction - selecting 'hi-fi' components for use throughout the amplifier, not just for the few 'critical' components.
The transformer manufacturers Avel linburg Lid have entered the fray with the announcement of a new range of toroidal high-power transformers especially made for very high quality audio. The range spans power outputs of between 500VA and 2 kVA and dual outputs of 60 to 70 V RMS can be ordered. They can supply
transformers with two sets of secondaries, as a compromise between using two transformers for a stereo amplifier and using just one transformer to supply both channels.
Avel-Linburg don't say what it is about their transformers that makes them particularly suitable for hi-fi, although they do claim that they can tailor transformers to suit designers' electrical and mechanical constraints, including the lowest possible radiated noise figures.
We must point out that audioenthusiast readers will be disappointed unless they're small manufacturers - Avel Linburg make it clear that these transformers are available only as 'relatively short production runs', and not as one-offs. Avel Linburg Ltd, South Ockendon, Essex RM15 5TD, tel 0708-853444.


Do you need an expensive instrument, but only for a short time? Then you could hire rather than buy. Microlease PLC of Forbes House, Whitefriars Estate, Tudor Road, Harrow, Middlesex HA3 5SS (tel 01-427 8822) will lease for as short a period as one week, and offer many instruments including the new Keithley 175 autoranging data-logging DMM with IEEE interface.

The latest data books from Hitachi can now be purchased directly from Hitachi Electronic Components (UK) Ltd, Hitec House, 221/225 Station Road, Harrow, Middlesex HA1 2XL, tel 01-861 1414. The most recent additions to the data books available are the 1984 editions of the microprocessor and memory data books, details of which appear in the new brochure from Hitachi.

The Decorative Lighting Association have issued a warning about an imported nightlight for children which they belive to be potentially dangerous. The Glowlight takes the form of an electrical plug which fits directly into a 13A socket and then gives off light, but there have been cases where the plastic cover has become detached and exposed the live mains connections. Anyone who has such a
nightlight and has any doubts about its safety should contact the Director of the Assocation on 05884658.

Coutant Electronics Limited, Kingsley Avenue, Ilfracombe, Devon EX34 8ES (tel 0271-63781) have published a short-form catalogue of switched mode, hybrid, linear, DC-input and lab PSUs.


# Handy New Iron 

Anew miniature soldering iron, the Oryx M3, has been introduced by Greenwood Electronics. Developed for light production applications but equally suited to the hobbyist market, it is rated at 17 watts and has a normal operating temperature of $380^{\circ} \mathrm{C}$. It has been ergonomically designed
and is perfectly balanced to give the correct 'feel' to experienced operators.
Supplied complete with a replaceable push-on tip and stainless steel storage hook, this new Oryx iron is available in 12V, 110V and $210 / 240 \mathrm{~V}$ versions and the 12 volt iron is fitted with a cigarlighter plug for mobile work. Greenwood Electronics, Portman Road, Reading, Berks. RG3 1NE, tel 0734-595844.


## New <br> Oscilloscopes

evell Electronics have released details of two new dual channel oscilloscopes that they supply. The HM204-2 has a bandwidth of DC to $20 \mathrm{MHz}(-3 \mathrm{~dB})$ and the HM605 (pictured) has a bandwidth of DC to $60 \mathrm{MHz}(-3 \mathrm{~dB})$.
These multi-function oscilloscopes have sensitivities of $1 \mathrm{mV} / \mathrm{cm}$ to $50 \mathrm{~V} / \mathrm{cm}$ with a signal delay line built in so that the trigger edge of a waveform can be viewed. A variable sweep delay from 100 ns to 1 is enables detailed
signal analysis by expanding any section of a waveform. The sweep range is variable from $10 \mathrm{~ns} / \mathrm{cm}$ (including $\mathbf{x 1 0}$ magnification) to $1.25 \mathrm{~s} / \mathrm{cm}$ for HM204-2 and from $\mathbf{5 n s} / \mathrm{cm}$ to $\mathbf{2 . 5 s} / \mathrm{cm}$ for HM605.

Both oscilloscopes have a built in component tester for checking electronic components individually or in circuit and a $1 \mathbf{k H z} / 1 \mathrm{MHz}$ square wave calibrator for probe compensation and system checks. A Z-modulation input is also provided.

Levell offer free delivery in the UK and discounts based on mixed total order value. Levell Electronics Ltd, Moxon Street, Barnet, Herts. EN5 5SD, tel 01-449 5028.

## Hullabaloo!

Next time you go to the loo in central London, you could attract some attention! Let us assure readers that this is extremely unlikely to occur, but if one of the new-fangled automatic public conveniences goes wrong with you inside, it will automatically summon a repair man.
This is because the auto-loo has a Dynamic Logic D1230 Microlog located in its technical compartment. This item will detect any faults, and then report the fault via

Semiconductor Supplies have issued a sixteen page catalogue and price list which covers their range of cases, racks, connectors, wiring systems, hand tools, PCB accessories etc. The minimum order charge is $£ 2.00$ and copies of the catalogue are obtainable upon request from Semiconductor Supplies International Lid, Dawson House, 128-130 Carshalton Road, Sutton, Surrey SM1 4RS, tel $01-$ 6431126

Bradford and Ilkley Community College is again running a course for those wishing to take the Radio Amateurs Examination. The course begins in September, enrolment will take place on September 11th, and in addition to the basic course there are also classes for existing
the ordinary telephone network to a master station. The master station logs the call from this and other loos in the area, and presents information to operators in plain English. The master station is also capable, if required, of ringing up the service engineer directly, and, with additional equipment installed, it can describe the fault and location to the engineer.
Let us assure you that if you're sitting in an auto-loo with the door jammed and waiting for the engineer to arrive, it should take the engineer no more than four hours to reach you!
'B' licence holders and for licensees who wish to gain a more indepth knowledge of radio topics. Contact P. Nurse, Department of Electrical \& Electronic Engineering, Bradford \& IIkley Community College, Great Horton Road, Bradford, West Yorkshire BD7 1AY, tel 0274-753111.

They're probably not the first to launch a peripheral for the dratted thing, but they're the first we've heard of: Cambridge Systems Technology now make a Centronics interface for the muchmaligned Sinclair QL computer. Contact Cambridge Systems Technology, 30 Regent Street, Cambridge CB2 1DB, tel 0223323302.


## Eagle Test Meter

F agle International have $E$ launched a pocket size $31 / 2$ digit LCD multimeter, model TS 350, which is available through all leading electrical wholesalers at a trade price of $£ 21.95$ (excluding VAT). Pocket size, budget priced analogue multimeters have long been popular and Eagle now feel the time is right to offer a similarly convenient instrument which can offer digital sensitivity and
accuracy.
The TS 350 has 12 mm LCD display, auto zeroing and polarity reversal. There are 13 measuring ranges including $1,000 \mathrm{VDC}, 500 \mathrm{~V}$ AC and 200 mA DC. Mode and range selection is by means of two simple slide switches.

The meter comes complete with shrouded, finger stop type safety probe and as with all Eagle instruments, it is covered by a two year guarantee. Eagle International, Precision Centre, Heather Park Drive, Wembley, Middlesex, HAO 1SU, tel 01-902 8832.

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software will maintain its superiority over all currently available similar programmers. The range of eproms handied has been widened. to include the eproms with lower programming voltage and eproms which can be has been moved to the keyboard. The screen display has been improved to give more information. The screan editing faclifites have also been modified to simplify the data entry.
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|  | 1×43-way | ${ }^{2800}$ |
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# SIMPLE ECHO UNIT 

## Are you a bathroom baritone or a cave contralto? Bring the same life to the living room and pzazz to the parlour with this simple echo unit. Design by Phil Walker.

Do you sing in the bath? Go on, admit it! How much better music sounds when there are a lot of hard, reflecting surfaces around to provide plenty of echoes, as you will know if you if you have ever listened to music in a concert hall. But when you move from the concert hall or bathroom to a small room which has thick curtains and soft, upholstered furniture all of that rich, reverberant sound is lost. If there is an echo in such surroundings, it is unlikely to be from the opposite wall which will provide a single, strong echo rather than the multiple echoes which add so much to the sound.

The ETI EZEKO (Easy-Echo, geddit?) is designed to help you recapture some of that life and richness when singing or playing in acoustically dead surroundings. It's a simple mechanical (spring-line) echo unit which operates from a nine-volt battery and provides a variable depth effect. It is designed to be used with a suit-
able microphone and amplifier and is equipped with an output level control. To make things even simpler there is no on-off switch: the unit switches on automatically when a jack plug is inserted into the output socket.

## Circuit

The basic component in this project is the spring line unit. It works as a mechanical delay line. A signal is fed into one of the drive coils which causes the springs to vibrate. A short time later the vibrations reach the other end of the springs and cause a voltage to be induced in the receive coil. In this particular unit there are two springs in parallel which naturally have slightly different delay times. This simulates the natural echo effect where sound would usually be reflected off of more than one surface. In addition, the vibrations in the springs do not traverse the springs once and stop but are


Fig. 1 Circuit diagram of the Ezeko

reflected back and forth many times, decaying slowly in amplitude.

Due to the nature of the device the spring exhibits a very uneven frequency response. This is not
altogether a bad thing but some of the major effects need sorting out. The circuitry we have developed to go with this project works in two stages. The first operates around the drive amplifier to boost

## HOW IT WORKS

This is a simple circuit which can be considered in four main parts. These are: - input amplifier, spring driver, spring output amplifier and output mixer. Of these, the first and third are vitually identical in operation. Each consists of a two transistor direct coupled amplifier whose overall gain is controlled by feedback from the emitter of the output device to the base of the input device. The output from this configuration is approximately equal to the voltage generated by the input current to the stage flowing through the feedback resistor (R4 or R14) mulitiplied by the ratio of the output transistor collector and emitter resistors (R5/R6 or R15/R16). In the case of the input amplifier the input voltage is converted to an equivalent current by R1. In the case of the spring output amplifier it is virtually a current anyway since the output coil is a fairly high impedance but, being a inductance, the impedance seen by the amplifier circuit is not constant with frequency. To overcome this effect R11 is placed in series with the pick-up coil and has the effect of flattening the frequency response somewhat. It also has the effect of damping the resonant peak caused by the interaction between C12 and the pick-up coil inductance. The small value capacitors C3 and 14 are present to ensure the highfrequency stability of the arrangement.

The second part of this project is the spring driver. This is configured around an IC power amplifier device, the LM 386. This chip was specially deigned with battery operation in mind and needs few external components for normal operation. In this project we are using it to drive into a very
inductive load and a small value resistor is put in series with the spring coil to keep the load impedance from falling too low. C11 prevents the DC component at the output of the chip from appearing across the drive coil. C10 and R8 are normally included where highly reactive loads are being driven to help maintain stability.

The gain of the LM386 is set internally at $\times 20$ but by connecting suitable components across pins 1 and 8 this can be raised to about $x 200$. For the purposes of this project we require the gain to be low up to aboul 200 Hz and then rise. This is accomplished by connecting a suitable value of capacitance across these pins calculated to produce the correct response with the internal resistances on the chip.

The final part of the project is the output mixer. This is a very simple device but it was found necessary to make it active rather than passive to avoid unwanted feedback of signal from the mixer input to the spring driver input. The direct input signal is taken from the master gain control (RV1) to the mixer input via R19 and C18. The echo signal goes via a simple band pass network C15, C16, R17 to the effect control (RV2) and thence via R18 and C17 to the mixer input.

The mixer circuit is a simple common emitter amplifier. Because of the feedback from the collector to the base it has a low impedance which effectively isolates the direct and echo signal paths from each other. The total current flowing in from the two inputs is made to flow through R21 and the resulting voltage appears at the collector of Q5. From here C19 couples it to the output voltage socket.
the higher frequencies relative to the lower frequencies. A single capacitor C8 connected to IC1 does this and gives a response which starts rising at about 200 Hz and levels off again at 2 kHz . A resistor is also included in series with the drive coil to reduce the low frequency loading effect on the amplifier output caused by the falling impedance of the drive coil.

The second stage of equalisation occurs after the output signal has been amplified by the receiving amplifier and consists of a passive shaping network before the echo level control. The combination of this and the driver compensation with the spring line response can never give a flat response overall but the effect is quite audible.

The rest of the circuit consists of a pre-amplifier before the overall level control RV1 and a final mixer circuit to combine the direct and delayed signals into the output signal. An active circuit was used here to reduce the amount of signal leaking back to the input of the spring driver amplifier causing spurious feedback whistles. Fairly extensive decoupling of the power supply lines was found to be necessary for battery operation and this is provided by R7/C4, C6 and R10/C22.

## Construction

The construction of this project is quite straightforward if the usual care is taken over the component polarity and placement. Do note that C13 and C14 are not mounted the same way around as the rest of the capacitors. The orientation of the transistors and IC should be followed carefully. There are spaces for an extra resistor and capacitor near C16 if you wish to alter the frequency response.

If you use the specified case there should be plenty of room for the components and a PP9 battery. In our prototype the PCB was attached to the spring unit which was then wedged and glued into the case with foam rubber backing pieces. If you have time it would be a good idea to work out an alternative which holds the battery more securely but leaves it accessible for replacement. The spring line should be mounted on something to avoid microphony and similar external noises being coupled into the spring.

The wiring to the front panel and spring unit shouild be done

## PROJECT: Ezeko



## PARTS LIST

| RESISTORS ( $1 / 4.45 \%$ carbon film) |  | C8 | $0 \mu 68$ Tantalum bead |
| :---: | :---: | :---: | :---: |
| R1,17 | 10k |  |  |
| R2,12 | 68k | C10,12 | 47 n |
| R3,13 | 33k | C11,20 | $100 \mu 16 \mathrm{~V}$ axial |
| R4,14,24 | 100k |  | electrolytic |
| R5,15 | 3 k 9 | C15 | 330n |
| R6,16 | $1 \mathrm{k0}$ | C16 | 2 n 2 |
| R7,10 | 470 R |  |  |
| R8 | 10R | SEMICONDUCTORS |  |
| R9 | 5 R6 | Q1,2,3,4,5, | BC184L |
| R11,18,19,20,21 | 47k | IC1 | LM386N |
| R22,23 | 3k3 |  |  |
| RV1,2 | 47k log. pot. | MISCELLAN |  |
|  |  | X1 | short spring line |
| CAPACITORS (ceramic or polyester unless stated) |  | SK1 | unit mono $1 / 4$ inch jack |
| C1 | 470n |  | socket |
| C2,5,9,13,19 | $10 \mu 16 \mathrm{~V}$ axial electrolytic | SK2 | mono $1 / 4$ inch jack socket with MAKE |
| C3,14 | 10p |  | contact |
| C4,21 | $1000 \mu 16 \mathrm{~V}$ axial electrolytic | Case | $281 \times 152 \times 80 \mathrm{~mm}$ see Buylines |
| C6 | $2200 \mu$ 16V axial electrolytic | PCB; battery clips; knobs, foam rubber, |  |
| C7,17,18 | 100n | wire etc. |  |

with screened wire and if possible as shown in the diagram. This should avoid earth loops and other unwanted effects. Take care with the connections to SK2 as this is the on/off switch as well as the output socket. The 0 V from the PCB is connected via the screen of the cable to the contact on the socket which will touch the sleeve of the plug. The core of the cable from C19 connects to the tip contact The remaining core of the cable from the + ve rail on the PCB connects directly to the - ve battery clip, so make sure you leave sufficient wire for this. On the specified jack socket there is another contact which is positioned so that it is normally unconnected but which connects to the contact which touches the sleeve of the plug when a jack is inserted. To this extra contact a length of wire must be connected and taken to the -ve battery clip. Thus when the jack plug is inserted the battery is connected to the circuit. When withdrawn the power is turned off.

## BUYLINES

> The spring line unit is available from Maplin Electronic Supplies. The jack socket with the make contact and the case are available from Electrovalue, jack type S2/MNS and case PI-CASE FP4 B. The PCB is available from our $P C B$ service.


Fig. 2 Overlay diagram; note the provision for an extra resistor and capacitor (see "Construction").

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# THE TECHNICA GUIDE TO TH MC68020 <br> <br> One small piece of silicon - or a giant stride in technology: <br> <br> One small piece of silicon - or a giant stride in technology: new 32-bit micro from Motorola has some impressive-looking new 32-bit micro from Motorola has some impressive-looking vital statistics. Phil Walker has been looking at the data to find out vital statistics. Phil Walker has been looking at the data to find out if it lives up to expectations. 

 if it lives up to expectations.}

This monolithic monster is claimed to contain about 200,000 transistors and able to operate at sustained rates of two to three MIPS (million instructions per second) and burst rates of over 8 MIPS. With address and data busses both 32 bits wide this gives it pretty awe-inspiring power.

The MC68120 is the latest addition to the 68000 family which includes the 16 bit MC6800 and the 8 bit MC68008 used in the Sinclair QL As such, it has been designed so that software written for the earliest members of the family will run on it. This is necessary these days as the investment in creating new software is a major consideration when using a new device.

Another feature of great interest to the system designer is that this device can use the currently available support devices in the 68000 family by the neat trick of varying the effective data bus width according to what type of device is being addressed. However, the designer is not limited to the existing support devices and there are at least 'two other devices in the pipeline, the MC68881 floating-point co-processor for


Fig. 1. Signal groups in and out of the MC68020.
high level maths functions and the MC68851 paged memory management unit to take care of allocation and protection of the 4 gigabyte addressing range of the MC68020.

The MC68020 is expected to find applications in computer aided design, personal and business computers, high performance colour graphics systems, telecommunications and robotics. It obviously provides another attack on the mainframe and minicomputer fields although it will probably expand the whole market rather than simply substitute for them.

## The Inside Story

Inside the unusual $13 / 8$ inch square package with its 114 pins there lurks a .375 by .350 inch slab of silicon. In this are the 200,000 or so transistors which make up the microprocessor. To get them on Motorola have used a 2 micron HCMOS process



Fig. 2 Block diagram.
which basically means that everything is small.
On the chip itself things are controlled by the master clock which is specified to run at 16.67 MHz or 60 nanosecond period. The whole circuit dissipates less than 1.5 watts ( pretty cool). All the registers, logic and arithmetic units, program counters, stack pointers and external address and data busses are 32 -bits wide. Because multiplexing has not been used on the external signals, it is the first true 32 bit microprocessor.

The 32 -bit address bus allows access to over $4,000,000,00032$-bit words (I wonder if anyone will ever provide this much memory?). However, the main purpose is to allow each program to access any of the 4 gigaword logical address space but, by using virtual memory techniques, only a small' fraction of the address space need exist as physical memory or other functions.

## Different Levels

The effect of this sort of approach is that there are two operating levels to a machine. The supervisor level takes care of resource allocation such as memory and $1 / O$ devices to each user program. The user level is where a program will run until it tries to make use of a resource which is not allocated to it, not present (memory space or data not physically present) or even not allowed to the user program. At this stage, control reverts to the supervisor mode which either makes the resource available (transfers from mass storage, reallocates memory or allocates the required I/O device etc.) or aborts the user program. In this sort of environment it is possible (at least in theory) to prevent user programs from getting unauthorised access to the operating system. This is very important in machines where many user programs may be running quasi-simultaneously.

The MC68020 is configured in a slightly different way for supervisor and user modes. In fact the main difference is than slightly more information is available in supervisory mode from the extra registers about the processor status interrupt and error handling functions. In the user mode there are eight data registers, a user stack pointer and program counter also 32 -bits wide. There is also a condition code register of which only five bits are useful to the user.
in the supervisory mode there are two more stack

| Mnemonic | Description | Mnemonic | Description |
| :---: | :---: | :---: | :---: |
| ABCD | Add Decimal with Extend | MULS | Signed Multiply |
| ADD | Add | MULU | Unsigned Multiply |
| ADDA | Add Address | NBCD | Negate Decimal with Extend |
| ADDI | Add Immediate | NEG | Negate |
| ADDO | Ada Quick | NEGX | Negate with Extend |
| ADDX | Add with Extend | NOP | No Operation |
| AND | Logical AND | NOT | Logital Complement |
| $\begin{aligned} & A N D I \\ & A S L, A S R \end{aligned}$ | Logical AND Immediate Arthmetic Shift Left and Right | $\begin{aligned} & \mathrm{OR} \\ & \mathrm{OR}, \end{aligned}$ | Logical Inclusive OR Logical OR Immediate |
| $\begin{array}{\|l} \mathrm{Bcc} \\ \mathrm{BCHG} \end{array}$ | Branch Conditionally Test Bu ard Cnange | $\begin{aligned} & \text { PACK } \\ & \text { PEA } \end{aligned}$ | Pack BCD <br> Push Effective Address |
| BCLR | Test Bit and Cleat | RESET | Reset External Devices |
| BFCHG | Test Bit Field and Change | RDL, ROR | Rotate Left and Right |
| BFCLR | Test $\mathrm{Blt}_{11}$ Field and Clear | ROXL, ROXR | Rolate with Extend Left and Right |
| BFEXTS | Signed Bit Field Extract | ATO | Return and Deallocate |
| EFEXTU | Unsigned Bit Field Extract | RTE | Return from Exception |
| BFFFO | Bit $\mathrm{F}_{\text {leld }}$ Find First One | ATM | Return from Module |
| BFINS | $\mathrm{Bit}^{\text {ctield Insert }}$ | RTR | Return and Restore Conditon Codes |
| BFSET | Test Bit Field and Set | RTS | Feturn from Subroutine |
| BFTST | Test Bit Field | SBCD | Subtract Decimal with Extend |
| BRET | Test Bit and Set | Scc | Set Conditionatly |
| BSR | Branch to Subroutine | STOP | Stop |
| BTST | Test Bit | SUB | Subtract |
| CALLM | Call Module | SUBI | Subtract immediate |
| CAS | Compare and Swap Operands | Subo | Subtract Quick |
| CAS2 | Compare and Swap Dual Operands | subx | Subtract with Extend |
| CHK | Check Register Against Bound | SWAP | Swap Register Words |
| CHK2 | Check Register Aganst Upder and Lower Bounds |  | Test Operand and Set |
| CLR | Clear | trapce | Trad Conditrnally |
| CMP | Compare | TRAPCC <br> TRAPV | Trap on Overflow |
| CMPA CMP | Compare Address | TST | Test Operand |
| CMPM CMP2 | Compare Memory to Memory Compare Register Aganst Upper and | UNLK UNPK | Unlink <br> Unoack BCD |
| CMP2 | Compare Register Aganst Upper and Lower Bounds | COPROCESSOR INSTRUCTIDNS |  |
| DBce DIVS, DIVSL DIVU, OIVUL | Test Condtion. Decrement and Branch Signed Divide Unsigned Divide | cpBcc <br> cpDBcc <br> cpGEN <br> cpRESTORE <br> cpSAVE <br> cpScc <br> cptraper | Branch Conditionalily <br> Test Coprocessor Condition, <br> Decrement, and Branch <br> Coprocessor General Instruction <br> Restore Internal State of Coprocessor <br> Save internal State of Coprocessor <br> Set Conditionally <br> Trap Conditionally |
| EOR | Logical Exclusive OR |  |  |
| EOR | Logical Exclusive OR Immediate |  |  |
| $\begin{array}{\|l\|l\|} \text { EXG } \\ \text { EXT } \end{array}$ | Exchange Registers <br> Sign Extend |  |  |
| JMP | Jump |  |  |
| JSR | Jump to Subroutine |  |  |
| LEA | Load Effective Address |  |  |
| LINK | Link and Allocate |  |  |
| LSL, LSR | Logical Shift Left and Right |  |  |
| MOVE | Move |  |  |
| movea | Move Address |  |  |
| MOVE CCR | Move Condition Code Register |  |  |
| MOVE SR | Move Status Regrster |  |  |
| MOVE USP | Move User Stack Pointer |  |  |
| MOVEC | Move Controt Register |  |  |
| MOVEM | Move Multiple Registers. |  |  |
| MOVEP | Move Peripheral |  |  |
| MOVEO | Move Quick |  |  |
| MOVES | Move Alternate Address Space |  |  |

Fig. 3 Summary of the instruction set. As you won't be able to get your hands on one of these for a while, we haven't bothered with the machine code . . . (Illustration taken from the data sheet).
pointers, a vector base register, alternate function code registers and two registers associated with the internal instruction cache. The 16 -bit status register available in supervisory mode includes the five-bits of the user condition in addition to a three-bit interrupt priority mask, two-bit trace enable flag and bits which indicate the supervisory/user and master/interrupt conditions.

The instruction cache in the MC68020 is organised as 64 entries of 32 -bits each. It is arranged that where possible the cache will be filled with suitable information so that the next instruction to be executed can be found in it rather than from external memory. There are two reasons that this is desirable, the first is that it takes less time to access the cache memory and the second is that data transfers can occur without being slowed down by the need to fetch instructions. It is basically a way of using otherwise wasted bus time in a useful way.

Some clever programming could be used to make further use of the instruction cache. It should be possible to arrange for small loops within the program to

## FEATURE : MC68020 Guide



USER PROGRAMMING MODEL

data registers

ADORESS REGISTER

USER STACK POINTER PROGRAM COUNTER

CONDITION CODE REGISTER

Fig. 5 Key to the parts of the die: Program Counter Execution Unit - calculates instruction addresses and maintains instruction stream pointers; Address Execution Unit calculates operand addresses and stores user visible address register set; $\mu$ ROM - provides sequence control for the bus controller and micromachine; nROM - controls the operation of the micromachine; A1 PLA - provides initial decode of instructions, determines legality and provides initial microaddress; A2/A3 PLA - generates successive microaddresses associated with the instruction decode; A5/A6 PLA - decodes coprocessor operations; Tag Cache - contains the instruction tag information which includes the address and validity bit; Bus Controller - manages the cache and memory accesses; Data Execution Unit - where all the data operations are performed, contains the user visible data register set, a barrel shifter and elements of the instruction pipe; Data Cache - stores instructions, not data at all; Control Section - provides overall control.

STATUS REGISTER


Fig. 4 The registers.
Fig. 6 Detail of the status register.
be entirely contained in the cache, so that the microprocessor will loop round for however many cycles are necessary without having to fetch any new instructions whilst it was in the loop.

## Ins And Outs

The MC68020 claims 18 addressing modes and 7 data types. The address modes are shown in Fig. 8. They are made up of nine basic types with the option of modifying some types to provide extra flexibility. There is great flexibility in the way that the 16 address and data register may be used to access memory as base and index registers.

Data types are quite numerous as already mentioned. These vary from bits to quad words ( 64 bits long) and include bit fields of 1 to 32 bits, BCD digits ( 1 or 2 digits/byte) and integers of $8,16,32$ and 64 bits long. Also operations on certain other types such as addresses and status words is possible.

As you can see from the summary of the instruction set there are most of the instructions you would expect of any processor system as well as a variety of test and branch and some less usual ones. All the instructions from previous members of the 6800 family are present with some additions and extensions to take account of the 32-bit capability and other enhancements. A useful feature for the large system is that upper and lower bounds checking is offered as opposed to the upper bound bound only in the MC68000 family to date.

## Exceptions To The Rule

There is a very powerful set of actions implemented in this device known as exceptions. These act rather like the interrupts of the normal eight-bit devices but can be generated in many more ways. Also, in many cases, the action taken when an exception is flagged is under the control of the user.

Exceptions can be generated either internally or externally. The external ones are the interrupts, bus error signal and reset request. The interrupt signals operate much as would be expected with the priority being flagged to the processor and a vector number being read in to define a particular action from the interrupting device. The bus error exception is used to prevent a failure of the data bus handshaking signals with any peripheral from locking up the processor indefinitely. To implement this, some form of timer is required on the bus control signals to detect the failure. The reset request input starts the system reset sequence.

Internal exceptions are quite numerous and have been expanded from the MC68000 set. They are generated by trace mode after each instruction, and by various conditional and unconditional trap and boundary checking operations. Also any errors in the


Fig. 7 Package mechanical details.


Fig. 8 Addressing modes (Illustration taken from the data sheet).
address range, co-processor or illegal instructions including divide by 0 will generate an exception.

Once an exception has been recognised, the processor is put into supervisor mode and after a few more operations to keep things tidy, control is passed to one of the exception handling routines. The routine is selected using a combination of the vector number associated with the exception being processed and the current contents of the vector base register.

A very useful feature of the MC68020 for system development is that hardware which is not yet developed or available can be emulated in software. When the non-existent hardware is accessed an error is flagged and the processor jumps to a section of software which stimulates the required hardware. After this is executed, the processor resumes operation as normal.

## Reflections

Starting at around 'less than $\$ 500$ ' it will probably be some time before this device is available outside the industrial and military markets and even when it is it will take a lot of money and nerve to sell a unit based on it in the consumer field. It will probably follow the usual price pattern of new semiconductor devices and fall to about a tenth (or so) of its launch price in five years. The speed and processing capabilities of this device and the market built up by previous members of the family will probably help it to carve out a large slice of the professional market in the future.

Initially I was going to try and compare this device to the typical 8 -bit processors familiar to many of our readers but I did not feel that this would be too useful. The address capability of the MC68020 is vast, if each of the 65536 addresses of a typical 8-bit held 65536 bytes of data this is still only a quarter of the MC68020 capability. However, the basic capabilities of the 8 -bit device are still there but very much enhanced and operating at a much higher speed. ETI
(1) Dawne Instruments \& Electronics

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# DIGITAL CASSETTE DECK 

## Why are our readers so impatient? Here is Bob Campell with the details of the project you've all been ringing, telexing, writing and generally pestering us about.



Construction is very straightforward as long as the PCB design is adhered to, but beware those of you that do not. The noise that can be induced, particularly in the read amplifier, through poor circuit lay-out must be heard to be believed. The extensive use of the earth or ground plane is a great help in alleviating this problem, but probably the most important feature is the single earth point.
Any configuration which has more than one route to mains earth

The PCB on the deck onto which you must solder SK4; the pin connections are as follows: a (left-most pin in this photo) record solenoid; b, play solenoid; $\mathbf{c}$, motor solenoid; $\mathbf{d}$, fast forward solenoid; $e$, rewind solenoid; $\mathfrak{f}$, motor; $g+12 \mathrm{~V}$; $\mathrm{h}, \mathrm{i}$, record protection contacts; $\mathfrak{j}$, not used.
will cause what is known as a 'hum loop'. Hi-fi enthusiasts, perhaps, are more familiar with this effect than the rest of us, but in effect the loop causes a 50 Hz
mains hum to appear at the input to the amplifier and with something like a 90 dB amplification.

Fig. 1, last month shows how to connect the system up; the



Fig. 7 Overlay diagram of the PCB
second power supply should be left floating and the screens on the signal lines should be connected at one end only, the computer end. It is also important to connect the metal frame work of the tape
deck to the control board ground by only one route.

The PCB is relatively simple to make, all the usual rules apply. Through board links are shown. After assembly, check all tracks for

## PARTS LIST


shorts, etc, etc. Although sockets can be used for the amplifier chips it is not really recommended, as they can be another source of noise. Also, with the Darlington drivers, mounting them directly on the PCB will improve the heat dissipation.

Setting up the system mainly consists of adjusting the two potentiometers RV1 and RV2. RV1 is the output volume control, in effect, and should be adjusted as such, ie as one would with your normal cassette recorder. The other pot, RV2, in a roughly similar way, is equivalent to the recording level control. This should be adjusted to give an output from IC6a. Listen to the output with a small speaker or a small piezo transducer and a capacitor to $A C$ couple it to the output of IC6a. The actual recording level is controlled by the two resistors R28 and R31. These control the current through the record head. The value used in the prototype, 10k, worked well giving adequate enough tape saturation without significant distortion, and was also sufficient to obviate the need to use the erase circuit.

The setting up of the EOT circuit is adequately covered in that section. It is worthwhile spending some time setting this up properly as proper automatic control of the tape deck is impossible without it.


Fig. 8 The timing considerations that must be built into the program for the deck.

## Programming Considerations

Although Table 1 last month lists all the required solenoid operations required to select any function, it is not sufficient just to switch them on or off as and when needed. Each solenoid has finite take-up and release times and these must be allowed for. Indeed, several conditions could develop excessive tension in the tape causing it to stretch or even break. The following conditions must therefore be avoided:

1) From STOP to CUE or REV : to ensure that the head is in contact with the tape before tape movement occurs, allow 500 msec between selecting pause or play and FFWD or REW (see Fig 8);
2) From STOP to REC/PLAY or REC/PAUSE to STOP : to ensure that no solenoid switching noises are recorded allow 400 msec between activating the REC and PLAY solenoids and release the PLAY solenoid at least 100 msec before the REC (see Fig. 8b);
3) From PAUSE to FF or REV: when selecting either FFWD or


## BUYLINES

REV from a PAUSE allow 100 msec from deselecting PAUSE to selecting FFWD or REV or else the tape may still be trapped between the pinch wheel and the drive shaft, thus causing excessive tension in the tape;
4) From FFWD or CUE to REV or REW (or vice versa): at least 100 msec should be allowed between selecting FFWD or CUE from REW or REV, or the inertia of the free running tape may cause the tape to stretch or snap.

Two items here could cause problems, both for the same reason; IC3 and 4 and RLA1 and 2 are both RS types; IC3 and 4 are RS part number 301-606 and RLA 1 and 2 are 346-845. If you can't find someone who will obtain RS components for you locally, try Crewe Allan \& Co, 51 Scrutton Street, London EC2. In both cases, alternative types should work although the odd change to component values or PCB-hacking may be required. The PCB is, as ever, available from us.

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ETI OCTOBER 1984


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# THE SOUND OF VIDEO 

# Getting high-quality stereo sound from a video cassette recorder may seem like magic - but it's been done. Vivian Chapel tells all. . . 

Hey Presto! We gasp in astonishment as the stage conjuror pulls bouquets, pot plants and nosegays without limit from a small box resting on a tiny support in the centre of the stage. There is seemingly no way in to the box, and no way it could possibly contain all the things coming out from it. Yet there is a logical explanation.

The domestic video cassette is much like the conjurour's box. It can contain several hours of video programme requiring millions of bits of information per second plus full colour, plus sound. Packing all that in requires quite a few technical tricks and some sleight-of-hand. For a start, the slant-azimuth trick allows recorded tracks to adjoin each other without gaps and with no cross-talk

The low-frequency colour signal is unaffected by slant-azimuth, so to avoid colour cross-talk, the phase of successive lines of colour signal are phase shifted by $90^{\circ}$ during recording and corrected to the original at playback Any crosstalk that appears is thereby displaced by $180^{\circ}$ and so is cancelled.

The main trick is that of helical scan whereby a tilted rotating head-drum lays diagonal tracks across the tape. This results in a writing speed of $4.85 \mathrm{~m} / \mathrm{s}$ for VHS and $5.85 \mathrm{~m} / \mathrm{s}$ for Beta, yet the actual tape speed through the recorder is only about one inch per second.

As the highest frequency recordable is proportional to the writing speed, such speeds permit the high video frequencies to be recorded. But there is a snag. Most of the tape width is taken up by the diagonal video tracks, which means there is little room for the 'poor relation' of television, the sound channel. Hitherto, this has been accommodated by a linear track along the top edge of the tape, and the writing speed is the same as the tape speed which is half that of the compact audio cassette, giving lo-fi sound.

This is too bad if the sound track carries only speech, but for music, the results are poor. Mind you, the sound circuits and speaker of the average TV set
do not encourage any effort to produce better sound, but nonetheless it has been a glaring deficiency in the home video recorder.

Evidence of an interest in better video sound has been seen in the marketing of video recorders and pre-recorded tapes with stereo. The existing linear sound track is split into two separate ones. To avoid cross-talk, a margin or guard-band must be left between them, reducing the track width to less than half that of the single mono track, to 0.35 mm . Comparing this with the 0.6 mm width of the stereo compact audio cassette reveals a further cause of deterioration of sound quality, because noise level and the incidence of drop-out increases as the track-width decreases.

Something obviously has to be done, so the rival technical wizards at Sony and JVC dusted off their crystal calculators and closetted themselves in their inner sanctums, from whence snatches of weird incantations such as "magnetic depth multiplex" could occasionally be heard. Finally they emerged still mumbling mysterious mystical mouthings into their ancient white beards, then with a wave of their magnetic screwdrivers, yet another very large bouquet appeared from the conjurers black box. No less than full stereo hi-fi, the like of which you have never heard from even the highest grade audio recorder, and this without taking up a single extra millimetre of tape space.

Sony was first with Beta hi-fi which they demonstrated at the Chicago Consumer Electronics Show in 1982, and again the following year at Las Vegas. After this, both machines and pre-recorded tapes were released in Japan and America.

## How It's Done

Before we can understand just where they put the sound channels we must take a look at the video spectrum as it is recorded. The luminance, or black-

Nice, but not cheap: the very latest Sony Beta hi-fi, the SL-HF 100 UB retails at a cool £599.95 or thereabouts.



Fig. 1 Tracks on videotape. video tracks (shaded) on top of audio tracks (white). Azimuth differences shown by diagonal lines across the tracks. Audio (white) tracks are not actually wider than video but shown thus for clarity.
and-white signal is frequency modulated on a carrier at around 4 MHz with a deviation of 0.5 MHz either side of the centre frequency. With the VHS system, the FM signal occupies the space between 3.8 MHz and 4.8 MHz as shown in the illustration. Colour or chrominance information requires less bandwidth, so this is down-converted from the 4.43 MHz of the PAL colour carrier to 627 kHz . With the Beta format the luminance signal deviation is from 3.8 MHz to 5.2 MHz and the colour, 688 kHz .

Although the luminance deviation extends down to just 3.8 MHz in both formats, the sidebands continue on down with diminishing amplitude to the upper chrominance sidebands.

America and Japan use the NTSC (never twice same colour - Ed.) colour system which is simpler than the European PAL and SECAM systems. Also the field rate is higher, 60 per second instead of our 50, which means that the head drum must rotate faster as one frame of two fields must be recorded at each revolution. Hence the writing speed is faster. Yet there are fewer scanning lines in each frame, 525 to the European 625.

All this means that the lower luminance and upper chrominance sidebands do not meet for most of the time, on average picture content. This gap then provides the secret 'pocket' in which to conceal the sound channels. These are modulated as FM signals on a pair of carriers between 1 MHz and 2 MHz , and are fed to the rotating video heads along with the luminance and colour information.

The result is superb sound in stero with no increase in tape width or speed. However, there have been reports that very loud sound signals can affect the picture as the sound FM sidebands overlap those of the luminance signal.

Unfortunately, this system will not work with our PAL, 625 -line, 50 -field video standard. The spectrum is already tightly packed and there just isn't a gap; the luminance sidebands, although of reduced amplitude at this point, extend right down to those of the colour signal for most of the time.

So, Tommy Cooper-style, if the trick won't work one way you try another. VHS manufacturers, with their sights on Europe as well as America and Japan, produced a system that would work with any of the existing TV standards, and it was announced in July 1983. Sony also wanted to tap the European market, so in September of the same year revealed their answer at Berlin Radio Show.

It turned out that both were similar, but while the VHS method is the same for all standards anywhere in


Fig. 2 Depth Multiplex recording. Wider gapped audio head lays track deep into tape coating. Smaller gapped video head lays surface track which partially erases audio, thus producing two-layer recording.
the world, Beta is stuck with their American system there, but has another quite different one for Europe. Not that that will affect users, because the different TV standards makes tapes incompatible anyway.

## The VHS System

As with the original Beta system, the two audio channels are FM modulated on to a pair of carriers in the region between the upper colour and lower luminance sidebands. The centre frequencies are 1.4 MHz and 1.8 MHz . These, especially the latter, would overlap considerably the lower luminance frequenices and create picture disturbance if fed to the video heads and recorded along with the video.

Instead, a pair of separate audio-channel heads are used on the video drum to produce what is known as a depth multilplex recording. Before we describe how that works, to avoid any possible confusion it should be noted that using a pair of heads does not mean that each head takes one audio channel. A pair are also needed for the video signal so that as one is finishing its stroke and leaving the top of the tape, the other is just commencing its track at the bottom.

The same applies to the audio channel, two heads are required to provide a continuous recording, Both carriers are fed to both heads in turn to make their stroke.

What happens now is like over-filling a suitcase and then sitting on the lid to fasten it! When a magnetic field is set up across the poles of a magnet, the distribution of the lines of force extend outward in a roughly hemi-spherical configuration. It follows that the radius of the hemisphere, hence the distance from the poles depends on the spacing between them. The farther apart they are, the greater the radius, providing all other factors are the same.

A magnetic recording head is designed so that the poles of the electromagnet appear either side of the head gap which is a vertical slit. As the gap is filled with non-magnetic material, the field extends outward across the gap, hence through the tape which is in intimate contact with it. A narrow gap therefore produces a small, though intense field, while a broad gap gives rise to a larger, less intense field.

The audio channel has a large gap, so when it passes over the tape, it magnetizes deep into the coating. It is followed by the video head which has a much smaller gap, and so records only into the surface regions. At the same time it erases the audio information just put there by the previous head. The result is a two-layer recording; the audio signal at the bottom and video on top.

To produce the required flux density over this larger area, a larger magnetizing current is needed through the head windings, but this can be easily be arranged. Some writers, when describing depth multiplex recording, attribute the difference in depth of magnetic field to frequency, asserting that the video signal is recorded on the surface solely because it is of a higher frequency. This is not the case, as the lower frequency colour signal is included with the luminance and recorded by the video heads in the upper layers of the coating. The colour signal is two octaves below the upper sound carrier, these are 627 kHz and 1.8 MHz respectively.

It is true that with audio recorders there can be a variation of the depth of magnetization with frequency, and this effect was made use of with the two-layer ferrous/chrome tapes. With these, the high frequencies were recorded mainly in the upper chrome layer, and the lower ones in the ferrous coating underneath.

The external field across a gap tends to contract when the gap width is greater than one half wavelength of the recorded sound, this being due to self-erasure. For a recording head gap of 5 microns and a recording tape speed of $1 \%$ in/s the effect begins at around 5 kHz

However, with the VHS video system, the head gap is 0.3 microns and the tape writing speed is 4.85 metres per second. This puts the half-wavelength frequency of the gap at about 8 MHz which is above the upper sidebands of the luminance signal. As all recorded frequencies are below this half-wavelength point, frequency difference plays little part in the depth multiplexing.


Fig. 3 Frequency spectrum of VHS system. Two hi-fi audio carriers occupy same space as lower luminance sidebands. Slant-azimuth recording by separate heads prevent interaction.

## On Playback

Having then produced this two-layer recording, how are the signals sorted out at playback? Wouldn't there still be interaction between them? No, because by physically separating the signals on the tape, another trick mentioned earlier can be applied, that of slant azimuth.

The slit in an audio head is vertical and therefore records a series of vertical magnetic stripes along the tape. If the slit in any head replaying the tape is inclined from the vertical, then it bridges across the adjacent stripes, resulting in an effective increase in the width of the slit. For the narrowest stripes representing the short-wavelength high frequencies, the simultaneous appearance across the gap of adjacent opposite-polarity gives cancellation and zero output from the head. With wide stripes which are low recorded frequencies, the effect is minimal. So, a difference in the tilt or azimuth between recording and playback heads produces a loss of high frequencies, the greater the difference, the lower the frequency at which the cancellation commences.

While audio recorders adopt a vertical slit as a con-
venient standard, it doesn't have to be vertical. Any angle would perform just as well, providing both recording and playback heads are the same. This incidentally explains why a tape recorded on one recorder, and which sounds perfect played back on that machine, sounds lacking in treble when played back on another instrument. One has a non-vertical azimuth, but it could be either.

Coming back to the video recorder, adjacent video tracks are recorded by the two video heads which are offset in inclination from each other. Should one wander to an adjacent track during replay the azimuth difference is such as to produce very little output from it, so cross-talk is reduced to a minimum.

This same principle is used for the audio channels. Each audio head has an azimuth difference of $30^{\circ}$ from the video head which follows it. So the audio track has a corresponding difference from the video track overlapping it. This is quite a large azimuth difference, the video heads are only $12^{\circ}$ from each other.


Fig. 4 Principle of slant azimuth. Head records magnetic stripes at the same angle as its gap, (a). Head with opposite tilt (b), has gap which straddles recorded stripes and gives zero output when they are narrow.

When the tape is played back then, the video and audio signals do not interfere. Hey presto!, the trick is complete hi-fi stereo has been literally buried in the tape, unsuspected until drawn out on command. And what hi-fi! a frequency response from $20 \mathrm{~Hz}-20 \mathrm{kHz}$, dynamic range greater than 80 dB , harmonic distortion less than $0.3 \%$, and wow and flutter less than $0.005 \%$.

It should be noted though that pre-recorded tapes with hi-fi sound will also have the conventional linear sound track, so that owners of non hi-fi machines will be able to play them. This applies both to VHS and Beta.

## Sound-Only Recorders

Helical scan obviously has much to offer the hi-fi enthusiasts. Musical recordings made on one of these video recorders when linked to a hi-fi amplifier and speakers sound much better than even top flight audio recorders.

The writing speed is far higher than necessary and could be reduced without loss of quality in a soundonly machine, giving extended playing times. Furthermore, digital recording which requires a much greater frequency bandwith than analogue, is possible for domestic recorders with modest tape requirements.

A hint of what may lie ahead can be gathered from a prototype helical-scan audio recorder made by Sony. It uses cassettes half the size of compact cassettes, and a linear tape speed of only one-eigth. This allows playing times of some three hours. Recordings are digital, using a 16 -bit code. The main problem is going to be finding enough space on the cassette to write the titles!

ETI

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## AUDIO

## Project

2W Power Amplifier
$50+50$ watt power amplifier module
$50 / 100 \mathrm{~W}$ amplifier modules


100W guitar amplifier
100W MOSFET power amplifier
100W stereo disco consol
150W MOSFET amplifier
200W power amplifier
200W power amplifier
300 W amplifier module
204011 Active Loudspeaker
Active crossover, two or three way
Active loudspeaker
Active loudspeaker, 2040 II
Amplifier, 2W power
Amplifier, 15 w.p.c. SQ quadrophonic
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| Simple amplifier, 1.5 W |  | Sep | 1974 | 32 |
| Simple bass-reflex cabinet |  | Apr | 1972 | 57 |
| Simple loudhailer |  | Oct | 1973 | 70 |
| Simple loudness control |  | Aug | 1975 | 25 |
| Simple stereo amplifier |  | Mar | 1975 | 26 |
| Sound bender (ring modulator) |  | Oct | 1981 | 88 |
| Sound pressure level meter |  | Feb | 1981 | 74 |
| Spectrum analyser, audio |  | Jun | 1978 | 27 |
| Spring line reverberation unit |  | Dec | 1974 | 46 |
| SQ decoder for quadrophonic systems |  | June | 1974 | 60 |
| Stabilised PSU for hi-fi systems |  | May | 1983 | 18 |
| Stage mixer, 16 into 8 | part 1 | Jul | 1975 | 26 |
|  | part 2 | Sep | 1975 | 33 |
| Stereo Image Co-ordinator |  | Jun | 1980 | 68 |
|  | Errata | Aug | 1980 | 13 |
| Stereo image width enhancer |  | Sep | 1972 | 38 |
|  | Errata | Oct | 1972 | 43 |
| Stereo power meter |  | Mar | 1984 | 35 |
| Stereo rumble filter |  | Jan | 1975 | 52 |
| Stereo Simulator (Short Circuit) |  | Sep | 1977 | 16 |
| Stereo to quadrophonic up-grade |  | Nov | 1974 | 54 |
| Super Stereo - effective width |  | Sep | 1972 | 38 |
| enhancer | Errata | Oct | 1972 | 43 |
| Sweet Sixteen stereo amplifier |  | Jul | 1976 | 38 |
| System 8000 tuner/amplifier | part 1 | Jun | 1979 | 30 |
|  | part 2 | jul | 1979 | 79 |
|  | Errata | Sep | 1979 | 8 |
|  |  | ETI | OBER |  |

Project
System A amplifier

Tape noise limiter
Tape recorder bias optimiser
Three channel tone control
(Short Circuit)
Tone burst generator
Tuner/amplifier, System 8000

TV Sound Tuner
TV Sound Tuner
Upgrading Amplifier PSUs
V3 Loudspeaker
Visual Complex Sound Analyser
Voice-over unit
VU meter, LED
Wattmeter, direct reading, 0-50W
White noise generator, digital

## CLOCKS AND TIMERS

1-2 Hour Timer
Comparator Module for the Digital
Stopwatch
Stopwatch
Digital alarm clock/calendar
Digital Clock
Digital Stopwatch
Digital Stopwatch
Egg Timer
Humane alarm - alarm clock add-on
Long Period Timer, 1 min - 20 hrs
Meter Beater
Micropower Pendulum
Modifying the ETI Digital Alarm Clock
Multi-Option Clock
Musical Alarm Clock
Process controller/timer
Rugby Clock

School Timer
Speaking Clock
STAC Timer
STD Timer
Stopwatch/calculator
Universal Timer
Universal Timer
COMPUTING
16 channel A to D board 64 K DRAM Board
6502 sound/DAC card
Ace keyboard/joystick interface
ADC, ZX81/Spectrum, 8 ch., 8 bit
ASCII keyboard, System 68
A to D board, 16 channel
Atom keypad
Cassette interface
Centronics interface for the Cortex
Centronics interface for the Sharp MZ8aK
Colour board for the Ace
Computer Output Driver
ETI OCTOBER 1984

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| part 2 | Aug | 1981 | 40 |
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| part 1 | Jun | 1979 | 30 |
| part 2 | Jul | 1979 | 79 |
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|  | May | 1980 | 78 |
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Project
Control port for the Spectrum
Cortex 16 -bit computer
DAC/ADC filter amplifier
Digital Cassette Deck
DRAM board, 64 K
DRAM Board,Z80
EPROM Emulator
EPROM Eraser
EPROM board for the Oric/Atmos
EPROM Programmer for the Triton
EPROM Programmer, Universal
EX42 Keyboard Interface
Fast light Pen
Joystick controller for 6502 micro-
computers (Reader's Design)
Low-cost VDU, ETI 560

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Mini-Mynah Speech Synthesiser Board

Multiple Output Port
Music board, ZX81

Numeric keypad for the Atom
Pseudorom
Real time clock/calendar for 6502
systems
Sharp joystick interface
Sound board, ZX (Design
Competition)
Sound/DAC card, 6502
Spectrum control port
Spectrum Joystick Interface
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Speech Synthesis Board
Supply line status check with DVM
Supply protector for $Z \times 81 \mathrm{~s}$
System 68 ASCIl keyboard
System 68 CPU board
System 68 CUTS card
System 68 PSU

System 68 Software
System 68 TTY Interface
System 68 VDU
System 68 VDU interface \& Bus Structure

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| part 2 | Jul | 1977 | 54 |
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| System reset generator for homebuilt computers |  | Feb | 1983 | 83 |
| Tape save modification, ZX80 |  | Oct | 1983 | 63 |
| Tape save modification, ZX81 |  | Feb | 1983 | 61 |
| Temperature sensor \& alarm for computers |  | Feb | 1983 | 86 |
| Time-out generator/system failure alarm |  | Feb | 1983 | 84 |
| Triton personal computer |  | Nov | 1978 | 16 |
| Triton 8 K EPROM Card |  | Jun | 1979 | 73 |
| Typewriter Interface |  | Oct | 1983 | 21 |
|  | Errata | Mar | 1984 | 25 |
| Universal EPROM Programmer | part 1 | Aug | 1983 | 45 |
|  | part 2 | Sep | 1983 | 37 |
|  | Errata | Jan | 1984 | 61 |
|  | Errata | Apr | 1984 | 33 |
| User-defined graphics, ZX81 |  | Mar | 1983 | 23 |
| Vector graphic display for home computers |  | Jan | 1984 | 19 |
| Z80 Control Computer | part 1 | Aug | 1983 | 65 |
|  | part 2 | Sep | 1983 | 59 |
|  | part 3 | Oct | 1983 | 56 |
|  | Errata | Nov | 1983 | 96 |
| Z80 DRAM board |  | Mar | 1984 | 45 |
| ZX80 DRAM upgrade |  | Feb | 1984 | 29 |
| ZX80 save modification |  | Oct | 1983 | 63 |
| ZX81 EPROM Programmer |  | May | 1984 | 26 |
|  | Errata | Sep | 1984 | 68 |
| ZX81 music board | part 1 | Apr | 1983 | 16 |
|  | part 2 | May | 1983 | 54 |
|  | Errata | Jun | 1983 | 15 |
| ZX81 save modification |  | Feb | 1983 | 61 |
| ZX81 user-defined graphics |  | Mar | 1983 | 23 |
| ZX ADC, $8 \mathrm{ch}, .8$ bit |  | Jan | 1983 | 61 |
|  | Errata | Aug | 1983 | 70 |
| ZX-based burglar alarm |  | Dec | 1983 | 31 |
| GAMES |  |  |  |  |
| Alcohometer (reaction fimer) |  | Dec | 1981 | 79 |
| Alien Attack |  | Jun | 1981 | 61 |
| Ambush | part 1 | Apr | 1979 | 61 |
|  | part 2 | May | 1979 | 48 |
| Cannibals and Missionaries |  | Mar | 1976 | 24 |
| Coin Toss |  | Feb | 1980 | 51 |
| Dice |  | Dec | 1979 | 32 |
| Double Die |  | May | 1979 | 26 |
|  | Errata | Jun | 1979 | 9 |
| Drunken Sailor Puzzle |  | Jan | 1978 | 46 |
| Dual Electronic Dice |  | Oct | 1976 | 16 |
| Electronic Decision Maker |  | Mar | 1973 | 62 |
| Electronic Dice |  | Jan | 1976 | 58 |
| Electronic One-Arm Bandit | part 1 | Aug | 1975 | 38 |
|  | part 2 | Sep | 1975 | 48 |
| Electronic Win-dicator |  | May | 1975 | 47 |
| Family Ferry |  | Dec | 1974 | 56 |
| Hammer Throw Game |  | Jan | 1978 | 29 |
| Heads or Tails (Short Circuit) |  | fan | 1977 | 34 |
| 1 Ching Computer |  | Feb | 1982 | 60 |
| Infinite Improbability Detector |  | Mar | 1982 | 35 |
| LED Dice (Short Circuit) |  | Feb | 1977 | 49 |
| LINC |  | Aug | 1975 | 26 |
| Mastermind |  | Jun | 1977 | 41 |
| Obedient Die |  | Mar | 1984 | 54 |
| Pinball Wizard |  | Nov | 1979 | 24 |
| Race Track Game |  | Jan | 1978 | 36 |
| Reaction Tester |  | Jan | 1977 | 20 |
| Reaction Timer |  | Oct | 1979 | 75 |
|  | Errata | Jan | 1980 | 11 |
| Reflex Action |  | May | 1976 | 62 |
| Rifle for the TV Games Unit |  | Jul | 1977 | 20 |
| Roulette Game |  | Feb | 1981 | 22 |
| Skeet Game |  | Nov | 1977 | 34 |
| Sound Track game |  | Aug | 1982 | 72 |
| Space Invasion Game | part 1 | Nov | 1980 | 65 |
|  | part 2 | Dec | 1980 | 44 |
|  | Errata | Dec | 1980 | 13 |

Project
Space Invasion Game--
$\quad$ Modifications
Spirit Level (reaction timer) (Short
$\quad$ Circuit)
Stars and Dots Cames
Superdice
Survival Game
Tank Battle TV Game
Touch Buzzer
TV Chess Game
TV Games Unit
Wheel Of Fortune
LIGHTING

|  | Month | Year | Page |
| :--- | :--- | ---: | ---: |
|  | Jul | 1981 | 94 |
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| Errata | Jun | 1978 | 13 |
|  | Nov | 1980 | 48 |
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| Audio light display using LEDs |  | Aug | 1979 | 87 |
| :---: | :---: | :---: | :---: | :---: |
| Automatic Porch Light |  | Jul | 1980 | 77 |
| Colour Organ sound/light unit |  | Feb | 1975 | 11 |
| Dimmer, 500 W |  | Jun | 1975 | 30 |
| Dimmer, 500W |  | Mar | 1978 | 55 |
| Dimmer for fluorescent lights |  | Nov | 1972 | 42 |
| Dimmer, Programmable Touch |  | Apr | 1980 | 71 |
|  | Errata | Aug | 1980 | 11 |
| Dimmer, push-button |  | Feb | 1975 | 30 |
| Dimmer, stage |  | Mar | 1979 | 50 |
|  | Errata | Apr | 1979 | 13 |
| Dimmer, touch |  | May | 1981 | 79 |
| Disco Lightshow Controller |  | Dec | 1978 | 44 |
|  | Errata | Apr | 1979 | 13 |
| Disco/party Strobe (Finesse) |  | Oct | 1984 | 52 |
| Ecolight |  | Jul | 1984 | 55 |
| Emergency Lighting Unit |  | Oct | 1972 | 41 |
| Finesse light chaser |  | Dec | 1983 | 44 |
| Fluorescent Light Dimmer |  | Nov | 1972 | 42 |
| Fluorescent light inverter |  | Mar | 1973 | 58 |
| High Power Beacon |  | Aug | 1976 | 30 |
| Hi-power Strobe |  | Jun | 1972 | 62 |
| Inverter For Fluorescent Lighting |  | Mar | 1973 | 58 |
| Lampsaver |  | Dec | 1983 | 69 |
| Light chaser ( Finesse) |  | Dec | 1983 | 44 |
| Light Wand |  | Mar | 1982 | 73 |
| Multiswitch - multi-point light switching |  | Nov | 1983 | 47 |
| Porch Light |  | Feb | 1978 | 28 |
| Push Button Dimmer |  | Feb | 1975 | 30 |
| Sound/light unit (ETI Colour Organ) |  | Feb | 1975 | 11 |
| Sound-To Light unit (free PCB project) |  | Oct | 1982 | 31 |
| Spactracolumn |  | Dec | 1982 | 65 |
| Stage Dimmer |  | Mar | 1979 | 50 |
| Stage lighting unit | Errata | Apr | 1979 | 13 |
|  | part 1 | Jan | 1983 | 22 |
|  | part 2 | Feb | 1983 | 34 |
|  | part 3 | Apr | 1983 | 42 |
|  | part 4 | May | 1983 | 79 |
|  | Errata | Aug | 1983 | 70 |
| Strobe, high power |  | Jun | 1972 | 62 |
| Visual Complex Sound Analyser |  | Apr | 1981 | 21 |

MISCELLANEOUS
Allez Cat pest scarer
Autocompass
Auto-lume light operated switch
Automatic Plant Waterer
Battery eliminators, two
Bike Speedometer
CCTV Camera
Coin Collector (metal locator)
Compass, auto
Desoldering Made Simple
Digital Display

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| Digital display module |  | Jan | 1979 | 35 |
| Drill Speed Controller |  | Feb | 1975 | 46 |
| Drill Speed Controller |  | Mar | 1977 | 56 |
| Drill Speed Controller |  | Sep | 1980 | 69 |
| Dry Cell Charger |  | Sep | 1984 | 53 |
| Earth Leakage Circuit Breaker |  | Dec | 1982 | 25 |
| Earth Resistivity Meter |  | Jul | 1973 | 30 |
| Easy Way To Make PC Boards |  | Oct | 1973 | 66 |
| Electromyogram |  | Mar | 1980 | 56 |
| Electronic Doorbell (free PCB project) |  | Oct | 1982 | 29 |
| Engineer's Stethoscope |  | Mar | 1981 | 63 |
| FM Mains Remote Control |  | Oct | 1981 | 56 |
| Garden Watering Systems |  | Jun | 1976 | 26 |
| Gas Monitor |  | Apr | 1978 | 33 |
| GSR Monitor |  | Jul | 1977 | 11 |
| Hear-And-Tell Unit |  | Oct | 1974 | 24 |
| Heartbeat Monitor |  | Aug | 1981 | 31 |
| Heart Rate Monitor |  | Dec | 1976 | 19 |
| Helping Hand (RNID competition winner) |  | May | 1978 | 16 |
| Homes For Ohms (Resistor storage system) |  | Jan | 1973 | 47 |
| Induction Balance Metal Locator |  | Feb | 1977 | 33 |
| Induction Balance Metal Locator |  | Feb | 1978 | 32 |
| Induction loop, portable |  | Jul | 1983 | 52 |
| Infra-red Remote Control |  | May | 1981 | 51 |
| Infra-red Remote Control, ETI IR60 | part 1 | May | 1980 | 33 |
|  | part 2 | Jun | 1980 | 73 |
| Intercom (Using The LM380) |  | Dec | 1974 | 32 |
| Kitchen Scales, digital | Errata | Jan | 1975 | 70 |
|  | part 1 | Jul | 1982 | 30 |
|  | part 2 | Aug | 1982 | 39 |
|  | Errata | Sep | 1982 | 9 |
| Laser, low-cost |  | Mar | 1974 | 34 |
| LCD Panel Meter |  | Mar | 1978 | 26 |
| LED Jewellery |  | Jun | 1981 | 45 |
| LED Pendant |  | Nov | 1977 | 41 |
| Light Activated Switch |  | Nov | 1980 | 81 |
| Light Activated Switch Module |  | Mar | 1981 | 52 |
| Low Battery Warning |  | May | 1975 | 48 |
| Mains-Borne Remote Control | part 1 | Apr | 1984 | 53 |
|  | part 2 | May | 1984 | 37 |
| Mains Seeker |  | Jun | 1979 | 46 |
| Message Panel |  | Oct | 1982 | 53 |
| Message Panel Interface |  | Nov | 1982 | 68 |
| Metal Locator |  | Jul | 1973 | 20 |
| Metal Locator |  | Mar | 1980 | 78 |
|  | Errata | Apr | 1980 | 9 |
|  | Errata | Jun | 1980 | 11 |
| Metal locator, induction balance |  | Feb | 1977 | 33 |
| Metal locator, induction balance |  | Feb | 1978 | 32 |
| Microwave Oven Leakage Detector |  | Nov | 1979 | 85 |
|  | Errata | Dec | 1979 | 13 |
| Mini-Drill Speed Controller |  | Jun | 1981 | 89 |
| Motor Speed Controller |  | Jul | 1979 | 47 |
|  | Errata | Nov | 1979 | 13 |
|  | Errata | Dec | 1979 | 13 |
| Muscle Meter (Electromyogram) |  | Mar | 1980 | 56 |
| Musical Doorbell |  | Dec | 1980 | 60 |
| Negative lon Generator |  | Jun | 1982 | 19 |
| NiCad Battery Charger |  | May | 1974 | 52 |
| NiCad Charger |  | Aug | 1979 | 29 |
| NiCad Charger/regenerator |  | Sep | 1983 | 27 |
| Noiseless Power Switch |  | Mar | 1981 | 13 |
| Optical Communications Circuits |  | Jun | 1976 | 68 |
| Pest Control - Allez Cat |  | Feb | 1982 | 89 |
| Polystyrene Cutter |  | Jul | 1982 | 73 |
| Portable induction loop |  | Jul | 1983 | 52 |
| Power supply, switched mode | part 1 | June | 1983 | 35 |
|  | part 2 | Jul | 1983 | 83 |
| Proximity Switch |  | Oct | 1978 | 75 |
| Rain Alarm |  | Apr | 1978 | 62 |
| Rain Alarm |  | Dec | 1979 | 35 |
| Remote-Controlled Power Switching |  | May | 1981 | 90 |
| Soil Moisture Indicator |  | Aug | 1977 | 19 |
|  | Errata | Sep | 1977 | 8 |
| Soil Moisture Indicator |  | Jul | 1979 | 67 |
| Soldering Iron Controller |  | May | 1981 | 24 |

Project
Super Selective Music Filter
Switched mode power supply
Tape/Slide Synchroniser
Tape/Slide Synchroniser
Telephone Bell Extender
Telephone Bell Shifter/Extender
Teletext System

Torch Finder
Touch Switch
Touch Switch
Touch Switch (free PCB project)
Two Battery Savers
Twonky - MPU Musical Box
Two-Tone Door Bell (Short Circuit)
Typewriter Interface
UFO Detector
UHF Aerial Preamplifier
Ultrasonic Switch
Utiliboard Breadboarding System
Vertical Speed Indicator
Videograph - TV audio display
Watchdog Power Saver
Wind Speed Indicator

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MODEL CONTROL

| FM Radio Control |  | Oct | 1980 | 15 |
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|  | Errata | Dec | 1980 | 13 |
| Model Train Controller |  | Nov | 1976 | 16 |
| Motor Speed Controller |  | Jul | 1979 | 47 |
|  | Errata | Nov | 1979 | 13 |
|  | Errata | Dec | 1979 | 13 |
| Radio Control Servo Failsafe |  | Apr | 1980 | 29 |
| Radio Control Servo Failsafe |  | Aug | 1983 | 61 |
| Radio Control System | part 1 | May | 1979 | 61 |
|  | part 2 | Jun | 1979 | 87 |
|  | Errata | Aug | 1979 | 13 |
| Servo Tester |  | May | 1980 | 52 |
| Slot Car Controller |  | May | 1982 | 79 |
| The Beast Model Train Controller | part 1 | Nov | 1979 | 42 |
|  | part 2 | Dec | 1979 | 86 |
|  | Errata | Feb | 1980 | 17 |
| White Line Follower |  | Apr | 1978 | 23 |

## MOTORING

Accurate Voltage Monitor
Alarm alarm
Alcohometer
Antenna Extender
Anti-theft auto alarm
Auto amp - car audio booster
Automatic Battery Charger
Automatic car theft alarm
Battery charger
Battery Indicator
Bodywork Checker
Brake light warning
Breadown beacon
Car alarm
Car alarm
Car Alarm
Car Alarm
Caravan Lights Checker (Reader's
Design)
Car Immobiliser
Car Security Device
Courtesy light extender

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## Project

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## MUSIC AND EFFECTS

| Accentuated Metronome |  | Feb | 1978 | 17 |
| :---: | :---: | :---: | :---: | :---: |
| Audio phaser |  | Dec | 1976 | 29 |
| Audio visual metronome |  | Nov | 1972 | 47 |
| Autochord rhythm generator | part 1 | Nov | 1978 | 56 |
|  | part 2 | Dec | 1978 | 80 |
| Black Hole Choraliser |  | May | 1980 | 90 |
|  | Errata | Sep | 1980 | 11 |
| Bomb drop sound effect CCD Phaser |  | Apr | 1982 | 50 |
|  |  | May | 1978 | 57 |
|  | Errata | Jul | 1978 | 7 |
| Chorus/Flanger |  | Jan | 1984 | 33 |
| Complex Sound Generator (Minisynth) |  | Oct | 1978 | 17 |
| Drum Machine |  | Apr | 1981 | 75 |
| Drum Synthesiser, ETI Staccato |  | Jun | 1980 | 84 |
|  | Errata | Aug | 1980 | 13 |
| Drum Synthesiser, Midi |  | May | 1984 | 62 |
|  | Errata | Aug | 1984 | 66 |
| Drum Synthesiser, Mini |  | Nov | 1983 | 36 |
|  | Errata | Apr | 1984 | 62 |
| EZEKO spring-line reverberation unit |  | Oct | 1984 | 18 |
| Electronic Bongos (Short Circuit) |  | Aug | 1977 | 24 |
| Fuzz box (Short Circuit |  | Apr | 1977 | 48 |
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Tuning Fork
Vocoder
Waa-Phase Unit
Waa-waa unit
Waveform multiplier (chorus)

PHOTOGRAPHIC
Automatic Contrast Meter
Electronic flash trigger
Enlarger Timer
Exposure meter
Flash Sequencer
Flash sequencer
Flash Trigger
Flash Trigger
Flash Trigger
Photographic process timer
Photo timer
Printimer - 11/2-3 minute timer
Shutter Timer
Slave flash
Sound/light flash trigger
Sound-operated flash
RADIO
Aerial Matcher for SW Receivers
Air Band Converter
Chipmonk FM/AM Radio
Crystal Calibrator
Digital Radio Dial
Headphone Radio, AM
Marker Generator
One-Chip Radio
RF Attenuator
RF Power Meter
Speech Compressor
Star Trek Radio
Tic-tac Radio
Two Metre Power Amplifier
Two Metre VMOS Power Amplifier

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Digital PWM Interface for the

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Alarm extender
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Automatic Car-Theft Alarm
Automatic Light Switch
Automatic Porch Light
Bansheee Siren Unit

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* Moignt Select Push Button
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$\star$ DC Voltage: 200 mV to 1000 V $\star$ Restance: 200 n to $20 \mathrm{M} \Omega$
* Input impedance: 10Mn
$\star$ Display. $31 / 2$ Diglt 13 mm LCD
* O/load Protection: All ranges


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# SPECTRUM CONTRC 

 Give your Spectrum the power to control virtually anything withthis versatile expandable I/O port. Design by Mike Wynne Jones.


Fig. 1 Overlay diagram of the control/PSU board

AIthough designed with no specific control application in mind, this digital I/O system has found a variety of uses including the control of a robot. It is extremely versatile and flexible in its configuration, being expandable from a single 8 -line-in and 8 -line-out board to a huge system with 128 input lines and 128 outputs - enough to control Sir Clive's notorious power stations several times over.

It's flexibility lies in its construction format: if more $1 / O$ lines are required than currently available, one merely builds another I/O board and plugs it into those already present, forming a tower of up to $16 \mathrm{I} / \mathrm{O}$ boards on top of the control board. Connections are made between them via four 8-pin DIL wire-wrap sockets.

The control board (for want of a better name) is the one which plugs into the Spectrum. It provides an edge-connector like the Spectrum's for connecting up other peripherals, an external back-up power supply and a certain amount of address-decoding logic and signal-buffering Each 1/O board has pins to plug into the board beneath, sockets to accept another board above, the I/O logic and two 15 -way D-type input and output connectors.

For flexibility in the Spectrum system, there are two ranges of 1/O map positions with a hardware switch which indicates which position is taken. In order to comply with constraints imposed by the Spectrum hardware, each board must be mapped to an address ( 31 + a multiple of 32 ). This can be done just at face value, taking up most or all of the space in the first page; this space, however, is required for other peripherals such as joystick interfaces (for manoeuvring your robot's arm) and analogue interfaces. By adjusting SW101, a switch whose position sets the valid state of A14, the address can be changed to $(168384+31+$ a multiple of
32). It is the number on lines BOB3 which indicates what that number is to be for a particular I/O


If the computer's supply is connected and the external one is not, the boards should still be allowed to function, drawing power from the computer. But if the external supply is connected and the computer's is not, they must not receive power this would allow ICs on the I/O boards to apply voltages to chips in the computer which otherwise have no power connected, possibly resulting in considerable damage. The I/O boards must only receive power from the external source when both sources are operational - at all other times they can be connected to the computer's +5 V rail. This action is achieved by the components associated with RLA 101 on the control board.

## Construction

It is strongly recommended that our double sided circuit boards should be used as it is extremely difficult to make the boards, especially where an edge connector is involved, and the four DIL socket connectors must be positioned precisely the correct distance apart.

Begin assembling the control board by cutting the pins of the edge connector to about half an inch and then bend them towards each other in pairs across the connector (Fig. 2). Insert the small edge connector mounting board between them and solder it in place at each pin. Now make the connections between this and the main board using thin flexible wire. The purpose of this arrangement is to prevent vibrations from disturbing the connections, and to allow flexibility so that the mis-
match in height between the computer and the external circuit boad does not matter.

DC unregulated source, capable of supplying 1.2A maximum. It is terminated with a 3.5 mm jack plug, wired so that the tip is positive.


Fig. 2 (left) Making a flexible connector.
Fig. 3 (above) Mounting details of alternative relays.

| RESISTORS |  |
| :---: | :---: |
| R101-104 | 1 k0 1/4W 5\% |
| CAPACITORS |  |
| C101 | 10u16V electrolytic |
| C102-105 | 100n ceramic disc |
| SEMICONDUCTORS |  |
| IC101 | 7805 |
| IC102 | 74LS367 |
| IC103 | 74LS27 |
| IC104 | 74LS133 |
| Q101 | BC182L |
| D101 | 1N4001 |
| MISCELLANEOUS |  |
| SW101 <br> RLA101 | SP C/O DIL switch <br> 6 V two-pole C/O |
|  | miniature relay (see text and Buylines) |
| SK101-104 | 8 pin standard or low profile DIL socket |
| SK105 | 3.5 mm jack socket |
| SK106 | 28 way doublesided edge connector, keyway at position 5, to suit Spectrum |
| PCB; M3 nut and bolt for IC101; $2 \times 16$ pin, $1 \times 14$ pin and $1 \times 20$ pin DIL sockets for ICs and RLA101; four off rubber feet; perspex sheet; nuts and bolts to secure perspex; 28 way Veroboard strip for edge connector. |  |

PARTS LIST CONTROL/PSU BOARD

The external power source is connected via jack socket SK105, and is regulated to +5 V by IC101, a 7805 voltage regulator. C101 and C102 form part of the regut lator's stabilising cicuitry.

The switching action between sources is done by RLA101. When it is in its normally closed state (ie the coil is not energised), the +5 V rail is connected to that of the computer. This occurs when the computer's supplyis absent (Q101 is switched off), or when the external supply is absent (relay coil receives no power), or both. However, if both sources are connected properly, Q1 switches on and the relay coil is energised. This allows the $1 / O$ equipment to take its power from the external source.

When RLA101 is switched off, the magnetic field in the coildecays, causing a large back-EMF. D101 shorts this potential, preventing damage to Q101.

Signals RD, WR and A5-8 are not required by the control logic. However, they are monitored by several gates on the I/O boards. As the fanout of the gates driving these lines in the computer may not be adequate, they are buffered by IC2. B0-3 are all tied to 0 V on the control board, as this is the number passed to the first I/O board.

The general enable line from the control board to the the $1 / O$ boards is SEL(ECTED); general because it indicates that one of them is addressed, but not which one. It is produced by IC4, whose inputs must all be high to produce a low (active) output. Thus, A0-A4 must all be high (necessary to avoid interference with other hardware already in the Spectrum system), and so must the outputs of IC103 $\mathrm{a}, \mathrm{b}$ and c . These are three-input NOR gates, and their inputs must all be low to produce high signals at the corresponding inputs of IC104: A9-13 and A15 must all be low as set out in the addressing requirements, and IORQ (standing for $\ln /$ Out Request (bar)) to indicate that the operation taking place is a valid //O operation rather than a memory operation. The valid state of A14 is set by the switch, SW101. It is set depending on whether we wish the system to be mapped from 31 onwards or from $16384+31$ (ie 16415) onwards. If the switch is set to " HI ", IC104 detects a high signal, and the input of IC103a is pulled low by resistor R102. If it is set to "LO", R104 pulls the input of IC4 high, and the input of IC103a, pin 9, detects a low signal on A14.

Thus when the correct general address is present on the bus (ie not including A5 - AB), and IORQ is low, SEL goes low, signalling to the 1/O boards that if A5-AB are correct for a particular board, that board should then activate.

The data bus signals are not required by the control board, so they pass over it to the stack of $1 / O$ boards.

Capacitors C103-105, are physically close to the logic ICs to increase stability in the circuitry, as recommen-


Fig. 4 Circuit diagtram of the control/PSU section.
ded by the manufacturers.
Turning to the I/O boards, IC1 is a quad exclusive NOR package. These have outputs which are low when the inputs are different ( $\mathrm{L}-\mathrm{H}$ or $\mathrm{H}-\mathrm{D}$ ) and high when the inputs are the same ( H Hor L-L
Each of the gates in IC1 has an open collector output. This means that it can only drive the output low if required the high level is being established by R1. If any of the address inputs BA5 -7 do not match their corresponding Bo - 3 inputs then the output of that section of IC1 will go low. This will drive all the other outputs low as well and thus the input to IC3b will be low. This is known as a "Wired AND" connection since it acts just like a 4 input AND gate without an actual device being used.

B0,1,2,3 (the $B$ is merely an arbitrary letter) indicate what number should be present on A5-AB for a particular 1/O board to activate. A5 is compared with B0, A6 with B1 and so on, by IC1a-d. For a particular board to be
activated SEL must be low, and so must the output of IC3b on that board. These two lines are monitored by IC2a, giving a high output and thus a low output on IC3a when conditions for that board to function are fulfilled.

The number passed to board one on the B-bus is 0000,0001 to board two etc., so to make the order in which the boards are plugged in irrelevant, each board must add one to the number passed to it from below before passing it on to the board above. This is implemented by IC4 - a four-bit adder with 0000 as one input and the carry input high.

The low output from IC3a is pro cessed with RD by IC2b, giving a high output to activate the input chip. It is, however, activated by a low signal, so inverter IC2C is included in the signal path to cause the necessary inversion when the input chip is activated, applying the input data to the data bus. Its internal latches are disabled through D1 to prevent the data from changing during read should external hardware

## PROJECT: Spectrum Control



Fig. 5 The I/O board circuit.
attempt to cause this to happen. The data on the data bus is thus read by the microprocessor. The latches in IC5 can be disabled by external cicuitry: inputs are ignored, and only the previous data read. This action is very useful if the inputs are connected directly to the data bus of another computer, and is caused by pulling the EXTERNAL LATCH DISABLE connection low.
For ouput (a write instruction) the ouput of IC2d goes high, clocking the D-type flip-flops in IC6, and thus mow ing the data from the data bus onto the output lines. The output can be made high-impedance (again useful if they are to be connected to the data bus of another computer), by pulling OUTPUT DISABLE high.

Next make connections, using single strand insulated wire, between the row of holes and the strips between the edge connectors again as shown in the overlay diagram. The rows of holes for connections are staggered to prevent the introduction of weakness into the board. Edge connector strips on the underside of the board are shown in red in the overlay diagram, and those on the component side in black

When all this interwiring has been completed and thoroughly checked, solder all the throughboard links top and bottom into
place and the resistors and the diode. All components should be soldered on the underside of the board. Now insert and solder the IC sockets, SW101, IC101, and the capacitors. The four sockets in the corners of the board for connection to the I/O boards are standard 8 pin DIL IC sockets.


Fig. 6 Mounting details of SK105.
Next insert the relay and jack socket. The orginal relay is a very compact device from Maplin, and it is a good idea to fit it in a 14 pin low profile DIL socket with pins $2-$ 4 and 11-13 removed or to use a suitably modified 16 or 20 pin socket if the alternative type is used The best type of jack socket is a chassis-mounting sort with a plastic housing, as these sockets have a flat side to them which rests on the surface of the PCB. Printed circuit mounting jack sockets are only widely available in the quarter inch standard size. The socket is held steady by a piece of 16 or 18 SWG wire passing over the threaded section on the front and through the two holes in the board, with the ends soldered firmly.

Hint - when inserting the through links, use the old resistor lead or 22 SWG tinned wire. Flatten the end with pliers or bend a small angle to retain it.

Next month, we shall conclude the description of the port with construction details of the I/O boards and notes on testing, use and connection between computers.

# TECH TIPS 

## Hiss Reducer

## S. P. Giles London

The circuit goes some way towards offering a cheap reduction in the annoying hiss present on bad VHF reception signals and hissy tape recordings. It is based on the principle that two signals equal in all respects other than phase will cancel out when mixed together. If we create an out-of-phase version of
the hiss and mixit with the original, it will cancel out.

The signal to be cleaned up is DC blocked by C 1 and then fed intotwo separate paths, to mixer IC1b's inverting input via R5 and into IC1a The latter is a unity gain inverting amp which inverts the high frequency content of the input signal passed through by C2, C3 or C4. The IC1a output is then passed to mixer IC1 $b$ via R4 and PR1, which for best results should be a multiturn preset.


To set up, connect a temporary link between R1 and IC1 a's inverting input, which will allow IC1a to pass all of the input. Hook up the input to a radio tuned to a hissy VHF station and the output to an

## A440 Tuning Reference

## C. Robertson Edinburgh

This circuit was developed as a result of a demand by several friends for a cheap and reliable A440 tuning reference which could be connected to an existing amplifier/studio talkback system. The frequency reference used was a 4.433 MHz crystal, this having the advantage of being cheap, accurate and readily available.

If 4.433 Hz is divided by 10075 or $10011101011011_{2}$ the resultant frequency is exactly 440 Hz . However, in orderto simplify the circuit, this is rounded to 10076 or $10011101011100_{2}$. This gives a
resolution of $0.5 \%$ which is quite sufficient for all but the most critical of applications, bearing in mind that a pitch difference of about $6 \%$ is equivalent to one semitone.

The crystal oscillator based round T1 has its output divided by two by IC1a, a D-type flip-flop which subsequently feeds the clock input of the 12 stage ripple counter, IC2. The appropriate outputs of the counter are ANDed via IC3 and Q2 and fed back to the reset inputs of both IC2 and IC1a, (Q2 is an NPN type in order to reduce power consumption). This produces a narrow negative-going pulse train of 880 Hz which is presented to IC1b, a second D-type divide by two circuit. This produces the 440 Hz reference with a precise $50 / 50$ mark space ratio. Q3 and its associated components reduce the signal to approximately line level and perform

## wave shaping

The power supply is a single 9 V PP3 battery connected via a simple SPST switch with decoupling performed by C7 and C8. Current drain is typically 4 mA .

The prototype was constructed on a small printed circuit board in an ABS box measuring $120 \times 80 \times 30$ mm . It should be noted that although the inputs to IC3 are arbitrary, the pin connections shown give the simplest PC layout if the chips are positioned side by side with the 4040 on the left, pin 1 top right.

Finally, although the unit was designed to run from a PP3 battery, it will run equally well on supplys of up to 18 V such as those found in many mixers and pre-amps, although it is advisable to increase R2 and R7 accordingly.


# Slide Advance Unit 

Ian Lamb<br>Harare, Zimbabwe


amplifier. Adjust PR1 for minimum output - headphones would help here. Now remove the temporary link and make sure S1 is switched to position (a); you should hear the original signal with its hiss. Moving S1 to positions (b), (c) and (d) will result in the hiss gradually becoming less audible with position (d) giving the most dramatic reduction at the expense of losing a little high frequency response.

The values of C2, C3 and C4 can be altered to suit the inidividuallC1, a dual op-amp, should of course be a low noise type such as NE5532 or TL072 and C2-4 should of course be polyester types.

This unit either encodes to or decodes from one channel of a stereo tape deck a 1 kHz tone to activate the remote advance of a slide projector. The other channel is utilised for commentary of the slide series.

When pin 2 is pulled low, either by closing switch SW1 or by applying an audio signal, the normally low output on pin 3 goes high, charging the capacitor on pin 6 . This eventually causes pin 4 to switch low, which pulls pin 1 low for a period of 3 seconds and pin 13 low for a period controlled by the setting of
the 100 k potentiometer. The output of this gate (pin 11) pulsing high activates the 1 kHz oscillator for the recorder and provides base drive to the Darlington pair connected across the remote jack of the slide projector. The 100 k potentiometer is adjusted for the correct length of pulse to advance the slide tray.

At the end of the 3 second period, pin 1 is again high and if pin2 has also returned high then the outputs on pin 3 going low discharges the capacitor on pin 6 which in turn switches the output on pin 4 to the a high, leaving the unit ready for the next negative going input signal.

ETI


# ETI PCB SERVICE 

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Note that these are all the boards that are available－if it isn＇t listed，we don＇t have it．
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| $\square$ | E／794－2 Click Eliminator．．．．．．．．． 7.64 |
| $\square$ |  |
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| $\square$ | E／808－3 Ultrasound Burg |
| 口 | E／8010－1 Cassette Interface ．．．．．．．3．37 |
| $\square$ | E／8010－2 Fuzz／Sustain Box ．．．．．． 3.76 |
| 1981 |  |
| 口 | E／811－1 LED T |
| $\square$ | E／811－2 Multi－Option S |
| 口 | E／814－2 Drum Machine（2 boards） |
| $\square$ | E／814－4 Guitar Note Expander ．．．．． 3. |
| 口 | E／816－8 Waa－Phase．．．．．．．．．．．．．．． 1.76 |
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| $7$ | E／8111－1 Voice Over Unit ．．．．．．．． 4.57 |
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| － | E／8112－4 Com |
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Triac Board．．．．．．．．．．．．．．
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（3 boards）．．．．．．．．．．．．．．．．．．．．．．．．． 3.62
E／836－5 Atom Keypad．．．．．．．．．．．．． 5.18
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（four－of）．．．．．．．．．．．．．．．．．．．．．．．．．．．．． 2.93
E／839－1 NiCad Charger／Regen．．．．． 3.77
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E／8311－2 Alarm Extender．．．．．．．．．．．． 3.21

| $\square$ | E／8311－3 Multiswitch ．．．．．．．．．．． 3.59 |
| :---: | :---: |
| $\square$ | E／8311－4 Multiple Port．．．．．．．．．．． 4.34 |
| 口 | E／8311－5 DAC／ADC Filter ．．．．．．．． 3.22 |
| $\square$ | E／8311－6 Light Pen ．．．．．．．．．．．．． 4.60 |
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# FINESSE DISCO/ 

## PARTY STROBE

# Ian Benton, whose Finesse Light Chaser/Sequencer design appeared in our December 1983 issue, here sets out to dazzle us with another example of his design skills 

A$n$ essential component in any disco's repertoire of lighting effects is the Xenon strobe light. The versions available at present range from the quite sophisticated (and horrendously expensive) to the rather boring constant speed variety which are still none too cheap.

The FINESSE strobe light can add an impressive strobe light display to the disco set-up at a reasonably small capital outlay, so it is ideally suited to the newcomer whose lighting equipment budget must take second place to the audio side of things. It might equally well be used to create atmosphere and stimulate dancing at any party, in the home or elsewhere.

The unit automatically flashes in time to the beat of the music so no speed control is required; a
level control is however, provided to compensate for music with less emphasised beat. The unit connects directly to the output of an audio amplitier or to the sound-tolight output which is provided on most disco consoles.

The operation of the circuit is best divided into two sections which will be dealt with separately: the audio amplifer and processor and its DC power supply; and the high voltage strobe and triggering circuitry.

Direct connection to the audio source is preferred for this circuit because with ALC (automatic level control) and a microphone, when the level of the sound drops, the strobe unit will be triggered by background noises. It may also be triggered by the sound made by the sudden expansion of the Xenon tube as the strobe flashes,
causing it to flash continuously at high speed under conditions of low ambient sound. This is a distinct advantage for a light chaser as it does not appear as though it has 'got stuck' when the volume is lowered but for a strobe light, the effect is, to say the least, annoying

Direct connection means than the audio circuitry must be completely isolated from the mains, and this is done by using a small transformer and an optoisolator. The output of the transformer is conventionally rectified and smoothed to provide a split rail power supply of approximately $\pm 12 \mathrm{~V} D C$ and earth and the optoisolator (IC2) is used to transfer the triggering pulse from the audio circuitry to the triggering circuit.



PARTS LIST

| RESISTORS ( $1 / 4 \mathrm{~W} \mathbf{~ 5 \%}$ unless otherwise stated) |  |
| :---: | :---: |
| R1,2 | 470K |
| R3 | 3M9 |
| R4,7,10 | 22k |
| R5 | 100k |
| R6 | 270k |
| R8 | 680R |
| R9 | 100k 1 W |
| R11 | 1 ko |
| R12 | 330k $1 / 2 \mathrm{~W}$ |
| R13 | 220 R 10W |
| RV1 | 47k |
| CAPACITORS |  |
| C1 | 10 u 16 V tantalum |
| C2 | 470 n 16 V tantalum |
| C3 | 10 n 400 V polyester |
| C4 | 22 u 400 V |
| C5,6 | 1000u 16V |
| SEMICONDUCTORS |  |
| IC1 | TL074 |
| IC2 | 1174 |
| Q1 | BC184L |
| SCR1 | C106D |
| D1,2 | 1 N4148 |
| D3 | 1 N5401 |
| D4,5,6,7 | 1 N4001 |
| ZD1 | 22 V 400 mW zener |
| LED1 | any LED |
| MISCELLANEOUS |  |
| T1 | strobe trigger transformer |
|  | 9-0-9V, 6VA PCB mounting |
|  | PCB mounting 20 mm fuse holder and 1 A fuse |
| LP1 | Xenon tube |
| PCB; case; reflector and lens assembly; DIN or other socket for audio input; mains cable and strain relief bush; wiring, PCB mounting pillars, etc. |  |

## Construction

The components of both the audio circuitry and the power supply and trigger circuitry are all mounted on one PCB. Solder in the resistors first followed by the diodes and the ICs last (IC1 is a JFET/biplolar device and does not
require handling precuations). As usual, make sure that none of the tracks are bridged by small pieces of solder, especially where the high-voltage and power supply components are concerned.

The strobe tube mounts on top of C4 as this reduces the length of the current wires to a minimum. It

## HOW IT WORKS

The left and right audio channels are summed by IC1a which is configured as an inverting summing amplifier with a gain of 18 dB to produce an audio signal of approximately $4 \vee$ RMS.

IC1b, D1, C1 and R5 form a peak level detector with a time constant of 1 second. This is smoothed by R6, C2 and fed to the inverting of the comparator, IC1 d . The peak level signal is buffered by IC1c and connected through D2 and R7 to $V_{\text {peak }}$ and $V_{\text {peak }}$ -0.65 V . This signal is compared to the smoothed sound level by IC1 d.

Thus the output of IC1d goes high, turning on LED1 and the emitterfollower Q1 which is used as a buffer. A pulse is transmitted to the optoisolator IC2, whenever the peak sound level exceeds the average (smoothed) sound level by an amount determined by the setting of RV1.

The effect of using a diode rather than a voltage divider is that when the music fades, the peak voltage is reduced by a greater amount than the smoothed voltage. This effects a reduction in sensitivity and cuts off the strobe towards the end of a record, keeping it turned off until the next record is faded in.

The mains is half-wave rectified by D3 and charges C4 up through R13 to approximately 340 V DC. With SCR1 non-conducting $C 3$ charges up over approximately 10 ms through R12 and T1 primary. This leaves one plate at 0 V and one plate at 340 V .

When the phototransistor in IC2 conducts, SCR1 is triggered forcing the + ve side of C3 to 0 V . The other plate goes to -340 V and discharges through T1 producing a pulse of about 5 kV on the secondary. This is passed to a metallised strip along the outside of the Xenon tube and ionises the gas inside. This then conducts strongly discharging C 4 in about 1 ms with a peak current of more than 50 Amperes and a peak power in excess of 1 kW depend ing on the tube used.

R9 and ZD1 limit the collector voltage of the phototransistor to 22 V since most opto-isolators have a 30 V $V_{\text {ceo }}$ limit.
The ringing of the tuned circuit formed by T1 and C3 turns off SCR1 on the first transistion through $0 V$ provided that the current flowing through R12 is less than the holding current.
is probably better if you can find a tag ended or radial leaded version for this component. The Xenon tube most be totally enclosed for reasons of safety as 340 V is present on its + ve terminal and 5 kV on the metallised strip along its length. Mounting is left up to the constructor, but one method is to use the lens unit from a RING halogen driving light, remove the bulb-holder and fix C4 to the back of the unit with the Xenon tube soldered onto its terminals.

It may also be possible to use the storage capacitor and tube from an existing simpler strobe the value of C 4 is non-critical - in which case the housing will not cause any problems. If a metal case is used it should obviously be earthed; if a plastic case is used do no let it come into contact with R13 which gets extremely hot

Before switching on check that the tube, C4 and D5 are all correctly connected as well as making the usual check for dry joints, etc. Electrolytics have a nasty pyrotechnic suicidal tendency when connected wrongly, big elec-
trolytics especially, so be particularly careful here. If spurious triggering occurs in use the input voltage is too high; R3 should be reduced until no distortion can be heard using a crystal earpiece on IC1a output.

## Modification

The design can be modified in a number of ways to suit individual requirements. If link LK1 is removed from the strobe live line and a switch inserted instead, the unit can be adjusted to flash correctly using D3 as an indicator without the Xenon tube itself flashing. The output of IC1 c provides a buffered peak level signal and this could be used to drive a VU meter if desired.

It is also possible to modify the circuit to drive two tubes simultaneously. To do this, duplicate the strobe circuitry from the optoisolator onwards, reduce R8 to 560 R and connect the additional optoisolator diode in series with the existing one. Bear in mind that the additional strobe
circuitry will carry high voltages and assemble and insulate it accordingly. If you can afford it, the best solution would be to use a second PCB, either a full board from our PCB service or a homemade one whch reproduces just the relevant part of the layout. If the full PCB is used, it is a simple matter to connect up the optoisolator diode using one of the earth pads and the LED cathode pad, and to wire this into the board carrying the power supply and audio supply and audio circuitry in series with the LED.

## BUYLINES

All components are standard except the Xenon tube and transformer (and possilbly the optoisolator; Type IL74 is specified but other types should work providing their $V_{\text {ceo }}$ is 25 V or more and the pinout is the same). Xenon tubes are available with trigger transformers from Tandy in two sizes and are surprisingly cheap. Maplin also supply a tube, but this has axial leads which may cause mounting problems. The PCB is designed for the Tandy transformer which should still work with the Maplin tube. The PCB is, of course, available from our PCB service.


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\hline \& \& \& \& \& $$
\begin{aligned}
& 10+19.98 \\
& 20+19.80
\end{aligned}
$$ <br>

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# ACTIVE-8 LOUDSPEAKER 

## This month, Barry Porter completes the design work on the latest ETI active loudspeaker.

The Active- 8 was evaluated with both 12 and 24 dB per octave filters and no difference could be heard between them, so the 4 pole version was chosen as this gives slightly more protection to the high frequency unit by virtue of its steeper slope. It also has the additional advantage of reducing the level at the resonant frequency of the T33 - about 950 hz - by about 40 dB , where its effects may be safely ignored. The response of both high and low pass sections is shown in Fig. 6 and the circuit of this part of the network in Fig. 7.

## Drive Unit Equalisation

If each drive unit had a flat frequency response over its range of operation, life would be much more enjoyable for all concerned. Unfortunately this is not the case, so additional circuitry has to be used to correct the major inaccuracies. The Active- 8 units were measured in free field conditions (free local playing field would be more accurate!) resulting in the plots of Fig. 8

Looking at the B200G response first, this shows a 6 dB rise between 300 Hz and 3 kHz which the equalisation circuit shown in Fig. 9 cancels with reasonable accuracy, as the corrected plot shows.

The T33A also exhibits a response that rises with frequency, so a similar circuit is used to counteract this.

It will be seen from Fig. 8 that the $T 33 \mathrm{~A}$ is slightly more sensitive than the B200C - about 3 dB if the low frequency output at 1 kHz is compared to the 10 kHz output from the high frequency unit. This difference will be corrected at a later stage by placing a 3 dB attenuator in the high frequency signal path.


Fig. 6 Crossover filter response.


Fig. 7 Crossover filter circuitry.


Fig. 8 Free-field response of the drive units.


Fig. 9 Equaliser section circuitry.


Fig. 10 The effect of the displacement of the speaker coils.

## Time Delay

Ideally, the two drive units should have their acoustic centres on a plane that is perpendicular to the speaker axis. This is not the case however, as the T 33 a radiates from a point approximately 38 mm in front of the 3200 G . Referring to Fig. 10, it can be seen that the radiation pattern will be tilted downwards at the crossover frequency by:

$$
B=\operatorname{Atan}\left(\frac{D_{2}}{D_{1}}\right)=11.9^{\circ}
$$

This could be compensated for by mounting the T33A on a different plane to the B200G, but this would introduce a number of mechanical difficulties in avoiding diffraction effects from the cabinet edges. The alternative solution applied to the Active- 8 is to delay the high frequency signal by the amount of time it takes sound to travel 38 mm , which is:

$$
\mathrm{t}_{\mathrm{d}}=\frac{\mathrm{D}_{2}}{\mathrm{~V}}=\frac{38 \times 10^{3}}{343}
$$

A suitable delay circuit formed from cascaded all-pass filters, is shown in Fig. 11. Each stage gives a delay at the crossover frequency of:

$$
t=\frac{2 R C}{1+(2 \pi f R C)^{2}}
$$

The use of this delay ensures that both units are in phase along the cabinet axis, so no vertical directivity shift occurs over the crossover region. Colouration in the critical mid-range is therefore minimised, and the improved dispersion characteristics assist in the production of a very stable stereo image with a considerable presence of depth information.

The previously mentioned 3 dB attenuator in the high frequency signal path is formed at the output of the delay circuit by R44 and R45.

## Closed Box Operation

Although the 'Active-8' may prove quite acceptable with reflex loading, there are certain advantages to be gained from replacing the vent escutcheon with a blanking plate and reverting to closed box operation.

Although curve A in Fig. 1 may not look too promising especially if your musical taste runs to
material with more than its share of bass emphasis, remember that this is the anechoic response. Under normal listening conditions, room boundary reflections will give a perceived increase in low frequency output.

Closed box response rolls off at 12 dB per octave, and therefore exhibits less transient overshoot and ringing than the 24 dB per octave reflex response. Although the Active- 8 will give good performance when used as a closed box in a small listening room, it will not have sufficient bass output for use in larger rooms. The technique employed to resolve this problem works like this:- as we have seen, the closed box response rolls off at 12 dB per octave, so if circuitry is placed in the low frequency signal path that introduces a counteracting 12 dB per octave lift, the acoustic output of the speaker will remain flat at lower frequencies. Obviously, the equalisation cannot continue to rise in level, so at the point where it flattens the speaker roll-off will start, still retaining a 12 dB per octave sope and with a $Q$ value that is decided by the electronics. A suitable low frequency equalisation circuit is shown in Fig. 12. The Active-8 values are based upon the following parameters.

$$
\begin{aligned}
f_{\mathrm{O}} & =48 \mathrm{~Hz} \\
Q_{\mathrm{P}} & =0.505 \\
\mathrm{f}_{\mathrm{p}} & =13.2 \mathrm{~Hz} \\
Q_{\mathrm{P}} & =0.5 \\
\mathrm{~A}_{\mathrm{DC}} & =22.4 \mathrm{~dB}
\end{aligned}
$$

This gives a considerable increase in bass output without too much danger of either the circuitry or bass unit running out of headroom. As an experiment, the author applied the same low frequency equalisation technique to a pair of large domestic speakers with 300 mm bass drivers, but kept the response flat to about 5 Hz . The bass was certainly impressive, although analogue records could not be played due to turn table and cutting lathe rumble causing excessive cone movement. Both analogue and digital master tapes caused no problems, and it was clear that, although there was no musical information at very low frequencies, the extremely good phase characteristics of the speakers gave weight and solidity to the lower register that is lacking in all but the largest studio monitors.

The bass equalisation circuit is


Fig. 11 Delay circuit.


Fig. 12 Low-frequency equaliser for closed-box operation.


Fig. 13 Protection unit.


Fig. 14 Remote switching unit and PSU circuitry.
only used when the speakers are operated in the closed box mode, and therefore provision must be made to bypass it when necessary. As the circuit of IC 8 b (Fig. 12) is inverting, a further inverting stage - IC8a - has been added to maintain phase integrity. The choice of $Q_{p}$ at 0.5 was made to minimise the low frequency phase shift, with $f_{p}$ being set at a frequency that allowed the use of standard capacitor values. The resistor values are shown as calculated, but the nearest E24 values may be used with no noticeable change in performance. Similarly, the 75 nF capacitor is approximated by the paralleled combination of 68 nF and 6.8 nF (C48, 49).

## Switch-On Delay

One problem encountered with the prototype Active-8 speakers was that switching them on or off required the adoption of a procedure not far removed from doing a pre-flight check on the family 747. It was all too easy, for example, to switch off the preamplifier while power was still applied to the filter units and power amplifiers, the reward being a superb example of transient handling as the drive units attempted instant selt-destruction. To avoid this, the power amplifiers had to be switched on last And switched off first In spite of several feet of advisory Dymo tape, this sequence was not always adhered to, so to avoid wear and tear on drive units and nerves, the circuits of Fig. 13 and 14 were incorporated.

Together, these units provide ETI OCTOBER 1984
remotely controlled mains switching, delayed connection of amplifier to drive units, disconnection of drive units before mains switch-off and continuous protection against excessive DC voltages at the power amplifier outputs.

Operation of these functions is best understood by considering a switch-on - switch-off cycle. The small transformer, T1 (Fig. 14) is permanently connected to the incoming mains so about 17 V DC sits on smoothing capacitor C63. Q6 is held off by R72, so the mains switching relay, RL2, is deenergised. The unit may be switched on locally, or by earthing the remote connection at the preamplifier; either action turning on Q6 by the pull-down of D4 and R71. RL2 is therefore energised, and contacts RL2A and RL2B apply mains voltage to the crossover unit power supply transformer, T2, and to the power amplifier via a mains outlet socket.

When the protection unit (Fig. 13) initially receives power, Q4 and Q5 are turned off, and the speaker drive units are disconnected due to RL1 being deenergised. As C58 charges up through R63, the bass voltage of Q4 rises until it reaches 6.8 V , at which point the transistor turns on. The current which then flows through R67 and R68 turns Q5 on and RL1 is activated, connecting the drive units to the amplifier. This take about 6 seconds, which allows all voltages to settle and switching transients to disappear.

In operation, any excessive DC voltage appearing at the power amplifier output will be detected
by Q1 or Q2. A positive voltage will turn Q1 on, pulling the base of Q4 down, so that both Q4 and Q5 are turned off, as a result of which RL1 will disconnect the drive units. If the offset voltage is negative, Q2 will be turned on. Current flowing through R65 will turn on Q3 which will pull down the base of Q4. Again, Q5 will also turn off, deenergising RL1 and disconnecting drive units.

At switch-off, the remote connection is removed from earth, immediately causing RL1 to revert to its relaxed state as its OV path via D4 (Fig 14) is broken. The drive units are therefore disconnected before the mains is switched. Q6 is held on for a short time by C62, so RL2 cannot switch the mains until the amplifier outputs are well and truly broken by RL1.

All this means that the Active-8 units can be switched on and off without fear of the clicks, bangs and thumps that are so often the hallmark of home produced equipment. The remote connections of each speaker can be joined together and taken by a single wire to the pre-amplifier where a single pole switch can be used to operate the speakers. The recent modular pre-amplifier article (ETI December, January and February) gave details for wiring the remote connections so that the speakers are automatically switched off whenever headphones are used, this being one of the several possible ways the system can be arranged.
Next Month: Construction.

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# COMMUNICATIONS SATELLITES Part 4 

# Drawing this short series to a close, Roger Bond takes a look at who's who and what all those long abbreviations mean. 

No UFOs here, just level headed scientists and businessmen who put their skills to work. The USA, being a much bigger country than the UK, has many satellite carriers and some are listed below.

American Telephone and Telegraph (AT\&T): Part of the Bell Company. It also has a long lines division who look after international cables.

## General Telephone and Electronics (GTE)

RCA American: The famous recording company also builds satellites and is now also a satellite operator.
Hughes Aircraft: Has been in the satellite building business a long time and now plans to become an operator.
Southern Pacific Communications: Well known for its railways, and now in the satellite business.
Western Union: This is a telephone company (remember the old western?).
American Satellite: A combined business venture of Continental Telephone and Fairchild Industries.
Satellite Business Systems: This is a partnership including IBM.

These are the established carriers but no doubt there will be others in this profitable field particularly with the US Government's liberalisation of the telephone industry.

INTELSAT is an operating body whose governor is COMSAT (Communications Satellite Corporation). INTELSAT is made up of member governments and telecommunications representatives. A board of governors then works through a director general who has deputies for finance, administration, development, etc.

INTELSAT was formed in 1964 by eleven member countries, but the ranks have grown to a hundred. The USA has a $25 \%$ voting share and the UK plus Eire $11 \%$, which reflects the usage of the satellites.

EUTELSAT was formed in 1977 by seventeen countries to operate the European Communications Satellite (ECS). The two biggest shareholders are France and the United Kingdom with $16 \%$ each, then West Cermany and Italy with $11 \%$ each.

The first ECS was the Orbital Test Satellite (OTS) which was destroyed when the American THOR DELTA launcher exploded in 1977. The next OTS was launched the following year with a three year lifetime. After that came the ECS flights lanched by the European Space Agency (ESA) by their Ariane rocket from Korou in French Guiana, South America.

Since French Guiana is on the equator, the positioning of satellites is easier. Launches from Cape Caneveral have to be placed in a elliptical transfer orbit first. The lowest point of the ellipse (perigee) is about 170 km above the earth. The highest point (apogee) is at the correct altitude for a geostationary orbit and when the apogee motor is fired, the satellite is drifted into its final position.

Table 1 shows the ownership of some of the satellites and the frequency bands in which they operate. The name 'PALAPA' of the Indonesian satellite, means 'goodwill to all men'.

UNISAT is United Satellites Ltd, a company formed by marconi Space and Defence, British Telecom and British Aerospace. The day of television pictures direct from satellites should be upon us in 1986 when two UNISAT satellites each using six transponders for transmission should start beaming pictures. Most local councils says they have no objection to people installing one metre dishes in their gardens for receiving these signals.

## ECS

ECS flight 1 (F1) has launched in June 1983 and has twelve transponders operating at $14 / 11 \mathrm{GHz}$. Each of the 20 W transponders has a 72 MHz bandwidth and at present television is being broadcast to the Continent. The programme called Skychannel reaches an audience of half a million in seven European countries. Such is the power of television aided by satellites.

ECS flight 2 (F2) is due for launch in 1984 and will work TDMA via Madley 4. Quite apart from TDMA, this satellite is due to provide a revolutionary new facility called multi-services which included voice, data and videoconferencing. The whole idea is to provide businessmen with international access at bit rates from $64 \mathrm{Kbit} / \mathrm{s}$ up to $2 \mathrm{Mbit} / \mathrm{s}$.

In this way a newspaper company can, for instance, print its foreign editors in the relevant countries making distribution thirty times faster. Other multinational companies like banks and oil companies will also welcome this facility.

ECS F2 to F5 will carry two extra transponders each for this service and later versions of ARIANE can carry this extra payload. These satellites will weigh about 650 kg compared to 440 kg for the OTS. The solar panels spanning 14 m altogether will deliver 1.3 KW compared to 750 W for OTS.

In addition to point-to-point services there will be point-to-multipoint facilities similar to a control tower
broadcasting to radio cars. The satellite will have aerials for a Eurobeam, three spot beams (east, west and Atlantic) and, of course, the multi-services global beam aerial. The satellite's shape is similar to that of INTELSAT V and the user should need a 5 m dish, which could be mounted next to the office or factory.

## INTELSAT VI

By the end of 1985, the combined capacity of the INTELSAT $V$ and VA satellites on the primary AOR and IOR routes will be saturated; the first of INTELSATVI will take up position in 1986.

INTELSAT VI will be similar in shape to IV, with a spinning drum-shaped body carrying the solar cells and a de-spun platform carrying the aerials. However, the cylindrical body will be extended after launch, ie, it will be telescopic, making the total satellite length 12 m , of which 3.8 m is the outer cylinder length and 2.2 m is the inner cylinder. When folded down for launch, the satellite will be 5 m in height. The weight of the satellite will be about 2000 Kg .

The solar cells produce enough power for the satellite, which requires 2 kW , and to keep the stand-by 44 AH batteries topped-up. The solar cells should last for the 10 years of life that the satellite is designed to have, rather longer than the life of the cells on INTELSAT VI which face the sun all the time.

The satellite is designed so that it could be launched either by the Shuttle or by AIRANE 4. With a Shuttle launch, it will need a perigee kick motor to get it into a high, equitorial orbit, and this will be carried in a cradle in the Shuttle's cargo bay.

The aerial array on VI is similar to that on V , except that where $V$ has 88 feeds, VI has 146 . This gives VI greater flexibility in the beam patterns that it can generate. VI will use satellite switched time division multiplex assignment, a technique presently used on the experimental L-SAT satellite, which means that the zone and hemi beams can be interconncected up to 64 times within each TDMA frame of 2 ms , making it exchange of information between users very flexible.

As with INTELSAT V, there are two steerable 14/11 GHz spot beams operating with linear polarisation. The receiver uses low-noise GaAs-FET preamplifiers and a frequency-changing oscillator whose frequency if controlled from the ground station. Transmit power is normally provided by travelling wave tubes, but the smaller zone beams require only about 3 W which can be supplied by semiconductors.

## Other Applications

We have looked at the narrow field of telecommunications, so far, but let us now take a look at some of the other uses of satellites that are related.
TV Broadcast: Satellites are already used quite extensively for the passing of TV pictures and programmes from one broadcasting station to another, and lately to cable TV companies relay stations (in community antenna television, or CATV. However, the next phase of this process is to be the direct broadcast of television to peoples' homes (direct broadcast by satellite, DBS, as already featured in ETI in March 1983.

DBS has already started in the USA by United Satellite Communications (USC) who are hoping to reach a total of 10 million homes. A US company, the Orion Satellite Corporation, has applied to the US authorities to launch two satellites to beam television to Europe in the Ku band. Costing $£ 230 \mathrm{~m}$, and receiving backing from banks in New York and London, could this be the start of a new era of pirate broadcasting?

| SATELIITE | FREQUENCY | COUNTRY |
| :---: | :---: | :---: |
| WESTAR-2 | 4-6 | USA |
| USASAT-6A | 11-14 | USA |
| US SATCOM-2 | 4-6 | USA |
| SATCOL-2 | 11-14 | CANADA |
| ATCOL-2 | 4-6 | COLUMBIA |
| GOES WEST | 1-3 | USA |
| LOUTCH P1 | 11-14 | USSR |
| STATSIONAR-8 | 4-6 | USSR |
| VOLNA-1 | 1-3 | USSR |
| SIRIO | 1-17 | ITALY |
| SYMPHONIE-2 | 1-4-6 | (FRANCE (W GERMANY |
| TELECOM-1A | 4-6-7-8-12-14 | FRANCE |
| COMSTAR D1 | 4-6 | USA |
| GEOS-2 | 1-3 | FRANCE |
| METEOSAT | 1-3 | FRANCE |
| ARABSAT 1 | 3-4-6 | ARAB STATES |
| ZOHREH-2 | 17-14 | IRAN |
| INSAT-1A | 1-3-4-6 | INDIA |
| PALAPA-2 | 4-6 | INDONESIA |
| CSE | 3-14 | JAPAN |

Table 1: Satellite owners and frequencies
However, economic worries continue to dog DBS. In the US, the prices being charged by USC compare badly with a competing CATV system provided by a cable company ( $\$ 40$ per month for five channels plus the cost of the antenna, around $\$ 300$, for USC, as opposed to $\$ 25$ per month for 54 channels for CATV).

The economic worries have reduced the plans of the BBC and the IBA steadily until, although neither will admit it publically, it appears in doubt that either will get in on the act; at the very most, the BBC will be opening two satellite channels, for which it will pay $£ 12$ million per channel for the use of the satellite Added to this will be the cost of producing the programmes.

Meterology: At present data is received directly by many Countries through the Automatic Picture Transmission System (APT). During the 1980s several geostationary systems will be introduced, including Meteostat 2 and SIRIO2 by ESA. The USA will put up GOES east, west and central and the USSR, GOMS. Japan will have GMS2 and India INSAT 1.

Safety at Sea: Although the maritime satellites operate panic frequencies for ships in distress, more ambitious
systems are operated by some countries. SARSAT, search and rescue satellite is operated by the UK, USA, Canada, Norway, Sweden and France. The USSR operates COSPASS, space system searching for aircraft and vessels in distress.

Radionavigation is an associate field of application and the World Administrative Radio Conference (WARC) has allocated specific bands in the range 1.5 GHz to 265 GHz . The US Navy will replace their Navigation Satellite System with the Global Positioning System (GPS), a much more ambitious project The USSR uses their Tsikada satellites both for their navy as well as oil rigs.

Remote Sensing: This is used to study geology, land use, the environment and natural resources. About seventy-five countries participated in the USA's LANDSAT programme which studied crops, evaporation and soil temperature.

More than a hundred countries including the USSR and India have remote sensing The frequencies used vary across the electromagnetic spectrum including infra red, microwave and the visible portion. Both active and passive sensors have been used and the advantage of microwave is that it is less prone to weather conditions.

Standard frequency and time signal: There is already a big demand for this from the general public, communications engineers, space scientists, navigators, instrument calibrators, astronomers, surveyors, TV and radio broadcast stations etc. At present a standard is derived from caesium by the National Physical Laboratory. A frequency standard is also broadcast by Rugby Radio Station.

## The Future

The future is here, rolling down on us faster than we realise. Technologically, satellites are headed for continuous improvement. Space-to-space links mean that signals can reach their final destination without being beamed to earth at intermediate points. The USA's Tracking and Data Relay Satellite (TDRS) enables data to be collected by several geosynchronous satellites and delivered to a common earth station.

SS-TDMA is also a technology for the future, the great advantage of TDMA links being that noise on the satellite up link can be separated from the noise on the down link hence reducing the overalll system noise. However TDMA equipment is expensive for small countries and CFM may be preferable.

The USSR has pioneered the use of highly elliptic

| APT | Automatic Picture Transmission | INMARSAT | International Maritime Satellite |
| :---: | :---: | :---: | :---: |
| AOR | Atlantic Ocean Region | INTELSAT | International Telecommunications Satellite |
| ARIANE | ESA's launch rocket | MARISAT | Maritime Satellite |
| CCITT | Consultative Committee for International Telephone and Telegraph | MAROTS | Maritime Orbital Test Satellite |
|  |  | MARECS | Maritime European Communications Satellite |
| CATV | Community Antenna Television | NASA | National Aeronautics and Space Agency |
| CFM | Companded Frequency Modulation | OTS | Orbital Test Satellite |
| COMSAT | Communications Satellite Corporation |  |  |
|  |  | PKM | Perigee Kick Motor |
| CES | Coast Earth Station | PSK | Phase Shift Keying |
| DBS | Direct Broadcast by Satellite | SCPC | Single channel Per carrier |
| DSI | Digital Speech Interpolation | SPADE | Single channel Per carrier, Puise code |
| ESA | European Space Agency |  | modulation, Assignment by Demand Equipment |
| eutelsat | European Telecommunications Satellite. | SSOG | Satellite System Operations Cuide |
| ecs | European Communications Satellite | SSOP | Satellite System Operations Plan |
| FDMA | Frequency Division Multiplex Assignment | Ss-tDMA | Switched Satellite - TDMA |
|  |  | TASI | Time Assigned Speech Interpolation |
| GEOS |  | TDMA | Time Division Multiplex Assignment |
| GOES | Environmental Satellite Geostationary Operational | TWT | Travelling Wave Tube |
| GPS | Global Positioning System | TWA | Travelling Wave Amplifier |
|  |  | TDRS | Tracking and Data Relay Satellite |
| GFRP | Craphite Fibre Reinforced Plastic | UNISAT | United Satellite Corporation |
| IOR | Indian Ocean Region | WARC | World Administrative Radio Conference |

Table 2: The jargon every communications engineer will have to have at his or her finger-tips.

Molniya orbits. These have a high point of the orbit $38,00 \mathrm{~km}$ above the earth, and from the earth below this point, the satellite will be visible for around 8 hours per day. Moreover, for much of the time, it will be high above the horizon, and therefore much less likely to be obscured by hills or buildings. For this reason, the use of satellites with such orbits for radiomobile communication in the UK is being investigated.

Administration: With the vast number of satellites in use and the number of beams transmitted by each satellite because of frequency re-use, there is a real need for efficient spectrum management. However there is little to stop a wayward country from breaking a gentleman's agreement.

The strong demand for orbital positions has prompted INTELSAT to suggest a reduction in satellite spacing from $3^{\circ}$ to $1^{\circ}$. This will require a thinner bearm which can be generated by Georgian aerials Fig. 8. This simple but clever idea prevents the tripod of the subreflector interfering with the main beam.

Politics: For twenty years INTELSAT has administered satellites for the Western World but the choice of both manufacturers and launchers has grown steadily.

Britain has a thriving portion of the satellite business but Japan intends to enter it in a big way. They intend not only to sell the world satellites but also launch them with a vehicle costing $\$ 1.3$ bn and launching more than seventy satellites between 1984 and 2000 . Their first launch, the CS-a with 4000 circuits, weighed 350 kg and cost $\$ 800 \mathrm{~m}$ to launch, three times the cost of a US launch using Shuttle.

Fig. 1 The Gregorian antenna; the difference between this and an off-set Cassegrain is that in the former the sub-reflector is concave, whereas in the latter it is convex.


## Conclusions

A mere thirty years has seen the first faltering steps in space become sure footed and accurate. Techniques and materials have progressed rapidly and up to now space has been used for peaceful purposes. But the super-powers are racing each other to put beam weapons in space so those harmless jelly beans squeaking across our video screens could well become reality. Meanwhile there is a real need for a space-age Steptoe and Son to go and collect all the jettisoned scrap iron floating about in space.

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The foil pattern for the Finesse Disco/Party Strobe.


The pattern for the simple echo unit.


The top and bottom foils of the Digital Cassette Deck.



The patterns for the top and bottom of the control card of the Spectrum Control Port (this board will not be available from the PCB service until next month).



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PROJECTS SPECIAL ISSUE

Hard to belive that the summer was so short; here we are, we've only just begun September, and we're already thinking about our November issue. But November is projects time for ETI, and there will be ten of them in our November issue. Not half-baked untried circuits, but ten working projects, complete with PCBs (as appropriate) and constructional details.

Also, this issue will be a little bit thicker than usual, with sixteen extra editorial pages - putting us even further ahead of the competition.

Projects planned for the November issue are as follows:

- Continuity Tester
- Electron Speech Card
- Mains Failure Alarm
- Temperature Controller
- Spectrum Stage-Lighting Controller
- Knite Light Display
- AM/FM Portable Radio
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