## AN ARGUS SPECIALIST PUBLICATION



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# LIFE 

Z80-based control computer for home, industrial and laboratory control

## modular

 construction for maximum flexibilityZ-80 MAIN BOARD



Dave Bradshaw: Editor Phil Walker: Project Editor Jerry Fowler: Technical Illustrator David Kitchener: Advertisement Manager
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Reep. to 6.5 KHz ., Sens. 98 dB . Price: $£ 22.00+£ 3$ carriage.
$12^{\prime \prime} 86$ wett R.M.S. McKENZIE C1286TC (P.A., DISCO) $2^{\prime \prime}$ atuminium voice coil. Twin cone. 8 ohm imp., Res. Freg. 45 HZ ., Freg. Resp. to 14 KHz . Price $£ 22+£ 3$ carriage. $15^{\prime \prime} 150$ watt R.M.S. MCKENZIE C15 (BASS GUITAR, P.A.) $3^{\prime \prime}$ aluminium voice coil. Die cast chassis. 8 ohm imp., Res. Freq. 40 Hz ., Freq. Resp. to 4 KHz . Price: $£ 47+\mathrm{f} 4$ carriage.

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Carriage: $\mathbf{£ 5}$ each $\mathbf{e 7}$ per pair

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put $1 \mathrm{~V} \uparrow \mathrm{KHz}$. Sweep output $0.9 \mathrm{~V} . \star$ Graticule blue ruled $10 \times 8 \mathrm{~cm} .(5$. C.RT.) Very sharp trace. $\star$ Size: H 235 mm $W 177 \mathrm{~mm}$. D $360 \mathrm{~mm} . \star$ Weight 6.5 kgs . $\star$ Supply: $200-240$ $\mathrm{V} .40-60 \mathrm{~Hz}$. $\star$ Price: $£ 241.50$ FREE Securicor Defivery. Probes: $\times 1$ £8.05, $\mathrm{X} 1 / \times 10$ Switched $£ 10.93$.

B.K. ELECTRONICS

## 

 to the rescue with this project which is just the ticket for 6502 or 6800 based systems. Latching it into your system just couldn't be easier, because the DRAM board uses an EPROM to do the address decoding. No more lash-ups, just program the PROM to define where the memory goes in the micro's space. Why didn't we think of it before?

## Smart NiCad Charger

Now there have been plenty of NiCad chargers that have graced these pages - but this one will automatically rejuvenate worn out cells. (Wonder if it would work on the cells in the Editor's brain?)

## New Series: Audio Design

John Linsley Hood is one of the best known audio designers, and he'll be guest-writing this series for us. The emphasis will be on practicalities, with easy-to-follow explanations and simple but useful calculations. Although as little as possible be assumed about the level of expertise of the reader, there will be something here for even the most experienced of you.

## Audiophile review

The Videotone Minimax speakers have provided a touch-stone in price-effective audio for many years. Now there's a Mark II version can it be as good, or, possibly, even better? Our intrepid Audiophile investigates.

## The Digger

No, nothing to do with our Australian colleagues, but a useful little device for digging around inside digital circuits - a logic oscilloscope trigger. The idea is very simple, really, all you do is . . . but if we told you that, you might not buy next month's mag, mightn't you?


ALL THIS AND MORE IN THE SEPTEMBER ISSUE OF ETI, ON SALE AUGUST THE 5th. PLACE YOUR ORDER NOW, OR RISK MISSING OUT!


DIGEST


The SC817 SMART CABLE from KPG-Hardware House provides a simple, low cost method of interfacing $98 \%$ of RS232 equipment. Ideal for equipment salesmen and engineers the Smart Cable's unique logic senses the RS232 configuraiton at each end and automatically configures the
correct pin to pin connection. Providing male to male and male to female connection the Smart Cable totally eliminates the need for break out boxes, debugging cables and custom cables. KPGHardware House, 578-586 Chiswick High Road, London W4 5RP.

## New Current Probe

> The Model 711 Miniature Wideband Current Probe manufactured by American Laser Systems, California, measures wideband current pulses without loading the circuit being tested. It induces no appreciable capacitive nor inductive effects on circuitry, therefore the signal under measurement does not change. The 711 exhibits only 0.02 ohm shunted by $4 \mu \mathrm{H}$ insertion impedance. The current is sensed by placing the conductor through the centre of the probe, which will accept a maximum lead diameter of No. 20 AWG. For general use as a test probe with oscilloscopes and test equipment,

Model 711 is available in a potted package with a 3 ft co-ax cable and standard BNC connector. The UK agent is Dynamic Technology Limited, Zonal House, Alliance Road, Acton, London W3 0BA.

## Whoops!

C totron Ltd were just a little $\checkmark$ annoyed with us for saying that their latest catalogue was dated 1982 (Digest Shorts, June). In fact, they have now supplied us with their latest, 1983/4 catalogue, to prove that the other dropped through a worm-hole in

## TOBIE Award For LCD Team

The TOBIE Award for Research Achievement has this year been won by the Physics Research team at the Royal Signals and Research Establishment, Malvern. For some years now this team has been working on the various uses of liquid crystals, including displays and addressing methods, this technology has made possible the $128 \times 256$ matrix liquid crystal cell developed in conjunction with Scopex Instruments Ltd., for use in the Voyager digital storage oscilloscope.

Many of the Voyager's ap-
plications are, apparently, linked to water or marine life. For instance, Water Boards find its lightness and ease of handling very useful on certain of their very remote pumping and control locations, and one of the strangest of applications to date has been for use on an expedition to the Amazon where it will be used for tests on a type of electric fish. Scopex ask: is this the dawn of a new era in ATE (Aquatic Test Equipment)? Scopex Instruments Limited, Pixmore House, Pixmore Avenue, Letchworth, Herts SG6 1HZ. Tel (04626) 72771.


## The Bead's Needs

A
new range of ferrite beads manufactured by Stackpole are vailable exclusively to UK manufacturers from Walmore Electronics Ltd, 374 City Road, 11-15 Betterton Street, Drury Lane, London WC2H 9BS. Particularly interesting is a lead tape bead for use with automatic insertion equipment.

Ferrite beads are a simple, inexpensive, yet effective way to obtain RF decoupling, shielding and parasitic suppression without sacrificing low frequency power or signal level. Unlike conventional RF chokes, beads are compact, have no DC losses, and will not couple to stray capacity and introduce detuning or spurious oscillations. Installation of beads is easy. Simply slip over the appropriate conductor for the desired noise suppression or high frequency isoltion.
the space-time continuum (that's our story, and we're sticking to it).

Stotron would also like us to point out that while they do continue to have offices in Hastings, readers should write to them at 72 Blackheath Road, Greenwich, London SE10 8DA, to obtain their copy of the catalogue.

## Fibre Optic

 Linksightdata have two new modules: on the left is the Blue Box 16-channel RS232 to fibre optic interface that is, apparently, simplicity itself to set up and use. It is intended to be mounted on a wall, and can provide a link over up to $1 \mathbf{k m}$ of optic cable. On the right is a Eurocard mounted version that requires a single 5 V supply. Lightdata, 4 Lias Road, Porthcawl, Mid Glamorgan, Wales CF36 3AH.

## Energy In The Outback

A new \$250,000 Solar Energy Research Centre is to be built in Western Australia. It will be located next to the site earmarked for a proposed new technology park, which is to be established at the Western Australian Institute of Technology in Bentley.

The new centre will contain testing and monitoring equipment, computer facilities for automated data analysis, an information and display area, and office space for the staff of the Solar Energy Research Institute of Western Australia (SERIWA). Outside areas will be used to test various types of solar equipment.

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most stringent spec and designed to for 24 hrs per day. Units are made to the $C C I T$ tone spec. With RS232 i/o levels via a 25 way 'D' skt. Units are sold in a tested and working condition with data. Permission may be required for connection to PO lines. MODEM 13A compact, async, same size as telephone base. Up to 300 baud, full duplex over 2 wires, but call mode only $£ 75.00$ MODEM 2B/C Fully fledged, up to 300 baud async, ANSWER \& CALL modes, auto answer, auto switching ideal networks etc. Just 2 wire connection to comms line. $\mathbf{8 8 5 . 0 0}$
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$\mathbf{\$ 1 3 0 . 0 0}$
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For more information or delails of other types

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## VIDEO MONITORS

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attention was given to construction and reliability of this unit with features such as internal transformer isolated regulated DC supply all components mounted on two fibre glass PCB boards - which hinge out for ease of service, many internal controls for linearity etc. The monitor accepts standard 75 ohm composite video signal via SO 239 is estimated around 20 Mhz and will display most high def graphics and $132 \times 24$ lines Units are second hand and may have screen burns. However where burns exist they are only apparent when monitor is switched off. Although unguaranteed all monitors are tested prior to despatch. Dimensions approx. $14^{"}$ high $\times 14^{\text {" wide by }} 11^{\text {" }}$ deep. Supplied complete with circuit. 240 volt A 24" CASED Again 24" CASED. Again made by the KGM Co Originally used for large screen data display Very compact unit in lightweight alloy case dim. $19^{\prime \prime} \mathrm{H} \times 17^{\prime \prime} \mathrm{D} \times 22^{\prime \prime}$ W. Al silicon electronics and composite video input make an ideal unit for schools, clubs, shops etc. Supplied in a used but working condition.
owlyess.00 pues e9.50 cante $\subset$ Iws.
14" COLOUR superb chassis monitor made by a subsidiary of the HITACHI Co. Inputs are TL RGB with separate sync. and will plug direct into the BBC micro etc. Exceptional bandwidth with good 80 co
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## The Phone <br> That Thinks It's A Clock

The new Clock Radio Phone from Fidelity Radio, the CRP 100, is a compact unit incoporating the three facilities of radio, alarm clock and telephone. Operation is precisely the same as a conventional clock radio but with an automatic cut off when the telephone receiver is lifted. British Telcom approval is already applied for and the CRP 100 can be installed by connection to the mains and a standard British Telecom socket. The Clock Radio Phone is expected to retail at around $£ 69$, and should be in the shops by September.

Also from Fidelity is a new colour television chassis design, the ZX 3000. The chassis is smaller, more advanced with reduced component volume and covers eight programme channels. Advantages across the range include greater reliability, less weight and an even more economical price for high quality vision and sound reproduction.

You'll find the new chassis in some new models, the CTV $22 T$ and CTV $20 T$ Teletext models, the CTV 22R remote control model and a new 14 inch colour monitor. The existing CTV 20R and CTV 14S portable will continue in the range but with the new chassis.

## Micros Go <br> Down The Drain

ntel Corp has introduced its "Data Pipeline" hardware and software designed to link mainframe computer data bases with personal computers. The Data Pipeline is built around Intel's Data-Base Information System, iDIS 86/735, a microcomputerbased "traffic-manager" that interprets, stores and distributes mainframe data to and from terminals and personal computers. The Data Pipeline uses an enhanced version of Intel's System 2000 Data-Base Management System (DBMS). System 2000 currently handles data storage and retrieval in hundreds of large mainframe computer installations.

New Data Pipeline extensions to System 2000 provide a relational data-base capability, graphics and a fourth-generation software architecture, System 2000 On-Line Operation (SOLO).

Intel also has broadened its 700 family of Data-Base Information Systems with the iDIS 86/730, an iDIS, version aimed at original equipment manufacturers and large-volume end users. Intel Corporation S.A., Rue du Moulin a Papier, 51, Boite 1, B-1160 Brussels, Belgium.


## Speedy Prototypes

Omputing Techniques (Mfg) Limited has introduced a fast and economic service on operational amplifiers designed and built to special requirements. Now, all but the most exacting orders can be met in two weeks or less. Many orders can be turned round in as little as seven days, and this British company is offering prices at up to $25 \%$ lower than those quoted by other major
manufacturers.
The service should prove of great interest to small and medium quantity users, who often experience difficulty in eqsily, or economically, meeting specifications when selecting standard linear integrated circuits from the major suppliers. Computing Techniques supplies a complete, packaged operational amplifier which it guarantees will
meet the customer's specification, however tight. Computing Techniques (Mfg) Ltd, Brookers Road, Billingshurst, West Sussex RH14 9RZ.

## Please . .

A the time of writing, ETI is A still without a replacement for Peter Green, our former deputy editor turned Computing Toady. So please, keep your enquiries of us to a minimum, and follow the guidelines on page $\mathbf{7 0}$.

## A Little Pull Helps

Miniature solenoids for use Viwhere space is at a premium have been introduced by Magnetic Components to augment its established recording head capability. Manufactured to meet manufacturers' specific requirements, the units are expected to have particular application to electronic security locks, access controls, low power system controllers, printer mechanisms, electronic camera shutters, etc.

Typical is one producing a latch pull force of 150 gms when 1.9 volts D.C. is applied to the 75 ohms coil. In addition to the solenoid component, MCL provides methods of controlling the unlatching force so as to give a higher level of release time repeatability; this is achieved by a specially developed keeper and/or the use of 'kick-off' electronic circuitry. Magnetic Components Limited, Bridge Wharf Industrial Estate, Chertsey, Surrey KT16 8L.J.

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 performanoust Karger plug gaps can be used, even wet or badly fouled plugs can be fired with this system.
* TOTAL ENERGY DISCHARGE is a unique system and the most powerful on the market - $31 / 2$ times the power of inductive systems $31 / 2$ times the energy and 3 times the duration of ordinary capacitive systems. These are the facts:
Performance at only 6 volts (max. supply 16 volts) SPARK POWER - 140 W , SPARK ENERGY - 36 mJ SPARK DURATION - $500 \mu \mathrm{~S}$, STORED ENERGY - 135 mJ LOADED OUTPUT VOLTAGE

50 pF load - $38 \mathrm{kV}, \quad 50 \mathrm{pF}+500 \mathrm{k}-26 \mathrm{kV}$ We challenge any manufacturer to publish better performance figures. Before you buy any other make, ask for the facts, its probably only an inductive system. But if an inductive system is what you really want, we'll still give you a good deal.

* All ELECTRONIZE electronic ignitions feature:

EASY FITTING, STANDARD/ELECTRONIC CHANGEOVER SWITCH, STATIC TIMING LIGHT and DESIGNED IN RELIABILITY ( 14 years experience and a 3 year guarantee).

* IN KIT FORM it provides a top performance system at less than half the price of comparable ready built units. The kit includes: pre-drilled fibreglass PCB, pre-wound and varnished ferrite transformer, high quality $2 \mu \mathrm{~F}$ discharge capacitor, case, easy to follow instructions, solder and everything needed to build and fit to your car. All you need is a soldering iron and a few basic tools.
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* 60 SECOND ALARM PERIOD flashes headlights and sounds horn, then resets ready to operate again if needed.
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* INSTANT ALARM OPERATION triggered by accessories or bonnet/boot opening.
* 30 SECOND DELAY when system is armed allows owner to lock doors etc.
t DISABLES IGNITION SYSTEM when alarm is armed.
* IN KIT FORM it provides a high level of protection at a really low cost. The kit includes everything needed, the case, fibreglass PCB, CMOS IC's, random selection resistors to set the combination, in fact everything down to the last nut and washer plus easy to follow instructions.

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## What Is It?

W
e know what it is - but do you? We get all sorts of obscure objects passing under our noses here at ETI, but this particular one seems to mark a new all-time height in enigma.

Actually, it's use is pretty boring, so we're not giving any prizes for the correct answer. However, the most ingenious incorrect answer will get some prize, though what it is, we haven't decided. It could be a guided tour of the ETI office, or it may even be one of these things - you'll have to wait and see.

## Filters For CD

D IFA have introduced a new series of hybrid active filters, which have been designed for anti-aliasing applications in equipment such as digital audio systems. Constructed in RIFA's thick film process, the PBA 3167 and PBA 3179 hybrid active filters feature an extremely flat response over their operational passband. Included in their circuitry is a group-delay equalising network, which effects a constant groupdelay response within $+\mathbf{1 0} \mu \mathrm{s}$ up to 19 KHz in the case of the PBA 3167 , and up to 13.5 KHz for the PBA 3179.

Cut off frequencies are specified as 20 KHz (3167) and 15 KHz (3179). Accordingly, the filters are ideally suited for applications in digital audio systems having sampling frequencies from 48 to 50 KHz , or 32 KHz . RIFA AB, Market Chambers, Shelton Square, Coventry.

## More Motorola Manuals

M
otorola have been busy scribling away, and the following new books bring the total number of Motorla tehcnical publications up to 22: B012A - 8 bit Microprocessor Data Manual; B012B - 16 bit Microprocessor Data Manual; B001 - A/D and D/A Conversion Manual; B0015 - Switchmode and TMOS Power

## Accurate Robot

Zehntel's Series 600 Robotic Board Handling System, the first fully operational robot aid for automatic testing equipment, is described in a 4 page brochure The RBHS is easily programmed and has a unique 6 axis motion. Positioning accuracy is $0.022^{\prime \prime}$, believed to be the best available.

As well as loading and unloading boards at the test head, the RBHS can also be used for light mechanical tasks connected with testing, such as operating switches etc. It will also handle simple assembly tasks if required. Zehntel Limited, 62 Tanners Drive, Blakelands, Milton Keynes MK14 5BP.

## Noisy Fault Locator

Anew British manufacturered and designed portable Audio/Visual faults locator is now available from Antron Electronics Ltd, Hamilton House, 39 Kings Road, Haselmere, Surrey GU27 2QA. Codenamed Toneohm 700, the instrument is used to locate short and partial short circuits caused by solder bridges, poor etching, partial device failure, i.e. substrate shorts, leaking capacitors etc., by tone or meter readout without removing com-

## Twin Stripper

A B Engineering Company has developed a unique new cable stripper for the fast and safe removal of outer sheathing from twisted pair coaxical cables. Also suitable for other cables of irregular cross section, the new Coax-3 incorporates a spring loaded cutting head which when revolved accurately follows contours to produce a precise circumferencial cut to a predetermined depth. The operator then simply twists the cutting blade through 90 degrees and the outer sheating is separated along the axis of the cable for ease of removal. AB Engineering Co., Timber Lane, Woburn, Milton Keynes MK17 9PL.
ponents or cutting tracks.
The instrument has four ranges to allow resistance measurement up to 20 k and D.C. voltages between $\pm 20 \mathrm{~V}$ to be made, so giving a complete range of diagnostic capability in one instrument. Kelvin needle probes are used for fault finding and are protected against accidental connection up to $\pm 30 \mathrm{~V}$.

The instrument can also source a test voltage to stimulate circuit under test, with a maximum output voltage of 0.55 V and maximum output current 150 mA .


Transistors; B038 - Linear/Switchmode Voltage.

These are available from Motorola Distributors, or write to

## Shorts

- United Components Ltd, Unit 5, Wye Estate, London Road, High Wycombe, Bucks HP11 1LH now carry stocks of Clare Reed and mercury wetted relays, as well as many other GI and related companies' products.
- Any manufacturer wishing to exhibit at the fourth Middle East Electricity and Electronics Exhibition in Kuwait in January 1984 should contact the British Electrical and Allied Manufacturers' Association (BEAMA) at 8 Leicester Street, London WC2H 7BN.
- Peripheral Hardware Ltd, Unit 13, Monkspath Industrial park, Highlands Road, Shirley, West Midlands B90 4NY have been appointed as an authorised dealer for Epson printers and personal computers.
- Apples have fallen. A new 256K Apple III will now cost a mere $£ 2,395$ (it was $£ 2,869$ ); the 128K Apple III is no longer available. Apple Computer (UK) Ltd, Eastman Way, Hemel Hempstead, Herts HP2 7QH.
- AKG are setting up a studio sound award, for professional and non-professional sound engineers. Details from AKG Acoustics Ltd, 191 The Vale, London W3 7QS.
- Instem Computer Systems Limited, formerly known as Kratos, has launched a new software programme for the Hewlett Packard HP-86 microcomputer to perform industrial monitoring and control functions using instem's range of Link-On input/output stations. Instem Com-

Motorola's European Literature Centre (at Milton Keynes) for the leaflet, describing each publication and detailing prices. Motorola Semiconductors, 88 tanners Drive, Blakelands, Milton Keynes.

- One of the consequences of the general election being called at fairly short notice was the fall of a bill that would have made it illegal to sell "any machine capable of reproducing a sound recording . . . where the naute of the machine is such that the primary or substantial use to which it will be likely to be put is ikely to result in the unauthoris ed making of any record embodying the recording ..." $"$. Sounds ike a cassette deck they were talking about, doesn't it?
- Thorn EMI have just released a leaflet describing a new range of rubidium caesium photocathode photomultiplier tubes. Contact the Sales Department, Thorn EMI Electron Tubes Limited, Bury Street, Ruislip, Middlesex HA4 7TA.
- Belling Lee have published a comprehensive 64 page catalogue of their range of RFI filters for up to $\mathbf{8 0 0}$ amps. Belling Lee, 540 Great Cambridge road, Enfield, EN1 3QU.
- Cotswold Electronics, Unit T1 Kingsville road, Kingsditch Trading Estate, Cheltenham GL51 9NX have issued a leaflet describing their "budget range" of toroidal mains transformers.
- Tasbian, the automated electronic assembly plant near Plymouth, have also issued a leaflet, describing their design, manufacturing and test facilities. Tasbian Ltd, $2 / 3$ Burrington Way, Plymouth, Devon PL5 3LS.
- You'll no doubt be pleased to hear that the 20 companies involved in the development of magnetic disc stills cameras have reached agreement of standardisation of the disc. This should prevent the usual problem of a multiplicity of standards confusing the consumer, such as happened in video.

OFFICIAL DEALER


WORD PROCESSOR 'VIEW' 16K ROM £52 TELETEXT ADAPTOR £195.00

Please phone for availability

BBC Model B $\mathbf{f 3 9 9}$ including VAT plus $£ 8$ carr.
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FLOPPY DISC INTERFACE
Incl. 1.2 operating system $\mathbf{£ 9 5}+£ 20$ installation

## BBC FLOPPY DISC DRIVES

Single drive $51 / 4^{\prime \prime} 100 \mathrm{~K} £ 230+£ 6$ carr. Dual drive $51 / 4^{\prime \prime} 800 \mathrm{~K} £ 699+£ 8$ carr.

## BBC COMPATIBLE DRIVES

These drives are self powered and supplied in BBC matching colour cases. SINGLE 100K £180; 200K £250; 400K £330
DUAL 200K £350; 400K £475; 800K £590
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Features include:
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£ $\mathbf{3 4 5}+\mathrm{£} 8$ carr.

## MONITORS

MICROVITEC 1431 14" Colour Monitor............ $£ 249$ + $£ 8$ carr. MICROVITEC 2031 20" Colour Monitor............ $\mathbf{£ 3 1 9}+\mathbf{£ 8}$ carr. KAGA 12" RGB Monitor . . . . . . . . . . . . . . . £255 + £8 carr. Lead for KAGA/SANYO RGB
 SANYO Hi Res RGB Monitor $\mathbf{\varepsilon 4 4 5}+\Sigma 8$ carr.


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80 Cols 30 CPS Full ASCII e GRAPHICS Full ASCII e GR Now onlye180+ £6 carr. GP250A £235 plus £8 carr.
Paraliel Printer lead for BBC/Atom to most printers f 13.50 Variety of interfaces, ribbors in stock
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alternatively these can be used for electrical alternatively these can be used for electrical
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## MICROTIMER

6502 Based Programmeable clock timer with

* 224 switching times/week cycle
* 24 hour 7 day timer

4 independent switch outputs directly imerfacing to thyristor/triacs
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Output to drive day of week switch and status LEDS.
Full details on request. Price for kit $£ 57.00$


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One of the ironies of life is that the largest single factor in determining how well any sound system performs has nothing to do with the system itself: it is the room

Now you could adopt an all-ornothing approach and take a pneumatic drill to your listening room, to get rid of all those resonances, throw out all the furniture, because it's too absorbent, and keep everyone else out of the listening area, because clothes absorb sound too (and bodies, so it's no good asking your friends to go naked). However this could lead to a certain amount of domestic unrest, and anyway, would be impractical at a large scale concert.

A less radical approach that has fewer disadvantages is to tailor the response of the electronics so that the overall response of the audio system plus the room is neutral. This is most usually done with a graphic equaliser.

While octave equalisers do have some use, most equalisers supplied for domestic use are pure gimmickry. If you're taking your equalisation seriously, the only real option is a $\frac{1}{3}$-octave device, which, by pure coincidence, is what we're about to describe here.

The ETI $\frac{1}{3}$-octave equaliser is of sufficient quality not to seriously degrade the sound of a high quality system. It should be noted however, that the use of any $\frac{1}{3}$-octave equaliser will affect the performance of the system simply because it is in circuit. Each of the filters has a

# 1/OCTAVE EQUALISER 

relatively high Q and will therefore cause significant modification to the overall phase linearity as well as the frequency response when cut or boost is applied. I have seen many otherwise high quality systems degraded significantly by the excessive use of $\frac{1}{3}$-octave equalisers and we do not recommend the incorporation of these units into a high quality system unless a specific need is apparent. Nevertheless, when modification of the frequency response is required, no matter how drastic or how modest, a $\frac{1}{3}$-octave graphic equaliser is an almost ideal way of doing this.

Each channel of the equaliser is controlled by a separate slider potentiometer so the array of pots gives an approximate indication of the response inserted.

## Design

Each filter is formed by a series resonant network incorporated into the feedback loop of a high quality operational amplifier. In this case we have used the NE5534N.
'Gyrators' are used to simulate


Fig. 1
The input resistor forms a potential divider with part of the potentiometer

Circuit diagram of the main section of the Graphic Equaliser

## GRAPHIC

# Is your listening room/concert hall dull and lifeless? Or does it echo and ring like the Whispering Gallery? David Tilbrook comes to the rescue with a project that will help get your response into shape. 

## HOW IT WORKS

(from the op-amp + input to the wiper) and the impedance $Z$ to common. The feedback resistor also forms a potential divider with the end of the pot from the inverting input and the impedance $Z$ to ground.

If the wiper of the pot is set to midtravel, the attenuation of the input signal due to the potential divider is compensated by the gain of the op-amp and the overall gain from input to output is unity. If the pot wiper is now moved toward that end of the pot connected to the op-amp's inverting input, the gain of the stage is increased as the feedback ratio is reduced owing to a reduction of the impedance from the op-amp's inverting input to common. At the same time less attenuation of the input signal occurs as the impedance from the noninverting input to common is decreased. The stage will have gain, maximum gain being determined by the impedance of the series resonant networks. If this is low, gain will be high. Series resonant networks exhibit very low impedance at resonance, rising either side of that frequency.

When the wiper of the pot is moved toward the non-inverting input of the op-amp, the attenuation due to the input potential divider is increased. The gain of the op-amp is decreased at the same time as the feedback ratio is increased because the impedance from the inverting input to common is increased. Once again, the overall gain of the circuit is a function of the impedance of the series resonant circuit, but this time the gain is at a minimum - in fact, attenuation occurs.

By choosing a suitable $Q$ for the series resonant network, the bandwidth
can be set to cover a desired frequency range. The potentiometer then sets gain or attenuation of the stage at the centre of the chosen frequency band.

The technique just described above can be used whenever it is desired to incorporate a relatively large number of filters into the signal path as in graphic equalisers or tone controls. The filter networks need not be bandpass or notch filters, simpler bass and treble controls can also be used.

Once this basic configuration is set up, all that remains is to design the filter networks. As mentioned before, series resonant networks were used since these give the required characteristic of low impedance at the resonant frequency. In their simplest form these networks consist of an inductor, capacitor and resistor in series. At the resonant frequency, the impedance of the circuit is equal to that of the resistor assuming a perfect inductor and capacitor were used. To eliminate the inductor an op-amp circuit has been used to simulate the characteristics of an inductor. Such a circuit is called a 'gyrator'.

The gyrator circuit can provide both the inductance and the series resistance required in the network so this can simply be placed in series with the capacitor to form the required resonant circuit. This is shown in block diagram form in Fig. 2.


Fig. 2


Fig. 3

the inductors necessary for the series of band-pass filters so there are no coils to wind. The gyrator is covered in more detail in the How it Works section, but the main problem associated with this approach is caused by phase shifts occurring in the op-amps used in the gyrators. The basic principle of a gyrator is to invert the phase response of a capacitor to simulate the characteristics of an inductor. The problem is that all amplifiers introduce a phase shift which increases towards the extremes of the frequency response. For this reason, care must be taken when choosing op-amps for use in gyrators at the top end of the frequency spectrum. This problem is accentuated when the $Q$ of the filters concerned is increased. Since the Q of the filters must be higher in a $\frac{1}{3}$-octave equaliser, an op-amp with greater phase linearity at high frequencies must be used. Fortunately, op-amps with the desired characteristics are not difficult to obtain and we are using the TL074 or uA774. These are both quad FET op-amps with almost identical performance and are capable of excellent results in the circuit, even in the top-most filter.

## Earthing

In any large multi-component audio system, earthing is a continuous nightmare because of hum loops. The theory says that the entire audio circuit should be earthed at only one point. However, adding an extra earth connection can sometimes cure a hum that you've spent many hours chasing round the circuit - this is one of those occasions when the best approach is to leave it like that (but keep your fingers firmly crossed), hope it holds while the band is playing, and forget about it afterwards . . .

The other problem is deciding exactly where this one earth should be in the first place. Some people say the very start of the signal path - or, at least, as close as you can get to it. Others say that it should be at the power amp input. But everyone agrees - it's definitely never at the graphic equaliser. OK, then, what connection should there be between the mains earth and the graphic equaliser electronics? Some opt for a capacitor ( 100 n is a reasonable value); others use a resistor (10 to 100R). We've left this up to you. However, all agree that there must also be a very low


Circuit of the power supply for the Graphic Equaliser.
impedance path to earth through the rest of the equipment for safety reasons: if there isn't, you're asking for a nasty shock. Note also that you must use insulated audio input and output sockets, and be careful not to inadvertently connect the signal and mains earth in any other way - watch how you secure the PCB to the case, for example.

## Capacitors

One possible area of trouble is in finding a supplier for the capacitors. If you look at the Parts List, you'll see that lots of the capacitors are in the E12 series: we hadn't realised how many suppliers had switched to supplying just the E6 series until we tried to locate a
supplier for a suitable type. In the end, we used Siemens polycarbonate layer types from Cricklewood for all the capacitors above 10 n in value, and mixture of these and polystyrene for those under 1 n 0 . Ours were all $5 \%$ types or better, and we'd strongly suggest that you use 5\% tolerance or better for all the capacitors in the gyrators. We've designed the PCB so that it should accommodate most types you're likely to want to use.

Next month we will give constructional details of the Graphic Equaliser, including details of a super specially designed case from Newrad Instruments.


This is the completed PCB - more details next month.



## CRICKLEWOOD ELECTRONICS LTD



# THE MOTOCAR 

# Could this fairly ordinary looking Lancia car be the Car Of The Future? Motorola certainly think so. Read on and see if you agree. 

At a press conference recently, Motorola revealed their Motocar (who says ETI has a monopoly on bad puns?). It's a Lancia Delta with a lot of electronics where there used to be switches and yards and yards of copper wire.

Even though they were very nice to us, impressing the press wasn't Motorola's reason for spending the equivalent of several Rolls Royces on building the car. Their main targets are the car and car component manufacturers. Not only are Motorola trying to persuade them that they need to bring car electrics up to date, but that they should use Motorola products to do this; and further, that they should work closely with Motorola to get the products they need. So, the car itself is by no means a finished design, it's a try out for a large number of different ideas.

## Why Go Electronic?

Conventionally-wired cars have been around for quite a while now, so why should anyone want to change? Well, from the manufacturers' point of view, there are a number of good reasons.

Firstly, and always firstly in manufacturing, there's cost. At present, a wiring loom for a car costs around $£ 35$. Motorola's electronic system, with a great host of additional features that we'll be looking at in a moment, would cost around $£ 100$ to build after a tidy up of the prototype. Obviously, Motorola learned a few things during the building, and some newer components better suited to the project have since come on the market, so with a little optimisation, the cost could probably be halved to $£ 50$. However, once you start to use specially made components, a lot more cost saving can be carried out, and the price would easily go below that of the conventional loom, especially if the price of copper rises. And the electronic system will almost certainly be a lot easier to install.

Secondly, there's reliability. Well designed electronics should always be much more reliable than electromechanics. An electronic loom would considerably reduce the number of connectors in the system. Not only would this improve the car's reliability after sale, it would also cut down on manufacturing costs. Did you know that around $40 \%$ of all cars straight off the production line require some remedial work to be done on their electrical systems?

Thirdly, by using electronic control, the engine's performance can be much improved. Fuel efficiency is very important in Europe, but over in the States, tough antipollution legislation has led to emphasis on emmission control. It was, in fact, this legislation that provoked the interest in electronic engine control in the first place - who says that conservation is always bad for industry?

Finally, an electronic control system can offer driver and servicing facilities very much more easily than the i conventional system; for example, fault finding and fault monitoring can be automated.

## Why The Rush?

Motorola are keen to get into the market as soon as possible. They reckon that by $1987,7 \%$ of the European semiconductor market will be for cars. Naturally, they're expecting a certain amount of competition, but they hope that their early start will catch their competitors napping.


This is what the Motocar no longer has - a conventional (messy) wiring loom containing about 500 metres of wire.

## Take A Bus

At the heart of Motorla's electronics is a four-wire multiplexed bus. This links the master control unit to a number of outstations that are responsible for controlling



The layout of the main bus and the fibre-optic bus: the fibre optic bus radiates out from where you'd expect the gearshift to be. The main bus is in a H -shape and is drawn with a solid white line. and monitoring the car's functions. The master control has a separate interface with the controls on the steering column, and there is also a completely separate fibre optic system that controls some 'luxury' items.

The four wires of the bus have the following functions: power (positive, negative return through chasis), signal earth, data, and bus control. Data transmission is sequential and bidirectional in half duplex mode with autoclocking and RZ. The control line is used to sort out which unit is sending what to where. The power line is for the controlled devices as well as for the outstations themselves, so it has to be fairly meaty. All the other conductors can be light weight.

## Outstations

All the outstations are based on the Motorola MC6805P2 microprocessor, as is the master control unit. Motorola now say that they would now use the cheaper and more appropriate MC6804P2, but it was not available when the design work was done.

The simplest of the outstations are the load-switching units, of which there are six, all identical. Each is capable of switching eight loads of up to 10 A , although in the present car, the most that is used in any one unit is seven (some units switch rather fewer). Because $N$-channel devices are cheaper to make than P-channel ones, $N$ channel T-MOS FETs are used to switch between the ground connection to the controlled devices and the car chasis; this means that the controlled load is always at positive supply potential with respect to the chasis, and this could lead to a corrosion problem.

Motorola's proposed solution is to use an on-board DC to DC converter to provide a higher voltage supply of 30 V . This will allow the N -channel FETs to switch reliably at the standard supply voltage (nominal 12 V ) in common drain mode, even when the system is suffering from a


Motorola's smart power device - to be used for future load switching stations.
reduction in supply voltage (for instance, while the battery is turning over the engine on the starter motor). Surprising though this may seem, it is apparently cheaper to do this than to use P-channel FETs.

Each load switching unit has an A-to-D converter on board, so that it can measure, for example, the output from the petrol tank gauge. It also has a load-state monitor that can sense blown bulbs. This information is all transmitted back along the bus to the central control.

Further development of the load switching unit will almost certainly involve Motorola's smart power device, soon to enter production. These devices have a T-MOS power transistor with logic and drive components on the same chip (together with, perhaps, a DC to DC converter). These may make it feasible to use single load switching units, with each load on the bus having its own dedicated switching unit. In order to avoid increasing the number of connections by using these units, they may well be housed within the controlled load itself.

## Condition Monitoring

Hard at work just gathering and transmitting data is the vehicle condition monitor, one of the four other types of outstation. It sits inside the engine compartment and monitors fluid levels and other information to be presented to the driver. A single A-to-D is used to monitor the alternator output, and, as well as being used to provide the system's equivalent of an ignition warning light, this also determines the control of the battery charging.

The other three outstations are semi-autonomous in that they are not under direct central control: for instance, you could not have the engine control unit having to deal with an interrupt when it was just about to fire a spark plug. Besides the engine control unit, there are the cruise control, so that you can take your foot off the accelerator and the car will maintain a steady (and legal) speed, and the climate control, a posh phrase for an air conditioner control unit.

The engine control has a pressure transducer in the inlet manifold; it also measures the position of the distributor arm in $0.35^{\circ}$ increments (by using a phase locked loop to multiply up the output from a position sensor). Thus it can control the spark and fuel injection timing to a resolution of $0.35^{\circ}$; it can also control the amount of fuel injected. However, the opening and closing of the inlet and outlet valves is still done mechanically.

## Alternative Bus

To avoid running lots of wires from the controls mounted on the steering column, Motorola engineered a much more simple multiplexed bus to link the steering column to the central control unit. It's a three wire bus, with unidirectional data flow; the encoding is done with standard CMOS, rather than a microprocessor (shame!).

Gone is the familiar needle and dial speedo - it's replaced by a flourescent digital display, as in the latest Maestro. The display itself is an off the shelf Futaba plasma display, driven by (you've guessed. it) a dedicated microprocessor, this time an MC6801. To be fair, the micro is kept busy displaying a range of messages (in a choice of several languages and imperial or metric units) on two separate LCD displays to tell the driver about the state of the car, distance travelled, etc. For instance, it is this that will tell you if one of bulbs has blown.

One interesting feature is the use of an EEPROM to store the odometer reading. When the ignition is turned off, power is held on long enough to allow the old reading to be erased and the new one written into the EEPROM. Motorola have put quite a lot of though into developing a system that is as secure as possible, to prevent


The Futaba display used, in ful action. Unfortunately, you can't see that the figures are in a rather reasuring green, and the main beam indicator is in a baby blue . . .
unscrupulous dealers from being able to wind back the reading.

## Multiperplexed?

Motorola claim that rather than confusing the poor mechanic, the multiplexed bus system should make ser-

vicing and repair easier - but only provided that the garage buys a special adapter! This special unit would, when plugged into the car, take control of the bus and run the car through a test sequence, with a readout that tells the mechanic what needs attention. The special adaptor units should be around the size of a largish pocket calculator and cost about $£ 150$ (a special one will be needed for each car type).

## Shine A Light

The other multiplex system uses optical fibres to control seat positioning servos, windows, door locking and window mirror positioning. This system is entirely separate from the other bus system, and it has its own control unit; it's also arranged in a different way, as a star network. Each unit has its own line running back to the control unit: this was necessary because of the lack of T-junctions for optical fibres.

Motorola's optical fibre system is unusual in two ways. Firstly, the same devices are used as transmitters and receivers, thus allowing two-way transmission along a single fibre. Because only short transmission distances are involved, signal strength is not a problem and a relatively inefficient detector can be tolerated.

Secondly, visible light is used because this will make it easier for a "mechanic" (perhaps Motorola had better invent a new term for this person while they're at it) to detect if a signal is present. The cost of the components needed for a visible light system is also considerably lower than those for an infra-red system.

Motorola say that fibre optic systems are used because they offer higher noise immunity and lower weight than copper wire systems. However, the fact that they've been confined to the luxury features of the car is probably evidence of a reliability problem - or is it just my cynicism?

## Parting Shot

One last system that we haven't mentioned yet is the radio. It too (yes!) uses a dedicated microprocessor, and even though it's a Grundig radio, the micro is a Motorla MC6220. It's worth reflecting that Motorola started out by making car radios; having partially surrendered that market and gone on to greener pastures, they're now set to invade the rest of the car.

Above: one nice touch not mentioned so far is that Motorola have tackled the problem of supply line transients at source, the alternator, by replacing three out of nine of the diodes with transient suppression types.

The present test unit (right) is just a little bigger than a hand-held calculator (though this does depend on how big your hands are . . ) Apparently the production model would need little more than a (yes, another) microprocessor, some ROM (possibly on board the micro) and a display.


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# TV STORAGE SCOPE 

The Tele-Scope 200S is a storage oscilloscope which utilises a number of electronic techniques to convert signals into a form suitable for display on a television screen. The unit is designed around the latest technology in analogue-to-digital conversion, using the Ferranti ZN441 flash converter to convert the input signal to a six-bit binary word at a point in time, which is determined by the sample rate. This sample rate can be selected to be up to 10 million samples per second (or one sample producing one word every 100 nS ).

The resulting words are stored in one of two memories, which take it in turns to store the incoming
data and display the stored data. In manual trigger mode, the incoming signal controls the changeover of the two memories, so if the input signal only occurs once, or is removed, the last signal is displayed on the screen. When measuring DC levels, the auto trigger mode is used to give a continuous trigger, as the DC level has no recognisable transitions for the circuit to begin operation.

## Functional Description

The analogue input is taken via the input coaxial socket to a switchable attenuator and is buffered with a source-coupled FET. A second FET in the source of this buffer puts a DC offset on the
buffer, which can be varied to produce a shift in the position of the display.

The resultant analogue signal is fed to the analogue-to-digital flash converter which produces a six-bit word for each sample. The sample rate can be up to 10 MHz (one sample every 100 nS ), and is selected using the front panel switch marked 'width' in the same way that a conventional scope has a time base selector. The six-bit word will appear on the data bus and the input address clock, which is running at the sample rate, writes the data in to the selected memory.

When both the memory cycle and frame scan are completed, the memory is switched to read and the


## We conclude our description of this innovative piece of test gear with the circuit of the data processing board and full constructional details. Design and development by Ian Gooderson.



Fig. 2 Overlay for the front panel PCB, containing the input attenuator.
output address counter is routed to this memory for the output sequence to begin. The output address counter is cycled at 5 MHz and the six-bit words are output to a comparator, which compares the word to the video line number. If. the six-bit word and the line number are equal, the comparator outputs a data bit to the video summing amplifier.

This data is then mixed with the line and frame sync pulses and output to the UHF modulator. The display has a memory map which is 247 pixels wide by 64 pixels high; each pixel is 200 nS wide and four lines high, utilising a screen area of 49.4 nS wide and 256 lines high. This data is repeated on the second field of the interlaced TV scan to prevent any flashing on the display, even at high input frequencies.

The memories are addressed by two counters and during the input cycle the input address counter is running at the same rate as the A-to-D, storing each data bit in a separate serial memory location. The output address counter is running at a constant 5 MHz as the display time is constant.

## Construction

The Tele-Scope contains two double-sided, plated-through hole circuit boards, one for the data storage and routing, one for the control logic and analogue input buffering. The case cover has a
small PCB on the reverse, which holds some of the components associated with the input and trigger controls and the corresponding input sockets. The power supply board is mounted in the rear of the case, with the transformer to the left (viewed from the front).

First build the data storage and routing board the circuitry for this board is described in this month's article). All the components are mounted on the circuit board (see the overlay in Fig. 5). All the connections to this board are made via a 16 -way jumper lead to the control logic board (whose circuitry was described last month). All the ICs used are LS TTL series, with the exception of the memories and the ZN441. Although this latter chip is not static-sensitive, great care should be taken when handling it because it is the single most costly item in the equipment. For this reason it should be mounted in a 24-pin IC socket.

Next, build the control logiclinput buffer board. All the chips on this board are LS TTL, with no special handling problems for them or any of the other discrete components used. A 16-way jumper lead connects this board to the data processing PCB. A 13 -way length of ribbon cable should also be connected at the other end of the PCB at this point in the proceedings, for later connection to the sampling frequency selector




HOW IT WORKS
IC1 is the ZN441 A-to-D converter. This converts the analogue input signal into a six-bit binary word at rates up to 10 million samples a second. The six-bit word from each sample is then passed to the memory buffers IC6 and IC7. If IC6 and IC20 are selected, then data is written to the memory block IC15, 16. In this case IC19 and IC7 are in the high impedance output mode, and IC20 enables data from the second memory block, IC17, 18, to be read to the comparator IC23, 24. The memory and buffer enables also select the address buffers, routing the input addressing to IC15, 16, and the output addressing to IC17, 18 in this example. In other words, in this phase of the operation IC11 is allowing the synchronous counters IC2 and IC3, which are acting as an eight-bit address counter, to cycle IC15, 16 synchronously with the sample rate, while IC14 is allowing the output address counter IC4, 5 to address IC17, 18. IC12, 13 are disabled to block the respective unwanted address lines. On the alternate phase, ICs 11 and 14 would be disabled, ICs 12 and 13 would be enabled, and the two memory blocks IC15, 16 and IC17, 18 would receive the opposite sets of address signals. The output of IC8 goes low when the input address cycle is completed, while the output of IC10 goes low when the output address cycle is complete.

IC21, 22 form the 256 -line counter, which is advanced every four video lines for a total of 64 steps. This six-bit output is compared with the six-bit output from the memory by IC23, 24, and when these are equal, a data bit is output to the video summing circuit. The output of IC25 goes low when a count of 256 is completed.

When the input cycle is complete (the input memory block is full), the input clock is disabled. The output clock is only enabled for 256 lines of the scan and is therefore disabled during the first and last lines of the display and the frame flyback time. When the input address is completed and the frame scan is completed, the memories are switched over and the new data is output to the comparator. The video data is therefore refreshed every 50 mS unless the incoming data cycle is longer.

The power supply unit has regulated +5 V and -5 V rails at 650 mA and 160 mA respectively, with a separate 5V8 supply for the UHF modulator.



Fig. 4 This is how you attach the front panel to the box to make it easier to do the interconnecting
switch (see the overlay in Fig. 1). Incidentally, Q4 was incorrectly identified on the circuit diagram last month - it is actually a BC107.

The power supply is mounted on a single-sided PCB and is supplied ready-built with the kit. The front panel switches are also PCBmounted as described earlier (see Figs. 2 and 6):

With the boards completed, the data processing board can be connected to the control logic


Fig. 5 Overlay of the data storage board


The ZN441 is an ultra-high-speed
parallel (flash) A-to-D converter comprising an array of 64 strobed comparators and encoding logic. A reference voltage applied across a tapped resistor chain defines 63 quantisation levels plus overrange and underrange, one input of each comparator being connected to the resistor string, while the other inputs are commoned and connected to the analogue input. When an analogue input is applied, all the comparators whose reference voltage is less than the analogue input will change state, ie if the input voltage is $V_{\text {REF }} \cdot n / 64$ then $n$ comparators will have tripped. The comparator outputs are decoded into a 1-of-64 format by NOR gates and then re-encoded into binary by a high-speed ROM.

Two or four ZN441s can be stacked to give a seven- or eight-bit converter with a minimum of external components. Applications include high-
speed data acquisition, video an radar data conversion, digital signal storage and image processing.

PARTS LIST the voltage control to 10 V full screen and the full screen width control (the time base) to 80 Hz FSW. On touching the input socket with your finger, a 50 Hz sine wave will be displayed (due to mains pick-up).

By switching to manual trigger and adjusting the trigger level, it is possible to remove your finger and keep the sine wave display on the screen. This feature of the TeleScope allows you to do some interesting experiments - for instance, you can examine the EMF induced in a coil when a bar magnet is dropped through it.

Try using the Tele-Scope as a sampling scope on repetitive waveforms above 250 kHz (not calibrated, but it works). You can even capture one-shot events up to 250 kHz .


BUYLINES
Hawk Electronics Test Equipment supply a full kit of parts for this project. All PCBs, the case (drilled and screenprinted), the ZN441 and all the other components are included, together with a comprehensive manual. The kit price is £89. The 'Tele-Scope' is also available built and tested for $£ 109$.

The manual may be purchased separately for $£ 1.50$ (no VAT required) refunded on the subsequent purchase of a kit or finished circuit.

Prices exclude 15\% VAT, and postage and packing is $£ 2.95$ extra. The case, $A$ -to-D converter, and the PCBs may all be purchased separately.

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## IC UPDATE

## Dear Reader,

What we've indulged in here is a slightly speculative venture, in that neither of the two ICs featured this month are actually available to the hobbyist, so far as we know (if you're in industry, then you'll be able to get the LM2877/8 from your Nat Semi supplier, and the RC4193 from Raytheon). However, we think that by bringing these to the light of day, we shall motivate someone, somewhere to stock them. And far be it from us to suggest our advertisers, but a word in their ear from you may not go amiss . . .

## RC4193 Switched Mode PSU

## What, not another switched mode PSU? Well, yes, but this one can keep your tranny going for longer, amongst many other applications.

The RC4193 is a monolithic switching regulator IC that, with very few external components is capable of stepping up or down, or inverting, with a typical efficiency of $80 \%$.


Fig. 1 RC4193 functional diagram.

## Principles of Operation

The circuit has an on chip 1.31 V band gap reference with a typical TC of 50 ppm . In addition to setting the bias currents in the 4193, this voltage is used as the reference at the input of comparators A1 and A3. The 1.31 V at the input of $A 1$ is used as the threshold to compare to the feedback output voltage at pin $7\left(V_{\mathrm{FB}}\right)$. The output of $A 1$ is fed to a NOR gate which is used to gate the output of the oscillator comparator (A2) which turns off Q1 whenever
$V_{\text {FB }}$ is greater than 1.31 V . note when the output of $A 1$ of $A 2$ is high the switch transistor Q1 cannot turn on.

The oscillator has its frequency determined by a single capacitor $\mathrm{C}_{\mathrm{x}}$ and outputs two waveforms, a square wave $V_{B}$ and a saw tooth $V_{x}$ (which appears at pin 2) both of which appear at the input of A2. The portions of the waveform where Q1 can be saturated as a result of the oscillator, are labelled $\mathrm{T}_{\mathrm{c}}$ in Fig. 2, and the portion of the waveform where Q1 would be off is labelled $\mathrm{T}_{0}$. Q1 can


Fig. 2 Switching timing diagram.
be saturated only when $\mathrm{V}_{\mathrm{FB}}$ is less than 1.31 V . The voltage of the sense line from the R3/R4 voltage divider at the $V_{F B}$ pin is directly proportional to the output voltage $\mathrm{V}_{\text {out, }}$ where $\mathrm{V}_{\text {OUt }}=1.31(\mathrm{R} 3+\mathrm{R} 4) / \mathrm{R} 4$. When $\mathrm{V}_{\mathrm{FB}}$ is less than $1.31 \mathrm{~V}, \mathrm{Q} 1$ will saturate on every negative going oscillator cycle until $V_{\text {out }}$ reaches the programmed value.

The 4193 also includes an uncommitted comparator and output transistor shown as A3 and Q2 in Fig. 1. The 1.31 V internal reference is used as the threshold at the + input of A3. The voltage divider R2/R5 is used to determine the voltage at which Q2 switches. When the voltage at LBR (pin 1) falls below 1.31 V then Q2 will saturate. A3 has about $0.7 \mu \mathrm{~A}$ of hysteresis to produce a positive turn on/turn off at LBD (pin 8). The voltage at which Q2 switches is determined by $V=1.31(R 2+R 5) / R 5$.

| Absolute Maximum Ratings |  |
| :---: | :---: |
| Internal Power Dissipation | 500 mW |
| Supply Voltage (without external series pass transistor) | 24 V |
| Operating Temperature |  |
| Range | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |
| Switch Current (lsw) | 150 mA |
| Reference Set Current (lc) | 1 mA |
| $\mathrm{V}_{\mathrm{FB}}$, LBR, $\mathrm{C}_{\mathrm{x}}$ Voltage |  |
| $L_{x}$, LBD Voltage | 24 V |

ed with any input between 9.3 V and 2.4 V :
If a lower supply voltage can be tolerated, the output of the 4193 can be set to start regulating at $7 \mathrm{~V}(1.16 \mathrm{~V} / \mathrm{Cell})$, effectively keeping the 4193 in its quiescent state (drawing only $150 \mu \mathrm{~A}$ ) until the battery voltage falls to 7.5 V . Below 7.5 V the 4193 will regulate the $\mathrm{V}_{\text {our }}$ at 7.0 V until the battery falls below 2.4 V . In this circuit R2 and R5 are used to indicate a low battery condition when the input voltage falls below 5.9 V ; any other voltage can be selected by changing the value of R2. If the Low Battery Detector circuitry is not going to be used, leave pin 1 and 8 unconnected. As mentioned previously, the top of R5 can also be connected to $\mathrm{V}_{\text {out }}$ when a low output indication is

| PARAMETERS | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Voltage, $\mathrm{V}_{\text {cc }}$ |  | 2.4 |  | 24 | V |
| Reference Voltage (internal), $\mathbf{V}_{\text {reF }}$ |  | 1.24 | 1.31 | 1.38 | V |
| Switch Current, $\mathrm{I}_{\text {sw }}$ | $V_{3}=400 \mathrm{mV}$ | 75 | 100 |  | mA |
| Quiescent Current, $\mathbf{I}_{\text {cc }}$ | Measure at Pin 5 $I_{3}=0$ |  | 135 | 200 | uA |
| Efficiency |  |  | 80 |  | \% |
| Line Regulation | $0.5 \mathrm{~V}_{\mathrm{o}}<\mathrm{V}_{\mathrm{cc}}<\mathrm{V}_{\mathrm{o}}$ |  | 0.08 | 0.5 | \% $\mathbf{V}_{\text {OUt }}$ |
| Load Regulation | $\begin{aligned} & V_{c c}=0.5 V_{o} \\ & P_{\mathrm{l}}=150 \mathrm{~mW} \end{aligned}$ |  | 0.2 ' | 0.5 | \% $\mathrm{V}_{\text {out }}$ |
| Operating Frequency, $\mathbf{f}_{\mathbf{o}}$ |  | 0.1 | 25 | 150 | kHz |
| Reference Set Current, $\mathbf{I}_{\text {c }}$ |  | 1 | 5 | 50 | uA |
| Switch Voltage | $\mathrm{I}_{3}=100 \mathrm{~mA}$ |  | 0.4 | 1 | V |
| Switch Leakage Current | $V_{3}=24 \mathrm{~V}$ |  | 0.01 | 5 | uA |
| Supply Current (Disabled) | $\mathrm{I}_{\mathrm{c}}<0.01 \mathrm{uA}$ |  | 0.1 | 5 | UA |
| Low Battery BiasCurrent | $\mathrm{V}_{1}=1.2 \mathrm{~V}$ |  | 0.7 |  | uA |
| Capacitor Charging Current |  |  | 5.0 |  | uA |
| Capacitor Threshold Voltage + |  |  | 1.78 |  | V |
| Capacitor Threshold Voltage - |  |  | 0.62 |  | V |
| Feedback Input Current | $\mathrm{V}_{7}=1.3 \mathrm{~V}$ |  | 0.1 |  | uA |
| Low Battery Output Current | $\mathrm{V}_{8}=0.4 \mathrm{~V}, \mathrm{~V}_{1}=1.1 \mathrm{~V}$ | 100 | 600 |  | uA |



Fig. 3 Basic circuit for stepping up mode.


Fig. 4 Basic stepping down circuit.

## Table 1 Electrical characteristics

The 4193 series circuits contain an internal bias network which maintains the correct operating currents to the 4193 independent of temperature and input voltage variations. The internal bias network is turned on by supplying a current $I_{c}$ between $1 \mu \mathrm{~A}$ and $100 \mu \mathrm{~A}$ into pin 1 , and no change will resuilt in the operating characteristics when $I_{c}$ is varied over this range. To shut down the 4193, simply force pin 1 to less than 0.5 V by using a mechanical switch or open collector transistor. The quiescent current in the 4193 will fall to less than $5 \mu \mathrm{~A}$ (typically less than $0.1 \mu \mathrm{~A}$ ).

## Design Equations

Table 2 is a set of design equations which can be used to determine resistor, capacitor and inductor values for step-down, step-up and voltage inverting applications. To use the equations, you determine the oscillator frequency $\left(\mathrm{f}_{\mathrm{o}}\right)$, input voltage $\left(\mathrm{V}_{\mathbb{N}}\right)$, output voltage ( $\mathrm{V}_{\text {OUT }}$ ) and maximum output load current $\left(I_{1}\right)$.

## Basic Step-Up Switching Regulator

Figure 5 is the basic low current step-up configuration to be used for most battery powered applications requiring less than 150 mW of load power (when $\mathrm{V}_{\mathbb{N}}>0.5 \mathrm{~V}_{\text {out }}$ ).

The output voltage $V_{\text {out }}$ is determined by the expression 1.31 (R3 - R4)/R4; in this case $V_{\text {out }}$ would be 9.0 V . This configuration makes a good subsititute for a 9 V transistor battery, since a stable output voltage will be achiev-

| R1(M) | Step Up | Step Down | Inverting |
| :---: | :---: | :---: | :---: |
|  | $\underline{V_{\text {IN }}-1.2}$ | $\mathrm{V}_{\mathrm{IN}}-1.2$ | $V_{\text {NN }}-1.2$ |
|  | $5 \mu \mathrm{~A}$ | $5 \mu \mathrm{~A}$ | $5 \mu \mathrm{~A}$ |
| R2(M) | $\underline{V_{\text {IN }}-1.31}$ | $V_{\text {IN }}-1.31$ | $\underline{V_{1 N}-1.31}$ |
|  | $5 \mu \mathrm{~A}$ | $5 \mu \mathrm{~A}$ | $5 \mu \mathrm{~A}$ |
| R3* | $\underline{V_{\text {OUT }}-1.31}$ | $\underline{V_{\text {Out }}-1.31}$ | $V_{\text {OUT }}$ |
|  | 1 | I, | 1 |
| R4* | 1.31 | 1.31 | 1.31 |
| R4 | $\mathrm{I}_{1}$ | $\mathrm{I}_{1}$ | 1 |
| R5 | $261 \mathrm{k} \Omega$ | $261 \mathrm{k} \Omega$ | $261 \mathrm{k} \Omega$ |
| $\mathrm{C}_{\mathrm{X}}(\mathrm{pF})$ | $2.14 \times 10^{6}$ | $\underline{2.14 \times 10^{6}}$ | $2.14 \times 10^{6}$ |
|  | $\mathrm{f}_{0}$ | $\mathrm{f}_{0}$ | f |
| $L_{\text {x }}$ | $\underline{0.3 V_{8}\left(V_{\text {OUT }}-V_{\text {IN }}\right)}$ | $\underline{0.3 V_{\text {OUT }}}$ | $0.3 \mathrm{~V}_{\text {IN }}\left\|\mathrm{V}_{\text {out }}\right\|$ |
|  | $\mathrm{f}_{\mathrm{O}} \mathrm{I}_{\text {LOAD }} V_{\text {IN }}$ | $\mathrm{f}_{\mathrm{O}} \mathrm{I}_{\text {LOAD }}$ |  |
| Cl | $2 \mathrm{~V}_{\text {out }}-\mathrm{V}_{\text {IN }}$ | $\mathrm{I}_{1, \mathrm{CAD}}$ | $\left.0.15 I_{\text {LADD }} N+2\left\|V_{\text {Out }}\right\|\right)$ |
|  | $4 \mathrm{f}_{\mathrm{o}} \mathrm{V}_{\text {OUT }} \mathrm{V}_{\mathrm{R}}$ | $4 \mathrm{f}_{\mathrm{O}} \mathrm{V}_{\mathrm{R}}$ | $\mathrm{f}_{\mathrm{O}} \mathrm{V}_{\text {IN }}\left(\mathrm{V}_{\text {IN }}+\mid V_{\text {OUT }}\right) \mathrm{V}_{\text {R }}$ |
| R6 | $35 \mathrm{~V}_{\text {IN }}$ | 35 | 35 V |
|  | $T T_{\text {IOAD }} V_{\text {OUT }}$ | $\overline{I_{\text {LOAD }}}$ | $\overline{I_{\text {LOAD }}\left(V_{\text {IN }}+\left\|V_{\text {OUI }}\right\|\right.}$ |
| R7 | R6 | R6 | R6 |
|  | 7 | 7 | 7 |

Units are ohms, fards, henries, and hertz unless indicated otherwise.
$l_{1}=$ current into pin 1 ; recommended value 100 uA .
Table 2 Design equations for the three modes.


Fig. 5 RC4193 low current step-up mode.
desired.
A feature of the 4193 is the ability to shut off when pin 6 is forced below 0.5 V . An example of this feature is shown in Fig. 6. In this circuit as long as $V_{c}$ remains low the output will regulate to 5 V . This type of circuit can be used to back up the main supply voltage when line interruptions occur, a particularly useful feature when using volatile memory systems.


Fig. 6 RC4193 battery-back-up circuit.
In step-up application, power to the 4193 can be derived from the output voltage by connecting the $\mathrm{V}_{\mathrm{cc}} \mathrm{pin}$ and the top of R1 to the output voltage (for example see Fig. 7). One requirement is that the battery voltage must be greater than 3.0 V when the circuit is energized or else there is not enough voltage at the $\mathrm{V}_{\mathrm{cc}}$ pin to operate the 4193. However, once running, the battery can decay to 1.0 V before the circuit stops operating.


Fig. 7 Bootstrapping the supply to the RC4193.

## Basic Step-Down Switching Regulator

Since the switch transistor in the 4193 is in parallel. with the load a method must be used to convert it to a series connection; the circuit of Fig. 8 accomplishes this.

The 2N2907 replaces S1 of Figure 4, and R6 and R7 are added to provide the base drive to the 2 N 2907 in the correct polarity to operate the circuit properly; refer to Table 2 for equations for component values. Since the $L_{x}$ pin is capable of sinking 100 mA , high current switching transistors can be used in place of the 2N2907.


Fig. 8. RC4193 step-down mode.

## Greater Than $\mathbf{3 0}$ Volt Application

Adding a zener diode in series with the base of the 2 N 2907 allows the input voltage to increase by the value of the zener, with only a slight decrease in efficiency. As an example, if a 24 V zener is used, the maximum input voltage can go to 48 V .


Fig. 9 RC4193 used as an inverter-regulator.

## Inverting Switching Regulator

Many single-supply systems require an occasional negative voltage for a specific application; this circuit will meet those needs.

The circuit is similar to the type used in Fig. 8, except the Icoation of $L_{x}$ and the diode are reversed and a micropower op-amp RC3078 is added to provide inversion for the sense voltage. The output voltage is 1.31 R3/R4.

The RC3078 operates at a closed-loop gain of close to one in most applications, therefore the value of $\mathrm{C}_{\mathrm{c}}$ must be such to frequency compensate the RC3078 for unity gain conditions. The value for $R_{\text {stt }}$ will determine the quiescent current in the 3078 and for micropower applications can be set to operate at approximately $10 \mu \mathrm{~A}$. Where a larger amount of amplifier quiescent current can be tolerated, a ground sensing op-amp such as the LM324 or RC3403A can be used, eliminating the need for $R_{\text {set }}$ and $C_{c}$.

An important point is that pin 7 must be below 1.3 V when $\left|\mathrm{V}_{\text {out }}\right|$ is below the design output voltage or else the circuit will not start.

Due to their output stage design, op-amp types similar in design to the 741 may cause the circuit to not start properly since they fail to keep pin 7 below 1.3 V when power
is first applied.

# LM2877/8 Audio Power Amps 

## The LM379 has had a good innings as linear ICs go, but it's now being replaced by the LM2877 and LM2878 in manufactured equipment.

The LM2877/8 are monolithic dual power amplifiers designed to deliver 4W (2877) or 5W (2878) continuous into eight ohm loads. Each power amplifier is biased from a common internal regulator to provide good power supply rejection and output centering. Both devices are internally compensated for gains greater than 10 and come in 11-lead single in-line packages.

| Absolute maximum ratings |  |
| :--- | :--- |
| Supply voltage | $26 \mathrm{~V}(35 \mathrm{~V} 2878)$ |
| Input voltage | +0.7 V |
| Operating temperature | $0^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$ |
| Junction temperature | $150^{\circ} \mathrm{C}$ |



Electrical Characteristics for LM2877: Where those of the LM2278 differ, they are given in brackets; otherwise the same figure applies.
$V_{S}=20 \mathrm{~V}(22 \mathrm{~V}), \mathrm{T}_{\mathrm{TAB}}=25^{\circ} \mathrm{C}, \mathrm{R}_{\mathrm{L}}=8 \Omega, \mathrm{~A}_{\mathrm{V}}^{-}=50(34 \mathrm{~dB})$ unless otherwise specified
Note 1: For operation at ambient temperatures greater than $25^{\circ} \mathrm{C}$, the LM 2877 must be derated on a maximum $150^{\circ} \mathrm{C}$ junction temperature using a thermal resistance which depends upon device mounting techniques.



Fig. 15 Non-inverting amplifier using split supply.

Fig. 13 Ceramic PU amplifier with bass control.


Fig. 14 Frequency response of bass control in Fig. 13.


Fig. 16 Internal circuit.

## If you missed out on Breadboard '79,'80,'81 and '82. '79,'80,'81 and time to catch up



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# UNIVERSAL EPROM PROGRAMMER 

## Get to grips with the bits using our Universal EPROM programmer. Design and development by Mike Bedford.

EPROMs provide a convenient - means of storing commonly used software, but many microcomputer users are deterred from using this media due to the difficulties of erasure and programming. EPROM programmers are both commercially available and have been featured as constructional articles in some magazines, but the former are relatively expensive and the latter tend to be limited as regards types of EPROM supported. For these reasons, it was considered appropriate to develop a universal EPROM programmer which could be built by the amateur for a modest sum. The programmer presented here will support the following EPROMs: 2758, 2716, 2516, 2732, 2732A, 2532, 2764, 2564, 27128 and 27256. This list includes every single supply version of the 27 -Series and 25 -Series devices up to and including 256k bit capacity, which seems likely to be the largest EPROM which will be produced as further development will probably be of EAROMs and EEPROMs. The programmer is intended for use in conjunction with a 6502 computer system and appropriate software is presented. Although the hardware is designed to support the 27256 , this one EPROM is not as yet supported by the software due to lack of preliminary data on this device, the chip itself having not been released by the manufacturer at the time of writing. To complete the requirements for programming EPROMs, some hints are given on building an erasure unit.

## Hardware

The hardware has been designed for a Tangerine Microtan system, the physical dimensions of the circuit board being selected such that it will plug directly into the system rack. Non-Tangerine users should not be deterred,
however, as electronically it should be quite simple to interface this card to any computer using the 6502 or 6809 processors. No special power supplies are required as all voltages required for programming are derived from a single +5 v supply.

Although the PCB has sufficient space for a zero insertion force socket to be mounted on board, it is probably more convenient, especially if the card is to be rack mounted, to mount this socket in a separate console, making a connection with a length of ribbon cable. Note that a switch should be provided on the console to isolate $\mathrm{V}_{\mathrm{pp}}$ and $\mathrm{V}_{\mathrm{cc}}$ on pins 1 and 28 respectively. This is necessary since inserting or removing an EPROM with these supplies present may result in its destruction.

If the console is not used, switch SW1 should be mounted on board to isolate these supplies. On EPROMS which have $\mathrm{V}_{\mathrm{pp}}$ and $\mathrm{V}_{\mathrm{cc}}$ on other pins, these supplies may be isolated under program control. It will also be noticed that only a single zero insertion force socket
has been provided on PCB. This decision wastakenon grounds of economy and some constructors may prefer to add a 24 -pin socket in addition. If a single $28-\mathrm{pin}$ socket is used, 24-pin devices should be inserted into the bottom part of the socket, ie leaving pins 1,2,27 and 28 empty. Table 1 shows the pin outs of all EPROMs which are supported.

The programmer applies a TTL level to all pins with the exception of pin 1 which is connected to $V_{\text {pp, }}$ pin $14(0 \mathrm{~V})$ and pin $28(+5 \mathrm{v}) . \mathrm{V}_{\mathrm{pp}}$ is program selectable to +25 V , +21 V or +5 $\checkmark$ and may also be applied to pins 22 and 23 as an alternative to a TTL level. To summarise this information, the programmer may be considered as a glorified 29-bit output port. This being the case, and since the circuitry required to implement the above would not completely fill a $8^{\prime \prime} \times 4 \frac{1}{2}$ " PCB, it seemed appropriate at the cost of only two more ICs to add four additional 8-bit output ports to make maximum use of the board space available. These ports are completely independent from the


The Universal EPROM Programmer. Note that this prototype differs slightly from the fiñal version - in particular, the component numbering is completely different!


Fig. 1 Circuit diagram of the Universal EPROM Programmer.
programmer.

## Software

In order to be a useful development tool, the following functions are the minimum requirements of an EPROM programmer:

1. Read data from an EPROM into computer memory;
2. Compare the contents of an EPROM with the contents of computer memory;
3. Test an EPROM for erasure; 4. Program an EPROM from data in computer memory, verifying each byte as it is written.
In fact, some additional functions have also been added and these will be described under the section on using the EPROM programmer.

Table 2 is a mode selection table for all supported devices, showing the read, program, verify and standby modes, these being the modes necessary to implement the

The design is based around four 6821 20-bit PIOs. The circuitry comprising IC3, IC4 and IC5 provides the interface to the Tangerine bus and, in conjunction with the links, allows the board to be configured to occupy a 16-byte block within the 1 k I/O area. IC1 and IC2 are used to buffer various signals in order that no more than one TTL load is presented to any bussed input. Of the four 6821 s , IC6 and IC7 provide the four independent l/O ports. These being connected to the outside world via SK1 and SK2, whereas IC8 and IC9 are used to drive the EPROM programmer. Most of the signals required to drive the pro-


HOW IT WORKS
grammer are TTL levels and are taken directly from the two 6821s to the ZIF socket, SK3. Pin 26 on SK3 is slightly different in that although it is a pure TTL signal on all 28 pin devices, it is the $V_{c c}$ supply on all EPROMs in 24-pin packages and hence requires a much greater current capacity than is available from a port on a 6821. Q1 and Q2 are therefore used to switch the +5 V supply to SK3 pin 26 under the control of IC9/CB2 (pin 19). A similar technique is used to switch $V_{p o}$ onto SK3 pins 22 and 23 using transistor pairs Q5/Q6 and Q7/Q8 respectively. Since these two pins on SK3 are also required to present TTL
levels under different conditions, they are also connected to 6821 ports, isolating these signals by use of Germanium diodes D1 and D2 respectively. It should be noted that when a 6821 is required to drive a transistor, $A$ ' $B$ ' port is used, these having a greater current sourcing capacity than ' $A$ ' ports, and when a port needs to be isolated by a diode, an ' $A$ ' port is used as these give a full +5 V high signal so that, even allowing for the voltage drop across the diode, a good TTL high is presented. The $V_{p p}$ supply is generated from the +5 V supply using IC11, a 78S40 switching regulator IC, in connection with timing
components L1 and C4. Although this component should be capable of regulating $V_{p p}$ to within the required limits, experiments showed that it is advisable to select R7 and R8 such that IC11 would output about +30 V and use a separate LM 317 MP regulator to give the required voltage. Since $V_{p p}$ may need to be $+25 \mathrm{~V},+21 \mathrm{~V}$ or $+5^{\mathrm{p} \mathrm{\rho}} \mathrm{~V}, \mathrm{Q} 3$ and Q4 are used, to switch out portions of the resistor chain between the regulator adjust terminal and 0 V hence altering the output voltage. These two transistors are connected to IC8 PB4 and PB5 (pins 14 and 15) hence allowing $V_{p p}$ to be changed under program control.
$\left.\left.\begin{array}{|l|l|l|l|l|l|l|l|l|}\hline 27 & 27 & 25 & 27 & 25 & 27 & 27 / & 27 & \text { IC/ } \\ 256 & 128 & 64 & 64 & 32 & 32 / \\ \text { PIN } \\ \text { 25 }\end{array}\right] \begin{array}{l}58\end{array}\right]$


| IC | $\begin{aligned} & 27 \\ & 58 \end{aligned}$ | $\begin{aligned} & 271 \\ & 25 \\ & 16 \end{aligned}$ | $\begin{aligned} & 27 / \\ & 32 \\ & 32 \mathrm{~A} \end{aligned}$ | $\begin{aligned} & 25 \\ & 32 \end{aligned}$ | $\begin{aligned} & 27 \\ & 64 \end{aligned}$ | $\begin{aligned} & 25 \\ & 64 \end{aligned}$ | $\begin{aligned} & 27 \\ & 128 \end{aligned}$ | 278 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 128 |  |  |  |  | vcc | vcc | vcc | vcc |
| 127 |  |  |  |  | $\overline{\text { PGM }}$ | $\overline{\text { CS2 }}$ | $\overline{\text { PGM }}$ | A14 |
| 24/26 | vcc | vcc | vcc | vcc | N/C | N/C | A13 | A13 |
| 23/25 | A8 | A8 | A8 | A8 | A8 | A8 | A8 | A8 |
| 22/24 | A9 | A9 | A9 | A9 | A9 | A9 | A9 | A9 |
| 21/23 | VPP | VPP | A11 | VPP | A11 | A12 | A11 | A11 |
| 20/22 | $\overline{O E}$ | $\overline{O E}$ | $\begin{array}{\|l\|} \overline{\mathrm{OE}} / \\ \mathrm{VPP} \end{array}$ | $\frac{P D /}{P G M}$ | $\overline{O E}$ | $\frac{P D I}{P G M}$ | $\overline{O E}$ | $\overline{O E}$ |
| 19/21 | AR | A 10 | A 10 | A10 | A10 | A10 | A 10 | A10 |
| 18/20 | $\begin{aligned} & \overline{\mathrm{CE}} / \\ & \mathrm{PGG} \end{aligned}$ | $\begin{aligned} & \overline{\mathrm{CE}} / \\ & \mathrm{PGM} \end{aligned}$ | CE | 411 | CE | Al1 | $\overline{\mathrm{CE}}$ | $\overline{\mathrm{CE}}$ |
| 17/19 | D7 | D7 | 07 | D7 | D7 | 07 | D7 | 07 |
| 16/18 | D6 | 06 | D6 | D6 | D6 | 06 | D6 | O6 |
| 15/17 | D5 | D5 | D5 | 05 | D5 | D5 | D5 | O5 |
| 14/16 | D4 | D4 | D4 | D4 | D4 | D4 | D4 | 04 |
| 13/15 | D3 | D3 | D3 | D3 | D3 | D3 | D3 | D3 |

Table 1 Pin-outs of all the EPROMs our programmer will process.


## PROJECT : EPROM Programmer

To get the full picture, the timing diagrams of each device should be scrutinised, but for the general user this information is probably not relevant. To summarise, however, EPROMs generally require about 50 ms to program each byte giving total programming times of from around 50 seconds for a 2758 to 13 minutes for a 27256. You may consider these times rather long, especially for the larger devices and although not implemented in the software presented here, there is an alternative programming method referred to as the intelligent programming mode which can reduce programming times by a factor of six for the 2764, 27128 and 27256. Figure 2 is a flow diagram of the functions which need to be performed in order to implement this method of prorgramming. Notice that $V_{c c}$ needs to be increased to +6 V for this process to
be carried out and that there is no provision in the hardware to select this value of $V_{c c}$ under program control. This need present no great problem, however, to the user wishing to implement this mode, as +6 v may be switchable from the programming console, perhaps by deriving this voltage from the system +12 v supply.

Any additions to, or modifications of the software will need to be made in the light of the information presented in Tables 3 and 4 . These may be described as a programmer's view of the EPROM programmer, Table 3 showing the address of each register and Table 4 indicating which bits of the various 6821 ports connect to which EPROM pins or perform the various control functions required.

Table 3 Register addresses.

## Construction And Alignment

Although the circuit of the programmer is of sufficient complexity that if it were to be produced commercially it would probably be double sided, it is not so complex that a single sided board would be impossible to design. It was considered that cost would be of prime importance to an amateur building a one-off project, and on these grounds it was artworked as a single-sided board. As a result of this, a number of insulated wire links need to be inserted prior to fitting the components. No special instructions are required on the fitting of components with the exception of socket SK3 and switch SW1. If a separate programming console is to be used, then an ordinary low profile DIL socket should be used as SK3, and two wire links should

| Table 4 Port functions. |  |  | FUNCTION | 6821 NUMBER | REGISTER | OFFSET FROM BASE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | IC9 | DDRA/ORA | 00 |
|  |  |  |  | IC9 | CRA | 01 |
| $\begin{aligned} & 6821 \\ & \text { NUMBER/ } \\ & \text { PORT } \end{aligned}$ | SK3 PROCRAMMER TUNCTION |  | REGISTERS | 1 C 9 | DDRB/ORB | 02 |
|  |  |  | ```FOR EPROM PROGRAMMER``` | IC9 | CRB | 03 |
|  | PIN | PROGRAMMER FUNCTION |  | IC8 | DDRA/ORA | 04 |
|  | NO. |  | PROGRAMMER | IC8 | CRA | 05 |
| IC9/PB0 | 10 | A0 |  | IC8 | DDRB/ORB | 06 |
| IC9/PB1 | 9 | A1 |  | 1 C 8 | CRB | 07 |
| 1C9/PB2 | 8 | A2 |  | 1 C 7 | DDRA/DRA | 08 |
| IC9/PB3 | 7 | A3 | REGISTERS | 1 C 7 | CRA | 09 |
| IC9/PB4 | 6 | A4 | FOR | 1 C 7 | DDRB/ORB | 10 |
| IC9/PB5 | 5 | A5 | GENERAL | 1 C 7 | CRB | 11 |
| IC9/PB6 | 4 | A6 | PURPOSE | IC6 | DDRA/ORA | 12 |
| IC9/PB7 | 3 | A7 | I/O | IC6 | CRA | 13 |
| IC9/PA0 | 25 | A8 | PORTS | IC6 | DDRB/ORB | 14 |
| IC9/PA1 | 24 | A9 |  | 1 C 6 | CRB | 15 |
| IC9/PA2 | 21 | A10 (EXCEPT 2758), AR (2758) |  |  |  |  |

IC9/PA3 23 A11 (2732/32A/64/128/256), A12 (2564)
IC9/PA4 2 A12 (2764/128/256), $\overline{\text { CS1 }}$ (2564)
IC9/CB2 26 A13 (27128/256), VCC (2758/16/32/32A, 2516/32)
IC8/PA0
IC8/PA1
IC8/PA2
IC8/PA3
IC8/PA4
IC8/PA5
IC8/PA6
IC8/PA7
IC8/CA2
IC8/PB0
IC8/PB1
IC8/PB2
IC8/PB3
IC8/PB4
IC8/PB5
replace SW1. On the other hand, if the programmer is intended to be self contained on a single board, a zero insertion force DIL socket should be used as SK3 and switch SW1 is needed. Once the construction is complete, the links need to be configured to place the board at the desired address. The offset from the start of the I/O area is 16 times the binary number represented by the links. The best way to illustrate exactly how the links are used is probably graphically; Fig. 3 shows a few examples.

The other part of the circuit which requires setting up is associated with $\mathrm{V}_{\mathrm{pp}}$ generation. This is very important as EPROMs will be destroyed if $\mathrm{V}_{\mathrm{pp}}$ is more than 0 V 5 too high. The best way to check this
is with a voltmeter probe on the output of IC2, with IC8 removed from its socket. Apply +5 V to pin 14 of the IC8 socket and adjust RV3 until +5 V is recorded on the test meter. Now replace +5 V on pin 14 by +5 V on pin 13 of 1 C 8 socket and adjust RV2 for +21 V on the meter. Finally, remove +5 V from pin 13 of IC 8 socket and adjust RV1 for a potential of +25 V at IC2 output. Setting up is now complete.

## Using The Programmer

The first stage in using the EPROM programmer is to load and run the support package software which is written in BASIC and assembly code. The user will first be prompted for EPROM type, valid responses being $2758,2716,2516$, 2732, 2732A, 2532, 2764, 2564 and 27128. A request for a base address will then be made and the user should respond with a four-figure hexadecimal number. The base address is the address relative to which all references to computer internal memory are made and this means that the user does not need to be concerned with the actual absolute addresses in computer memory. The reasons for being able to select the base address are two fold. Firstly, to fit in with the memory map of any computer on which the software may be run and, secondly, to allow more than one set of data to be maintained at one time by using different base addresses. A table showing all the commands available will the be printed before the *? prompt appears on the screen. When this prompt indicates that the program is waiting for input of a command, it is safe to insert or remove EPROMs from the ZIF socket, provided, of course, that the isolating switch is in the OFF position for 28 pin devices.

The following describes the function of the EPROM programmer support package commands. It should be noted that in each case, either the whole word or the initial letter may be used:-
(N)EW, (B)ASE These two commands cause the user to be prompted for a new EPROM type or a new base address respectively, hence allowing these two parameters to be altered without exiting and re-running the program. (T)EST This performs a test on the EPROM, reporting whether or not it has been erased.
(R)EAD, (P)ROGRAMME, (V)ERIFY

These commands cause the user to be prompted for start and finish addresses which should be entered


Overlay diagram of the programmer.
as two four-figure hexadecimal numbers separated by a comma. These addresses define the portion of the EPROM to be used and also the portion of computer internal memory into which data will be written for a read operation or from which data will be read for program or verify operations. The computer
addresses are offset from the base address. In program and verify, any discrepancies between EPROM and computer memory will be reported. Such discrepancies will be printed, one per line, stopping after every 16 lines. Pressing return at this point will return to the *? prompt, whereas pressing any other key will

## PROJECT : EPROM Programmer


continue the programming or verification.
(L)IST, (M)ODIFY These commands probably duplicate facilities available in the computer monitor but are included here to allow minimum changes to be made to computer memory or data to be checked without the need to exit from this package. LIST will request start and finish addresses and will list on the screen the addresses and data of the portion of memory requested, offset from the base address. The listing will stop after every 16 lines at which point pressing return will exist to the *? prompt, whereas pressing any other key will continue with the listing.


Fig. 3 Setting the address offset by wire links.

MODIFY will prompt for a single address, the contents of the base address offset by the value given being displayed on the screen. Entering an X at this point will return to the *? prompt leaving the data in that location unchanged, whereas entering a two-figure hexadecimal number will cause the relevant address to be updated with the data entered.
(H)ELP Lists all the commands available and reminds the user of the currently selected EPROM type and base address.
(E)XIT Causes the program to terminate.

In the above commands, any wrongly entered information will result in self-explanatory error messages. The one error message which, perhaps, requires a word of explanation is TYPE/RANGE INCOMPATIBLE. This error is a result of entering a start and finish address to any command which defines a range larger than the capacity of the selected EPROM type.

## Erasing EPROMs

EPROMs are erased by exposure to ultra-violet light through the transparent window on the top of the package. Small commercially available erasing units with capacities of up to six chips cost typically in the $£ 40-£ 50$ region. If the requirements for erasing EPROMs are considered, it becomes obvious that an erasing unit can be constructed for considerably less than the price of commercial equipment. The requirements stated by EPROM manufacturers to erase such a device are a 20 to 30 minute exposure of $2357 \mathrm{~A}(253.7 \mathrm{~nm})$ wavelength ultra-violet light at an intensity of $12000 \mathrm{uW} / \mathrm{cm}^{2}$. The Philips TUV 15 W tube emits UV at the required wavelength and at an intensity of $37 \mathrm{uW} / \mathrm{cm}^{2}$ at about one inch from the tube, the distance metre from the tube, which
corresponds to about 12000 $\mathrm{uW} / \mathrm{cm} 2$ at a distance of one inch, as used in most EPROM erasers.

The tube costs in the region of $£ 10$ and will fit into an ordinary 15 " fluorescent light fitting, it provides the basis for a relatively inexpensive unit which could accommodate about ten ICs.

A few words of caution are appropriate at this point. Ultra-violet radiation, and in particular shortwave UV as emitted by the TUV 15 W , is harmful to both the eyes and the skin. It is therefore essential to build the tube into a light-tight cabinet, ideally with a micro switch fitted under the lid to isolate the supply when opened, which will prevent UV light from coming into contact with skin or eyes.

EPROMs may also be erased by UV of longer wavelength ( 3000 A 4000 A) although longer exposure times will probably be required. Since 'black light' tubes of the type used for disco lighting emit at about 3500 A and are more easily available than short-wave UV tubes, it may be worthwhile experimenting with this type of light source.

One final point on the topic of erasing EPROMs is that both sunlight and ordinary fluorescent tubes emit some radiation in the 3000 A - 4000 A region with the result that prolonged exposure to these light sources will result in erasure. For this reason it is recommended that an opaque adhesive label is used to cover the windows of programmed EPROMs.
Next month, we'll describe the
software for the unit.

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## Hyconomiser

## Dear Sir,

I have been a more or less regular reader of ETI since its inception and I think generally you do a pretty good job; however, you have now made a serious error by recommending a product which you are not really qualified to pronounce upon. I refer to the feature on the Hyconomiser in your June issue.

If, instead of questioning the garages involved in selling the device, who you must admit have a vested interest, you had spoken to a recognised authority on the subject, I am sure you would have been told not to waste your time on it, and certainly not to give any recommendation. Talking to garage mechanics about engine fuelling and exhaust emission technology (a subject not far behind aerospace and electronics for expenditure and complexity) is rather like asking an electrician to comment on the latest microprocessor.

For many years, including a number as a senior development engineer at Jaguar Cars, fuel economy had been a specialist subject of mine and devices such as the Hyconomiser invariably come my way for appraisal. In fact, two or three years ago this device was being marketed as the PED with a rather different story behind it. I enclose a copy of the advertising material relevant to that incarnation which you will see employs the same diagram you reproduced. In an attempt to win me over I was visited by the alleged inventor of the device, a certain Canadian called Andrew MacGuire, and was presented with an impressively thick report, produced in the USA, purporting to show how effective it was. To the uninitiated it was most convincing but on close examination most of the tests were based on unrealistic baseline figures, in one case an exhaust CO level of $8 \%$ (a vastly over-rich condition) was used as a basis for comparison. I still have a copy of the report if you should wish to see it.

Whilst it may be that your reviewer obtained a measurable improvement after fitting the device, any air bleed into the inlet manifold (which in essence is what the Hyconomiser is) will have a mixture
weakening effect more or less compensating for wear of the carburettor metering orifice. For the car in question a new carburettor, or even simply replacing the metering needle costing about $£ 3$, would undoubtedly have shown similar, if not better gains. In any event the only way to test a device such as this is under carefully controlled conditions and even then so many variables can be involved that results are likely to have a spread of $10 \%$ or so.

As for the claim that fuel droplets are broken up with sonic waves generated by three pulsating balls, if you believe that I would also expect you to look for fairies at the bottom of your garden. For sonic waves to pulverise fuel droplets it is necessary for a large amount of energy to be expended in shearing the mixture and even then economy gains are unlikely to exceed $10 \%$.

The claim that the device maintains the air/fuel ratio close to stoichiometric is quite unfounded. If you can find out how this is achieved pleased let me know - nobody has been able to explain to me how it can happen.

Finally, if a device such as this was able to offer the improvements claimed, the motor manufacturers would be already using it rather than running multi-million pound retooling programmes to obtain relatively modest gains, and electronic engine management systems (surely a more appropriate subject for you to become involved in) would be unnecessary.

If you wish to maintain the high reputation of your magazine I think you would be well advised to print a retraction on the subject as soon as possible.

$$
\begin{aligned}
& \text { Yours faithfully } \\
& \text { Roger Bywater } \\
& \text { Roger Bywater Engineering Ltd } \\
& \text { Stockport } \\
& \text { Cheshire }
\end{aligned}
$$

## The author replies:

Much more about the Hyconomiser, its inventor and history was known to me before undertaking the review, but my brief from the editor was to confine the report to 1,000 words so much had to be omitted. The device reduces pollution as well as effects fuel economy. In Canada where it was first produced, pollution control
is of greater interest than economy because of government controls, hence it was called the PED., (Pollution Elimination Device). Out of 400 devices tested by the American Environmental Protection Agency, the PED came out first. To the British motorist economy is the more important factor, hence the change of name. There is nothing sinister in that.

Mr Bywater scorns the idea that sonic waves produced by plastic balls can break up fuel droplets. Perhaps he would likewise scorn the suggestion that a piece of flapping paper could permanently damage scores of human eardrums at a range of many yards, yet if he would visit his nearest disco he will find loudspeaker cones doing just that. It is the power actuating the medium, not the 'medium itself that is important. Here, the power is provided by the vacuum generated by the engine intake or induction stroke. If this has sufficient power to atomise fuel from the carburettor in the first place why should it be insufficient to complete the job by means of the Hyconomiser?

The balls are in fact tuned to resonance, and when resonating any physical object is capable of high amplitude oscillation which can even be destructive, as any student of mechanical stress engineering would know. The effect has in fact been observed in tests carried out by a motor manufacturer. An intake manifold with transparent quartz window was assembled to a production engine. With normal running, drops of fuel could be seen coursing the over the inside surfaces to be ejected with the exhaust, but when Hyconomiser was switched in by a control valve, the drops instantly vaporised into a mist. Special rapidexposure photographic techniques and precision measurements revealed the particle size to be in the region of eight microns.

The motor manufacturer, (a wellknown name that I cannot reveal at present) will in fact be using the Hyconomiser in future production models. It has not been used before because car design and planning is usually at least three years ahead of current production. Other manufacturers are actively considering it.

Mr Bywater's assertion that my economy figures were due to a leaner mixture produced by bleeding air past the carburettor can be refuted on two counts. First, the carburettor mixture setting must be made slightly richer before fitting the Hyconomiser to compense for the extra air thus restoring the correct air/fuel ratio. Se-
cond, when the device went off tune for reasons described in my review, the consumption climbed to its former figure. On re-tuning, the economy returned. According to Mr Bywater tuning should have made no difference as air was still passing through. He alleges that the air bleed merely neutralizes wear in the fuel needle and orifice of the carburettor, and that a replacement would have the same result. However, the device has been fitted to brand new cars with notable economy improvements.

The 8\% CO emission figure he complains of does indeed represent an abnormally rich mixture, some 9:1 airffuel ratio. However, this was merely the starting point of a graph plot which shows the comparative CO emissions with and without the Hyconomiser, over a wide range of mixtures from 9:1 to 20:1. You have to start a plot somewhere, usually it is best to do so outside of the area of intersst so that a complete picture is presented.

As to the question as to how the stoichiometric (optimum) 14.6:1 ratio is maintained by the device, this is done firstly by improved atomisation and distribution of fuel. Thus the anomalies of lean and rich areas in different cylinders and parts thereof are avoided. Secondly, the lowest ball in the Hyconomiser seats against its exit orifice. This expands and contracts according to the vacuum in the inlet manifold and so controls the airflow. Thereby the amount of air, hence mixture ratio, varies in response to engine speed and throttle setting, so compensating for the mixture vagarities produced by the carburettor under differing conditions.

Garages who fit the Hyconomiser may indeed, as Mr Bywater says, have a vested interest, but not of the sort he insinuates. All those I contacted were reputable main-agent establishments with large factory-trained staffs and the latest in elecronic tuning and diagnostic equipment. Their vested interest is in their reputation which would soon be in tatters if they fitted rubbish that didn't work. As the proprietor of a fuel injection and economy development company, may I suggest that HE is the one that has a vested interest in knocking the Hyconomiser!

Vivian Capel.
We doubt whether we have heard the last of this topic. For those of you still sending us missives by various means, the Hyconomiser is now made under licence by: Atwell Construction Ltd, Station Road, Wrington, Avon, Tel: Bristol 719441.

## Compact Disc

Dear Sir,
The article in your May '83 issue concerning recommended highquality turntable systems was punctuated by a small section on the new Compact Disc format. As the author observes, 'in about a year' $C D$ players will be altogether affordable, probably considerably cheaper than specialist analogue turntables and therefore likely to oust them on grounds of both performance and value. At that time, second hand (one year old), once-expensive analogue players would presumably be demoted to duties such as doorstopping or pot-throwing in view of their likely marketability. I would therefore query the advisability of any recommendation to buy high quality analogue disc equipment.

Of course, my premis in such a sweeping statement is that CD will be a vastly superior format, bringing me onto my second beef; why so little space for such an important breakthrough, about which plentiful information has been distributed in advance certainly to many Hi-Fi magazines who have seen fit to publish reports on the subject? Now I would not expect you to give a high priority to reporting on Hi-Fi matters normally, though surely the presence of an audio article anyway might have been used to better advantage.

I would certainly be interested to know what levels of fidelity are claimed for the pressing and then optimal reading of vinyl discs so that these could be compared with the plentiful information available for digital format.

The world of audio performance assessment has become altogether cluttered with highly subjective judgement. Hi-Fi magazines persist in, what seems to me, the odious convention of allowing poets with 'golden ears' to subjectively assess nebulous qualities, transferring little hard information to the reader. For reference, it would be interesting to see, published alongside a reviewer's observations, the results of a thorough hearing test performed on him at a relevant time. Could controlled levels of distortion, on identical pieces of recorded music, be used to test a 'golden auditor's' true calibration?

Personally, I find it difficult to give more credence to the doubtless limited-bandwidth response of ageing reviewers than laboratory test equipment. Admittedly, standards of distortion measurement are confusing, and it is not sufficiently relevant
to music reproduction to be testing pure tones, but why not simply compare 'scope photos of microphone signals at the recording studio with the ultimate, played-back signal from analogue and digital discs? Could such a test be arranged or would vested interests prevent it?

An article devoted to bringing more rigour into Hi-Fi assessment along these lines would certainly have my fullest attention, and should not be irrelevant to the aims of your magazine, since it would not labour the subject of the Hi-Fi market or the present state of the manufactured art, but would deal rather with 'transduction'.

Yet more relevant, of general interest and, as yet, unreported (to my knowledge) would be an investigation of the digital disc format and its protocol; how are the discs manufactured, what is this error correction business? Would it be at all possible to produce an alternative erasible and programmable, ideally compatible, optical recording system, perhaps by populating a disc fully with reflective logic cells but positioning an optically switchable window in front of each?

Presumably it is the variability in performance of D-to-A convertors that accounts for the present variability in CD player performance. Would it be conceivably possible to delay the mapping to as late a stage as possible, having a (perhaps integral) D-to-A power output stage, or is the process of amplification (and transduction) best left to conventional analogue techniques? An acquaintance has described to me his attempts to make a digital switching amplifier. (A-to-D-to-A), but, presumably digital speakers could not successfully operate composed of an array of switching drivers, (one twice the size of the next) whilst the idea of linear stepper motors attached to conventional diaphragm(s) would seem unrealistic.

Any ideas?
Yours faithfully
Mr P.E. Cox
Derby
Mr Cox raises far too many points for us to answer in the space available! Suffice to say that CD will not make analogue players obsolte because there will still be a lot of analogue discs around for many years to come. We'll obviously be reporting further on CD in the future.

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# TECH <br> TIPS 

## Keyboard <br> Alphanumeric Lock Switch

G. Franklin, Mid Glamorgan

If your ASCII keyboard is like mine, then you too will have become tired of having to press 'SHIFT' for capital letters. SHIFT lock is useless because you have to 'unlock' to use the numbers. This circuit does away with this problem by providing you with yet another key to press. This is the 'ALPHA LOCK' switch.

If you study the ASCII codes you will find that to change ' $a$ ' into ' $A$ ' requires only the removal of the logic ' 1 ' on the data bit 5 line (of the 8 bit bus from the keyboard to your computer).

| e.g.:- | a | A |
| :--- | :---: | :---: |
| ASCII code | $\$ 61$ | $\$ 41$ |

in binary:- 0110000101000001
We require to alter the complete alphabet, this stretches from a (\$61)

to $z(\$ 7 A)$. The circuit cheats slightly, by altering codes $\$ 60$ to $\$ 7 F$ inclusive, the only problem with this is that $\$ 7 \mathrm{~F}$ is the ASCII code for 'DELETE'. IC1 deals with this, by detecting the $\$ 7 \mathrm{~F}$ code and making sure D5 stays at a logic 1 via the NAND gate at the end of the circuit.

One quarter of the 74LS132 is used to give a degree of switch bounce to S1. Every time S1 is pressed, the Q output of the latch (IC 3) will change, R1 and C1 ensure that on power-up the output is at a logic 1. This, along
with data bit 6 being ' $\mathrm{HIGH}^{\prime}$ will make D5' a 'LOW', thus shifting a $\$ 60$ code down to $\$ 40$ (unless of course it was the 'DELETE' key that was pressed). The LED will come on to show when the ALPHA mode is selected. To insert this circuit into your keyboard, it is only necessary for D5 to come via this circuit, D5' then continuing in its place to the computer, the other data lines just connect to the circuit. The only other requirement is that you insert a switch somewhere.

## Controller For Model Traffic Lights

## P. Bailey, Glasgow

The circuit shown was devised to control LED traffic lights at a road junction in a model railway layout, for added realism. IC2, a decadé counter, together with IC3a and IC3b, generates the normal traffic light sequence continuously at a rate determined by the oscillator formed around IC1a and IC1b. A monostable formed by IC1c and IC1d is triggered by a ' 1 ' on the 'GREEN' output, and inhibits IC2 for a period set by $\mathrm{C}_{\mathrm{T}}$ and $R_{T}$, thus causing the green light to be on for a longer period than the others.

IC4 selects either LIGHT 1 or LIGHT 2 to display the sequence, these being selected alternately. When one light is changing or at green, the other is held at red.

The controller is easily expanded to operate more than two lights by using further outputs from IC4, but if
 (eg a 4013), set to toggle, LIGHT 1 and LIGHT 2 being connected to the Q and $\overline{\mathrm{Q}}$ outputs respectively. With a little ingenuity, realistic-looking traffic
lights can be constructed from miniature LEDs and empty pen refill tubes.


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# RADIO CONTROL SERVO FAll-SAFE 

## Up, up and away? Bring your model aircraft down to earth without a bump using the ETI Conservo. Phil Walker shows you how to make your servos fail safe.

No-one who has spent many hours and pounds constructing their radiocontrolled boat, plane or animated aardvark would want it to sail, fly or crawl out of range of their transmitter. However, there is always the remote possibility that some nasty little gremlin will creep in and throw the spanner in the works. When that happens all your controls may well be stuck in the last position you used; and by a well-known Law of Nature (Lex Divoti*), the position will make it as hard as possible to retrieve your model.

Help is at hand! This project will set your servos to any position you require (if you fit it beforehand) and if set up correctly your precious model should sail, fly or waddle home safely.

## Use

The ETI Conservo is simply connected into the servo leads (get the polarity right) and the variable resistor can then be set to make the servo take up what you hope is a safe position. This setting is best done with slightly used batteries, to get a true position. Do not have the radio link operating while doing this or you won't be able to make the setting.

If your system uses low voltage (ie, around 3.6 volts) don't forget to increase the size of C 2 to its higher value.

## The Circuit

The circuit of this project is designed around a 4093 CMOS quad Schmitt trigger. This device will operate down to quite low supply voltages while still providing reliable switching performance.

One section of the device (IC1a) is used to detect the possibility that the input signal has stuck at a high


Fig. 1 Circuit diagram of the Conservo.

## HOW IT WORKS

R1 and R2 protect the inputs of the CMOS devices from overload or static damage. The normal input to the device is a series of 1 to 2 ms positive pulses repeated at about 20 ms period. When the input is low, IC1a holds C1 high via D1 thus maintaining IC1b pin 2 at a high level.

When a pulse arrives, the output of IC1b goes low, discharges C2 and forces the output of IC1d high for the duration of the pulse. When the input pulse goes low again, C2 starts to charge via RV1 and R4 but will be discharged again by another input pulse before its voltage reaches the trigger threshold of IC1c. This means that the output of IC1c will stay high permanently in normal operation.

If another pulse does not arrive and the input stays low, the output of IC1b will stay high and C2 will charge up until its voltage reaches the threshold of IC1c
whose output will then go low and discharge C2 via RV1, R5 and D3 until the lower threshold of IC1c is reached. IC1c's output will then go high again and the cycle will repeat. When the output of IC1c is low, the output of IC1d will be forced high. The component values used in the circuit are such that this cycle approximates to the normal servo control signal with RV1 controlling the pulse width.

If for any reason the input sticks at a high level, C1 which is normally kept charged up by IC1d will have time to discharge effectively putting a low level on the input to IC1b. This will cause the same sequence of events to occur as when the input stuck low but after a short delay.

Finally, D4 and C3 form a reserve power supply to ensure that the circuit is not affected by supply line transients.


A small project (this is actual size) but a long name - and a large degree of usefulness.
level. Since normally the input only goes high for relatively short periods at a time, the output from IC1d spends most of the time at a high level. This fact is used to charge a capacitor to near the supply potential. This capacitor can only be discharged by a resistor connected in parallel with it and then only if it has enough time to do so. In normal operation this will not be the case.

The second part of IC1 (IC1b) combines the original input signal with the 'stuck at high' signal and its output goes high when either is low. Note that this output only usually goes low when normal control pulses are being received. The low periods of the output from $\mathrm{IC1b}$ are used to discharge the timing capacitor of the oscillator formed around IC1c. The output from this oscillator would normally be a series of low going pulses of approximately the same frequency as the usual control pulses. The width of these pulses can be set by the variable resistor to simulate any servo position required. However these pulses will only be generated if the normal input fails to discharge the timing capacitor within a reasonable time.

The output from the gated oscillator described above and the output from IC1b are combined in IC1d to give the final output from the unit.

In normal operation the output will follow the input with no alteration as C 1 will not discharge enough and C2 will not charge enough to have any effect. In a fault condition, however, the output form IC1b will stay at a high level (driven either by the input being low or by IC1a detecting the input high) and enable the gated oscillator (IC1c) whose output will be passed on to the servo being controlled. This will send the controlled servo to its preset position.

## Construction

This should pose no real problems provided that care is taken to put the diodes, IC1 and C3 the right way round. This is a little more awkward than usual since some components must be mounted vertically to conserve PCB area.

A socket is recommended for IC1 - at least the first time you build it. Take care when handling IC1 to discharge yourself of any static charge. This also applies to your soldering iron which must be earthed. CMOS devices are still
delicate things.
RV1 can be either vertical or horizontal mounting and the PCB is laid out for both. If you want to save space and weight use a vertical mounting device and cut off the end of the PCB as shown by the dotted line. Inputs and outputs can be soldered directly to the board or alternatively, there are some 0.1 in . pitch PCB connectors available from Maplin in 3 way versions which would do the job.

The whole unit could be mounted in a box or possibly just varnished to keep out moisture (but not if the device is likely to be immersed!). For use on low voltages, ie less than 5 V , it may be necessary to increase the value of C2 up to 100 nF in order to maintain the required output timing.

PARTS LIST

| RESISTORS ( ${ }^{1} \mathbf{W}$ 5\% carbon film) |  |
| :---: | :---: |
| R1 | 10k |
| R2,3,5 | 1 MO |
| R4 | 47k |
| RV1 | 100k min. preset horizontal or vertical mounting |
| CAPACITORS |  |
| C1 | 4n7F min. layer polycarbonate |
| C2 | 47nF min. layer polycarbonate ( 100 nF for 3.6 volt operation) |
| C3 | 47uF 16 V min electrolytic |
| SEMICONDUCTORS |  |
| D1,2,3,4 | 1N4148 |
| IC1 | 4093 |
| MISCELLANEOUS |  |
| PCB: see connectors | ylines. 2 off 3 way 0.1 in . if required). |


$k=$ CATHODE
Fig. 2 Overlay diagram of the Conservo. Here we show a horizontal preset, but a vertical one could be used to save space - just cut the PCB along the marked line.


## FOOTNOTE

*For those of you who are not scholars of Latin, Lex Divoti is a Law of Nature known by a number of names, most of them not printable, but can be summed up as: "If something can possibly go wrong, it will, at the most inconvenient moment and in the way that causes maximum damage."

## BUYLINES

None of the components in this project should be at all difficult to obtain. The PCB is available through our PCB service - see page 77 for details.


| All devices guaranteed Brand New and to full spec. Items ex stock despatched same day. Prices are EXCLUSHE of VAT. Please add $60 p$ to order to cover P\&P BEF ORE calculating VAT. Payment by cheque, Educational institutes Welcome. TradeAccounts opened subjectto Official orders from Govt. Depts and We are pleased to receive overseas orders (please allow adequate postage - surplus refunded) which unless accompanied by Bankers Draft in UK Currency may be subject to Bankers Commission and slight delay in despatch. Overseas trade enquines welcome - send for full details. VAT not applicable to export orders. Price List detailing full range of components available - 30p refundable with first order. Please send SAE. |  |  |  |  |  |  |  |  |  | COMMQUIP <br> LTD <br> HAMILTON HOUSE 11 WALKERN RCAD STEVENAGE HERTS SG: 3QD <br> Telephone (0438) 729771 Telex 825378 G |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| LINEAR |  |  |  |  |  | CMOS |  |  |  | 74LS |  |  | TANTALU | 1N5408 19 |
| $555 C M O S$ | 80 | LM3909 | 70 | ZN459 | 285 | 4000 | 11 | 4518 | 39 | LSOO | 11 |  | BEADS |  |
| 556CMOS | 140 | LM3911 | 120 | ZN1034E | 200 | 4001 | 11 | 4520 | 48 | LS01 | 11 | LS155 38 |  | ZENER DIODES |
| 709 | 25 | LM3914 | 175 |  |  | 4002 | 11 | 4521 | 90 | LSo2 | 11 | LS156 $\quad 39$ | $\begin{array}{ll}0.1 / 35 & 12 \\ 0.22 / 35 & 12\end{array}$ |  |
| 741 | 14 | LM3915 | 195 | LOGIC ICs |  | 4007 | 14 | 4522 | 105 | LSO3 | 11 | LS157 25 | 0.22/35 12 | 2V1-75V 1.3 WW 14 |
| 9480001 | 35 345 | LM13600 | 105 |  |  | 4008 | 34 | 4526 | 55 | LS04 | 12 | LS158 25 | $\begin{array}{ll}0.33 / 35 & 12 \\ 0.47 / 35 & 12\end{array}$ | 5V1-75V 1.3W 14 |
| AY-3-1270 | 710 | MC1496 | 68 120 | AY5-2376 | 590 | 4009 | 24 | 4527 | 55 | LS05 | 12 | LS160 30 | 0.68/35 12 | BRIDGE |
| AY-3-8910 | 370 | MF10CN | 350 | MC1489 | 55 | 4011 | 11 | 4528 4531 | 65 | LS08. | 12 | LS161 35 | 1.0/35 12 | RECTIFIERS |
| AY-3-8912 | 540 | ML924 | 195 | MM5303 | 625 | 4013 | 20 | 4532 | 60 | 1510 | 12 | LS162 . 35 | 1.5/35 14 |  |
| CA3046 | 60 | NE529 | 225 | MM5307 | 1250 | 4015 | 39 | 4538 | 60 | LS11 | 12 | LS163 35 | 2.2/35 16 | $1 \mathrm{~A} / 100 \mathrm{~V} 20$ |
| CA3080 | 65 | NE531 | 135 | MM58174 | 700 | 4016 | 20 | 4539 | 80 | LS12 | 12 | LS164 40 | $\begin{array}{ll}3.3 / 35 & 17 \\ 10\end{array}$ | $1 \mathrm{~A} / 200 \mathrm{~V} 23$ |
| CA3089 | 190 | NE544 | 180 | TMS6011 | 365 | 4017 | 32 | 4543 | 60 | LS14 | 22 | LS168 50 | 10/25 18 | $1 \mathrm{~A} / 400 \mathrm{~V}$ |
| CA3090AO | 370 | NE555 | 16 | ULN2003 | 75 | 4020 | 42 | 4555 | 35 | LS. 5 | 12 | LS168 S170 | 15/25 28 | 1A/800V 38 |
| CA3130E | 85 | NE556 | 45 | $8{ }^{8 T} 26$ | 99 | 4021 | 39 | 4556 | 35 | LS20 | 12 | LS173 47 | $22 / 16$ $33 / 16$ | 2A/100V 36 |
| CA3140E | 36 | NE565 | 110 | $8 \mathrm{8T} 28$ | 120 | 4022 | 39 | 4561 | 100 | LS21 | 12 | LS174 36 | $\begin{array}{ll}3.3 / 16 & 14 \\ 4.716 & 16\end{array}$ | 2A/400V 40 |
| CA3161E | 100 | NE566 | 140 | 8795 | 90 | 4023 | 12 | 4583 | 80 | LS22 | 12 | LS175 | $4.7 / 16$ $6.8 / 16$ | 2A/400V 40 |
| CA3189 | 200 | NE567 | 100 | 8797 | 90 | 4024 | 32 | 4584 | 40 | LS27 | 12 | LS181 87 | $\begin{array}{ll}6.8 / 16 & 16 \\ 10 / 16 & 18\end{array}$ | 25/100V 52 |
| CA3240E | 110 | NE570 | 370 | 81 LS95 | 80 | 4025 | 12 | 4585 | 50 | LS28 | 14 | LS183 105 | $\begin{array}{ll}15 / 16 & 27\end{array}$ | $25 / 200 \mathrm{~V}$ |
| ICL7611 | 680 | NE571 | 370 | 81 LS96 | 80 | 4027 | 20 |  |  | LS30 | 12 | LS190 35 | 22/16 27 | $25 / 400 \mathrm{~V}$ |
| ICL7621 | 180 | RC4558 | 45 | 81 LS98 | 80 | 4028 | 37 | COMPUTER | ICs | LS32 | 13 | LS191 35 | 33/16 40 | 25/600V |
| ICL7622 | 180 | SL490 | 250 | 6522 | 310 | 4035 | 45 | 1802 | 650 | LS37 | 14 | LS192 35 | 47/16 40 | REGULATORS |
| ICL8038 | 290 | SL76477 | 380 | 6532 | 675 | 4040 | 40 | 2650A | 1175 | LS38 | 14 | LS194 36 | 100/16 75 |  |
| ICL8211A | 150 | SP8629 | 250 | 6821 | 110 | 4042 | 38 | 6502 | 320 | LS40 | 12 | LS194 LS195 | 15/10 22 | 78L05 30 |
| ICM7224 | 775 | TBA120S | 70 | 6845 | 650 | 4043 | 40 | 6800 | 220 | LS42 | 28 | LS195 LS196 | 22/10 24 | 78L12 30 |
| ICM7555 | 80 | TBA800 | 75 | 6847 | 650 | 4044 | 40 | 6802 | 250 | LS47 | 35 | LS197 43 | 33/10 30 | $78 \mathrm{L15} 30$ |
| LF353 | 85 | TBA810 | 95 | 6850 | 110 | 4049 | 21 | 6809 | 615 | LS48 | 40 | LS221 <br> 15 | 47/10 $\quad 35$ | $78 \mathrm{~L} 24 \quad 30$ |
| LF356 | 90 | TBA820 | 70 | 6852 | 250 | 4050 | 22 | 8035 | 345 | LS49 | 50 | LS240 50 | 100/10 $100 / 65$ | 7805T 36 |
| LM10 | 325 | TBA950 | 220 | 6875 | 485 | 4051 | 42 | 8060 | 1090 | LS51 | 14 | LS241 55 | 100/6.3 42 | $7812 T \quad 36$ |
| LM311 | 74 | TDA1008 | 310 480 | 8155 8212 | 350 | 4052 | 48 | 8080A | 250 | LS54 | 14 | LS242 5 | 63 V MINI | 7815 T |
| LM318 | 120 | TDA1022 | 480 115 | 8212 8216 | 110 100 | 4053 | 48 | Z808 ${ }^{\text {8 }}$ | 345 | LS55 | 14 | LS243 5 | MONOLYTHIC | 7824 T |
| LM324 | 30 | TL061 | 40 | 8224 | 110 | 4066 | 22 14 | Z80A | 315 | LS74 | 16 | LS244 5 | CERAMIC | $79 L 05$ 79112 |
| LM334Z | 90 | TLO62 | 60 | 8226 | 250 | 4068 | 13 | MEMORIES |  | LS76 | 16 16 | LS245 70 |  | $79 \mathrm{L12} 560$ |
| LM335Z | 120 | TL064 | 95 | 8228 | 220 | 4071 | 13 | MEMORIES |  | LS78 | 16 | LS251 | $\begin{array}{ll}10 n F & 10\end{array}$ | 7905T 60 |
| LM3339 | 45 | TL071 | 25 | 8243 | 270 | 4072 | 13 | 2101 | 395 | LS83 | 33 | LS257 3 | $\begin{array}{ll}22 n F & 10\end{array}$ | 7912 T |
| LM348 | 60 | TL072 | 45 | 8250 | 865 | 4073 | 13 | 2114(200ns) | 85 | LS85 | 39 | LS258 | $33 n \mathrm{~F}$ | 7915T. 42 |
| LM358 | 55 | TL074 | 95 | 8251 | 250 | 4075 | 13 | 2532 | 295 | LS86 | 15 | $\begin{array}{ll}\text { LS259 } & 32 \\ \\ \end{array}$ | $\begin{array}{ll}47 \mathrm{nF} & 10 \\ 68 \mathrm{nF} & 10\end{array}$ | 7924 T |
| LM377 | 165 | TL081 | 24 | 8253 | 400 | 4076 | 44 | 2708 | 225 | LS90 | 22 | LS266 18 | $\begin{array}{ll}68 \mathrm{nF} \\ 100 \mathrm{nF} & 10\end{array}$ | LM309K 130 |
| LM380 | 65 120 | TL082 | 45 | 8255 | 225 | 4078 | 13 | 2564 | 1000 | LS92 | 25 | LS273 18 | 100nF 14 | LM317K 270 |
| LM381 | 120 | TL084 | 90 | 8257 | 400 | 4081 | 12 | 2708 | 225 | LS93 | 22 | LS279 30 |  | LM317T 120 |
| LM382 | 110 | TL170 | 49 | 8259 | 395 | 4082 | 12 | 2716 (5V) | 210 | LS95 | 36 | LS283 38 | DIODES | LM323K 350 |
| LM386 | 130 65 | UA2240 | 115 | 8279 | 385 | 4093 | 23 | 2764 | 750 | LS109 | 21 | LS290 40 | OA47 10 | LM723 35 |
| LM387 | 120 | ULN2004 | 75 | 9602 | 250 | 4099 | 70 | $4116(200 n s)$ | 80 | LS. 112 | 20 | LS293 40 | 1 N 4001 | 78H05-5A/5V 550 |
| LM393 | 95 | XR2206 | 285 | Z80ACTC | 260 | 4508 | 110 | 4164 | 420 | LS114 | 20 | LS365 27 | $1 N 40024$ | RESISTORS |
| LM711 | 60 | ZN414 | 79 | Z80ADART | 775 | 4510 | 45 | $5101(450 \mathrm{~ns})$ | 150 | LS 123 | 34 | LS366 27 | 1 N 4003 S | RESISTORS |
| LM725 | 325 | ZN423 | 130 | Z80ADMA | 975 | 4511 | 45 | 5204 ( | 725 | LS125 | 24 | LS368 | 1N4004 1 - 5 | Carbon Film, |
| LM733 | 69 | ZN424 | 130 | Z80AP10 | 270 | 4512 | 42 | $6116(150 \mathrm{~ns})$ | 375 | LS126 | 25 | LS368 27 | $1 N 4005$ $1 N 4006$ | High Stab 5\% |
| LM1458 | 40 | ZN425E | 340 | ZN425E8 | 320 | 4514 | 110 | 6514 | 330 | LS132 | 29 | MICRO-MINI | $1 N 4007$ | \%W 10S-1M 2 |
| LM2917 | 185 | ZN427E | 575 | ZN427E8 | 320 575 | 4515 | 110 | 6810 | 115 | LS136 | 23 | 100V CERAM | - N 4148 | 1/2W 10S-10M 2 |
| LM3900 | 45 | ZN428E | 395 | ZN428E8 | 395 | 4516 | 50 |  |  | LS138 | 24 | E CAPS | 1 N5401 12 | Metal Oxide/Film |
|  |  |  |  |  |  |  |  |  |  | LS151 | 30 | 1pF 10nF 7 | $1 N 5404$  <br> $1 N 5406$ 14 | 1\% Met. Film E24 8 |

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# z80 CONTROLLER COMPUTER 

This article describes the design and construction of a simple microcomputer control unit. (We called it MARVIN for reasons that would take too long to explain.) The unit is designed specifically for control functions which usually require complex purpose-built hardware. Consisting of a
microprocessor and support chips, the unit is cheap enough to be used as an alternative to hard-wired logic with the advantage that its function is determined by easily-changed software.

The system can be used in conjunction with analogue devices, heavy duty switching, light dimmers, remote
controllers, etc, to control anything from a Stirling Moss style computer controlled
futuristic home to a morse code tutor.

Since there are so many possible uses, the construction of only the central computer parts (CPU, I/O, interrupt board) will be described; you will be left to chose external circuits according to your needs. Some additional interfaces will be available from the kit suppliers (see Buylines for details).

## Split Personality

For maximum flexibility, MARVIN is divided into a CPU
board and two other types of board. The CPU board houses the micropocessor, a 2 K EPROM containing the operating system, a second 2 K of EPROM for the user's programs, 1 K of RAM accessible to both the operating system and the users, and the selection circuitry for the various types of external port
details of programming will be given in a further article.

Since the I/O ports are on individual boards, you can add as many or as few as you need. MARVIN's I/O board provides 16 latched output lines and 16 input lines, which are TTL compatible. You can use up to five of these boards.

The other type of board we shall describe provides up to eight individual lines which can interrupt the CPU. These are useful for allowing external devices to force the CPU to carry out a predesignated routine. A typical use might be for controlling devices that need servicing at regular intervals, using an external clock to provide the timing.

This first part describes the functioning and construction of the CPU
board. This will be followed by a description of MARVIN's I/O and interrupt board and of MARVIN's operating system.

## EPROMs

A Z80A running at 4 MHz theoretically needs memory capable of 375 nS access time. Most 2716 s have an access time of 450 nS and although some may work adequately, it is strongly recomended that for reliable operation you make sure that you buy 350 nS EPROMs. If you decide

Computer projects are usually complex affairs, as anyone who saw the Cortex will testify. This one isn't. Design and development by Peter Grigson and David Harris.


ETI AUGUST 1983


ICs $2,8,9,10,11$ should be socketed as they are NMOS devices; Sockets are optional for the remaining TTL ICs, and there should be no problems in soldering them directly provided the usual precautions are observed.

To proceed with the construction, first insert and solder, on the underside only, all the TTL ICs, and the sockets for the NMOS. It is a good idea to use a fine-tipped soldering iron and to be careful to check that you do not make any bridges between tracks, especially where they pass between IC pins.

Next insert and solder the crystal, resistors and capacitors

## BUYLINES

The following will be available from ARK Electronics, Ashes Lane, Wentworth, Rotherham, S. Yorks S62 TTY:
EPROM containing the monitor program, 4 MHz clock, $£ 6.00 ; 3 \mathrm{MHz}$ (or lower) clock $£ 4.00$;
Double sided PCB, £6.00;
Complete 4 MHz kit excluding the operating system EPROM £26.00.
Please add 50p p\&p and make cheques payable to ARK Electronics. Sending an SAE to ARK will secure further details of D-to-A and A-to-D converters, lighting dimmers, remote controllers and a speech synthesiser.
(noting the polarity of C 1 ). Where there are pads on both sides of the board the components' leads should be soldered to both. There will remain a large number of unused holes, and many of these are used for the pins that link tracks on both sides of the board. You can tell these holes by looking to see if there are pads on both sides of the hole: if this is the case, then you should insert a pin and solder on both sides.

You can use a 24 -way 0.1 in . pitch edge connector to connect the
board to the others; we recommend that power connections to all the bords be hard-wired to the positions provided and not taken through the connector. If you're very costconscious, you could hard-wire direct; holes have been provided for you to insert pins for this purpose next to the edge connector strips.

Finally, solder SW1 (reset) into position, or, if you prefer, take off wires to a control panel.

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We receive a very large number of enquiries. Would prospective enquirers please note the following points:

- We undertake to do our best to answer enquiries relating to difficulties with ETI projects, in particular non-working projects, difficulties in obtaining components, and errors that you think we may have made. We do not have the resources to adapt or design projects for readers (other than for publication), nor can we predict the outcome if our projects are used beyond their specifications;
- Where a project has apparently been constructed correctly but does not work, we will need a description of its behaviour and some sensible test readings and drawings of oscillograms if appropriate. With a bit of luck, by taking these measurements you'll discover what's wrong yourself. Please do not send us any hardware (except as a gift!);
- Other than through our letters page, Read/Write, we will not reply to enquiries relating to other types of article in ETI. We may make some exceptions where the enquiry is very straightforward or where it is important to electronics as a whole;
- We will not reply to queries that are not accompanied by an SAE (or international reply coupon). We are not able to answer enquiries over the telephone. We try to answer promptly, but we receive so many enquiries that this cannot be guaranteed.
- Be brief and to the point in your enquiries. Much as we enjoy reading your opinions on world affairs, the state of the electronics industry, and so on, it doesn't help our already overloaded enquiries service to have to plough through several pages to find exactly what information you want.


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Even if the copy of ETI you need is not listed, all may not be lost, because we run a photocopying service. For $£ 1.50$ (UK and overseas) we will photocopy an entire article (note that parts of a series of articles count as separate articles). Your request should clearly state what article you require and the month and year in which it appeared (the index for 1980 and 1981 was published in January 1982, and the index for

1982 appeared in December 1982). Send your request to ETI Photocopies, Argus Specialist Publications Ltd, 145 Charing Cross Road, London WC2H OEE (cheques should be made out to ASP Ltd)
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## Write For ETI

We are always looking for new contributors to the magazine, and we pay a competitive page rate. If you have built a project or you would like to write a feature on a topic that would interest ETI readers, let us have a description of your proposal, and we'll get back to you to say whether or not we're interested and give you you all the boring details.
We don't bother with the bureaucracy for Tech Tips - all you do is to send in your idea, stating clearly if you want an acknowledgement of receipt. If possible, please type your explanation of why the circuit is different, what it does and how it works, on a separate sheet from the circuit diagram; both sheets should carry your name, address and the circuit title. We'll let you know (within a month or so) if we want to use your Tech Tip.

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If you exhaust the above procedure and still do not obtain a satisfactory response from the supplier, then please drop us a line. We are not able to help directly, because basically the dispute is between you and the supplier, but a letter from us can sometimes help to get the matter sorted out. But please, don't write to us until you have taken all reasonable steps yourself to sort out the problem.

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## OOPS!

We have in the past published small corrections to projects on the letters page, and major corrections separately. From now on corrections will appear on this page, and will be repeated for several months (just to increase our embarrassment). If a correction is too large to fit on here, we will publish it just once, but will note the fact that a correction does exist, and that copies of it can be obtained from us provided you send in an SAE. But please - request copies only if you really do need them; if this service is abused, we may be foreced to withdraw it.

## ZX A to D (Jan '83)

D 2 is shown the wrong way round on the overlay; wires on the RH side of the switch SW1 should go to top contacts. Some of the early PCBs had an error: pins $2 \& 4$ of IC1 should go to pin 16 (top) of edge connector (published foil pattern is OK).

## .Stage Lighting Unit (Jan, Feb, April, May

 '83)Transformer specs are as follows: Primaries all 250 V ; secondaries $\mathrm{T} 1: 0-6,0-6 \mathrm{~V}, 12 \mathrm{VA}$ tot; T2: 0-12, 0-12 V, 12 VA tot; T3: 0-6 V, 3 VA. ICs $34,35,36$ are 78055 V regulators.

ZX Sound Board Design Comp. results, Feb '83)
The first line of the program has to be entered in reverse order to get it to go in (COS, GOSUB, COPY, ASN and RND are functions). The line should read:

10 REN :"Y - =? COS GOSUE 5 COP Y ??ASN ?RINDF??RND

## Alarm Module (MArch '83)

R21 is 220 k (parts list OK, circuit diagram wrong) Q5 is BC182L (left off parts list).

Max Min Thermemeter (April '83)
A revised foil pattern was published in July ETI. To get original PCB to work, replace D4 and D5 with wire links, cut tracks from pins 7 and 8 on IC6, and solder 15k resistor across cut - remove ICs while doing this! (It's messy but it works.)

Real Time Clock (April '83)
Frequency of XTAL. 1 is 32.768 kHz .

NDFL Power Amplifier (May '83)
C13 is 33 p (parts list correct, circuit diagram wrong). Table 1: lengths of wire quoted do not allow for lead lengths - add 40 mm or so to them. This is particularly important for L3. Resistors R29 and R30 can be wirewound types, it isn't necessary to use carbon types (thier inductance will be small).

Flash Sequencer (July '83)
Q1 should be BC184L; Q2-5 should be BC182L.

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your T.V.'s speaker. The display graphics are of amazing quality, having a detailed resolution of $320 \times 192$, comprising 24 lines of 40 characters. Atari personal computers have a standard 10 K ROM operating system. In addition the standard Atari 400 ( 149 ) comes) 16 K (h 48 K as standard Both the version with 400 and 800 are the Basic Programming Cartridge, as well as a 120 page Basic Referic' 184 page Self Teaching Manual by in progr OVER 500 PROGRAMS AVAILABLE: The Atari computers are supported by well over 500 programs available for your use, a larger selection than you will find on any other television game or home computer! The wide selection puts Atari way ahead of the competition. Just fill in the coupon and we will be pleased to send you a full price list which gives details of our range of software available for entertainment, home education, programming and home office use. We think you'll agree when we say it's quite impressive. 100 FREE PROGRAMS FROM SILICA SHOP: If you buy your Atari Home Computer rom Silica Shop, you will recieve a FREE presentation pack of 6 cassettes, containing 100 programs including games, utilities and demonstrations. A 16 page booklet giving full your can buy the set of 6 cassettes for $£ 30$. What's more, Silica Shop offer a two year guarantee on all computers as well as a FREE joystick. This adds Shop atfor the ATARI $40016 \mathrm{~K}-£ 129.57+\mathrm{VAT}=£ 149$
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## COLECOVISION



THE SYSTEM: The CBS Colecovision offers new standards in video game play. The excellent graphics are well implemented with arcade titles such as Zaxxon, Lady Bug. Gorf, Wizard of Wor, Carnival, Mouse Trap and the lovable Smurts. The console comes supplied with a three screen arcade quality version of DONKEY KONG Parker and other companies have also announced ranges of cartridges fo olecovision, to further enhance the wide range of quality titles available for this new relevision games machine
THE CONSOLE: The CBS Colecovision video games system has advanced technology which produces superlative graphics resolution and excellent sound effects. The styling of the console and hand controllers has been carefully researched; the console is designed to complement modern hi-tech equipment, and has clear features for easy operation. The hand controllers allow fingertip control via the 8 direction joystick, and feature 2 independent fire buttons. The push button keyboard is used for game selection and for game control with some cartridges. The hand controllers are detachable and are connected to six feet of telephone coil cable, storing neatly away in the console when not in use. ATARI EXPANSION MODULE: The Atari converter module allows Atari VCS software cartridges to be played on the Colecovision console, allowing owners the feeans that purchase from the extensive range of Atari compatible cartridges.
existing Atari owners can buy the CBS Colecovision games system without discarding their software library. Silica Shop offer part exchange facilities if you wish to upgrade. TURBO EXPANSION MODULE: The Turbo Driver Expansion Module allows you to actually drive the vehicle that appears on your T.V. screen. The module consists of a steering wheel, dashboard and accelerator pedal. One hand controller is mounted on the dashboard to provide a gear change unit. The module comes complete with a rurbo Driver cartridge, the first of several cartridges to make use in the action of sitting in the driving seat. This facility is unique to ces Colecovision.
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[^0]:    $x$ in part no. indicates primary voltage. Piease inseri

[^1]:    There should be relatively few problems in obtaining components for this project. The $0.22 R$ resistor can be found in several suppliers' lists, including Watford's. The RM6 potcore is available from RS Components.
    our PCB service - see page 77 for details.

