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1000V: 1nF 17p; $10 \mathrm{nF} 30 \mathrm{p} ; 15 \mathrm{n}$ 40p; 22n 36p; 33n 42p; 47n, 100 n 42 p .


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RESISTORS Network S.1.L.
7 Commoned; 18 pins $1008,680 \mathrm{~N}, \mathrm{~K} 2 \mathrm{k} 2,4 \mathrm{K7}$,


## DIODES 

## RAM FO BBC MIC 4816 10 22 <br> 225p

 $4118-250$$4164-200$
4330
$2143=\mathrm{C}$
$4816-100 \mathrm{~B}$


夺す!











Walt Disney Productions
Well, there's this person called Sark, that's him up above with the electronic Frisbee, except he isn't really a person, he's a program, and a pretty evil one too, a really nasty piece of code. Then there's this other program, called Flynn, except he's really a person, and he's the good guy, but he's trapped with Sark inside a computer where he's fighting to the death against some video games that he wrote himself. That's Flynn's alter ego down below, called Clu, and he's driving around inside the computer in that art deco tank looking for Invader-like things to zap, except they zap him. Confused? You won't be after the next edition of ETI, where we'll be reviewing Tron, the big Christmas film from Walt Disney and a milestone in moviemaking. Sorry, I forgot about the Common Market and Eurometaphors: it's a kilometrestone
in moviemaking. Stuffed full of computer animation and other clever and unique techniques, plus a lot of video gaming mythology, this is great entertainment.

Almost as great, that is, as our extensive review of as many video games as we can get our hands on, in our 'Buyer's Guide to Conquering the Universe'. This will be just in time to help you make up your mind before the Christmas spending spree, and containing our maximum scores so you can pit yourselves against us. We'll also be presenting all our usual features plus a bumper collection of excellent projects, including a digital stage lighting dimmer and a programmable power supply. You can't afford to miss the January edition of ETI, on sale December 3rd.


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# DIGEST 

## Will Industry Standardise on the 3 " Floppy?

Hitachi think it will, and they have launched a new disc drive on to the UK market to cater for the projected demand. Their product is called the Model HFD 306S, and it's pretty small as you can see from the photograph. The recording speed, recording capacity per track and other specifications are claimed to be exactly similar to those of a standard $5 k^{\prime \prime}$ floppy dise driver, so that existing disc controllers can be used to handle it. Single and dual drive units will be available, and they will accept single or double density double-sided discs.

The disc itself will be housed in a rigid plastic case with a sliding metal shutter to protect it
against contamination. While prices have yet to be decided, Hitachi say that both the drive units and the discs will be considerably cheaper than the $5 \frac{1}{}{ }^{\prime \prime}$ equivalents. How long this unit will take to find its way on to the hobbyist scene remains to be seen, but if it lives up to its maker's claims, we look forward to its arrival.

Meanwhile, four other manufacturers - Dysan, Tabor, Shugart and Verbatim - are trying to establish an industry standard for three to four inch discs. Their idea is to create a standard that will accommodate future technological advances rather than just accepting and standardising what is around at the time.


## Pac-Man Champ

$T \begin{aligned} & \text { he Under-25 UK Pac-Man } \\ & \text { Champion, } \\ & \text { 16-year-old }\end{aligned}$ Craig Heap, with BBC TV
presenter John Craven, and PacMan (he's the furry one), photographed at the National Finals of the UK Pac-Man Championship held at the Barbican Centre, London on 30 August 1982. Craig's winning score was 14,174.


## Scope for a Multiplexer?

Amultiplexing device which A converts a generalpurpose single- or dual-channel oscilloscope into an eightchannel instrument has been developed by GSC. The new Model 8001 multiplexer which functions in the same way as a simple logic analyser minus its memory, and allows simultaneous events on different channels to be compared and displayed in direct relationship to one another. The UK price is $£ 225$.

The instrument allows oscilloscope users to view events occurring synchronously or asynchronously, and the user can observe all eight channels at once or one of two 4-channel combinations. Details from Gobal Specialties Corp, Shire Hill Industrial Estate, Saffron Walden, Essex CB11 3AQ.

## Doctoring Your Memory

New from Dataman Designs is the Microdoctor; it can be plugged into your micro in place of the MPU (or clipped in over the MPU with the latter in DMA or RESET mode). The Microdoctor will 'look' around in address space, and report what it finds via the printer. Memory map, data tables, peripheral driving routines can all be located, and if your system is $\mathbf{Z 8 0}$-based, the disassembler can be used to check them. All form of memory, including dynamic RAM can be checked. The Microdoctor costs E295 plus VAT from Dataman Designs, Lombard House, Cornwall Road, Dorchester DT1 1RX, or from retailers.


## Easy-To-Use Fuse

A new high performance, low profile fuse mounting system is now available from Littlefuse-Olvis. Designated OMNIBLUCK, the system provides fuse mountings for three different terminal styles, comprising solder type and quick connect blade type for $0.25^{\prime \prime}(6.35 \mathrm{~mm})$ and $0.187^{\prime \prime}(4.78 \mathrm{~mm})$ receptacles. These low height fuse mountings feature a one-piece, high amperage, self-aligning fuse clip/terminal design which eliminates resistance build-up and allows for operation at high current levels of up to 30 amps. They come in one through 12 pole units with individual pole barriers to prevent clip damage and provide electrical protection. Standard colour is grey but optional colours are available to special order including blue, green, red, white, black or yellow. In addition, two different style clip types can be supplied for circuit identity or polarisation as well as an anti-rotation boss device for single pole units only. For further information contact LittlefuseOlvis, Crowther District 3, Washington, Tyne \& Wear NE38 OAH.

## Big Ni-Cads

The new 15 range of nickel cadmium general purpose cells from Chloride Alcad Limited, of Redditch, will provide standby power where essential loads are required to be maintained for 24 hours or longer. Ampere hour capacities range from 525 to 1,300 and a battery capacity of several thousand ampere hours can be provided if cells are connected in parallel. Chloride Alcad Limited, Union Street, Redditch, Worcestershire B98 7BW, England. Tel: 052762351

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$5^{n}$ 30W system - recommended cabinet size $160 \times 17 \times 295 m$

Deaigner approved flat pack cabinet kits, including grill fabric. Can be finished with iron on veneer or self adhesive vinyl etc.



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Comprising of a top panel and tape mechanism coupled to a record/play back printed board assembly. Supplied as one complete unit for horizontal installation into cabinet of builh and tested. Foatures: Three type keys, record, rewind, fast forward, play, stop and gject. Automatic record leval contrord, play, stop and secondary inputs for stereo microphones.
 chamnels. Output 400 mV to both left and right hánd ratio: 45 dB . Wow and flutter: $0.1 \%$ Signal to nolee requirement: 18 V DC at 300 mA . Connections: The requirementa: 18 DC at 300 mA . Connections: The ien and right hand stereo inouts and outputs are via (phono sockets provided), Dlmenaions: Top panel 5 ty $x$ Ilin. Clearance required under top panel $2 t_{i m}$.
Suppliod complete with circuit diagram and connecting Suppliod complete with circuit diagram and connecting
diagram. Attractive black and silver finish. Price $28.70+\mathbf{2 7 . 5 0}$ postage and packing. Supplementary parts for 18 V D.C. power supply
(transformer, bridge rectifier and smoothing capacitor) (transformer, bridge rectifier and smoothing capacitor)
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6 piano type keys
NEW RANGE QUALITY POWER LOUDSPEAKERS ( $15^{\prime \prime}, 12^{\prime \prime}$ and $8^{\prime \prime \prime}$. These loudspeakers are ideal for both hi-fi and disco applications. Both the 12 and 15 and aluminium centre domes. All three units have white speaker cones and are fitted with aitractive cast aluminium (ground tinish) fixing escuicheons. Specificat on and Price:
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niage each

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8' 50 watt R.M.S. Impedarice 8 ohms, 20 ozz , $1^{\prime \prime 2}$ aluminium voice coil, Resonant
Frequency 40 Hz . Frequency Response to $6 K \mathrm{~Hz}$, Sensitivity 92 dB Also avaithle with Frequency 40 Hz . Frequency Response to 6 KHz , Sensitivity 92 dB . Also available with black cone fitted with black metal protective grill. Price: White cone $\mathbf{f 8 . 9 0}$ each. Black
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TYPE 'E' (KSN1038A) 3\%" horn tweeter with attractive silver finish trim. Suitable for $\mathrm{H}_{1}-\mathrm{fi}$ monitor sysiems etc. Price f4.35 each.
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Build a quality 60 watt RMS svstem 8ohms Build a quality 60 watt R.M.S. system.
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Attractive blue cone with aluminium Attractive b
centre dome. centre dome. Price £17.99 each + £3.00 P\&P


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# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS:NEWS 



# Flashy New <br> Portable 

Anew portable computer is being introduced into the UK by Epson, a Japanese manufacturer well known for its dot matrix printers and LCD displays. The computer, the HX 20 , is designed to be used anywhere, anytime. About the size of an A4 notepad, it is claimed to offer computing power and capabilities comparable to many desk-top computers.

Language is an extended version of Microsoft BASIC, operating from a 32 K RAM (expandable to 72K). Standard memory is 16 K , expandable to 32K, but just under 4 K is taken up by the operating system, leaving 12.6 K and 28.6 K respectively. Keyboard is full size ASCIIencoded, with $\mathbf{1 0}$ special function keys.

To those of you who remember as far back as October, the LCD display may look familiar. It can display four lines of 20 characters at any one time; however, there is a virtual screen
area of 255 lines by 255 characters that the real screen can be used to window. There is scope for the use of a CRT as a monitor.

Finally, there are the integral micro-cassette tape deck and dot matrix printer - fairly standard items in themselves but useful to have on board. The whole system is powered by four nicads that give a total typical operating time of $\mathbf{4 0}$ hours, and are rechargeable from the household supply.

The HX 20 is expected to be hitting the shops around the new year at a cool $£ 500$ or so.

## Shorts

- From November 1st, viewers in the LWT and Thames area will be able to receive a 100 -page local teletext service. There are already local services operating in the STV and Channel areas.
- Just published by Northwood Books: Cipher Systems, the Protection of Communications, by Henry Becker and Fred Piper. It's all about cryptology, and no, you should be able to read ETI without it.
- Over fifteen million US homes will have roof-top direct broadcast satellite terminals by the end of the decade, predicts a report from International Resource Development Inc. The report goes on to say that the likely price will be $\$ 350-\$ 500$, and that only the largest equipment manufacturers will be able to compete at this cut-throat price.
- The dotty display (made by Epson) featured in Digest, October is available from Norbain Displays Ltd, Norbain House, Arkwright Road, Reading, Berks, RG2 0LT, and from Datac Ltd, Tudor Road, Altrincham, Cheshire WA14 5TN.
- While we're on the topic of Norbain, they tell us that they have started selling Vactec discrete phototransistors, photodarlingtons, and matched GaAs LED/sensor pairs.
- Sifam have launched a new range of test equipment including a low-cost DMM and a digital logic probe. Sifam Ltd, Woodland Road, Torquay, Devon TQ2 7AY.
- Ross Electronics, 49/53 Pancras Road, London NW1 2QB have just issued a new catalogue, containing their ranges of microphones, leads, intercoms, headphones, multimeters, cassette tapes and other goodies.
- Another new catalogue, this time from Draper Tools. At £6.50, it's a bit pricey for the hobbyist (the Ross cat is free).
- Why can't all suppliers make their catalogues free - there are even some people who think their's is a magazine, believe it or not. Luckily, Bernard Babani Ltd have resisted the urge to turn their catalogue into a book, even though they publish plenty of the latter in subjets that would be of interest to ETI readers. Oh yes, the cat is free, from Bernard Babani Ltd, The Grampians, Shepherds Bush Road, London W6 7NF.
- Thandar have introduced a prescaler for use with the TF100 frequency meter. It will extend the upper frequency limit of the counter to 1 GHz . Called the TP1000, the unit costs $£ 65$ plus VAT.
- Philips have launched a twiceyearly magazine for business systems users; it's called 'Connections', but, so far as we can see, it, doesn't have anything to do with either James Burke or John Julius Norwich.
- Crunchie bars to be computerised - official. Ferranti Computer Systems will be supplying the hard and software to monitor the production line of this computerised confectionery.
- Is electronics all hot air? Cooper Tools have just unveiled a new soldering or desoldering tool that uses hot air rather than a bit to heat the job. Cooper Tools Limited, Sedling Road, Wear, Washington, Tyne \& Wear NE38 9BZ.
- NEC Electronics Ltd have developed a 1 megabit mask ROM; the device should be available in the UK this autumn.
- FREDs (fast recovery epitaxial diodes - bet it took quite a lot of head-scratching to think of a product with that acronym) are being produced by Siemens litd. Reverse recovery times are claimed to be better than 35 nS .
- Yet another catalogue, this time from Aries Electronics, Eastways, Witham, Essex, CM8 3YQ; this one's full of sockets and DIP switches and jumpers.
- British Telecom have placed a firm order for 8,600 Cardphones (the type that uses bits of plastic rather than real money). It seems that the old style of 'phones can't take the money off you fast enough.
- Read/Write ROM? Surely some mistake? No, the unit in question is from Camel Products, and is a two kilobyte RAM with battery support (for when your computer is switched off) and function switches so that the memory can be written to, then further write operations locked out. The battery allows several years of data retention. It's available for $£ 29.95$ inclusive, from Cambridge Microelectronics LItd, One Milton Road, Cambridge CB4 1YU.


## Inexpensive 'Scope

A $\begin{array}{ll}5 \mathrm{MHz} & \text { oscilloscope for } \\ £ 115.72 & \text { plus VAT? New }\end{array}$ $£ 115.72$ plus VAT? New, via Verospeed, from Trio is the CO-1303D with DC to 5 MHz bandwidth and a sensitivity of up to 10 mV per division. With direct access to the deflection plate terminals, the 'scope can be used at higher frequencies. There is also a 10 MHz dual-beam version at $£ 249.65$ plus VAT. Details from Verospeed, Stanstead Road, Boyatt Wood, Eastleigh, Hants SO5 4ZY.

## Digital Noise Source

Using entirely digital techniques, the DNS03 digital noise source developed and manufactured by Marconi Space and Defence Systems produces a true random digital output. The device, produced as a metal case thick film hybrid measuring 1.3" $\times 1.0^{\prime \prime} \times 0.2^{\prime \prime}$, is extremely versatile and is claimed to overcome the problems of existing noisesources based on noise diodes.

The device will operate at any supply voltage between 4 volts and 15 volts and typical consumption at 5 volts is 2 mA . A disable control reduces this consumption yet further permitting its use in battery powered equipments. The hybrid will operate over the full military temperature range of $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$. For further information contact: Marketing Department, Military Communications Division, Marconi Space and Defence Systems, Brown's Lane, The Airport, Portsmouth, Hampshire, PO3 5PH.

## Computer Talk

Talking computers, whaf will they think of next? Using the Votrax SC-01 IC, the ADS Synthetalker is an IEEE 696/S-100 compatible speech synthesis board. Available from Appledore Electronics, you have the choice of bare board and IC, or kit, or fully assembled and tested versions. Details and data are available from Appledore Electronics (see ads index).

Another entry into this field is from DCP Microdevelopments Litd, who have introduced a speech unit for the ZX81. Designated the DCP Speech Pack (hard to remember, that one), it plugs straight into the back like so many of the Sinclair add-ons, and costs a princely $£ 49.95$ including VAT and p\&p. (A Spectrum adapter is available for $£ 2.95$.) DCP Microdevelepments Ltd, 2 Station Close, Lingwood, Norwich NR13 4AX.
(PS: Neither of these suppliers mention whether their units have an American or English accent!)

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## Quite Dishy

A new small-dish aerial hoisted on to the roof of a British Telecom building in the City of London, will be the first to be shared between a number of users. The aerial and its associated equipment will be used in further trials of British Telecom's SatStream - a satellite-based X-Stream digital service which is to be offered to UK businessmen in 1984 to provide specialist private communications within the UK and to Europe. It will subsequently serve in this location as one of the first SatStream smalldish terminals in commercial service. SatStream will offer three main benefits to its users:

- flexibility: service can be introduced at very short notice and expanded or reconfigured equally quickly
diversity: digital operation allows many different services, such as speech, telex, facsimile or data, to be integrated on the same transmission path while advanced services can be added quickly at comparatively little extra cost
- multi-destination broadcasting: of particular advantage for oneway information flow, such as news dissemination to branch offices for local distribution.

Trials of SatStream began last year with the aim of proving the service, and of creating a solid base of installation and operational experience on which British Telecom can draw when establishing a commercial service early in 1984. To provide such a service, small-dish aerials, from 3.7 to 5 m ( 12 to 17ft) in diameter, would be installed at rooftop level on or near customers' premises. While a single terminal may be dedicated solely for a particular customer's use, it is pro-
bably that in City centres where demand is likely to be concentrated they will be shared between several users. This would help to spread the cost.

The new 3.7 m aerial hoisted into position last weekend will provide British Telecom International - the division of British Telecom marketing SatStream - and its users with valuable first-hand experience of shared use. Trials are expected to start in a few weeks' time. The first organisations taking part will be mainly multinational, engaged in news disemination and in the oil and chemical industries. They will use the dish aerial simultaneously to communicate between offices in London and in other European cities. Experimental activities are likely to include videoconferencing and integration of a variety of services. Each user will have its own dedicated link to the aerial.

The trials will be conducted using Europe's orbital test satellite (OTS). When they are completed, the equipment will be modified to enable the aerial to work to one of the satellites to be used for commercial service - the European Communications Satellite (ECS) or Telecom 1, a French government project. It will then serve as one of SatStream's strategically located earth stations.

The first small-dish trials involved an international newspaper, providing a digital link used to transmit facsimile pages for printing its European edition. More recently small-dish terminals were installed at University College, London, and at Cambridge and Loughborough universities, for Project Universe, and experiment in computer communications by satellite.

## New 'Phones from AKG

The 'phones pictured above are the K1 (yes, they really do fold up like pieces of garden trellis); they're claimed to give hifi performance and should sell for around $£ 17.25$ inc VAT. The K4
phones are the super-fi versions, and have a slightly more conventional headband. However, they'll set you back about $£ 62$. AKG Acoustics Limited, 191 The Vale, Acton W3 7QS.

## First Plastic Packaged 10-Bit A/D Converter?

Eerranti Electronics has Fintroduced the ZN432E believed to be Europe's first 10-bit converters to not need a ceramic package, with the attendant expense of both ceramic and gold materials. Now Ferranti Electronics has developed a new moulded packaging technique which results in the price of the ZN432E being less than half that of its ceramic equivalent.

The ZN432E operates over the commercial temperature range $\left(0-70^{\circ} \mathrm{C}\right)$ and is available in a 28-pin D.I.L. moulded
package. A conversion time of $20 \mu s$ is guaranteed, with no missing codes. The device is TTL/CMOS compatible and includes an on-chip 2.5 volt reference.

Full details of this and all of the Ferranti Electronics range of monolothic data converters can be obtained from the Publicity Department, Ferranti Electronics Limited, Fields New Road, Chadderton, Oldham, Lancashire, OL9 8NP. Tel: (061) 6240515.


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# THE ZOOM MICROPHONE 

## Zoom lenses have been around a long time and are standard issue on video cameras; but matching zoom microphones? How can such a thing be possible, you ask - and Vivian Capel answers.

Video sound is not much to write home about at present. This is because the track is recorded in linear fashion along one edge of the tape, unlike the video tracks which are laid down at a high writing speed diagonally across the tape by heads on a rotating drum. Slow tape speed means a low writing speed for the sound track, so it produces a poor frequency response and noise factor.

Efforts are being made to counter this, and there is a report that Sony have a prototype Beta recorder that modulates the sound on a low-frequency FM carrier and records it along with the video signal. As low recorded frequencies penetrate deeper into the tape coating, while high frequencies remain near the surface, the sound and vision recorded signals are physically separated and so do not interact. It seems that the existing audio linear track is retained so that the tapes will be compatible and will be playable on existing machines. There is little doubt that, not wanting to be outdone, the VHS camp will do something similar, so it looks as if video sound will be much improved in the new generation of machines.

## Microphone Characteristics

All video cameras have zoom lenses and, to be realistic, the sound should change according to the lens setting. At the wide-angle setting, we should hear the general sounds of the surroundings, but at maximum telephoto, the ambient sound should diminish and the sounds originating in the field of view should stand out. As the lens zooms in, the transition should be gradual between the two different acoustics.


Fig. 1 Cardioid polar response (a) and super-directional response (b) obtained from a pair of out-of-phase cardioids. The response is at a nominal $1 \mathbf{k H z}$ and changes for other frequencies.

In practice, nothing happens to the sound field at all, because the microphone is fixed to the camera and is not affected by anything done to the zoom lens. Professional camera teams overcome this by following the changes in the field of view with a microphone on a boom. Keeping the microphone out of camera shot is one of the everpresent problems.

Most video camera microphones are of the omnidirectional type; they pick up sound equally from all directions. These are used because they are less prone to handling noise than the directional type, although the latter could be used to advantage if shock-proof mountings were employed.

To overcome the incongruity of a fixed sound acoustic at different zoom lens settings, IVC have produced a breakthrough in the field of microphone technology; a microphone that zooms with the lens and gives an appropriate acoustic for all settings. Before we can understand just how it works, we must consider the elements of microphone polar response.

## Omniscient?

An omni-directional microphone has a diaphragm which is exposed to the environment at the front, but sealed at the back by an airtight chamber. Pressure fluctuations produced by the sound wave exert force in all directions (not merely along the axis of propagation) and this leads to pressure differences across the diaphragm that make it move backward or forward in sympathy, irrespective of the direction of the sound source.

Direct particle velocity, caused by the backward-andforward movement of air molecules along the axis of propagation, has little effect on the diaphragm because of the damping effect of the trapped air. There is a small effect though, and this gives the omni microphone a not quite equal response all around it, but the deviation is small enough to ignore for most purposes.

## The Heart of the Matter

If vents are made in the rear chamber, local air pressure can reach both sides of the diaphragm so the microphone is not pressure sensitive. However, the damping has been removed, so it is sensitive to particle velocity.

Also there is a secondary effect due to slight pressure differences that exist because of the phase difference between sounds arriving at the front and rear of the diaphragm. This is dependent on frequency, and when the physical path through the vents to the rear of the diaphragm equals a wavelength, there is pressure cancellation. At half a wavelength there is reinforcement,

and below this the phase difference decreases all the way down. If the main force on the diaphragm were the pressure differences, as is sometimes erroneously stated, there would be a high peak in the treble frequency response with continual downward slope toward the bass.

Being sensitive to direct particle velocity yet relatively insensitive to pressure, the microphone responds in a directional fashion, favouring sounds coming from the front. The variation of output with angle of incidence, $\theta$, is proportional to $1+\cos \theta / 2$; plotting a polar response curve gives a heart shape (Fig. 1a), hence the term cardioid which is used to describe microphones of this type.

If we were to place two cardioid units back-to-back we would have an omni-directional response. There seems little practical point in doing this, but as we shall see later, it does have an application.

## Super Directivity

A marginal improvement in directivity can be obtained by modifying the vents to produce the hypercardioid, which has less response to sound from the sides, but a pair of small lobes with a high-ish response at the rear (Fig. 1b).

It is sometimes convenient to use a distance factor to describe directional microphones. This is the scaled distance from a wanted source at which the microphone will give the same results as an omni in terms of proportion of wanted to ambient sound. A cardioid can be placed 1.75 times further from the source than an omni, and a hypercardioid, two times.

For greater directivity, there is the gun microphone, which relies on interference and cancellation of sound waves coming from the sides. It is only effective down to the frequency at which a half-wavelength is equal to the length of the tube. Short tubes are not particularly
directional other than at the high and mid-high frequencies, while long ones are unwieldy when fixed to a camera and could intrude on the camera shot. Distance factors of around three times are obtainable, depending on length.

## $1-1=$ ?

Supposing we mount two cardioid capsules one behind the other and connect them in opposite phase. Sound waves coming from the sides affect both equally so their electrical signals cancel and there is no output.

For sounds arriving from the front, there is a phase delay between the outputs from the two units. When the microphone spacing is equal to a half wavelength, the outputs reinforce to produce a maximum signal. At shorter wavelengths (higher frequencies) the net output drops towards complete cancellation at a one wavelength spacing. At lower frequencies (longer wavelength), output slowly falls linearly to zero at zero frequency (see Fig. 2), This double microphone is super-directional with similar polar response to the gun and the net output varies as $(1+\cos \theta) \cos \theta$. However there are two major snags. One is that the capsules must be closely spaced as this establishes the upper frequency limit above which the output drops rapidly. Moving-coil units are too bulky to get close enough, but electret capsules are quite suitable. Closely spaced anti-phase units, though, tend to give low output.

The other snag is the falling low-frequency response. This can be equalised electronically, but the amount of lift needed at the lower end will greatly emphasise thermal and handling noise.

## Fiddling With Phase

However, it is possible to trade directivity for response at low frequencies. This would then be no worse than a gun microphone in which low frequency directivity reverts to just that of the cardioid unit at the bottom of the barrel. This can be done by combining the signals from the two capsules through a frequency-dependent phaseprocessor. At the highest frequencies, the units are in antiphase and function as described to give maximum directivity. They continue in this fashion down to the midfrequency range, when the phase begins to rotate until in the bass region the capsules are in phase.


Fig. 2 The frequency response for a pair of out-of-phase cardioids (mounted in line) peaks at the frequency at which the half-wavelength equals the spacing between them. Below, it decreases at $6 \mathrm{~dB} / 0 \mathrm{ctave}$, requiring high gain at low frequencies to equalise. Above, the response drops rapidly to zero at the frequency corresponding to a one-wavelength spacing.

## FEATURE : Zoom Mike



Fig. 3 The response of a cardioid pair when the phase is progressively rotated from $180^{\circ}$ to $0^{\circ}$ below the mid-range. The bottom chart shows the phase rotation.

Directivity is maintained down to the phase change point and then it degrades until it is that of a cardioid in the bass register. Figure 3 shows the frequency response, which falls from the treble to mid-range, then picks up to full amplitude again in the bass. The other graph indicates the phase change. This can be equalised without too much trouble, as no boost is required in the low frequencies, and so handling and thermal noise are not accentuated.

This principle is used by JVC, and the mid-frequency phase-change point is around 500 Hz . By using a potentiometer in the adding ciruit, the polar response can be continuously varied. When at maximum, both outputs are combined to give the super-directional characteristic; when turned fully down, the second capsule is off, and only the first works to give a normal cardioid response.

While this would give a useful acoustic variation, it is not enough for the purpose of changing to correspond


Close-up of the S100 camera controls, showing the auto, omni and super-directional settings.
with a wide change of zoom lens setting. Earlier, though, we saw how an omni pattern could be obtained by mounting two cardioids back-to-back. Hence in the JVC microphone, a third unit is introduced directly behind the second and facing forwards. Thus we have all the necessary elements for the maximum possible change in polar response, from omni right through to superdirectional.

Control is effected by a pair of ganged potentiometers with centre taps, which neatly avoids the use of multipleganged components. The operation can be seen by reference to the circuit diagram (Fig: 4). To make life easier, we will consider the two extreme potentiometer positions, $A$ and $C$, and the intermediate position $B$. At $A$, the adder receives the full output from the third capsule, and also that of capsule no. 2. Signals from capsule no. 1 pass through the opposite side of the pot to earth via the centre tap. With only capsules 2 and 3 'live', the result is the omni-directional response.

Moving to the B position, the wiper is earthed through the centre tap, so the adder never receives the output from capsules 1 or 3 , leaving number 2 on its own to provide the cardioid pattern. In the position $C$, the adder receives output from capsules 1 and 2, which produces the superdirectional characteristic.

In addition to changing the outputs between the three


Fig. 4 Block diagram of the JVC zoom microphone.
capsules, it is also necessary to vary the gain so that maximum gain is obtained for close-ups with the superdirectional pattern, and to take the equaliser out of circuit for cardioid and omni operation. All this is done by the second potentiometer which is connected in a negative feedback loop across the preamplifier.

In position A the potentiometer is shorted out and so is the equalisation network, so the amplifier is at minimum gain and the frequency response is flat: these are the conditions required for the omnidirectional operation. When in the central $B$ setting, the gain is increased but the frequency response is still flat. When at the opposite extreme, position $C$, there is maximum resistance in the loop and hence minimum feedback and maximum gain. The equalisation network is now in circuit, so we have the required circuit conditions for the super-directional characteristic.

Of course there are an infinite number of intermediate positions afforded by the potentiometer, so an acoustic is obtained which is appropriate for all zoom lens settings. The control is linked to the lens control, so the setting is automatic and the user needs to give no thought to it.

The microphone described (type MZ-500) has been specifically designed for the S 100 and $\mathrm{GX} 77 / 88$ colour cameras, but undoubtedly it, or future versions of the principle, will find an application in many audio fields where a continuously variable polar response between two extremes is required.

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# EARTH LEAKAGE CIRCUIT BREAKER <br> Earth-fault currents from mains-operated equipment can kill you. Circuit breakers have featured in house-mains installations for some years: now this portable ELCB lets you take your protection anywhere. Design by Phil Walker. 

fa fault occurs in a piece of mains-operated equipment, any external metal parts may be placed at earth potential. Should you complete a path to earth from the appliance, the statisticians could well be chalking up another death from electrocution. A more subtle but equally lethal danger is caused when inflammable material creates the path to earth - current flowing to ground might then generate enough heat to start a fire.

> Even if no faults are present, building, servicing or tinkering with mains equipment is a dangerous pastime. One slip with a screwdriver . . . Our easy familiarity with electricity not only breeds contempt but a steady stream of fatalities. Many of these could be avoided if earth leakage circuit breakers were used more often (in an ideal world, of course, they'd be built into every piece of mains equipment by the manufacturer).


IMPORTANT!
Used properly, this project could help to make your home a safer place by providing added protection against electric shock. However, this doesn't mean that you can forget about all the precautions that you would normally take, because, like any piece of safety equipment, you shouldn't trust it to be your sole protection from the great hereafter. Belt and braces is the order of the day where human life is concerned! In any case, it won't protect you against shocks from most types of high-voltage generator or from a shock between live and neutral. Nevertheless, this device will considerably improve protection against the most common electric shock, from live to earth.
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 The ELCB is designed to be plugged into a normal 13 A wall socket. Any normal household or small workshop device may then be plugged into the integral socket. The ELCB continuously monitors the current flowing to and from the device along the live and neutral wires; if at any time the amount of current flowing in these wires differs by more than a (small) pre-set amount, the ELCB will assume there is a fault and quickly disconnect the power from both lines. Thus any current flowing to earth (possibly via you) will trip the device, as will an
turns as the other new primaries． To find out if the transformer is working satisfactorially，follow the section．
The transformer must be a toroidal type to avoid spurious
tripping．We tried using the older
laminated type，but we found that it
ग！ fluctuations in the ambien
field；obviously a severe disadvantage！
 the operation of the ELCB，it is vital that it is of the highest quality．In particular，the relay drop－out time milliseconds，and together with a maximum possible delay of 10 milliseconds from the electronics， this will guarantee a maximum operating time of 40 miliseconds （two whole mains cycles）．The current switching capacity of the Rue ie рәрәәәхә әq 100 pinous керә」 0
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0 time，so it＇s best to use a relay that is capable of switching the full 13 A maximum that you will ever draw from a socket．Alternatively，if you switching capacity，then we advise ＇yıed ұuәjins aył u！asnf e 8u！pnjou！ shown as FS2 in the circuit diagram． Apply a de－rating factor of at least

 relay should be capable of switching Construction
 quite straightforward provided that
component polarities are carefully observed．Before assembling it into the case，attach short lengths（about
transformer was quite sufficient． What would normally be the 110 V primary winding was used as the secondary．（The mains input wires should be passed through in the

If you use the ILP transformer， two passes of the mains input wires
may be necessary to achieve the required sensitivity（note that the transformer is operated as a current transformer and not a voltage transformer），as the OT 226 has only a 240 V primary．If you use a similar transformer with a 110 V sufficient for the primary．In any case，the test winding conductor must have the same number of

rom a standard toroidal transformer with extra windings added，and what would normally be the primary used as the secondary．We used a miniature 10 VA transformer supplied by RS Components，but this type is potted，and to
 in the middle．A better approach would be to use a type that is taped，such as the OT 226 made by ILP．In any case，ILP products are more readily available to the home constructor．
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The Current Transformer The transformer used to sense
the difference between the live and neutral currents，T1，can be made
accident like running an electric The particular method of fault detection we used ensures that the very hazardous condition of a person＇s body making contact between the mains supply and an independently earthed object can
be acted upon immediately；it is also independent of the integrity of normal outlet．The trip point of the
 balance current of about 25 mA will trigger it． CUURENT
TRANSFRORM


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At this stage the components
can be mounted into the lid of the
号 226， $12 \mathrm{~V}+12 \mathrm{ti}$ secondary，${ }^{2}$ ）．We







Fig. 2 Component overlay for the ELCB.

## PARTS LIST


box. This must be done with some care to avoid fouling the components in the bottom of the box. In our device the normallyclosed contacts of the relay were accessible when it was in the box and connecting the wires for the red neon was easily done. The free end of the test conductor is connected together with one of the wires from the yellow neon to the test switch (which is normally open). The 10k test resistor is fitted to the other side of the test switch. In our model this resistor is self-supporting between the switch and an insulated terminal. The other wire from the yellow neon, together with a wire from the neutral side of the power transformer, is connected to this terminal.

The wires to the reset switch can now be attached, making sure that the normally-closed terminal goes to the negative supply on the PCB, the normally-open terminal goes to the resistor and the pole goes to the capacitor.

Finally the thick output wires can be fitted into the outlet, together with the wires from the green neon. The earth conductor from the input cable is taken direct to the earth contact on the outlet and an extra wire then goes from here to the power transformer frame and to the centre tap on its secondary winding.

With a bit of luck it should now be possible to fit the box lid and base together and secure them with the bolts provided.

## Setting Up

Once the device has been assembled there is very little more to be done. It should be possible to adjust PR1 so that when the TEST button is pressed the relay immediately opens. If this cannot be set up or if there is very little adjustment to spare on PR1, then to increase the sensitivity R1 may be increased in value: conversely, reduce it to reduce the sensitivity. If the sensitivity is still low when R1 is up to 47 k then take the mains (and test) wires through the centre of T1 twice instead of once.

Once this has been done the device is set up to trip at about 24 mA . Note that it responds only to the out-of-balance current flowing through T1 on the mains wires and will not protect against contacts between live and neutral which result in balanced currents.

If you want to test the device we recommend that you use another 10k resistor and NOT YOURSELF . . .

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Block B is the trigger circuit and provides faultless spark timing. The emitter of TR1 is biased from the supply to provide a variable trigger threshold, allowing triggering with the supply down to about 3.5 volts but rejecting noise and signals from contact shuffle and vibration. Capacitor C3 and its associated resistors provide a variable inhibit period, after the contacts close, which filters out extreme contact bounce on 4 cylinder engines yet still allows 8 cylinder operation to over $7500 \mathrm{rev} / \mathrm{min}$. In effect the longer the contacts stay open the longer they must remain closed before the next spark can be triggered. (Be warned:- untimed sparks can seriously damage your engines health).

Block C is the inverter, the power behind the spark. It's a 'ringing choke' type. Well designed, this type can not only be regulated and charge the capacitor from zero volts, effectively a short circuit, but is also more efficient than the traditional push-pull type. Even though it provides around 3 times the power, it still doesn't need the usual finned heat sink. Transistors TR4 and TR5 regulate the invertor output, by controlling the amount of feedback, and are in turn controlled by TR3 which compares the voltage on the storage capacitor with the reference zener D5. The output voltage is set by the zener voltage so the full output is available over the whole supply voltage range, a powerful spark is produced even with the battery down to 4 volts.

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## SPECIFICATION

| (using a typical ignition coil) |  | ORDINARY CAPACITIVE DISCHARGE |
| :---: | :---: | :---: |
| Spark Power | 140W | 90W |
| Spark Energy (stored energy) | $\begin{gathered} 36 \mathrm{~mJ} \\ 135 \mathrm{~mJ} \end{gathered}$ | 10 mJ 65 mJ |
| Spark Duration | 500) S | 160 $\mu \mathrm{S}$ |
| Output Voltage clean spark plug fouled spark plug | $\begin{aligned} & 38 \mathrm{kV} \\ & 26 \mathrm{kV} \end{aligned}$ | $\begin{aligned} & 26 \mathrm{kV} \\ & 17 \mathrm{kV} \end{aligned}$ |
| Voltage Rise Time to 20 kV | 25 $\mu$ S | 30رS |

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# DESIGNER'S NOTEBOOK 

## Last month, we looked at some of the new switched capacitor ICs. This month, Tim Orr gets down to some circuits using them.

## Seven-Octave Audio Analyser

The R5606 is a single octave filter. Each R5606 is clocked with a square wave generated by a seven-stage binary divider, so that successive filter break-points are spaced at exactly one octave intervals. The resulting circuit is very simple and may be used as a real-time audio analyser or as an audio equaliser with a steep filter roll-off. Half-octave or even $\frac{1}{3}$ octave resolution could be obtained by using the R5605 or the R5604 respectively. The output signal is filtered by a simple single-pole low-pass filter to remove the effects of the sampling and the residual clock breakthrough. A simple anti-aliasing filter can also be used at the input to each filter, but this may not be considered necessary. A dynamic range of about 76 dB per channel should be obtained.

*The R5609 has a rollitiff stope of $100 \mathrm{~dB} /$ octave.
System bandwidth $=\frac{32 \times F_{f}}{100}=0.32 F_{c}$
Maximum theoretical bandwitth, as predicted by the sampling theorum $=0.5 \mathrm{~F}_{e}$

## Audio Converter With Tracking Filter

The R5609 is a steep low-pass filter which can be used as an anti-aliasing filter and recovery filter in an audio converter, such a digital delay line. If the clock for the filter is derived from the system clock and the A-to-D converter, then the low-pass filter frequency will track any changes in the conversion speed.


## FEATURE: Designers' Notebook.

## Low-Pass Response Using the MF10

The frequency responses of second, fourth and sixth order maximally-flat low-pass filters are shown in the graph. These can be realised by cascading second order low-pass filter sections together. The table shows the break frequencies and Q factors for both maximally flat (Butterworth) and 3 dB ripple (Chebychev) responses. The maximally flat responses are easy to realise because all stages use the same clock frequency. The 3 dB ripple response requires awkward clock frequencies. A simple design example will illustrate how to use the filter.

The figure shows a design for a fourth-order 2 kHz maximally-flat low-pass filter with an overall gain of 1 in the pass band. From the table, the first stage should have a Q of 0.54 and a frequency of 2 kHz , the second stage a Q of 1.306 and a frequency of 2 kHz . Mode 1 a is the most simple realisation of the second order low-pass filter. For the first section let R3 $=10 \mathrm{k}$. Then $\mathrm{R} 2=18.48 \mathrm{k}(15 \mathrm{k}+$ 3 k 6 would do). For the second stage let R2 $=10 \mathrm{k}$, then R3 $=13.06 \mathrm{k}$ ( $9 \mathrm{k} 1+3 \mathrm{k} 9$ is near enough). Both clock pins can be tied together and driven with a single 200 kHz clock (pin 12 grounded gives a clock-to-filter frequency ratio of 100 to 1).



| LOW-PASS FILTER RESPONSE | 1st STAGE |  | 2nd StAGE |  | 3rd Stage |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | \% | L | 0 | 0 | 10 | 0 |
| 2nd ORDER BUTTERWORTH <br> (FLAT RESPONSE) | 1.0 F | 0.707 |  |  |  |  |
| 2nd ORDER CHEBYCHEV <br> (3dB RIPPLE) | 0.84 F | 1304 |  |  |  |  |
| 4th ORDER BUTTERWORTH | 10 F | 0.54 | 1,0\% | 1,306 |  |  |
| 4th ORDER CHEBYCHEV | 0.4435 | 1076 | 0.95 F | 5.58 |  |  |
| 6th ORDER BUTTERWORTH | $1.0 \%$ | 0.518 | 10 F | 0.707 | 1.0 F | 1.931 |
| 6th ORDER CHEBYCHEV | 6.298 F | 1.044 | $0.722{ }^{2}$ | 3.46 | 0.975 F | 1278 |

* For the equivalent highpass response, use the same a factor but use the reciprocal of the frequency multiplier


## Band-Pass Response Using The MF10

A simple band-pass filter can be constructed using the circuit shown as mode 1 in the first article on switched capacitor ICs, and shown again to jog your memory! For a $Q$ of $10, R 3=100 \mathrm{k}$ and $R 2=10 \mathrm{k}$. To give the filter unity gain at resonance, $\mathrm{R} 1=\mathrm{R} 3=100 \mathrm{k}$. The external clock frequency determines the resonant frequency. By cascading two filters with a Q of 10 , a very sharp resonance curve is producedasyou can see in the graph below. If the Q factor of each filter is increased further then an even sharper response can be obtained, although this may result in a double peak if the relative resonant frequencies of the two filters deviate.



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| RECHARGEABLE BATTERES |  |  |  |
| :---: | :---: | :---: | :---: |
| CODE | TYPE | CAPACITY | PRICE |
| S401 | AAA | 200 mAH | 51.30 |
| 5101 | AA | 500 mAH | m. 50 |
| C1200 | C | 1200 mAH | 2. 21 |
| D1200 | D | 1200 mAH | [2. 0 |
| R×22 | Universel Charger for math |  |  |
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100 mW , receiver sensitivity 1 micro volt PRICE......E17.96 each OR ........... 34.95 per pair

# CIRCUIT <br> SUPPLEMENT 

Knowing how intelligent all you experimenters are, we've just given you the raw data of some of the latest integrated circuit technology there is, so you can get on with it without further ado. So we'll be expecting lots of Tech Tips based on these devices. . . . What's that? Oh, alright then, just a few circuits.

## TL011, TL012, TL014, TL021 (Texas

Instruments) Fixed ratio current mirrors

- Wide inpute range, 1 nA to 1 mA
- 35 volt output capability
- high output impedance
- typically less than $\pm 1 \%$ error at 25 deg C

Ratio of input current to output current varies with device code.

| Code. |  |
| :---: | :---: |
| OUTPUT TO INPUT | DEVICE |
| CURRENT RATIO | TL1 |
| $2: 1$ | TL011 |
| $4: 1$ | TL012 |
| $1: 2$ | TLO21 |

Types with different sufix have different temperature ranges and guaranteed current ratio tolerances over those ranges.
C suffix: 0 to $70 \mathrm{deg} \mathrm{C}, \pm 10 \%$ over full range
I suffix: -40 to $85 \mathrm{deg} \mathrm{C}, \pm 8 \%$ over full range
$M$ suffix: -55 to $125 \mathrm{deg} \mathrm{C}, \pm 7 \%$ over full range


Fig. 1 Pin out and simplified internal circuitry of the TL011, TL012, TL014, TL021.

| Electrical characteristics: TL011, etc | min | typ | max |
| :---: | :---: | :---: | :---: |
| Input voltage (V) (note 1) $\mathrm{I}_{\text {IN }}=1 \mathrm{uA}$ | 0.4 | 1.0 | 1.5 |
| $\mathrm{I}_{\mathrm{IN}}=1 \mathrm{~mA}$ | 0.9 | 1.4 | 1.75 |
| Input current (mA) |  |  | $\begin{aligned} & 5 \\ & \text { (note 2) } \end{aligned}$ |
| Output voltage | $\begin{aligned} & 1.2 \\ & \text { (note 3) } \end{aligned}$ |  | $\begin{aligned} & 45 \\ & \text { (note 2) } \\ & \hline \end{aligned}$ |
| Output to input isolation (dB) | 80 |  |  |
| Output resistance (M) $\underline{I}_{\text {IN }}=1 \mathrm{uA}$ | 1000 |  |  |
| $\mathrm{I}_{\mathrm{IN}}=1 \mathrm{~mA}$ | 1 |  |  |
| Maximum operating frequency ( MHz ) |  | 10 |  |
| Continuous power dissipation ( mW ) |  |  | 775 (note 2) |

## Key to footnotes

Note 1: figures for M suffix; I and C suffix types will have slightly higher voltages all round

Note 2: absolute maximum rating
Note 3: this is the guaranteed maximum minimum necessary to maintain current ratio.


Fig. 2 A phototransitor amplifier using a TL014.


Fig. 3 A two wire current-mode transmitter using the TL012.

## AD536, AD636 (Analogue Devices) RMS to DC

## Convertors

- true RMS to DC conversion
- dB output with 60 dB range ( 50 dB 636 )
- low power consumption: 1 mA 536, 800 uA 636
- dual or single supply operation over a wide range of supply voltages
- current output available
- available in DIL or TO 100 packages.

Fig. 4 (right) Pin out of the AD536 and AD636.


* $=25 k$ ON 636

14 PIN DIL PACKAGE


| Electrical characteristics |  | AD536 (note 1) | AD636 (note 1) |
| :---: | :---: | :---: | :---: |
| Input | peak max. for rated performance | $\pm 20 \mathrm{~V}$ for $\pm 15 \mathrm{~V}$ supply $\pm 5 \mathrm{~V}$ for +5 V supply | $\pm 5 \mathrm{~V}$ for $\pm 5 \mathrm{~V}$ supply $\pm 5 \mathrm{~V}$ for $\pm 2 \mathrm{~V} 5$ supply |
|  | max safe input | $\pm 25 \mathrm{~V}$ | $\pm 12 \mathrm{~V}$ |
|  | input resistance | approx $17 \mathrm{k} \Omega$ | approx $7 \mathrm{k} \Omega$ |
| Accuracy | without ext. trim | $+5 \mathrm{mV} \pm 0.5 \%$ (7VRMS | $+5 \mathrm{mV} \pm 1 \% \quad(200 \mathrm{mVRMS}$ |
|  | with ext. trim | $\pm 3 \mathrm{mV} \pm 0.3 \%$ input) | $\pm 3 \mathrm{mV} \pm 0.3 \%$ input) |
| Frequency response <br> (note 2) | $\mathrm{V}_{\mathrm{IN}}=10 \mathrm{mV}$ | 6 kHz | 12 kHz |
|  | $V_{\text {In }}=100 \mathrm{mV}$ | 40 kHz | 80 kHz |
|  | $\mathrm{V}_{\text {IN }}=1 \mathrm{~V}$ | 100 kHz | $130 \mathrm{kHz}\left(\mathrm{V}_{\mathrm{IN}}=200 \mathrm{mV}\right)$ |
| Averaging time const. | multiply by value of $\mathrm{C}_{\mathrm{AV}}$ in uF | 25 mS per uF | 25 mS per uF |
| Output from buffer | max output voltage $(\min =0)$ | $\begin{aligned} & +10 V( \pm 15 \mathrm{~V} \text { supply }) \\ & +2 \mathrm{~V}( \pm 5 \mathrm{~V} \text { supply }) \end{aligned}$ | +1 V 4 ( $\pm 3 \mathrm{~V}$ supply) <br> $1 \mathrm{~V}(+3,-5 \mathrm{~V}$ supply) |
|  | current | $+5 \mathrm{~mA},-130 \mathrm{uA}$ | + $5 \mathrm{~mA},-130 \mathrm{uA}$ |
| $\mathrm{I}_{\text {OUT }}$ | scale factor | 40 uA per volt RMS $(+25 \%)$ | 100 uA per volt RMS ( $+20 \%$ ) |
|  | voltage compliance | $-V_{s}$ to $+V_{s}-2 V$ | $-V_{S}$ to $+V_{s}-V$ |
| dB output | scale factor | -3 mV per dB <br> $(1 \mathrm{VRMS}=0 \mathrm{~dB})$ | $\begin{aligned} & -3 \mathrm{mV} \text { per } \mathrm{dB} \\ & (0 \mathrm{~V} 1 \mathrm{RMS}=0 \mathrm{~dB}) \end{aligned}$ |
|  | error | $\pm 0.5 \mathrm{~dB}$ | $\pm 0.5 \mathrm{~dB}$ |
|  | $\mathrm{I}_{\text {ReF }}$ range | 5 uA to 80 uA | 2 uA to 8 uA |
| Crest factor | error with 3:1 peak: average signal level | -0.1\% | -0.2\% |
| Power supplies | minimum voltage | $\pm 3 \mathrm{~V}$ or +5 V | $+3 /-5 \mathrm{~V}$ (note 3) or +5 V |
|  | maximum | $\pm 18 \mathrm{~V}$ or +36 V | $\pm 12 \mathrm{~V}$ or +24 V |
|  | current (quiescent) | 1 mA (max 2 mA ) | 800 uA (max 1 mA$)$ |

## Key to footnotes

Note 1: higher specification versions available
Note 3: may be operated on $+2 \mathrm{~V} /-2 \mathrm{~V} 5$ but will not give specified performance.

## SPECIAL : Circuit Supplement

## Normal Mode of Operation

Only one external component, $\mathrm{C}_{\mathrm{AV}}$ is needed. For 50 Hz operation, $\mathrm{C}_{\mathrm{AV}}$ should be at least $0 u 7 . \mathrm{C}_{\mathrm{F}}$ is an optional output ripple filter, and would normally be twice the capacity of $C_{A V}$ if used, though this will increase the setting time.


Fig. 5 Normal mode of connection of the AD536 and AD636.

## Circuit for dB Output

VR1 should be adjusted to give the correct 0 dB point, and $I_{\text {ref }}$ should be within the range quoted in the main specifications table. An inverting and amplifying stage could be used to obtain a positive-going suitably scaled output, and also to compensate for temperature drift.


Fig. 6 Output connections for dB output.

Single Supply Connections
Note that only AC signals can be measured in this mode.


Fig. 8 Connections for a single supply rail.

## Connections to trim errors

VR2 adjusts the total offset; ground the input and adjust VR2 to obtain zero output.
VR1 adjusts the gain; after adjusting VR2, apply a DC input of full scale and adjust VR1 to give the same output.

| Component Values | AD536 | AD636 |
| :--- | :--- | :--- |
| R1 | $249(180+68)$ | 100 |
| R2 | 470 k | 470 k |
| VR1 | 470 | 220 |
| VR2 | 47 k | 470 k |



Fig. 9 Connections necessary to trim out errors.


Fig. 7 A complete AC digital voltmeter using the AD636 and a 7106 (or similar) ADC/LCD driver.

## CEM 3350 (Curtis) dual VCF

- Dual state variable filters with independent exponential frequency and Q control
- Wide frequency range: 15 octaves typical
- Choice of two simultaneous outputs: low-pass, bandpass, or high-pass
- Wide supply voltage range: $\pm 3 \mathrm{~V}$ to $\pm 18 \mathrm{~V}$


## Definition of terms

| $V_{\text {IF }}$ | fixed gain input |
| :--- | :--- |
| $V_{V \mathrm{~L}}$ | variable gain input |
| $V_{\mathrm{LP}}$ | low-pass output |
| $V_{\mathrm{BP}}$ | band-pass output |
| $V_{\mathrm{CQ}}$ | Q control voltage input |
| $V_{\mathrm{CF}}$ | pole frequency control input |
| $V_{\mathrm{CC}} V_{\mathrm{EE}}$ | positive and negative supplies |
| $\mathrm{I}_{\mathrm{REF}}$ | reference current input |

## Application notes

The transconductors inside the IC are NPN differential pairs with current mirror active loads (similar to CA3089) so input levels must be kept low $(20-80 \mathrm{mV})$ for acceptable distortion. Inputs must normally be attentuated and output level should be restored using a BIFET op-amp to avoid problems with input offset currents that might be caused by the transconductors' high output impedance.

Note that applying increasing negative $V_{C O}$ will increase $Q$, and that pole frequency decreases with increasingly positive $V_{C Q}$. For negative $V_{C F}, g_{m} F$ (and hence pole frequency) is approximately linear, but becomes exponential when $\mathrm{V}_{\mathrm{CO}}$ is positive.


Fig. 10 CEM 3350 pin out and configuration as voltage controlled four-pole low-pass filter. Band-pass outputs could be cascaded by connecting $V_{i v}$ and $V_{i f}$ in the second section to $\mathrm{V}_{\mathrm{Bp}}$ in the first section, and taking the output to the op-amp from $\mathrm{V}_{\mathrm{Bp}}$.

| Electrical characteristics: CEM 3350 (Supplies $\pm 12 \mathrm{~V}, \mathrm{I}_{\text {ReF }} 400 \mathrm{uA}$ ) | min | typ | max |
| :---: | :---: | :---: | :---: |
| Pole frequency control range | 4000:1 | 12000:1 |  |
| Sensitivity of pole frequency control scale, midrange ( $\mathrm{mV} /$ decade) | 57 | 60 | 63 |
| Exponential error of frequency and Q control scale (\%) (note 1) |  | 1.0 | 3.0 |
| Transconductance of Q transconductors (mmho) (note 2) | 4.5 | 6.9 | 9.3 |
| Maximum transconductance of pole and Q transconductors (mmho) | 11.0 | 14.2 | 16.0 |
| Distortion in passband (\%) (note 3) |  | 1,0 | 5.0 |
| Maximum Q without enhancement transconductance output impedance ( $M \Omega$ ) (note 2) | $\begin{aligned} & \hline 50 \\ & 1.0 \end{aligned}$ | $\begin{aligned} & 150 \\ & 4.0 \\ & \hline \end{aligned}$ |  |
| Supply voltages (V) | $\pm 3$ |  | $\begin{aligned} & \pm 18 \\ & \text { (note 4) } \end{aligned}$ |
| Supply currents (mA): $\begin{aligned} & \text { positive } \\ & \text { negative }\end{aligned}$ |  | $\begin{aligned} & 2.5 \\ & 6.5 \end{aligned}$ | $\begin{aligned} & 3.0 \\ & 7.5 \end{aligned}$ |

There is a choice of fixed gain and variable gain inputs on both filters in the IC. The difference between these inputs is shown in Fig. 14. Signals applied to the fixed gain input will have gain Q at the resonant peak and unity elsewhere in the pass-band. Signals applied to the variable gain input will have unity gain at the resonant peak, while the gain in the pass-band will by $1 / Q$. Thus the fixed gain input can give overload problems, while the variable gain input will lead to changes in output volume as Q is adjusted. One way of trying to reach the best compromise is to aportion the input between the two inputs using an attenuation network (or, in more simple circuits such as Fig. 10, feeding the signal to both inputs). Another method would be to use the fixed gain input only, and to reduce the $Q$ if a certain signal output level is exceeded.

Q is given by:

$$
\mathrm{Q}=\frac{3}{2} \sqrt{\frac{C_{L P}}{\bar{C}_{B P}}} \exp \left(-V_{C Q} / V_{T}\right)
$$

where $V_{T} \sim 25 \mathrm{mV}$ at room temperature.

## Key to footnotes

Note 1: $+60 \mathrm{mV}<\mathrm{V}_{\mathrm{CF}}<+240 \mathrm{mV}$
Note 2: control voltage $=0$
Note 3: $\mathrm{V}_{\mathrm{IF}}$ or $\mathrm{V}_{\mathrm{IV}}=40 \mathrm{mV} \mathrm{p}-\mathrm{p}$
Note 4: maxımum total differential supply for guaranteed operation is 26 V .


Fig. 11 High-pass filter using CEM 3350: response will fall by 12 dB per octave below the cut-off frequency.


(a)

Fig. 14 Low-pass (a) and band-pass (b) responses for sections, with $Q=1$ and $Q=10$. In the case of curves with $V_{I F}$ or $V_{I V}$ input, other input has been grounded.


Fig. 12 (above) Low-pass and a high-pass filters can be combined to give a band-pass filter with a voltage-controlled band width. The circuit shown is an example of series interconnection; parallel interconnection is also possible.

Fig. 13 (left) The $\mathbf{Q}$ of the circuit may be enhanced above the normal maximum of $100-200$ by applying regenerative feedback as shown: but beware too much feedback, as this will cause oscillation at the resonant frequency.

Available from Digisound Ltd.


## ZN428E-8 (feranti) Eight-bit D-to-A convertor

- D-to-A convertor, data latch and reference voltage in a single package
- single supply operation
- CMOS and TTL compatible
$\bullet 800$ uS setting time


Fig. 15 ZN428 pin out.



Fig. 16 (above) Normal mode of operation of the ZN428.
Fig. 17 (left) Block diagram of ZN428.

## Operational notes

When ENABLE is low, the data inputs drive the D-to-A directly. When ENABLE goes high, the data is held in the latch until ENABLE next goes low.

Internal reference voltage source is a band gap diode circuit and it needs an input current to operate. Using a decoupling capacitor is recommended. There is no internal connection between the internal reference voltage source ( $\mathrm{V}_{\mathrm{REC}} \mathrm{OUT}$ ) and the reference input to the $\mathrm{R}-2 \mathrm{R}$ ladder ( $\mathrm{V}_{\text {ReF }}$ IN ).

| Electrical characteristics: ZN428E-8 |  | min | typ | max |
| :---: | :---: | :---: | :---: | :---: |
| Supplies | voltage (V) | 4.5 | 5.0 | $\begin{aligned} & 5.5 \\ & \text { (note 1) } \end{aligned}$ |
|  | current (mA) |  | 20 | 30 |
| Internal reference | voltage output (V) | 2.475 | 2.550 | 2.625 |
|  | current (mA) | 4 |  | 15 |
| D-to-A convertor | linearity error (note 2) |  |  | 0.5 |
|  | offset voltage, $\mathrm{V}_{\text {OS }}(\mathrm{mV}$ ) |  | 2 | 5 |
|  | reference voltage input ( V ) | 0 |  | $3.0$ <br> (note 3) |
|  | settling time to 0.5 of LSB (uS) |  | $\begin{aligned} & 0.8 \\ & \text { (note 4) } \end{aligned}$ | $\begin{aligned} & 1.25 \\ & \text { (note 5) } \end{aligned}$ |
|  | output resistance (k) |  | 4 |  |
| Logic | enable pulse width ( nS ) | 100 |  |  |
|  | data set-up time ( nS ) | 150 |  |  |
|  | data hold time ( nS ) | 10 |  |  |
| Ground | max. discrepancy between an. and dig. gnd (mV) |  |  | 200 |

## Key to footnotes

Note 1: absolute maximum is 7 V
Note 2: expressed as fraction of least significant bit
Note 3: absolute maximum $+V_{\mathrm{Cc}}$

Note 4: average after one LSB transition, $\mathrm{R}_{\mathrm{L}}=10 \mathrm{M}, \mathrm{C}_{\mathrm{L}}=10$ pF
Note 5: average after all bits switching, $\mathrm{R}_{\mathrm{L}}, \mathrm{C}_{\mathrm{L}}$ as before.

## SPECIAL : Circuit Supplement

## LF 347 (National Semiconductor) quad JFET op-amp

- pin-for-pin replacement of LM148
- approximately full gain and band width down to $\pm 4 \mathrm{~V} 5$ supply voltage
- no special anti-static handling of op-amp required
- internally trimmed offset voltage.


## Absolute maximum ratings:

Supply voltage:
Input voltage range, per input (note 1)
Output short circuit deviation (note 2)
Power dissipation (whole IC)
$\pm 22 \mathrm{~V}$
$\pm 19 \mathrm{~V}$ continuous
900 mW

Available from Rapid Electronics, and other suppliers.

| Electrical Characteristics: LF 347 (supply voltages: $\pm 15 \mathrm{~V}$ ) | min | typ | max |
| :---: | :---: | :---: | :---: |
| DC voltage gain ( $\mathrm{V} / \mathrm{mV}$ ) | 50 | 100 |  |
| Slew rate (V/uS) |  | 13 |  |
| Output voltage swing, load $=10 \mathrm{k}(\mathrm{V})$ | 12 | 13.5 |  |
| Gain-bandwidth product ( MHz ) |  | 4 |  |
| Input resistance (ohms) |  | $10^{12}$ |  |
| Common mode rejection ratio (supply voltage $\pm 20 \mathrm{~V}$ ) (dB) over input voltage range (V) | $\begin{aligned} & 80 \\ & \pm 11 \end{aligned}$ | $\begin{aligned} & 100 \\ & +15 /-12 \end{aligned}$ |  |
| Supply voltage rejection ratio (note 3) (dB) | 80 | 100 |  |
| Input offset voltage (mV) |  | 1 | 5 |
| Amplifier to amplifier coupling (frequency range 1 Hz to 20 kHz , supply voltage $\pm 20 \mathrm{~V}$ ) (dB) |  | - 120 |  |
| Supply current (all four op-amps - but no load)(mA) |  | 7.2 | 11 |

## Key to footnotes

Note 1: input voltages should not be allowed to go below negative supply voltage, otherwise op-amp may be destroyed.

Note 2: only one op-amp output should be shorted at anytime, otherwise IC may overheat.
Note 3: measured for both supply voltages decreasing and increasing simultaneously.


Fig. 18 Output voltage swing vs. supply (a) and output load (b) undistorted output vs. frequency (c), output impedance vs.
frequency (d), open loop frequency response (e), and bode (f) plot for LF347.

## SPECIAL : Circuit Supplement



Fig. 19 Pin out of LF347.


Fig. 20 Example of a long-time integrator, with reset, hold and starting threshold adjustment, using LF347s.

Fig. 21 We've used BIFET op-amps so much with the CEM 3350, it seems unjust that they shouldn't have a circuit or two to themselves; so here they are: a) a high accuracy sample-andhold, b) a peak detector, and c) a low-drift peak detector.


## TV Alarm

Our final circuit is intended to make it harder for a thief to walk off with your TV. The basic idea is to use the aerial as the detector of the TV's non-presence. This is done by sending a small DC signal round the loop formed by the aerial pick-up loop (or the signal transformer for the aerial) and the signal transformer (or balun) in the TV. The circuit is isolated from TV signals by RF chokes RFC1 and RFC2, and C1 is inserted into the signal path to block the DC.

When the TV is disconnected, there is a 10 second delay (set by the values of R1 and C2) before the alarm goes off, so that an unwitting burglar will not know what has turned on the alarm.

This circuit is not suitable for use with TVs that have live chases. Also, there must be DC path through the TV's aerial circuit for the alarm to work.

The idea for this circuit originated in Australia. This doesn't mean that you have to turn your TV upside-down for the circuit to work. However, so doing may help with your appreciation of 'Blankety-Blank'.

ETI would like to thank the manufacturers of the ICs featured for their help.


ETI

# Jupiter 

## The Jupiter Ace uses FORTH

The Jupiter Ace personal computer runs in FORTH, an easily understood language, typically four times as compact and ten times as fast as BASIC. Before the Ace all personal computers used BASIC and FORTH was only available to a privileged few. The Jupiter Ace also features a full-size moving-key keyboard, high-resolution graphics, sound, floating point arithmetic, a fast and reliable cassette interface and 3 K of RAM.

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## Plug-on 16K Memory Expansion

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Ace to 19 K giving you instant access to enormous amounts of information.

## Software

A catalogue will be sent with every machines, and includes, initially, programs for education and entertainment.

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Even if you are a complete newcomer to computers, the manual will guide you step by step from first principles to confident programming.
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The Jupiter Ace is available only by mail order.
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## Technical Information

Hardware
Z80A running at 3.25 MHz .
8 K bytes ROM
3 K bytes RAM
Keyboard 40 Moving-key keyboard with auto repeat on every key and Caps Lock.
Screen Memory mapped 32 column $\times 24$ line flicker-free display with upper and lower case ascii character set.
Graphics Chunky graphics ( $64 \times 46$ pixels) may be plotted, unplotted or over-plotted (XOR operation). Also, the entire character set (128 characters and their video inverses) may be redefined allowing intricate shapes to be drawn with a resolution equivalent to $256 \times 192$ pixels.
Sound Internal loudspeaker may be programmed to operate over the entire audio spectrum.
Cassette Programs and data in the compact dictionary format may be saved, verified, loaded and merged. Blocks of memory can be saved, verified, loaded and relocated. All tape files are named. Running at 1500 baud, the Ace will connect to most portable tape recorders.
Expansion Port Contains D.C. power rails and full $\mathbf{Z 8 0}$ Address, data and control signals. May be used to connect extra memory and other peripherals. IN and OUT words allow port-based peripherals to be addressed.
Data Structures Integer, Floating point and String data may be held as constants, variables or arrays with multiple dimensions and mixed data types. There are no restrictions on names.
Control Structures IF-ELSE-THEN, DO-LOOP DO-+LOOP. BEGIN-WHILE-REPEAT, BEGIN-UNTIL, all may be mixed and nested to any depth.
The Jupiter Ace closely follows the FORTH 79 standard with extension for floating point, sound and cassette. It has a unique and remarkable editor that allows you to list and alter words that have been previously compiled into the dictionary. This avoids the need to store screens of source, allowing the dictionary itself to be saved on cassette. Comprehensive error checking removes the worry of accidentally crashing your programs.


## Designed by Jupiter Cantab

Computer Designers Steven Vickers and Richar Altwasser played a major role in creating the ZX Spectrum and then formed Jupiter Cantab to develop advanced ideas in personal computing. The Ace is the result, another all-British computer to lea the world.

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# DESIGNING MICRO 

# SYSTEMS part 5 

# So far we've covered the brains of a computer, but it's still deaf and dumb, electronically. This month Owen Bishop takes on the role of ear, nose and throat specialist. 

The CPU, its ROM and its RAM, the subjects of previous parts of this series, are a tightly-knit section of all computer systems. In most micros, they are mounted together on a single computer board. This month, we are concerned with the way in which this section of the computer circuit communicates with the rest of the circuit and with devices outside the computer proper. This aspect of computer design is known as Input/Output, or I/O for short.

## In The Right Key

Leaving aside special-purpose computers such as those used in control applications, the most important source of input to the computer is its keyboard. This is where our finger-tips send information (instructions on what to do, and data to do it with) to the computer. As I write this sentence, my fingers are pressing keys on a computer keyboard. Each key is marked with a letter of the alphabet, a numeral or other symbol. There are also a space bar and two shift keys. How does the computer know which keys I have pressed? If I press the fifth key from the left of the second row down, I want it to put ' $r$ ' on the screen. If I also press a shift key, I want ' $R$ '. How does it know which key means which letter?

If a keyboard is to provide input to the CPU, it must somehow place information on the data bus. The keyboard of the computer which I use for word-processing does this in a simple way. The method is one which is commonly used in micros at the lower end of the price range. Figure 1 shows the main features of the circuit. The first point to note is that there is a bank of eight buffers between the keyboard circuit and the data bus. It would be no good if data were put directly on to the bus every time I happened to touch a key. That might be just the moment when the MPU is reading from RAM. My pressing key ' $r$ ' just then could have disastrous results! It is essential that there is something between the keyboard and the data bus. This is the function of the buffers.

The buffers are under the control of the MPU. Each buffer has a data input, a data output and an enable input. The keyboard uses eight such buffers and they are all enabled together. When the enable input is held high $(+5$ V) the buffers are in the high-impedance state: in effect, the outputs are disconnected from the data bus. The buffers are held in this state when the MPU is busy reading RAM, or, for any other reason, does not want to know what is happening at the keyboard. When the enable input is made low ( 0 V ) the outputs of the buffers take the states opposite to their data inputs (they are inverting buffers). The data present at the inputs appears inverted on the data bus lines.

## Addressing The Problem

Enabling is under the control of a logical circuit, an address decoder. In Part 3 we described how an address is decoded in order that a particular memory cell in ROM or RAM can be read from or written to. The same technique is used here. Although the keyboard is not memory in the sense that it stores information, it is addressed in the same way as memory. Most addresses are allocated to RAM or ROM, but a few are allocated to the keyboard.

In my computer, the keyboard is addressed at 3800 to 38FF, though only a few of these addresses are actually used. The address-decoding logic gives a low output (to enable the buffers) whenever ' $00111000^{\prime}$ appears on the upper eight address lines (A15 to A8). The lower eight address lines (A7 to A0) go to the keyboard matrix. As it


Fig. 1 A typical keyboard circuit. To simplify this, only one row of keys has been drawn.
enters the matrix, each line goes to a buffer. These are inverting buffers with open-collector outputs.

You will see from Fig. 1 that the matrix consists of eight address buffer output lines crossed by eight data buffer input lines. The keys are simple press-to-make pushbuttons, joining an address output to a data input. The buffer input lines are normally held high because of the resistors connecting them to the +5 V supply line. When a key is pressed, an address buffer output becomes connected to a data buffer input. The fact that the address buffers have open-collector outputs means that if a buffer has a low output, it pulls the level down to 0 V . Otherwise the level remains at +5 V .

## The Soft Solution

The rest of the input procedure depends on software: the monitor program in ROM contains a routine for reading the keyboard. The MPU addresses the keyboard by putting '0011 1000' ( $=38$ in hex) on the high address lines (A15 to A8) and putting ' 1 ' on only one of the remaining address lines. For example, to address the first row of keys, the full address is '0011 100000000001 ' ( $=3801$ ). For the next row we have '0011 $100000000010^{\prime}$ $(=3802)$, then ' $0011100000000100^{\prime}(=3804)$ and so on through 3808, 3810, 3820 and 3840 to 3880 (all hex numbers, remember). The MPU puts these eight addresses in rotation on the address bus. When any one of these addresses is on the bus, the address decoder circuit enables all the data buffers. If no key is being pressed at that moment, all data outputs are low. But if one of the keys is being pressed at the same time as its address buffer output is low, a 'high' appears on one of the data lines. Thus if I press key ' $r$ ' when the MPU is addressing 3802, line A2 is high, so its buffer output is low. Since key ' $r$ ' connects this output to the buffer for data line D2, 0000 $0010^{\prime}$ ( $=02$ in hex) appears on the data bus. The MPU now has to go to a monitor routine to interpret this data. Using this routine, it finds out that if the data is ' 02 ' when the address is 3802, then key ' $r$ ' has been pressed. An instant later, it will be addressing 3880 and, if the data becomes ' $00000001^{\prime}(=01)$ it can then tell that the shift key also has been pressed, and that the upper-case ' $R$ ' is intended.

The MPU continually scans the keyboard in this way when waiting for input, decoding the data according to which address is in force at that instant. This approach to input relies heavily on software, and it takes several operations to detect and decode each key-stroke. Response is relatively slow. The routine required is further complicated by the need to deal with two keys being pressed simultaneously or in very rapid succession. It is necessary to check that a pressed key has been released before attempting to decode the next key that is pressed. This feature is known as two-key rollover. Fortunately, microprocessors work so quickly that even an experienced touch-typist is not able to outpace the keyboard decoding routines.

## Encoding Made Easy

Although the circuit described above is simple and cheap to build, the MPU is required to do a lot of work. If this work could be done elsewhere, it would leave the MPU with more time to spend on other and perhaps less routine jobs. The alternative approach to keyboard decoding is to employ a special decoder IC (Fig. 2). Again, the keys are connected at the intersections of a matrix, but now both sets of lines come from the encoder IC. The IC has its own clock circuit and scans the matrix rapidly to find which $X$ line and which $Y$ line have been connected by a pressed key. Having detected a key-press, the output
latches of the IC are set to produce a seven-bit code corresponding to the pressed key, taking into account whether or not the shift key or possibly the 'control' key has been pressed at the same time.

You can think of the keyboard encoder as having some of the features of a ROM. When a set of eight memory cells in ROM is addressed for reading by the MPU, its output latches deliver to the data bus the byte stored in that cell. Similarly, the memory cells of the keyboard encoder each contain one code byte. The $X$ and Y lines from the keyboard correspond to address lines. When a particular address is set up by pressing a particular key or combination of keys, the corresponding memory cells place their stored byte in the output registers of the IC. The data stored in the registers remains there until the MPU addresses the encoder. Then its register puts the stored code on the data bus and the MPU reads the code. Note that the MPU only has to perform one addressing operation: the keyboard address in the Apple II, for example, is COOO . This operation is much quicker than the laborious scanning operation described earlier. The only other thing the MPU has to do is to address the encoder reset (address C001) to reset the latches, ready for them to be set by the next key-press. Note that the encoder holds the code until the MPU requests it. In the previously described system, if the MPU is expecting input from the keyboard, it must continually scan the keyboard in case it should miss a key-press.

## Ask Me In ASCII

Whereas the code generated by the circuit of Fig. 1 depends on how the circuit is wired, the code generated in Fig. 2 depends on the codes programmed into the memory of the IC during manufacture. In order to promote good communication between keyboards, MPUs and other $1 / O$ devices, a standard code has been drawn up for use in computer systems. This is the American Standard Code for Information Interchange, known as the ASClI code (Table 1). Most keyboard encoders produce ASCII code and most computers understand it!


Fig. 2 A keyboard circuit using an ASCII encoder (simplified circuit; only a few keys drawn).

A quick glance at Table 1 reveals that the seven-bit codes cover more than the printable alphabetical and numerical characters and symbols. The first two columns contain what are usually termed control codes. These are instructions for the control of peripheral devices, especially printers. They are generated when the CONTROL key is pressed at the same time as one of the alphabetical keys. The code BS, for example, is generated by pressing CONTROL and H, and means 'backspace'. Since this is a frequently used command, many keyboards have a special 'backspace' key ( - ) which generates this command with a single keystroke. CR means 'carriage return'. When you press the RETURN (or ENTER) key, the keyboard sends a CR code (000 1101) to the computer. This can be used, for example, to tell the computer that the program line which has just been typed in is complete and ready to be stored in program RAM. If the MPU sends such a signal to a printer, it instructs the print-head to return to the left-hand edge of the page. The DC1 to DC4 codes are Device Control codes, available for miscellaneous functions differing from one machine to another. On the TRS-80, code DC4 instructs the line printer to print at 16.7 cpi , whereas on the Apple II it is a toggle instruction to the Silentype printer to echo its printout to the monitor screen.

A further refinement found on some systems is a FIFO, or first-in-first-out device. It is wired between the encoder IC and the data line buffers. As each key is pressed, the encoder sends the corresponding ASCII code to the FIFO, which stores it. Typically, it can store up to 16 ASCII codes. The codes are sent out to the buffers in the same order as they are fed in. When the MPU is ready to read a code, a strobe signal to the FIFO results in the next available code being sent to the buffers. In this way, we have asynchronous transfer of data between keyboard and CPU. 'Asynchronous' means that the MPU and keyboard do not have to keep in step. If the MPU is temporarily busy and not able to accept input from the keyboard, the data queues up in the FIFO until the MPU is ready to accept it.

## Plugging In Peripherals

Now that micros are becoming more commonplace, people are beginning to recognise that they are capable of far more than just playing arcade games or taking charge of the book-keeping. There is an increasing interest in being able to connect external devices to the micro anything from a simple games control to a robot arm. The more recently made micros, even those in the lower price range, now incorporate ICs which allow a variety of peripherals to be attached. These I/O channels are often referred to as 'ports'.

There are two main types of port IC. The parallel I/O device (or PIO ) allows data to be transferred between the computer and the peripheral several bits at a time. Commonly there are eight lines, allowing transfer of one byte at a time. The serial I/O device (SIO) transfers data a bit at a time, but groups bits into eights (usually) so that a byte is transmitted as a series of eight bits. We will deal with SIOs in a later issue.

## Parallel Lines

Although it is only recently that PIOs have become standard on many low-cost micros, they have always been an almost essential feature of the simple computers intended principally for control applications. A wellknown example of a PIO is the INS8154 (Fig. 3). Our old favourite, the Sinclair MK-14, had a socket to take an 8154, though the MPU used in this system (the 8060 or SC/MP) has a few direct I/O terminals of its own. Its three 'Flag' outputs can be programmed to have high or low outputs, giving a three-bit data output. The MPU also has

| TABLE 1 : THE ASCII CODE |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| High nibble | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| Low nibble |  |  |  |  |  |  |  |  |
| 0 | NUL | DLE |  | 0 | (1) | $\bar{p}$ | * | $\stackrel{\rho}{\rho}$ |
| 1 | SOH | DC1 | $!$ | 1 | A | Q | a | $q$ |
| 2 | STX | DC2 | " | 2 | B | R | b | r |
| 3 | EXT | DC3 | \# | 3 | C | S | c | s |
| 4 | EOT | DC4 | \$ | 4 | - | T | d | t |
| 5 | ENQ | NAK | \% | 5 | E | U | e | $u$ |
| 6 | ACK | SYN | \& | 6 | F | $v$ | f | v |
| 7 | BEL | ETB | , | 7 | G | w | g | w |
| 8 | BS | CAN | < | 8 | H | X | h | $x$ |
| 9 | HT | EM | ) | 9 | Y | Y | i | $y$ |
| A | LF | SUB | * | ; | , | z | j | z |
| B | VT | ESC | + | $\stackrel{ }{ }$ | K | [ | k | ( |
| C | FF | FS | \% | $<$ | L | 1 | 1 | I |
| D | CR | GS | - | = | M | 1 | m | \} |
| E | SO | RS | * | $>$ | N | 1 | n | $\sim$ |
| F | SI | US | 1 | ? | O | - | o | DEL |
| The code is obtained by combining the high nibble (top margin) with the low nibble (left margin) to make a byte. For example the code for upper case $W$ is ' 57 '. The code ' 20 ' represents a space. |  |  |  |  |  |  |  |  |



Fig. 3 Pin connections for the INS8154 I/O device.


Fig. 4 The $\mathbf{Z 8 0}$ PIO, showing its main connections when linking the $\mathbf{Z 8 0}$ to a peripheral device.
two 'Serial' inputs which allow two sets of input data to be fed directly to the MPU. This feature of built-in I/O is quite enough for simple control applications and may dispense with the need for a separate I/O IC.

The Acorn System 1 is a well-established control computer. It has sockets for two 8154s, the second of which is used for 1/O between the CPU and the cassette recorder. As with the keyboard, an 1/O device has to be 'located' in a certain part of memory: we say that it is 'memory-mapped'. When addressing the 8154, the top eight address bits (A15 to A8) are used for establishing the base address of the IC in the way we have already described. The IC has two chip-select inputs, one of which (CS1) is active-high, and the other (CSO) is active-low. Either or both inputs can be used to enable the chip, making it easier to work out an economical addressdecoding circuit.

The $M / \overline{I O}$ input is unusual, for as well as being an $I / O$ device, the 8154 carries 128 bytes of RAM. This memory/IO combination is handy for control systems, for which 128 bytes may be all the RAM that is needed. The $\mathcal{M} / \overline{\mathrm{OO}}$ input is usually controlled by line A7. The remaining lines (A6 to AO) are decoded inside the 8154. To operate the 8154 as RAM, the $M / \overline{I O}$ input is made high. If the base address is AOOO (as in the Acorn System 1), RAM extends from A080 to A0FF (bit A7 always high for memory operations). To use the IC for I/O the M/IO input is made low (bit 7 always low for I/O). This section of the IC thus comes in the range A000 to A0FF. Actually, only a few of these addresses are used. Some of the addresses are used to initiate certain modes of operation; others are used when sending or receiving data. The method of programming the $I C$ is too complex to go into here, but we can outline what it is possible to do.

Data is passed between the CPU and the IC by way of the eight-bit data bus. Data is passed between the IC and the outside world (TTL levels only) by the 16 I/O lines. These are organised as two eight-bit ports, A and B. Each port can be controlled and addressed separately. Reading and writing to the device is totally under the control of the MPU. The registers in each port can be instructed by the MPU to act as outputs, or as inputs. It is also possible to control each line of a port individually, so that some of them are inputs and others are outputs.

When data is being output, it is transferred to the IC and appears on those lines which have been selected as outputs. The data stays there, even though the original
signals may have been removed from the data bus and the MPU is busy doing something else. The data can remain until the external device is ready for it, allowing the asynchronous transfer of data, as mentioned earlier. When the CPU reads from input lines, the data it receives is that which is being transmitted from the peripheral at that instant.

## The Hardware Handshake

Obviously there can be problems in transmitting data through an I/O. How does the MPU know that the peripheral has received the data which has been sent to it? It is no use for the MPU to send a new set of data until it is sure that the peripheral has actually received the previous set. Conversely, how does the MPU know that there is a set of data waiting at the input port? How does the peripheral know when this data has been read by the MPU? Again, it is no good the peripheral inputting data to a port if the CPU has switched that port to the output function.

In some systems the sequence of operations and their timing may be such that complete transfer of data is assured. In other systems it is necessary to provide for signals to be sent between the MPU and a peripheral to control the flow of data. This is known as 'handshaking'.

The Z80-PIO (Fig. 4) has special control inputs and outputs and the necessary logic circuits to provide for handshaking. Like the 8154, it has two eight-bit ports, each of which can be individually programmed to act as an input port or an output port. Port A can also be programmed as a bidirectional port, allowing direct communication between the peripheral and the data bus. Alternatively, the individual lines of the port can be set for input or output, as described for the 8154. Figure 5a shows how data is sent from the MPU to a peripheral. As soon as data has been written to the IC and has appeared at an output port, the READY output goes high: this is a signal to the peripheral. When the peripheral receives this signal it knows that it must read the data. As soon as it has read the data, the peripheral puts a low pulse on the STROBE line. This causes the IC to generate a low pulse on the $\overline{\text { INT }}$ line. This goes to the MPU, telling it that the data has been read. The MPU may now send a further byte of data to the peripheral.

When inputting data (Fig. 5b), the peripheral begins by making STROBE low. The TNT pulse generated by the I/O device interrupts the MPU to tell it that there is data to

be read. At the same time, the READY output goes low, indicating to the peripheral that the data is being held, waiting for the MPU to read it, and that no more data should be sent in the meantime. As soon as the computer has read the data, the end of the $\overline{R D}$ pulse resets READY, so that the peripheral knows that reading is complete and more data can be sent. Thus the sender and receiver each know which stage the other has reached. Data is transferred between them in either direction without loss.

The 8154 has a similar handshaking procedure but this is limited to port A. The INTR line has the same function as the $\mathbb{N T}$ line, but Fig. 3 shows that there are no special control lines to correspond with READY and STROBE. instead, two of the lines of port B are taken over for this purpose when port $A$ is to be used in the handshake mode. The remaining six lines of port $B$ can be used independently, in the usual way.

## Dealing With Interruptions

We have seen how the interrupt is an essential part of handshaking by PIO devices. The interrupt may also be used when other peripherals want to communicate with the MPU, either through an I/O device or directly to the data bus. Often, there are several peripherals connected to a system yet all give the same interrupt signal. How is the MPU to know which one of these peripherals it is dealing with?

One method is 'device polling'. Each device has a latch circuit which gives a high output when the device is trying to input data to the MPU. The latches are enabled by an address decoder, and each is separately addressed. When interrupted, the MPU goes to its interrupt routine program, disabling the interrupt function for the time being: this prevents it being interrupted again while it is attending to the current interrupt. The interrupt routine instructs it to read each register in turn to find out which device is interrupting and to jump to a particular subroutine according to which device has interrupted. Note that this program polls the devices one at a time in a pre-determined order. We can program the MPU to test first the registers of devices which cannot wait long to be serviced, leaving other less urgent devices until later. In this way the software establishes a system of priorities.

The Z80 has a vectored interrupt mode which simplifies the process of finding out which device is interrupting: at the same time as the device interrupts, it
puts certain data on the bus. This data is read by the MPU and combined with other data already in memory to form the address where the appropriate interrupt routine begins. Each peripheral identifies itself by putting this particular set of data on the bus, causing the MPU to jump to the corresponding servicing subroutine.

## Who's Shouting The Loudest?

Most I/O devices have two ports, some have three, and many computers have more than one $1 / O$ device. If the MPU has two or more peripherals and all are trying to communicate with it at the same time, the situation is like a political meeting with everyone trying to shout at once! There must be a system of priorities so that, when one of the more important peripherals is communicating, the less important ones are ignored. We have seen that software provides priority, but only after the interrupt has occurred. Hardware priority ensures that a high-priority peripheral will always get preference whenever it interrupts. The most commonly used method is known as daisy-chaining.

Daisy-chaining works like this. All the PIOs or other peripherals are connected to the INT line by opencollector outputs. The line is normally held high by a pullup resistor connected to +5 V , but when any one or more interrupt outputs goes low, the voltage on the line is pulled down and the MPU goes into its interrupt routine. In order to be able to generate an interrupt output, a peripheral must be receiving a high voltage level at its interrupt enable input (IEI). Normally, the interrupt enable output (IEO) of the peripheral has the same level as its interrupt


Fig. 6 Daisy-chain priority control: all PIOs are connected to the INT line. PIO no. 3 is interrupting and passing a low signal to nos. 2 and 1 to prevent them interrupting.
input. The IEI on a peripheral receives its input from the IEO of the peripheral with the next higher priority. In Fig. 6, if none of the PIOs are interrupting, every one of them is receiving a high level at its IEI from the PIO next above it in the chain. Every one of them is able to initiate an interrupt when it wants to do so. When a peripheral is interrupting or is waiting for the MPU to respond to its interrupt request, its IEO becomes low. All peripherals below it (with lower priority) then have the low level fed down to them, and are then unable to generate interrupts.

Another method involves the use of a special priority encoder IC such as the CD4532. It is the hardware equivalent of the device-polling software mentioned above. It has eight inputs, each of which is connected to a peripheral. When any peripheral is causing an interrupt, it also puts a high level on its own encoder input. The encoder also has four outputs which can be connected to the data bus through buffers which are enabled whenever the MPU wants to read the encoder. Their outputs indicate in binary code which peripheral is interrupting. For example, if peripheral no. 6 (connected to input 6 ) is interrupting, the outputs put binary code 6 ( 0110 ) on the data bus. By reading the bus, the MPU can find out which device is interrupting. If more than one peripheral is interrupting at the same time, the binary code for that with the higher priority (highest number) appears at the output.

## Sending A Cable

We have been so preoccupied with logic that we have largely ignored one of the main problems of the input and output of data - the wiring between the computer and the peripheral. If this is to be long, special line-driving buffers must be employed though, if the computer and
equipment are in the same room, this is rarely necessary. Computers work so fast that electrical signals can travel only a few centimetres during one cycle of operation. If wires are long, it may be impossible for the computer and its peripherals to remain perfectly in step with one another. This is one of the reasons for employing I/O ports with asynchronous interchange of data, as described above.

A more practical problem is the sheer number of conductors required. An eight-bit connection (the minimum commonly use) requires eight lines, plus a ground line and probably several control lines as well. There is a wide variety of multi-way connectors available for joining cables to computers and peripherals. Most are designed for use with ribbon cable.

Electromagnetic interference between adjacent conductors is a serious problem, especially with long runs of cable, and can lead to errors in the data being transferred. The data signals themselves are not so likely to interfere with each other, since they are all put on to the lines at the same instant, and there is a short period before they are read (again, all at the same time) during which switch-on and switch-off disturbances can settle. However, if the cable carries control signals, which are generally not turned on and off at the same time as data signals, these may interfere with the data carried in adjacent conductors. One solution is to ground alternate conductors, and use only those between them. A better solution is to use twisted pairs: one wire of a pair is used for the signal and the other wire is grounded. Special ribbon cable is made with twisted pairs with untwisted regions spaced along it, where it may be cut and linked to connectors using insulation-displacement.

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We regret we are as yet unable to accept orders or
enquiries concerning the above products, but we'll let you know as soon as they become available.

# CORTEX Part 2 

# Build yourself a better brain: this month we explain the remaining Cortex circuitry and the construction of the main board. 

Serial I/O on the Cortex is handled by a versatile UART, the 9902. The CPU communicates with the UART via its serial I/O bus, based on the Communication Register Unit or CRU, which requires only three wires; thus the device fits easily into an 18-pin package. The 9902 is fully programmable and the range of variations is so great that it's outside the scope of this article. In the Cortex the chip is configured to handle RS232 eightbit codes with even parity and $1 \frac{1}{2}$ stop bits; the communication rate can be set from BASIC using the BAUD command and the device is activated using the UNIT statement. The parameter for UNIT is a 16 -bit word, each bit corresponding to a channel that can be either on (1) or off (0).

Channel 0 is the keyboard/ screen channel; channel 1 is the 9902 that is already wired into the PCB. Channels 2-15 are implemented in software and only require the addition of extra

| SIZE | DDEN | TRANSFER <br> RATE <br> (kHz) | DIVISION <br> RATIO <br> (IC87) | MONOSTABLE <br> PERIOD <br> (US) | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 125 | 12 | 3.0 | $51^{\prime \prime}$ single density |
| 0 | 0 | 250 | 6 | 1.5 | $5 \frac{1}{4}^{\prime \prime}$ double density |
| 0 | 1 | 250 | 6 | 1.5 | $8^{\prime \prime}$ single density |
| 1 | 1 | 500 | 3 | 0.75 | $8^{\prime \prime}$ double density |

## BUYLINES

Powertran are supplying complete kits of parts and component packs for the Cortex. A complete 64 K Cortex kit will cost $£ 295$ plus VAT, carriage free A ready-built 64 K Cortex will cost $£ 395$ plus VAT, carriage free. Prices for addons (eg floppy discs, RS232C interface, memory expansion etc) and for component packs (eg PCB, semiconductors etc) can be found in Powertran's brochure. Powertran Cybernetics, Portway Industrial Estate, Andover, Hants SP10 2NM. Telephone 026464455.

9902 s on the CRU bus. The Cortex powers up set to UNIT 1. Executing UNIT 2 disables the keyboard and passes control to the 9902. UNIT 3 enables both simultaneously.



Fig. 2 Circuit diagram of the RS232 and cassette interfaces.

## HOW IT WORKS - RS232 AND CASSETTE PORT

The RS232 port consists of IC68, a TMS9902 Asynchronous Communications Controller (ACC) and the TTL-toRS232 signal level shifters (IC74a,b and IC71b,c), IC68 is a completely softwarecontrolled device; its baud rate can be set at anything from 46 baud to over 100,000 baud. The number of bits to be transmitted or received can also be changed, as can the type, the parity and number of stop bits. The CPU drives the ACC through the serial I/O (CRU) bus. The ACC is decoded as a 32-bit block, each bit being selected by the five address lines A10-A14.

The cassette interface uses another ACC, IC67. First a 4.8 kHz op-amp oscillator (IC72c) drives a level shifter (IC71d) before being divided by two in the first flip-flop (IC73a). This ensures
that the waveform has a unity markspace ratio. The serial output from IC67 then controls the action of the second flip-flop, IC73b, via the EXOR gate IC3b. When the output is high, IC73b acts as a shift register, passing through the 2.4 kHz tone; however, when the ACC output goes low then synchronously at the next clock pulse, IC73b starts to divide by two, hence generating 1.2 kHz . The key point here is the synchronous switch from one tone to the other. The signal is high-pass-filtered and attenuated by R46, R47 and C23 betore passing to the tape recorder.

On playback the signal is first amplified by a factor of 100 and buffered in IC72b before going through an all-pass filter, IC72c. This is necessary because of the nature of tape recording.

When square waves are recorded on tape they are accurately captured; however, on playback frequency equalisation is carried out in the tape recorder but the phase relationship is destroyed, resulting in a 'spiky' sine wave. This is corrected by the linear phase-shift-versus-frequency characteristic of the all-pass filter. Thus the original square wave shape is recovered at the output of IC72a. This is then level-shifted by IC71a and used to trigger a monostable (IC70a). At the end ot the monostable period ( 312.5 uS ) the state of the signal is sampled by the $D$ type flip-flip IC69a. As the half-periods of the two tones lie either side of the monostable period, each tone generates the opposite logic level at the sample point.

## HOW IT WORKS - FLOPPY DISC CONTROLLER

The TMS9909 (IC76) is a highly complex micro-controller, designed to work in conjunction with the TMS9911 DMA controller to transfer data from floppy discs. The FDC can control up to four drives which can be a mixture of two sizes or types.

All signals that go to the drives are open-collector buffered by IC80,82,83 and terminated by a resistor pack on the last drive in the chain. The signals from the drives are terminated on the board by a resistor pack and then buffered by IC84.

The raw data pulses from the drive, after being buffered by ICB4a, are stretched by a monostable (IC70b) by an amount dependent on the data transfer rate selected by the 'SIZE' I/O bit and the 'DDEN' (double density enable) signal (see Table 1). The output of the
monostable is used to control IC77, a digital phase-locked loop. The output of IC77 is, in the unlocked state, half the input clock frequency. When the loop is locked to a signal then the PLL inserts or deletes clock pulses in the pulse stream, thus shifting the average frequency. The programmable divider IC87 and divider IC69b are controlled by the 'SIZE' and 'DDEN' signals to select the correct clock frequency. The raw data is synchronised by IC88 to the PLL clock and then fed to the FDC. The FDC separates the interleaved clock and data bits from the pulse stream and sends data bytes via single byte DMA transfers to main memory.

Mini-floppy ( $51_{4}^{\prime \prime}$ ) drives require a motor control signal to start and stop the disc rotating. Upon starting, the disc will not be ready for data transfers for one
second while the disc gets up to speed. To reduce the time required to access the disc repeatedly IC79b keeps the motor running for five seconds after it is de-selected and IC79a provides the initial one second 'not ready' signal to the FDC. For standard ( $8^{\prime \prime}$ ) drives that don't generate a 'ready' signal there is a set of four jumpers.

The BASK interpreter has a 'BOOT' command which causes the FDC to read the first track from disc 1 and execute it as a machine code program. This could, for example, then search for and load the UCSD interpreter. In order that the system can boot from any type of disc there are two jumpers called 'SIZE' and 'DENSITY' which are read by IC63. This enables the BASIC interpreter to set up the FDC correctly.


Fig. 3 Circuit diagram for the floppy disc controller section.


Fig. 4 Component overlay for the Cortex main board. Note that the numerous unmarked capacitors are for supply decoupling and are 47 n ceramics. The grey tracks are those on the top (component) side of the board. Some changes in the IC numbering have occurred since last month due to a board redesign. To make last month's circuits agree with the above overlay, alter the


THE EXHIBITION YOU CANT AFFORD TO MISS


# EXHIBITION GUIDE 

## Introduction

BREADBOARD exhibition has now been on the scene for five years and has proved that there is a place for an exhibition for the serious electronics hobbyist. We normally use the term electronics enthusiast but one must remember that often beginners are as enthusiastic as those of us with many years experience - often more enthusiastic!

Various local exhibitions or club shows occur during the year, all of which offer something of interest to see and often to buy. Breadboard, being a centralised exhibition professionally run, can offer facilities a local club show cannot. As well as having the venue and stands that you'd expect at the premier amateur exhibition, we are fortunate in being able to attract exhibitors more used to professional exhibitions, and who are perhaps unwilling, for whatever reason, to attend the smaller shows.

Breadboard ' 82 not only has the stands you would expect with components, books, magazines, computers, kits etc, but also there will be a series of lectures and demonstrations for those that wish to improve their minds (or rest their feet!).

We will also be introducing a Computer Forum for the newcomers to computing, where some of the more popular home computers will be available for you to try out. Our staff wil be on hand to help you understand those areas that are giving you a late-night nervous breakdowns!

This year we are fortunate in having two particularly interesting exhibitions/demonstrations. One is a computer moderated wargame using computers together with a scale terrain, troops, etc., that enables the visitor to assume command of the overall tactics of a modern battlefield. Should be interesting to see if Ruritania really could be next years number one super-power! Secondly we will be having a fascinating exhibition of holograms. These will be supplied by Light Fantastic and really have to be seen to be believed. For not even an arm or a leg could you buy one for your own home.

For those parts that need special restoration we will have the usual bar and restaurant open for your use beneath the exhibition hall. Don't miss Breadboard '82, you could even save yourself some money on some of the exhibition's special offers!

## Peter Freebrey, Exhibition Manager

## SPECIAL ATTRACTIONS

## COMPUTER MODERATED WAR-GAMES

Dave Rotor sponsored by Amplicon Micro Systems, Brighton; figures supplied by Adventure Worlds, London, SW1
Wargames give you the chance to be your own general! The game that will be played at the exhibition is based on a small-scale encounter somewhere in Europe during World-War II. The players each have a small force at their command - made up of infantry, tanks and/or artillery and have to fight out their encounter on the terrain of the board. Each game turn represents a relatively small interval of time (eg, 3 minutes) and during one move, the commander of each side can tell any or all of his forces to move or fire selected weapons. The men and machines involved in the conflict will be represented by $1: 1 / 200$ scale models specially for the humans, however the computer will have an 'image' of the battle-field stored in memory.

Fed with each players' move, the computer works out the practical consequences, governed by data on the weapons in the possession of each side, the conditions of the terrain, the men, the weather, etc. The performance of the weapons, and even the men, is deduced from known details of real-life battlefield performance.

Suppose you have a squad of ten men and you decide to move them into battle; it's known that armed men can travel at 3 miles an hour in reasonable conditions. Depending on the time that each move represents, the squad will move a proportional scaled distance (worked out by he computer) in the direction you specify. If you order them to fire their weapons (or if your opponent's tank fires at them, for instance), the effectiveness will be gauged by the distance, the known effectiveness of the weapons against the type of target they are firing on, and
all the other factors programmed. The computer will then tell you what degree of damage you have inflicted on each other.

The sort of calculation involved in the evaluation of the tables, etc, used to take human moderators some considerable time; now a fair sized home-computer can do the calculations involved in less than a second. During the exhibition, both war-gamers and computer programmers will be on hand to give detailed explanations of the programming and the theory behind the game.

## HOLOGRAMS

## Light Fantastic Gallery, Covent Garden, London.

Light Fantastic is the first permanent gallery of holography in Britain, and was set up after the success of the 1977 and 1978 Light Fantastic exhibitions at the Royal Academy.

Holography itself has progressed a long way since the first indistinct three-dimensional images were produced in 1947 by Gabor, a scientist working at the Rugby Electrical Company in Scotland. Gabor was subsequently awarded a Nobel Prize for his invention.

The invention of lasers in 1960 made holography much more of a practical proposition. Most of the early laser-produced holograms had to be lit by laser in an area with low ambient light level. Later in the 1960s, the technique was improved to allow holograms to be lit with a standard tungsten halogen light source. The development continued from here, now allowing low cost high-volume production in acceptable commercial quality.

Holographic Exhibitions Ltd (holding company for Light Fantastic) provide a total design to installation service for commercial holography.

Light Fantastic will be showing a selection of some of the most striking items from their permanent collection.

## EXHIBITORS

Here are just a few of the many leading companies who will be exhibiting their latest lines. More and more companies are booking all the time, and electronics is a rapidly changing field, so we won't have full details of all the exhibitors until the last minute - this is just a foretaste of what is to come. A full catalogue will be available at the exhibition.

## ELECTRONICS TODAY INTERNATIONAL

You've read the magazine, you've built the projects, now visit the stand and meet the people who are responsible for it all.

On display will be a large number of our projects, including the brand new 16 -bit home computer, the robot arm, and many, many more, all springing into action before your very eyes! Besides this, you'll be able to put your questions to us, and we'll do our best to help. So come and see us on our stand.

## HOBBY ELECTRONICS

An intelligent robot in a plastic basin is but one of the marvels on show to those of you who come to visit the Hobby Electronics stand at this year's Breadboard Exhibition.

As well as being able to see some of our best projects at close quarters - yes, they really do exist - you will get the chance to meet the people who produce HE. So, if you've been having some problem with getting your prototypes to work, or you'd just like to air your views on the mag, then pop along and we'll do our best to enlighten you. Even if you're the shy retiring type, don't be discouraged, just stroll up and play with something that takes your fancy - there's so much to choose from amongst test gear, audio, RF, gadgets, games and the like, that we'll be surprised if you want to look at any of the other exhibitors. Though, of course, there are plenty of others around, should you be that way inclined!

## COMPUTING TODAY

Computing Today is the leading magazine for the serious home computer user looking for the professional approach. Written by micro users for micro users, inside each issue you will find feature articles, projects, general topics, software listings, news and reviews. You'il also be able to buy copies of the current magazine (as well as back issues where available) and any of our popular range of CT Software. So, if you're a committed micro user, come and meet the editorial staff and we'll show you a truly personal approach to microcomputing.

## PERSONAL COMPUTING TODAY

Since its first issue in August of this year, PCT has become the magazine for the not-so-experienced computer enthusiast. We provide lots of helpful advice on choosing and using a home computer and associated peripherals, a directory of off-the-shelf software, plus lots and lots of programs from the very simple to the stunningly sophisticated. Come and visit our stand, and see how we can help you find your way through the maze of computing.

ETI, $\mathrm{HE}, \mathrm{CT}$ and PCT are all magazines published by ARGUS SPECIALIST PUBLICATIONS LTD. Other magazines include Electronics Digest, ZX Computing and Personal Software.

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## BRADLEY MARSHALL LTD

Bradley Marshall is one of the leading electronic component distributors in the UK, building a reputation for the highest quality items in every area of the micro-electronics business. At Breadboard ' 82 they will be exhibiting a select range of items from their diverse spectrum covering over 3,000 individual product categories.

Whilst it is almost impossible to keep pace with change in the electronic market, Bradley Marshall feel confident that their new 1983 catalogue is as up-to-date as it is possible to be. As well as the complete range of Bradley Marshall components, the catalogue contains a great deal of component data to aid the hobbiest. Bradley Marshall are delighted to be able to make available advance copies of the catalogue exclusively for Breadboard ' 82 at a special exhibition price of 50 p.

Bradley Marshall are the sole London distributors of Crimson Electrik Professional Audio Amplifier Modules. Crimson Electrik Modules are internationally renowned with a reputation based on quality, reliability and value for money as witnessed by the BBC, IBA and KEF to name but three. Bradley Marshall will be displaying the complete range of these extraordinary amplifiers at Breadboard ' 82.

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Velleman will have a large selection of their kits available at Breadboard for inspection and sale, and an engineer will be on hand for most of the time to advise and answer questions. Their illustrated catalogue will be obtainable from the stand and is always available on request from the UK office.

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Watford Electronics was established just over nine years ago. From a very modest start, we have now grown to our present size which makes us one of the leaders in the hobbyist/OEM Electronic components supplier's market. In 1973 our range of components was no more than 500 items; today the range has increased to more than 8000 items and keeps on increasing every week to keep pace with the changing technology.

Our two aims at Watford Electronics are to supply first grade components at very competitive prices and to provide an excellent service to both mail order and shop customers. The former we have been able to achieve by bulk buying direct from the manufacturers wherever possible, thus eliminating the middleman and passing the price advantage over to our customers. The latter we have been able to achieve by sheer hard work and dedication on the part of our staff. $80 \%$ of the mail-order orders received are processed and despatched the same day. The remainder (except where items may be out of stock) are despatched the next day. Access orders received by telephone are processed and despatched the same day.

We stock a comprehensive range of components, including linear, computer, CMOS and TTL ICs, transistors and other discrete semiconductors, nearly every variety of passive component, transducers, hardware and a large variety of connectors at very reasonable prices.

On our stand at Breadboard Exhibition, we shall be displaying some of the thousands of components that we sell. (N.B. We shall not be selling components from our stand due to sheer volume and variety that we would have to transport every day, but we will be accepting orders for postal dispatch. As a special concession, all orders over $£ 5$ accepted at the exhibition will be post free.) We shall be demonstrating our latest 'Ultimum' Micro Expansion System linked to various Micro Computers. Our Managing Director, Mr. N. Jessa will be in attendance. He will be pleased to meet and have a chat with the thousands of our customers who we have no opportunity to meet otherwise.

WATFORD ELECTRONICS, 33/35 Cardiff Rd, Watford, Herts. WD1 gED, England, Tel Watford 40588/9

Booking If your company would like to take a stall at the exhibition, ring Colin Mackenzie on 01-286 9191 soon.

## Lectures and Demonstrations

| Wednesday | 1100 | ETI Music Demonstration |
| :--- | :--- | :--- |
| 10th November | 1200 | Cable TV |
|  | 1300 | ETI Music Demonstration |
|  | 1400 | BICC-Vero: Speedwire |
|  | 1500 | Gateway to Electronics |
|  |  |  |
|  | 1100 | ETI Music Demonstration |
| Thursday | 1200 | Cable TV |
| 11th November | 1300 | BICC-Vero: Wire-wrapping |
|  | 11400 | The Digital Solution |
|  | 1500 | ETI Music Demonstration |
|  |  |  |
|  | 1100 | ETI Music Demonstration |
|  | 1200 | Cable TV |
| Friday | 1300 | The Digital Solution |
| 12th November |  |  |
|  | 1400 | BICC-Vero: Speedwire |
|  | 1500 | ETI Music Demonstration |
|  |  |  |
|  | 1100 | Electronic Music Techniques |
| Saturday | 1200 | The Digital Solution |
| 13th November | 1300 | BICC-Vero: Wire-wrapping |
|  | 1400 | Holography |
|  | 1500 | Electronic Music Techniques |
|  | 1600 | Cable TV |
|  |  |  |
|  | 1100 | ETI Music Demonstration |
|  |  |  |
| Sunday |  |  |
| 14th November | 1200 | BICC-Vero: Speedwire |
|  | 1300 | Cable TV |
|  | 1400 | ETI Music Demonstration |

## all Lectures will take place in the lecture THEATRE, WHICH IS APPROACHED BY THE LIFT OR STAIRS IN THE MAIN FOYER

WHILE EVERY EFFORT HAS BEEN MADE TO ENSURE THE ACCURACY OF THIS PROGRAMME, PLEASE CHECK FOR DETAILS OF ANY CHANGES WHEN YOU ARRIVE

## ETI Music Demonstration

Music projects that have appeared in ETI over the past few years will be put through their paces by a professional musician. This is a good opportunity to decide, with your ears, which synthesiser or fuzz-box to build.

Cable TV - G. Brant, BSc
Cable and satellite TV systems are the newcomers to the broadcasting world of the ' 80 s . A brief description of the existing transmission network will be given, followed by a look at these new media.

## BICC-Vero

BICC-Vero Electronics will be giving audio-visual demonstrations of their new insulation displacement system called Speedwire, ideal for fast positive contacts. On alternate days, there will be lectures on wire-wrapping, an alternative system for solderless connections.

Gateway to Electronics - Dave Bradshaw, MSc
This is a lecture for beginners in electronics, and will offer a mixture of very basic circuit theory and practical advice.

The Digital Solution - Owen Bishop, BSc
In these lectures I propose to cover the whole range of applications of digital electronics, including digital computing, D-A conversion, digital recording, remote control, etc. There will be a selection of working demonstration circuits to illustrate points made in the lectures.

Electronic Music Techniques - Tim Orr, BSc
The lecture demonstration will consist of a technical explanation coupled with a musical demonstration of a polyphonic music synthesiser, a digital delay line and a vocoder: all these have been designed by the lecturer.

## Holography - Andrew Pepper

This will be an introduction to the principles, methods and techniques of practical holography.


Other exhibitors will include:
BICC-Vero
Leighton Electronics
Micro Aids Electronics
British Amateur Electronics Club
Assn of London Computer Clubs
Thames Valley Electronics
Marco Trading
Electronics \& Computing Monthly
SGS Electronics
Expo Drill Company
and many more.


labelling thus: IC6a to IC1c, IC6b to IC1d, IC6e to IC1e, IC12a to IC2b, IC1c to IC6c, IC1d to IC12a, IC1e to IC12b, IC1f to IC12c, IC14a to IC4b, IC2b to IC17b. IC14 and IC75 are not used in the new numbering. R26 is not needed in the PAL circuit, but the modulator needs a 10 k pull-up to +5 V , so we've called this R26. IC60b clock goes to 0 V , IC60b SET goes to SYNC.


## Construction

The main board and the keyboard both have plated-through PCBs, ie there are tracks on both sides and connections between the sides are made by the copper that has been plated onto the sides of each hole. There are therefore no track-link pins; it is, however, good practice to apply solder to EVERY hole to reinforce the connections which in some cases carry power. This happens automatically when boards are 'flow soldered' by passing over a wave of solder in a solder bath during factory assembly. With plated-through boards it is particularly important not to make errors of construction as removal of soldered-in parts is more difficult soldered-in parts is more difficult
than on conventional boards and


$\overline{\text { RESET }}$


Fig. 6 Circuit diagram for the keyboard.

## KEYBOARD

## HOW IT WORKS - E-BUS

The E-BUS is a powerful and compact bus which allows many intelligent cards to share a common resource of memory and I/O cards. In order to share out the resources on the bus, each card has a priority according to its position. This is done by passing a signal down the bus which goes into each card as GRANTIN and comes out as GRANTOUT to form the GRANTIN of the next card. A second signal, BUSY, tells each card if the bus is in use or free. If the bus is free and a card requires the bus, it disables the lower priority cards with the GRANTOUT signal and if the GRANTIN signal and BUSY are OK it asserts BUSY and enables its data and address bus buffers.

Once the bus transfers are complete or if a higher priority card requires the bus, then the card will relinquish control. All these events are synchronised by a backplane clock, BUSCLK. Each data transfer that takes place must signal its completion using READY.

The 74LS2001 gate array (IC89) contains the bus arbitration and control logic to gain and release the bus with timeouts upon error conditions. If the card cannot gain control of the bus after 128 clock cycles, it aborts with a timeout interrupt. Also, if after 16 clock cycles the transfer has not been signalled as complete using the READY line, the controller completes and issues a timeout interrupt.

The E-BUS has provision for a multibit interrupt code signalled by the INTEN signal. This interface only provides a single interrupt level using the INTEN signal. The data, address and interrupt signal are multiplexed onto the same pins to conserve connections. The ALATCH signal is used to enable the address latches when the address is on the bus. Then either $\overline{D E N}$ or $\overline{W E}$ will be signalled, to show that either a data read or write is occurring and that data is now on the bus. The INTEN signal can be used to latch the interrupt code.

The keyboard is a separate unit providing a fully encoded output. Most of the work is carried out by the 2376 keyboard encoder (IC4). This IC contains a 50 kHz oscillator and two ring counters of eight and 11 stages, the outputs of which form an XY matrix across which the switches are connected. By this means each key is sequentially scanned. The closing of one of the switches for a sufficient length of time for switch bounce to be completed causes the scanning to stop; a 'valid' signal now appears on the strobe output. The encoder also contains a 2376-bit ROM (hence the IC name) arranged as three groups of 88 words of nine bits. The shift and control inputs select one of the three groups and the individual word is addressed by the ring counters.

IC3 is a data selector. D2 is either the output $B 6$ or $B 8$ depending on whether upper or lower case characters are selected by the CAPS LOCK switch. Repeated entry of a character is accomplished by multiple strobe signals from IC1, which is a dual monostable arranged as an oscillator and is enabled by a high level on the clear inputs.


Fig. 7 Circuit diagram for the power supply.
the chances of this being required are much reduced by fitting ALL parts before soldering - if the last part left for fitting is not the one required for the last space you can be pretty sure that the required part is in the wrong holes! IC sockets should be regarded as essential; these are provided with the kits and should be fitted with the index mark corresponding with the index mark on the overlay.
The final part appears next month.

The computer main board and keyboard together require a 5 V at 3 A supply, together with low current $\pm 12 \mathrm{~V}$ rails. One amp plastic voltage regulators on small finned heatsinks are used for the 12 V supplies; for the 5 V supply a 1 A regulator is also used but the currentcarrying capacity is boosted by bypassing it with a 15 A power transistor, the base current of which passes through the regulator. R1 prevents the off-load input current of the regulator from turning on the transistor when there is no load during testing. The resistor also increases the
speed of operation of the transistor. The luf capacitors are for the stability of the regulator and the 100 nF capacitors are used to remove fast transients orginating from the mains. The zener will clamp any spikes that reach the output.

To simplify the addition of floppy discs these are powered from the same board. The drivers require about 0A7 at 5 V which is also supplied by Q1; they also require +12 V at 1 A 6 with higher surges at switch-on, and this is provided by a separate section using Q2 controlled by IC4.

PARTS LIST - MAIN BOARD

| Resistors (all stated) | $1 / 4 \mathrm{~W}, 5 \%$ except where | $4-6,9,10,$ | 100n ceramic | $\begin{aligned} & \text { IC23 } \\ & \text { iC25,65,78 } \end{aligned}$ | 74LS20 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| R1,2 | 470R | C7 | 470 n ceramic | 91 | 74LS32 |
| R3-5,11,32 | 4 k 7 | C8 | 33u 16 V PCB electrolytic | IC26 | 74LS612 |
| R6-8,20,21, |  | C11,12,16 | 33p ceramic | IC28,29 | 74LS27 |
| 28,37,41,45 | 330R | C14 | 47p ceramic | IC32 | TMS4500 |
| R9,12,13,15, |  | C15,18,27 | 22u 16 V PCB electrolytic | IC33,85 | $74 \mathrm{LS139}$ |
| 39,40,46,52, |  | C19 | 100p ceramic | IC36-43 | TMS4164 |
| 55,61,69 | 10k | C20 | 22 n ceramic | IC44,97 | 74 LS 245 |
| R10,14,47, |  | C21,23 | 10n ceramic | IC45-47 | TMS2564 |
| 58,63 | 100R | C22 | 330n ceramic | $1 \mathrm{C48}$ | TMS9929 |
| R16-19 | 560R | C24 | 5 n 6 ceramic | IC49-56 | TMS4116 |
| R22 | 120k | C28 | 100u 16 V PCB | IC57,58 | 4016B |
| R23,24,31, |  |  | electrolytic | IC59 | LM1889 |
|  | $1 \mathrm{k0}$ | C29 | 330 p ceramic | IC60 | 4013 |
| R25,29,33 | 2 k 7 | CV1 | 6-30p trimmer | IC62,63 | 74LS251 |
| R27 | 390R |  |  | IC64 | 74LS259 |
| R30 | 1 k 5 | Semiconductors |  | IC67,68 | TMS9902 |
| R34 | 1 k 8 |  |  | IC70 | 74LS123 |
| R35,60 | $2 \mathrm{2k} 2$ | ${ }_{81} 1$ C1,6,12,27 | 74LS04 | IC71 | 75189 A |
| R38,53,54 | 100k |  |  | IC72 | TL084 |
| R42 | $6 \mathrm{k8}$ | IC2,17,18, | 74LS74 | IC73 | $74 \mathrm{LS73}$ |
| R43 | 3 lk 39 k |  | 74LS86 | IC74 | 75188 TMS9909 |
| R48-50 | 8 k 2 | IC4,21,31,93 74LS00 |  | IC86 | $74 \mathrm{LS297}$ |
| R51 | $1 \mathrm{M0}$ | ${ }_{\text {IC7,24 }}$ | 74LS02 | IC87 | 74LS163 |
| R56,59,68 | 4 k 7 resistor array | IC8 ${ }^{\text {c }}$ | 74LS9911 | IC89 | 74LS2001 |
| R57 | 22 k | $1 \mathrm{C} \mathrm{C}_{108} 84$ |  | Q1,3,4 | 2N3904 |
| R62 ${ }_{\text {R64-67,71 }}$ | ${ }_{150 \mathrm{R}}^{27 \mathrm{k}}$ resistor array | 94-96,98,99 | 74LS244 |  | ${ }_{\text {BC212 }}$ |
| R70 | 18k | IC1 1 <br> IC13,77,90 <br> IC15,34,35 | TMS9995 | D1-4 | 1N4148 |
|  |  |  | 74LS08 | LED 1-4 | LEDs to choice |
|  |  |  | 74LS138 | Miscellaneo | S |
| C1 | 1 n 0 ceramic | ${ }_{82,83}$ | 74LS07 | PCB (see Bu | ylines); case (see Buylines); |
| ${ }_{C}$ | 4 u 716 V PCB electrolytic | -1819 | 74LS164 | IC sockets; | ( connectors to suit; UHF M1233 or UM1286). |
| C3,25,26 | 10u 16 V PCB electrolytic | IC20,79 | L.M339 |  |  |

## 



## THEN <br> DISK DRTVES <br> Diablo/DRE 8eriee 302.5 mb. fully refurbished DEC RKO5 media and sot tware compatabia. Front load Es50. Top loadsies <br> PSU for 2 drives Cl 125 <br> Diablo-Dre 44A-4000A or $4000 \mathrm{~B} 10 \mathrm{mb} 5+5$ removable pack new and refurbished from EPOS. <br> CDC 80 mb removable pack DEC RM03 media and sottware compatlbie brand new from $\mathbf{E 2}, 930$. <br> Honeywell $5+510 \mathrm{mb}$ drives $\mathrm{E} 450 \mathrm{good} \mathrm{s} / \mathrm{h}$ condition. For more information on controliars, expansions and ready to <br> DTSIMT (C) <br> The UK's FIRST free of charge, 24 hr . public access data base. Get information on $1000^{\circ} 8$ of stock items and order via your computer and credit card On line now, 300 baud. CCITT tones, full duplex, fully interactive. DON'T MIS THOSE EARCANS CNLL NOW, IT'S FREE 7 daye per $01-6831133$ -

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# SPECTRACOLUMN 

## With this project we throw some light on the problem of how to jazz up your disco or party. This cost-effective, crammed-witheverything light column can be used singly or in groups to dazzle the dancefloor. Design by Rory Holmes.

The ETI Spectracolumn is an up-market sound-to-light system; by this we mean its lighting effect is a cut above the average 'three bulb' systems, although its cost is not. Ten mains bulbs, arranged in a column, respond to the intensity of music (or any sound signal) within a preselected frequency range. It works like a giant bargraph voltmeter; the more energy in the chosen frequency band the more bulbs will illuminate, forming a column of light that rises up from the floor and follows the rhythm of the music. The display system is very versatile; it can be built with any type of bulb in any configuration, and may be expanded for large parties or discos. Multiple columns can be set to adjacent frequency bands to build into a giant spectrum analyser and display system. Imagine - a kilowatt light column devoted to each octave across the whole audio spectrum!

In designing the band-pass filter system we have made use of the latest switched capacitor filter IC the MF10. This device contains two second order filters whose cut-off frequencies are directly controlled by a square-wave clock input. Clock frequency control removes the constraint of having to use high tolerance filter network components
and the associated difficulty of altering the filter frequency. The clock, and thus the filter frequency, can be set from a logic divider chain to provide any frequency in octave increments. We have configured the MF10 as a low-pass filter in cascade with a high-pass filter to allow complete control of the filter band. The upper and lower frequency limits may be set independently under logic control using rotary switches. There is no setting up or filter tuning required and the entire range of octaves is implemented with very few components.

## On The Circuit

With the price of modern triacs and some economical design work from ETI, what seems to be a complex system in fact turns out to have only about $£ 18$-worth of parts (less the PCB and lightbulbs). Since the triacs don't need heatsinking, we adopted the 'let's get it all on one board' philosophy, and did exactly that. Even the small crystal mike that picks up the audio signal is mounted on the PCB to provide complete isolation between the sound equipment and the mains. Mounting a single board directly with all the bulbs in the column housing also removes the inconvenient cables that often make

Fig. 1 Block diagram of the Spectracolumn.


Ten white light-bulbs, hanging on a wall ...



Fig. 2 Circuit diagram for the complete Spectracolumn.
TABLE 1


 PULSES ON
Q1 BASE


Fig. 3 Triac zero-crossing switching waveforms.
the dancefloor a dangerous place to negotiate. Finally, the design features zero-crossing triac control, so your sound equipment won't be plagued with RFI.

Using the system could $n$ 't be easier; just plug it into the mains and switch on! No other connections are needed, because the internal mike picks up the music signal. The sensitivity control is turned up as required for the sound level, and a 'background' control is available which moves the illumination 'baseline' up or down the column, so increasing or decreasing the amount of light. With no sound it acts as a giant dimmer control.

The display could be hung on the wall, as we did for our photograph, or stood vertically on the floor. Large sheets of 'cinemoid' acetate (available from most good art shops) may be wrapped around the entire column to provide a coloured tube, which also tones down the display. But keep the plastic well away from the light bulbs!

The alternative is to use coloured bulbs. A three column system, using red, green, and blue for the bass, middle, and treble ranges would be an ideal starting system for most disco light shows. The filters could, for example, be

set at 20 Hz to $312 \mathrm{~Hz}, 312 \mathrm{~Hz}$ to 2.5 kHz , and 2.5 kHz to 20 kHz . As more Spectracolumns are added into the system the filter ranges can be instantly amended according to taste; but watch out for the current rating of your mains sockets!

## Construction

All the components except the controls are mounted on our PCB. The triacs, the transformer, and even the microphone are mounted on board, as the overlay diagram of Fig. 4 illustrates. Assembly should begin first with the links, then resistors, followed by ICs and so on. IC sockets should be used as a good precaution, but note that IC5 is an 18 -pin device and IC2 is a 20 -pin DIL! Follow the overlay diagram for the orientation of all the components and solder in everything except the PCB-mounting transformer, the triacs, and the crystal mike.

The metal heatsink tab of the triacs has been used to form a screw terminal for the lamp connections (it's connected internally to the central leadout wire MT2). Hence the middle terminal lead of all the triacs must be completely cut off, which immensely simplifies board design too. The remaining two leads are inserted into the board and a 6BA

The block diagram of Fig. 1 illustrates the different parts of the system. Sound from a microphone is amplified and fed through both low-pass and high-pass filters (digitally controlled); the resulting audio signal is then rectified to produce a voltage envelope proportional to the sound intensity within the pre-defined frequency band. This envelope is displayed using a bargraph voltmeter IC to drive triac-switched mains bulbs which light up in a column according to the instantaneous sound level. A simple power supply provides both the 10 V DC rail and the 100 Hz signal for zerocrossing triac control.

Figure 2 shows the complete circuit diagram for the Spectracolumn. The audio signal provided by the music or other sound is picked up by the microphone insert MIC1 and amplified by IC1a, which is configured as a straightforward non-inverting amplifier with a gain of 100 . The high input impedance required by the crystal mike is set by R1 to be about 2 M 0 .

The audio input from this gain stage is taken via the sensitivity control RV1 (acting as a potential divider) to the input of the filter system at R4. The audio filter system is built out of an MF10 monolithic switched capacitor filter. This IC (featured in last month's Designer's Notebook) contains two identical second order ( 12 dB per octave) filter systems which can be configured in a number of different modes, with the filter corner frequency being determined by a single square wave clock input.

We have used the MF10 to construct both a low-pass and a high-pass filter, which are wired in cascade. The resistor values shown have been chosen to give a pass band gain of 3 and a $Q$ of 1 . The cut-off frequencies are set to be $1 / 50$ th of the applied clock signals, which can be independently varied for each filter. Using high and low-pass filters in cascade results in a band-pass type of response, where the bandwidth can be very effectively controlled using the two input clocks, and positioned in any part of the spectrum. The clock on pin 10 of the MF10 controls the low-pass filter determining the upper frequency limit, and the clock input on pin 11 determines the high-pass filter's corner frequency, thus setting the lowest frequency that will be passed.

The clock signals are generated and selected using a separate block of CMOS logic circuitry. IC4c and $d$ are configured as a standard CMOS astable to provide the master clock of 2 MHz . This clock is fed directly to the counter divider chip IC3 (a 4040). The Q outputs progressively divide the clock frequency by two to give those frequencies shown in Table 1. As music lovers will know, dividing the frequency thus will give us equal octave increments; the entire audio bandwidth is thus catered for using the 11 outputs of the 4040. The two remaining gates of IC4 take their inputs from the common pole of each 10 -way rotary switch, SW1 and SW2, buffering the outputs from the divider chip and
providing selectable clock frequencies to program the high and low-pass filters.

The band-pass filtered audio signal is coupled via C2 to a precision half-wave rectifier, built around IC1b. A positivegoing audio envelope thus appears across C3. R16 determines the attack time constant and R15 the decay timeconstant. Potential divider RV2 supplies an offset voltage derived from the 1V4 reference to the non-inverting input terminal of the op-amp IC1b. This allows a 'background' voltage level to be superimposed on the envelope voltage, giving an independent control of the light column's illumination. The 1V4 reference is created by the forward voltage drop across D1 and D2 which are biased by resistor R14; this reference is also used to feed the internal resistor chain of the LM3915 at pin 6 of IC5. The LM3915 converts the envelope voltage applied at the pin 5 signal input to an array of 10 switched outputs. Pin 4 is the earth reference for the signal and resistor chain voltages; it is tied to the 5 V 'pseudo earth' rail. This half supplyvolts rail is derived from the lowimpedance potential divider R13, 20.

Direct drive from IC5 to the triacs is achieved by tying the mains neutral to the positive rail on IC5 and the common MT1 terminals of all the triacs. The switched outputs of IC5, which provide constant current, are taken directly to the gates of the triacs and the bulbs are placed in series with the triacs in the returning mains live lead. Now, resistor R21 is normally used for setting the output drive current of the LM3915, going from the pin 7 reference to ground.

In our arrangement, however, it is switched to ground using Q1. Thus when Q1 is off, the constant current sources that drive the gates of triacs SCR1 to 10 will all be disabled, and the triacs cannot turn on. Q1 is driven by brief pulses derived from the zerocrossings of the mains cycle; in other words when the AC mains cycle reaches $0 \vee$ (which occurs 100 times per second), transistor Q1 turns on and allows the triacs to be triggered on only at this moment. The triacs automatically turn off again as the mains current falls away to zero, assuming there is no further drive signal. For the triac to turn on, then, the corresponding output from IC5 must be 'active' due to the sound level, and at the same time as a zero-crossing pulse occurs. By turning on the triacs and thus the lamp current flow only when the mains voltage is close to zero, the problems of radio frequency interference are effectively avoided.

The circuitry is powered from a 10 V supply rail, regulated by the 10 V zener diode ZD1, and decoupled by C5. The centre-tapped 9-0-9 $V$ transformer is full-wave rectified by D6 and D7; Q2 is driven by the 100 Hz signal at the junction of D6 and 7 to detect the zero crossing points. As the voltage cycle falls down to zero the voltage on the base of Q2 also falls. When it goes below 0V6 Q2 will turn off (the zero crossing point), thus allowing Q1 to turn on. D5 and R17 isolate the full wave rectified DC from the 10 V power rail.
nut and bolt are used to clamp the metal tab to the PCB. The bolt protrudes above the component side and a further washer and nut can be added to create a screw terminal. When all the triacs are bolted in place their leadout wires should be soldered and cropped as normal. The lamp wires will be retained on the screw terminals using solder tags.

The PCB-mounting transformer has been used simply for convenience and should be soldered in as a normal component. Other types could also be used provided they are connected to the PCB pads as per the circuit diagram. Bolts should also be fitted, in the same manner as the triacs, to make screw terminals on the pads marked for the mains connections. The photographs of our completed PCB show these terminal connections.

Our crystal microphone insert was 23 mm in diameter; it should be mounted last. The metal screening case of the insert is connected internally to one of its terminals. This screen terminal should be identified (use an ohmmeter) and wired to the mid-rail reference as shown on the overlay; ensure that the wire used is very thin and flexible. A piece of sponge foam about the size of the mike should be stuck to the PCB and the mike may then be glued on top of this to provide a resilient mounting, free from direct vibration pickup.

An electret condensor type of mike insert could also be used and would probably give better quality sound pickup. They usually come with their own internal FET preamplifier, which requires a 1V5 power supply. Luckily, the 1V4 reference terminal indicated on the overlay is ideal for this job, and may be wired directly to the insert.

When the board is completely assembled the two control pots and the mode switch can then be wired up as indicated. Veropins should be inserted as terminals at the appropriate points. The two rotary switches for the frequency selection should also be wired up using ribbon cable as shown in the diagram. Note that the rotary

## BUYLINES

All of the electronic components, including the hard-to-find MF10, are available from Rapid Electronics, Hill Farm Industrial Estate, Boxted, Colchester, Essex CO4 5RD. The fluorescent fitting, bulbs and holders will be available from any electrical store, while the order form for the PCB Service can be found on page 99.


With the front panel removed, you can see the single PCB we employed. All the pots and switches are mounted on the sides.
switches are both set to select one out of 10 corner frequency outputs from the PCB and the rotary switches are offset by one frequency band relative to each other: ie the upper limit switch ranges from 40 Hz to 20 kHz while the lower limit ranges from 20 Hz to 10 kHz .

## Testing And Setting Up

After wiring up the controls some initial tests can be made before completing the assembly. Initially, do not connect any light bulbs and do not plug in any ICs; but do remember that all parts of the circuit are effectively live. Connect the mains as shown via a double pole toggle switch and a 5 amp fuse, and then switch on. Using a voltmeter check that there is about 10 V across C 5 and 5 V across C 7.10 V should also appear across pins 8 and 4 of IC1, pins 8 and 13 of IC2, pins 16 and 8 of IC3, pins 14 and 7 of IC4 and pins 3 and 2 of IC5. If all is well, unplug from the mains and insert all the ICs. One light bulb can now be wired onto the SCR5 terminal, its other lead returning to mains live. Set the upper limit switch to 5 kHz , and the lower limit to 640 Hz ; this gives a fairly broad frequency band for vocal testing. The unit should be turned on again with SW3 set in bar mode. Altering the background control RV2 should cause the bulb to switch on and off at some point. As the bulb switches off continue to turn RV2 in the same direction to the end of its travel. The background illumination control is then at its zero setting. Now, depending on the sensitivity setting, a loud noise should re-illuminate the bulb. Increasing the sensitivity control should eventually allow the bulb to come on with normal speech volume. If this test works

PARTS LIST

satisfactorily then all the bulbs can be wired up to their corresponding terminal posts and the entire display can be tested.

Turning the background control up should result in the successive illumination of bulbs; now turn it down to zero, when all the bulbs should be off. Increasing the sensitivity control will now allow sound to illuminate all the bulbs. Having established a good sensitivity setting, different types of music from a record deck or radio can be used to check the different frequency bands available on the rotary switches. The display can be
switched to dot mode at any time, which provides an interesting effect with constant light level.

## A Case In Point

The actual hardware construction of the light bulb arrangement is very much a matter of personal choice. We used large white plastic bulb holders, and mounted the entire column and PCB in a fluorescent light case that was to hand. The case was earthed and provided a nice self-contained unit. Batten-mounting bulb holders could equally well be screwed down to a long strip of wood and


Fig. 4 This diagram shows how to wire up SW1 and SW2.
the electronics mounted in a separate diecast box. The photographs illustrate the construction method we used.

A number of important points should be noted with the final assembly. Owing to the circuitry used, the positive rail is directly connected to the mains neutral; therefore all parts of the circuit should be treated as being effectively live since somebody could easily swap the mains and neutral leads by accident at the mains plug end. Consequently we suggest:

- The PCB should be mounted in a metal case on insulating pillars or blocks.
- The case should be earthed to the mains but there should be no other connection between the PCB and the case. Circuit ground must not connect to mains earth.
- The mode switch and on-off switch should both be 250 V mains rated and have a current rating sufficient for the total power of the bulbs used.
- The pots and rotary switches should all have plastic spindles and plastic knobs. Ideally the metal pot cases should be insulated from the chassis, or they could be soldered directly to the PCB terminals such that only the plastic spindles pass through the chassis.
- For the reasons of mains isolation the microphone must stay inside the

.Fig. 5 Component overlay for the Spectracolumn. Use spade connectors for the triac and mains connections.
case; and on no account try to connect up the mike input to a direct audio signal from your sound equipment (this could be done only with an audio isolating transformer).


## Notes On Modifications

For those with the urge to experiment here are some notes on modifying circuit values: R3 decreases the mike preamplifier gain; decreasing R4 and R8 increases the filter gain; increasing R6 and R10 will increase the Q of the filters; R18 alters the frequency of the master clock, currently set at 2 MHz ; R21 determines the drive current to the triacs; increase C3 or R16 to increase the attack/decay display time constant; R16 could be a 22 k variable pot.

This close-up of the business end of the Spectracolumn shows how we cut away part of the front panel of the fluorescent fitting to allow sounds to reach the crystal mike insert. A cover can be built using speaker cloth and a stiff card frame, as shown in the photograph on the first page of this article.


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# CONFIGURATIONS 

## Transistors as amplifiers, transistors as multivibrators - now we consider transistors as sawtooth generators. If you want to know the timebase, ask Ian Sinclair.

The timebase is a circuit which generates a sawtooth waveform, one whose voltage changes linearly with time: a graph of voltage plotted against time will be as shown in Fig. 1 (though it may be either positive-going or negative-going). The bestknown application is in oscillosope timebases, but the circuit can also find use in digital-analogue converters and in timing circuits.

The most simple timing circuit is, of course, a capacitor charging through a resistor (Fig. 2). The time constant CR determines the total charging time which, though theoretically infinite, is in practice about four or five times the length of the time constant. The graph shape of voltage plotted against time is, however, exponential rather than linear because the charging current drops as the capacitor charges. All timebases of the capacitorcharging type therefore need some method of keeping the charging current constant as the voltage across the capacitor rises.

## Transistor Control

In the days of valves, many elaborate circuits were devised to overcome the problem of constant current control, but it took the development of the transistor to come up with a really simple system with good perform-


Fig. 1 The waveform of a perfect timebase - this should be a straight line.


Fig. 2 Capacitor charging. When a capacitor is charged through a resistor the waveform is an exponential rather than a straight line.


Fig. 3 Using a transistor in place of a resistor for capacitor charging. Since the current through the transistor remains constant, the sweep waveform is straight rather than exponential.


Fig. 4 Block diagram of an oscilloscope timebase.
ance. A transistor whose base-emitter junction passes a constant current will also pass a (larger) constant current between its collector and its emitter, and this current can be maintained up to the level where the collector voltage is less than half a volt different from the emitter voltage.

Figure 3 shows a simple timebase circuit using this principle. Q1 is a switching transistor which is normally conducting, keeping the voltage across the capacitor low. Q2 is a PNP transistor whose base current is set by the resistor chain R2, R3, RV1, and which can be varied by altering the value of RV1. Since the base current is constant, the collector current will also be constant. Q3 is simply an emitter-follower to avoid non-linear effects which would be caused by a resistive load connected across the charging capacitor (since a resistance takes more current as the voltage across it is increased). For best results, Q 3 should be a transistor with a high $\mathrm{h}_{\mathrm{fe}}$ value, and a double emitter-follower is often preferable to ensure the highest possible input resistance.

The action is as follows. When Q1 is cut off by a negative pulse at its base, capacitor C 1 can be charged by current flowing through Q2. This current will not change until the collector voltage of Q2 has reached a value close to the positive supply voltage, so that the wave form is linear up to this region. If Q 1 remains cut off, the waveform will then flatten off, but if Q1 is switched on again before this point is reached, then a good sawtooth shape is preserved.

## Timing The Timebase

The action depends to a large extent on switching the transistor Q1 at the correct times, and all timebases consist
basically of two sections - a square wave generator which handles the switching and a sawtooth generator which provides the desired waveform. An oscilloscope timebase would use a level-detecting circuit at the output to ensure that the switching transistor Q1 was switched off before the voltage level at the output reached the non-linear region - a block diagram with waveforms is shown in Fig. 4. In this arrangement, the repetition rate of the timebase is determined by an astable which provides a trigger pulse. The trigger pulse sets the bistable, which in turn cuts off the switching transistor of the timebase generator and so starts the charging of the capacitor. When the charging has reached some preset voltage level, the level detector (comparator) circuit switches the bistable back, so discharging the capacitor ready for another sweep. For many oscilloscope purposes, the astable is set to run freely at a low speed, and is synchronised to whatever waveform is to be displayed - this is the auto timebase system found on most modern oscilloscopes. The sweep speed is then determined by the time constant of the charging capacitor.

The use of a transistor as a constant current device for a timebase is good enough for many purposes, but two other methods of creating linear sweep waveforms from the basic capacitor charging circuit have been well established for many decades in oscilloscope circuitry. One of these is the bootstrap circuit. Bootstrapping is positive feedback applied over a circuit in which the gain is less than unity, so that it does not cause oscillation.

## By His Bootstraps

The principle of the bootstrap is shown in Fig. 5. A capacitor is charged through two series resistors, and a unity-gain amplifier is connected so that the voltage across the capacitor can be applied, in phase but with its DC level shifted, to the point where the resistors join. When the capacitor starts to charge, the increase of voltage across the capacitor causes a matching increase of voltage across $R 2$, so that the voltage across $R 2$ has not changed in this time. Since the voltage across R2 is constant, the current through R2 is also constant, which is the condition for a linear sweep.


Fig. 5 The principle of the bootstrap timebase circuit.


Fig. 6 A practical form of the bootstrap circuit, using an emitter-follower as the unity-gain amplifier.

The bootstrap depends on being able to keep the voltage at the junction of the resistors at a constant amount greater than the voltage across the capacitor. The whole idea seemed so absurd when it was first proposed that the (US) inventor remarked that it seemed "rather like lifting yourself by your own bootstraps". As so often happens, the name stuck.

A practical form of the timebase is shown in Fig. 6. Q1 is, as before, the switching transistor which starts and stops the sweep. The charging resistor chain consists of R2, R3 and RV1, of which R3 is a limiting resistor whose value is set so that excessive current does not flow through Q1 when the variable is set at its minimum value. D1 is used to prevent C 1 from discharging below about 0 V 7 , so ensuring that Q2 will not switch off, causing non-linearity. If Q2 is allowed to switch off, then the timebase output will have a decided 'kink' at the voltage at which Q2 switches on.

Q2 is an emitter-follower, whose emitter is connected through a zener diode ZD1 to the junction of R2 and R3. The zener diode, along with the base-emitter voltage drop of Q2 determines the voltage across R3 and RV1, so that the charging rate can be calculated. For example, suppose the voltage is 6 V , the values of RV1 and R3 add to 56 k and C 1 is 22 nF . The charging current 1 is $6 / 56 \mathrm{~mA}$, which is 0.107 mA , and the rate of change of voltage across C 1 is I/C1. Using units of milliamps and nanofarads, the rate of rise of voltage will be in volts/microsecond, and the example gives 0.00486 , equivalent to 4.86 volts per millisecond. If you know the sensitivity figure for the cathode ray tube for which the timebase is to be used (in terms of centimetres of deflection per volt), then you can calculate what amount of amplification will be needed to obtain full screen coverage, and what time constants will be needed for the various scan speeds.

There are limitations on the voltage gain of the emitter follower and the frequency range over which the zener diode remains effective, but with suitable choice of components, good timebase circuits can be designed around this core configuration. Commercial circuits of this type often look remarkably complicated, but once the bootstrap section is separated from the other parts of the complete timebase (the triggering and the comparator sections), the essential simplicity of the circuit can be seen.

## The Miller Alternative

The other basic capacitor charging circuit is the Miller integrator. These two circuits, the bootstrap and the Miller, were curiously polarised for many years, with the bootstrap used on US equipment and the Miller on UK equipment almost exclusively. This is no longer completely true, but though you will see bootstrap timebases appearing on equipment designed in this country, you will even now seldom see a Miller timebase used on the other side of the pond.

The Miller timebase is named after (yes, got it!) Miller, who discovered the result of negative feedback across the anode-grid capacitance of triode valves. The name became attached to the timebase (which was not designed by Miller) because the Miller timebase makes deliberate use of such feedback to achieve linearity. The basic circuit is shown in Fig. 7, and the most startling thing about it is its simplicity, because the switching transistor is also the current regulator! If we imagine the transistor starting cutoff, then a square wave applied to the input will raise the base voltage until the transistor starts to conduct. When conduction starts, however, the collector voltage will drop, and the negative feedback through C1 will prevent the base voltage from rising to the level of the input voltage. Once this has happened, the base voltage can rise

## FEATURE: Configurations



Fig. 7 The basic Miller timebase circuit.
only as fast as the capacitor C1 can be discharged, and the discharge is at a steady rate because of the negative feedback.

The time constant for the Miller integrator is given by the value of R1 and C1 rather than R2 and C1 as you might expect, and the conventional use of the circuit as shown here produces a timebase waveform which is negativegoing, with a small 'step', as shown in Fig. 8, just at the point where the transistor switches on.

The circuit will operate in the opposite direction, when the 'free' end of R1 is at ground potential. In this case, the voltage at the transistor's collector rises just quickly enough to keep sufficient current flowing into its base (and also R1) to keep it on. In both cases, the simplest way to achieve the fly-back is to connect a diode, D1, in parallel with R1. For operation in the opposite direction from that first described, the direction of the diode must be reversed.

More elaborate versions of the Miller use two stages of amplification with the output in phase, and a lowimpedance stage driving the capacitor. Very good results can be obtained, and with a wide-band op-amp used in place of a transistor, excellent timebase linearity is possible.


Fig. 8 The waveform from the simple Miller circuit.
Before we leave the subject, timebases can also make use of the growth of current through an inductor. The effect that is used here is the inductive equivalent of capacitor charging, and it is useful because if the inductor is also a deflection coil for a cathode-ray tube, then the timebase and deflection system can be combined. Linearity is much less easy to achieve, however, and one method is the use of a saturable reactor in series with the inductor which carries out the timebase action. The inductance of a saturable reactor will vary with the amount of voltage across it in order to keep the current constant. Using this and other components, it is possible to balance out the worst of the non-linearity of the charging process. For truly linear timebases, however, the capacitor charging circuits which we have described in this article are considerably superior to inductive timebases. No-one watching TV seems to care too much if the characters are very slightly fatter on the right hand side of the screen than on the left, but we need to know the truth from our oscilloscopes!

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# SERVO INTERFACE 

## PART 2



Are you being servoed? This month we get to grips with the construction of our arm interface board, which can also be used to control up to four servos for any other application you can think of. Design by Rory Holmes.

The servo interface is built on a single-sided PCB. An additional double-sided PCB is used to make a lead-through type of edge connector plug, similar to that used on the $Z X$ printer. The interface electronics are too bulky to be mounted directly on the Sinclair edge-connector, but our small Verobox-enclosed plug, wired to the main board via ribbon cable, puts less strain on the expansion connector.

Start construction with the main PCB, soldering in the links and resistors first (there should be eight links altogether), followed by the IC sockets and other components. Insert Veropin terminals at all the computer bus connections, since this makes wiring up to the ribbon cable easier. Veropins, or a five-way Molex connector socket should also be used at the servo output terminals as illustrated on the PCB overlay diagram of Fig. 1.

## Adjust Your Address

The three DIL switches can be replaced by appropriate wire links if the address combination that you wish to use is going to be a permanent fixture. The address selection details given below should be studied to appreciate the possible configurations of the address decoder. Observe that the two switches corresponding to the Z80 control lines (those nearest C4) should always be set to logic low, ie
closed. Also note that IC3 is positioned the other way round to the other ICs.

We used the Pactec type HP case to house the main PCB which was mounted by four bolts at the corners. Four ordinary grommets were used as spacers over the bolts to allow room under the PCB for the ribbon cables. These ribbon cables are wired up to the two PCBs as shown in the main overlay and edge-connector wiring layout -- an 11 -way ribbon is used for the topside and a 14-way for the bottom side.

## Pot Luck

The edge connector PCB is cut to exactly fit into the smallest Vero potting box. By a lucky coincidence the 23 -way Sinclair expansion bus will exactly fit the inside of this box. The solder tags on the edge socket are spaced wider apart than the PCB thickness and must be adjusted slightly - don't forget the keyway orientation shown in the wiring layout. One row of tags should first be soldered as they are to the 'underside' PCB terminals and then the other row can be bent down to reach the topside terminals, allowing the assembly to fit in the Verobox.

Figure 3 shows how slots should be cut in the box to house the edge connector plug and socket. Two large size stick-on rubber feet should be positioned on the inside
of the lid to hold the board firmly in place as the lid is screwed down. If one of the feet is stuck above the ribbon cable entry point, it will act as a cable clamp. A very neat and solid connector system will result from this construction method.

## Address Selection

If the Spectrum computer is to be used then IC7 and IC8 should not be plugged in the address lines that would normally reach these ICs from the ZX81 do not go to the same pins on the Spectrum bus), but IC9 must be used. Under these circumstances the switches SW1 and SW2 and the associated pull-up resistors are not actually required on the board though they can be left in place (open circuit) if future ZX81 use is anticipated. Jumper JA must be fitted while JB and JC are left open. The switches on SW3 should all be set to logic low, ie closed, and then the four servo addresses will be

Servo 1 OUT 65340, X
Servo 2 OUT 65341, X
Servo 3 OUT 65342, $x$
Servo 4 OUT 65343, X
For use with the ZX81, IC8 and 9 must be fitted while IC7 is optional depending on the degree of address decoding required. Jumper links JB and JC should be fitted but not link JA. The memory map given in part 1 showed the address line logic levels needed to decode different address ranges.

## PARTS LIST

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Fig. 1 (Right) Overlay for the servo arm interface board.


Fig. 2 (Below) Overlay for the edge connector PCB; this will allow the use of other perípherals, such as the ZX Printer in our lead photograph. Ribbon cable with 14 ways and 11 ways is used, although one of the wires from the 11 -way piece is soldered to the other side of the PCB as shown.



Fig. 3 Construction of the edge connector box.


A completed interface board; the ribbon cable colds under the PCB.

These logic levels are set on the switches to decode the required addresses for the servo locations.

As an example, the switches on the PCB could be set to the following logic levels (address bits 2 , 3,4 , and 5 are uncommitted so the decoder will respond to a range of addresses):
switch $\begin{array}{llllllllll}7 & 7 & 8 & 9 & 10 & 11 & 12 & 13 & 14 & 15\end{array}$ logic
level $1 \begin{array}{lllllllll}1 & 1 & 1 & 1 & 1 & 1 & 1 & 0 & 0\end{array}$ Thus servo 1 will respond to an address in binary of

$$
0011111111111100
$$

or $3 \quad F \quad F \quad C$
in hexadecimal. In decimal this gives servo addresses of:

Servo 1 POKE 16380, X
Servo 2 POKE 16381, X
Servo 3 POKE 16382, X
Servo 4 POKE 16383, X
The servo 4 location is the highest byte of the second 8 K block.

## Testing

Once all the cables are wired up the interface can be tested by plugging in to the Sinclair expansion port, either on a Spectrum or a ZX81. Ensure that the jumper links and IC/address switch combinations are set up for the type you are using, and start with no ICs plugged in. If the computer resets correctly and still seems to work, then the first hurdle is over. Check that the 5 V power rail appears at all the IC sockets and then disconnect the interface to plug in all the ICs. With both presets at mid-travel turn PR1 $45^{\circ}$ anti-clockwise and PR2 $45^{\circ}$ clockwise; this will give a suitable pulse width to start with.

Plug in the interface again, reset the computer, and write zeros (using either the POKE or OUT command) to your chosen servo locations. On checking the servo outputs with a scope the 20 to 25 mS repetition (frame) rate should be observed, and the positive-going

BUYLINES
The 23-way edge-connector socket specified in the Parts List is avaliable from Watford Electronics. Electroware stock the Pactec case used for housing the interface board; you can find them at Dutton Lane, Eastleigh, SO5 4SL (telephone 0703 610944). The two PCBs, one for the servo arm interface and the other for the connecting plug, can be purchased using the PCB Service order form on page 99.
pulses should be at their smallest width of about 1 mS . PR2 may be used to adjust the 'minimum' pulse width. To decrease the pulse width, turn PR2 clockwise. All the servo output channels should be producing identical pulse sizes but with the appropriate phase lag according to the time slot where they occur.

Now, choosing a specific servo channel, observe the pulse output on a scope as the number 127 is written to this channel. The pulse width should shift to be about 2 mS , and this 'maximum' pulse width can be adjusted using PR1. If a servo is at hand it can be connected up as shown in the diagram of Fig. 4, whereupon it should immediately take up the position dictated by the pulse width. Different numbers can now be POKEd to the servo to test a number of pulse positions.

identical for up to four servos
Fig. 4 Servo connections.


Rubber feet in the case help support the connector board.

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# READ/WRITE 

Letters for this page should be addressed to Read/Write at our Charing Cross Road address.

Dear Sir,
I read with interest Tim Orr's series on electro-music techniques, and plan to use some of his circuits in a project. The problem is, as far as I can find out, your advertisers do not seem to stock the Curtis devices mentioned. Could you please give me some indication as to where these items may be obtained?
D. J. Stephenson,

Cheltenham
Digisound Ltd are the agents for the Curtis ICs used in Tim Orr's circuits. In particular, the CEM3310 costs $£ 4.20$, CEM3320 is $£ 4.00$, CEM3330 is $£ 4.40$, and the CEM3340 is $£ 6.10$, plus $\mathbf{3 0}$ p postage and packing, and VAT must be added to the total order. Digisound are at 14/16 Queen Street, Blackpool, Lancs FY1 1PQ (Tel. 0253 28900).

## Dear Sir,

I am presently constructing an audio amplifier which consists of the System A preamp and the Audiophile power amp split into two mono amps so that they may be sited next to the speakers.

Do you think it will be necessary to attenuate the output of the System A preamp down to 500 mV to match the Audiophile preamp output or is this. unnecessary as the maximum voltage swing from each preamp is almost the same?

If the DC offset of my System A preamp is sufficiently low as to make the capacitor at the output unnecessary, can I also dispense with the electrolytic capacitor at the input to the Audiophile power amp?

Finally I am using toroidal transformers in the power amps which, although they function correctly, hum quite loudly. The manufacturers (ILP) suggest that the cause is a poor mains waveform. Is there any way of reducing this effect for a modest cost?

## D. A. Davies,

W. Sussex

No, it shouldn't be necessary to attenuate the output of the preamp, because the mis-match in levels is fairly small, and the right way round in any case $(775 \mathrm{mV}$
output into a 500 mV input - the other way round would stop you from getting maximum output from the power amp). Ideally, you could leave out both the DC blocking capacitors, but in practice, we wouldn't recommend doing so because then any DC fault in the preamp would be amplified and fed through all the way to the loudspeaker's coils, which is one of the best ways we've discovered of destroying them. So for safety's sake, leave one of the caps in circuit, though keeping both is not necessary.

In our opinion, a well-designed transformer (toroidal or otherwise) should not be obtrusively noisy. However, you may have accentuated the relatively innocuous noise any mains transformer is bound to make by the way you have mounted it. We suggest experimenting with alternative techniques, for example, mounting the transformer on something soft and accoustically dead (foam rubber would be ideal but for its inflammability - so try whatever you have to hand until you find something that works).

## Dear Sir,

First of all, congratulations on an interesting magazine. I am, at present, in the process of building the System A preamp and the 150 W MOSFET amplifier. I would appreciate it if you could let me know of the modification involved in matching these units. I note that the preamp's output is 775 mV and the amp's input is 7 V .

Also, could you tell me which configuration to use to match an Elite EEI 700 MM cartridge to the preamp's input?

Yours faithfully,
W. Suzor,

South Africa.
Hmm - the opposite problem to Mr Davies. Well, you can settle for leaving things as they are and possibly not getting quite the maximum volume out of the system (it all depends on the outputs of the signal sources): or you can tinker with the preamp. Referring back to the circuit diagram in the July 81 issue, R37
may be increased to 15 k : on the other hand you could reduce RV2 to a 470R pot or solder a 1 k 0 resistor from either side of RV2 to ground. The first modification might affect the preamp stability, the second will alter the operating characteristics of the balance control. You'll just have to suck it and see . . .

As for the cartridge matching, we recommend the use of option $H$ in this instance. Good listening.

## Dear Sir,

It was intriguing to read in the September 82 issue of Which? (with Money Which?) of the possibility of an electronic solution to the ancient problem of ascertaining when it is possible to have sex without contraception and no danger of pregnancy. Your designers will enjoy working on a project which will interpret temperature changes in the breasts and indicate if sex without conception is possible. I do hope that you can come up with such a design - it is bound to be popular.

Yours sincerely,
W. K. C. Townley,

Morecambe.
Not only does this suggestion win our Raincoat of the Month award, but if followed up would probably offend our female readership.

## Dear Sirs,

With reference to ETI September, page 11; Digest News. 'Eric' is the Tangerine Users Group mascot; yes, a small IC is the mascot of that fairly large users group. Eric is more commonly known as a 24-pin 2708 EPROM. Yours faithfully,
Master N. P. Leirs,

## Swansea

PS How about a binder for my five years of back issues of ETI or a year's subscription (even better) for the above info.
Thanks for identifying Eric; as promised in the Digest item, you don't win a prize! (Sorry if we got your name wrong, but we couldn't make out your signature.)

## OOPS

Two small errors crept into the Spectrum Analyst project last month. In Fig. 3, the circuit diagram of the filter-rectifier block, R37-52 should be 10k, as listed in the Parts List. In Fig. 5, the overiay for the main board, the +10 V and -10 V connections marked 'FROM PSU' (at the corner of the PCB nearest the caption) should be swapped over. In the Cortex article, the block diagram showed two TMS9995s; the VDP is, of course, a TMS9929.

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[^5]
# AUDIOPHILE 

## Ron Harris has got lots of arms. This doesn't mean he can write articles faster than the rest of us, but it's useful for testing cartridges. Here he tells you how to give your hi-fi the (Gold)ring of confidence.

The G910IGC represents something of a rethink for Goldring. Some time ago, they launched their successful G900IGC with a compliance approaching $50 \mathrm{cu}\left(\times 10^{-6} \mathrm{~cm} /\right.$ dyne); they subsequently issued the G910 with an identical generator system and stylus, but a much reduced compliance. Ostensibly this was to bring the G900 cartridge a potentially wider audience by allowing its use in higher mass arms. (The G900IGC itself is really only viable in an SME, since any higher arm mass is liable to bring the resonance into the range where it will affect the audible reproduction.)

The resonant frequency generated by the compliance of the cantilever and the combined arm and cartridge mass can be calculated from:

$$
f_{r}=\frac{1}{2 \pi \sqrt{(M+m)} C_{o}}
$$

where $f_{r}=$ resonant frequency $(\mathrm{Hz})$
$\mathrm{M}=\mathrm{arm}$ effective mass (grams)
$\mathrm{m}=$ cartridge mass (grams)
$\mathrm{C}_{\mathrm{o}}=$ Compliance (cu)
Ideally this figure should lie above the frequencies of warps on records ( $4-7 \mathrm{~Hz}$ ), yet below the audio range of 15 Hz upwards: a suitable compromise has been established as being between $9-12 \mathrm{~Hz}$. If you do the arithmetic, it becomes very clear that with a compliance figure up around 50 cu the G900IGC is going to have a hard time with any arm having an effective mass higher than about $6-7 \mathrm{~g}$.

Thus the 910 iGC , with its compliance of 25 cu , will be more suitable for a much wider range of arms and thus allows a more secure performance in more decks. (Goldring recommend arm masses between 3 g and 12 g .)

## No Arm Done?

It is not just the frequency of the resonance that matters, but the strength of the resonance is important too. Damping is becoming an increasingly popular option on tonearms these days, as the importance of this subsonic resonance is recognised. The effect of energy entering the resonance is to excite the arm into motion (thus affecting the cartridge's hold upon the groove) and/or to degrade the reproduction of the lower registers by pouring energy into the system at a frequency sufficiently close to the bass register to 'modulate' the signal and colour the sound severely.

In the past, ultra-high compliance figures of around 60 cu have been present in cartridge designs, notably from Empire. Shure and Goldring too were following that path, and Shure's new V15V still has a cantilever with compliance of around $35-40 \mathrm{cu}$. It does, however, incorporate its own damping system. The thinking behind this idea is that the ideal pickup system consists of a massless arm and cartridge, tracking at zero grams. Such a unit would have no inertia and no overshoot. With no mass it could not wear out records and would track every groove perfectly. Ever lower arm mass figures were

pursued earnestly, the SME Series III reaching a low of around 6 g . The Ortofon Concorde and the Shure MV30HE are both attempts to reach as close as possible to the unobtainable ideal.

The closest thing, in practice, to the massless groove scanner is probably the laser-beam of the Philips Compact Disc player. It's impossible to wear out what you don't touch. Surface noise is also eliminated by this method and even scratches on the surface can be 'correlated' out by the following circuitry. Visions of silent background, massive dynamic range and practically invulnerable records are promises that lie temptingly just beyond the next technological revolution. One more miracle to await, headphones in hand and conductor's baton raised.

Speaking as a professional cynic, l've yet to hear a convincing demonstration of this 'new-wave' hi-fi. Good ol' analogue - warts and warps et al - can still beat the sleeves off anything else l've heard. Listen to a top flight analogue recording of a live performance - the Deutsche Gramophone 'Ring Cycle' for example - and I think you'll see what I mean.


Fig. 1 Cross-sectional drawing of the Goldring G901IGC cartridge.

## Lots of Life - But Not Live

A vital part of a live recording is the effect of the hall in which the recording was made. Any bounded space will affect sound generated within it. Resonances are (again) present and they reinforce certain sections of the sound spectrum, effectively adding peaks to the overall frequency response. These are directly related to the size of the hall or room and will in addition limit the bass response obtainable.

Reflections from the boundaries reach the recording mics later than the original signal and add 'reverberation' to the sound. With a different frequency spread for each reflection, depending upon the nature of the boundary doing the reflecting, a definite character will be added to the overall result.

This is because few materials reflect all audible frequencies equally, and most will absorb some quite readily. High frequencies are soaked up by brick, cloth, carpet, people - even plants! Bass frequencies are harder to absorb and travel quite effortlessly through walls into next door's living room, for example, adding little to a sense of neighbourly good-will in the process.

Thus overall, each concert hall - and living room will have a distinct sonic character to impart to sound produced within it and it is this low-level information that digital recording is, at present anyway, lousy at replaying. Recordings of even the best orchestras and musicians are apt to sound decidedly lacking in life and character. No 'ambience' is a more up-market method of expressing the same sentiment. In plain terms they just sound dull and flat.

## A Bit Short?

Perhaps as the sampling rates and number of bits used in each sample increase, this problem will be resolved. For the moment, though, we are left with the uneasy feeling that the circuitry is filtering out the life with the noise! Almost enough to justify a science fiction story or two, that - emotion classed as surface noise and filtered out at the reproduction stage! Worth a quiet shudder or two over a glass of wine, methinks.

If your tastes don't run to opera, try the new Ry Cooder LP, 'Into the Slide Area'. It was recorded live and the quality is absolutely magnificent. Frequency balance has been well maintained and the voices come across superbly. Match that one, Philips!

## Back To The Plot

At least this all goes to show that there is quite a few years left in the record deck yet. It will be a considerable time until G910IGCs and Karat Diamonds become as hard to get as pine needles! It is interesting to note, though, that Quad mention in the release for their new preamp (see later in this article for more details) that the auxiliary input is now intended for 'compact disc players' ......

So now that we've established that the G910IGC possesses a future, let's take a closer look at it. As I said, it is identical to the G900IGC in all respects, save that lowered compliance.

This means it has the van den Hul stylus point, with its Improved Groove Contact geometry (IGC). This means a minor radius of a mere 3.5 um and a major (contact) dimension of 85 um lying perpendicular to the groove. Thus the stylus as a whole closely approximates to the shape of the head which cut the master disc in the first place.

Being of the same shape means that it is supposed to have less trouble following the groove - and staying in it - than other stylus profiles. Claimed benefits are improved definition of detail, better imaging through


The point of the exercise? The van den Hul in close-up. The advantages of the shape are claimed to be decreased wear, due to increased contact radius, and improved groove following abilities.
channel separation and stability, low record wear through increased contact area and lower intermodulation distortion. Not quite everything but the kitchen sink, though close. The elliptical tip may as well pack up and go home!

The really disquieting thing is that the IGC cartridges deliver on all the claims made for them! The surface noise really is very low, stereo image is excellent and you won't find better detail in a cartridge anywhere. Worrying that.

## Stiffer Upper Lip

The older G900ICC works exceptionally well in an SME Series III, and I've encountered no problems playing the unit in this manner. Many are the tales I have heard, however, of horrendous bending cantilevers, sound breaking-up faster than the Labour Party, and unstable bass response which rocks speaker cones on their suspensions.

Most of these, I suspect, can be put down to poor matching between cartridge and arm. Still, it does a manufacturer no good to get lumbered with such tales, whether they are his fault or not. The 910IGC is thus a most sensible answer to the criticisms.

## TEST RESULTS

G910IGC SERIAL NUMBER 1142
OUTPUT VOLTAGE (AT $5 \mathrm{~cm} / \mathrm{s}$ ): 6.6 mV (L)
$6.7 \mathrm{mV}(\mathrm{R})$
CHANNEL SEPARATION: $\quad 1 \mathrm{kHz}: 30 \mathrm{~dB}$
FREQUENCY RESPONSE:
$10 \mathrm{kHz}: 24 \mathrm{~dB}$
$20 \mathrm{~Hz}-20 \mathrm{kHz} \pm 1.5 \mathrm{~dB}$
(see graph)
STATIC COMPLIANCE: 25 cu
EQUIVALENT TIP
MASS:
0.4 mg

CHANNEL BALANCE (AT
1 kHz ):
within 1.5 dB
VERTICAL TRACKING
ANGLE:
$24^{\circ}$
OPTIMUM TRACKING
WEIGHT:
OPTIMUM ELECTRICAL
LOAD:
WEIGHT:
TYPICAL PRICE:
1.7 g

47k/200p
4.3 g
$£ 59.00$ including VAT


Fig. 2 (above) Frequency response plot of the G901IGC. Would that it contained a few deficiencies so that I could moan about them.

I tried the 910IGC in a wide variety of arms, from the SME itself to an Audio Technica of dubious parentage. It gave a good account of itself in all. Curiously it also appeared to offer a more refined performance than the 900IGC, even with the SME. Could it be there are more refinements lurking within the strangely shaped shell than Goldring are admitting?

The treble in particular seemed to have been cleanedup somewhat with the 910 producing a clearer and sharper rendition of transients than its stable companion. The overall performance could simply be described as more confident and controlled, but can in no way be totally ascribed to the mere lowering of compliance.

## Bench Testing

Putting both a 900 and 910 through a series of tests side-by-side failed to reveal any significant technical differences between them. The 910 measured out at $20 \mathrm{~Hz}-20 \mathrm{kHz} \pm 1.5 \mathrm{~dB}$ with a slightly falling upperend response, which may in some way account for the tolerance of surface noise.

Output was very high at around 7 mV and overload problems may well arise on lesser preamps. Using the cartridge with lower grade systems is a waste anyway, but check nonetheless. You will need around $90-100 \mathrm{mV}$
overload to be reasonably safe.
Separation was very high also, at around 30 dB at 1 kHz and 24 dB at 10 kHz . Optimum tracking was achieved at 1.7 g and no practical improvement is to be gained beyong this.

Under a microscope the finish on the diamond was very good and the alignment appeared to be spot-on. Goldring have obviously gone to a good deal of trouble with their van den Hul point and it shows in the product.

## Competition Results

At around $£ 60.00$ the G910IGC is not cheap. By today's inflationary standards it is difficult to justify calling it expensive, however. Taking into account the very fine performance offered, the cartridge can justifiably be labelled as value for money. The sound quality is nicely open and well detailed. Bass is extended and free of 'boom', a characteristic which has, perhaps, been gained at the expense of a little 'weight'. Treble is clean and extended also, with no sign of the hardening on difficult material which can so easily beset lesser designs.

A good product, then, and one which has a great deal to offer a wide range of users. I personally preferred the G910IGC to its more specialised companion the 900 . A worthy contender in the $£ 50-£ 100$ market.


Above: the new Quad 34 preamp - sorry, 'control unit' - lined up with the FM4. nother new Quad model is quite an event. That's more than one THIS YEAR . . . must be a rush of blood to the design department. The versatile filtering is retained from the existing 44 and the price is, well, interesting. Audiophile is trying to lay its hands on one, so more details when we hear from Huntingdon. Mind you, after that crack about design departments . . .


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| TYPE | $\underset{\substack{\text { SERIES } \\ \text { No }}}{ }$ | $\begin{aligned} & \text { SECONDARY } \\ & \text { Volls } \end{aligned}$ | ams Current | PRICE |
| :---: | :---: | :---: | :---: | :---: |
| 30 Va | 18010 | $6+6$ | 250 |  |
| $70 \times 30 \mathrm{~mm}$ | $1 \times 011$ | $9+9$ 12.12 | 166 | $E 5.12$ |
| 045 Kg | 1 $\times 0+2$ | 12.12 | ! 25 |  |
| $\left\|\begin{array}{l} \text { Reguraino } \\ 18 \% \end{array}\right\|$ | 12013 | $15 \cdot 15$ | 100 | - Diocion |
|  | $1 \times 014$ | $18+18$ | 083 | Wat 097 |
|  | $1 \times 015$ | $22+22$ | 068 | rotaltom |
|  | 'x016 | 25+25 | 060 |  |
|  | ${ }_{4 \times 017}$ | $30+30$ | 050 |  |
| 50 VA | 22010 | $6+6$ | 416 |  |
| $80 \times 35 \mathrm{~mm}$ | 2x011 | 9*9 | 277 |  |
| 09 kg | $2 \times 012$ | 12+12 | 208 |  |
| $\begin{gathered} \text { Regulalion } \\ 131 / \% \end{gathered}$ | 2x013 | $15+15$ | ${ }^{1} 66$ |  |
|  | $2 \times 014$ | 18+18 | 138 | c.10 |
|  | 20015 | $22+22$ | 113 | -0,0tis |
|  | $2 \times 016$ | $25+25$ | 100 | - watel 0 S |
|  | 2×017 | $30+30$ | 083 085 | toial 560 os |
|  | 2×028 | 110 220 | 045 0.22 |  |
|  | $\underset{2 \times 030}{2 \times 29}$ | 240 | 020 |  |
| 80 va | 3x010 | $6+6$ | 6.64 |  |
| $90 \times 30 \mathrm{~mm}$ | 3x011 | $9+9$ | 444 |  |
| 1 kg | 3x012 | 12*12 | 333 | 60 |
| Regulation $12 \%$ | 3x013 | $15 * 15$ | 2.66 | 0.08 |
|  | $3 \times 014$ | 18+18 | 222 | -0tact 68 |
|  | $3 \times 015$ | 22+22 | 181 |  |
|  | $3 \times 016$ $3 \times 017$ | $25 * 25$ $30+30$ | ! ${ }^{60}$ |  |
|  | +3x017 | $30+30$ 110 | 133 0 0 |  |
|  | 3x029 | 220 | 036 |  |
|  | 3x030 | 240 | 033 |  |
| 120 va | $4 \times 010$ | 6*6 | 1000 |  |
| 90x 40 mm | $4 \times 019$ | 9+9 | ${ }^{6} 66$ |  |
| 12 kg | $4 \times 012$ | 12+12 | 500 |  |
| $\begin{aligned} & \text { flegulation } \\ & 11 \% \end{aligned}$ | $4 \times 013$ | $15+15$ | 4.00 | 690 |
|  | 4x014 | 18+18 | 333 |  |
|  | $4 \times 015$ | $22 \cdot 22$ | 272 | 0\%fi6 |
|  | $4 \times 15$ | $25+25$ | 240 | - watriz 18 |
|  | $4 \times 017$ | $30 \cdot 30$ | 200 | total 9936 |
|  | $4 \times 018$ $4 \times 028$ $4 \times 28$ | $35 \times 35$ 110 | 171 109 109 |  |
|  | $4 \times 029$ | 220 | 054 |  |
|  | 4×030 | 240 | 0.50 |  |
| 160 VA | $5 \times 011$ | 9+9 | ${ }^{8.89}$ |  |
| $1110 \times 40 \mathrm{~mm}$ | 58012 | 12* 12 | 666 |  |
| 18 kg | 50013 | 15+15 | 533 |  |
| Regulation$8 \%$ | $5 \times 014$ | $18+18$ | 444 | 7.91 |
|  | $5 \times 015$ $5 \times 016$ | $22+22$ $25+25$ | 3 363 | -p/pri 67 |
|  | $5 \times 016$ | $25+25$ $30+30$ | ${ }^{3.68}$ |  |
|  | $5 \times 018$ | $35+35$ | 2.28 | Totals/10 0 |
|  | $5 \times 026$ | $40+40$ | 200 |  |
|  | $5 \times 028$ | 110 | 145 |  |
|  | $5 \times 029$ $5 \times 030$ | 220 240 | 1072 068. |  |

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| TYPE | $\underset{\substack{\text { SERIESS } \\ \text { No }}}{ }$ | $\begin{array}{\|c\|} \hline \text { SECONDARY } \\ \text { Volis } \end{array}$ | $\begin{gathered} \text { RMS } \\ \text { Current } \end{gathered}$ | PRICE |
| :---: | :---: | :---: | :---: | :---: |
| 225 va | $6 \times 012$ | $12+12$ | 938 |  |
| $110 \times 45 \mathrm{~mm}$ | $6 \times 013$ | $15+15$ | 7.50 |  |
| 22 kg | 6x014 | 18+18 | 6.25 |  |
| $\begin{gathered} \text { Requlation } \\ 7 \% \end{gathered}$ | $6 \times 015$ | $22+22$ | 5.11 | 997 |
|  | $6 \times 016$ | $25+25$ | 450 |  |
|  | 6x017 | $30 \cdot 30$ | 375 | -8ipf7 00 |
|  | 6x018 | $35+35$ | 3.21 | - watcices |
|  | 6x026 | $40+40$ $45+45$ | 281 2.50 2. | Totalsiz es |
|  | $6 \times 033$ | $50+50$ | 2.25 |  |
|  | $6 \times 028$ | 110 | 204 |  |
|  | 6x029 | 220 | 1.02 |  |
|  | 6x030 | 240 | 0.93 |  |
| 300 va | 7x013 | 15+15 | 10.00 |  |
| $110 \times 50 \mathrm{~mm}$ | 7x014 | 18+18 | 833 |  |
| 26 kg | $7 \times 015$ | $22 \cdot 22$ | 5.82 |  |
| $\begin{aligned} & \text { Requatation } \\ & 6 \% \\ & \hline \end{aligned}$ | $7 \times 016$ | 25+25 | 500 |  |
|  | $7 \times 017$ | 30+30 |  |  |
|  | $7 \times 018$ | 35+35 | 428 | 18 |
|  | $7 \times 026$ $7 \times 025$ 7 | $40+40$ $45+45$ | 3.75 3.33 3 |  |
|  | 7x033 | $50+50$ | 3.00 |  |
|  | $7 \times 028$ | 110 | 2.72 |  |
|  | 7×029 | 220 | ${ }^{1} 136$ |  |
|  | 7 7 030 | 240 | 125 |  |
| 500 Va | $8 \times 016$ | $25+25$ | 1000 |  |
| $140 \times 60 \mathrm{~mm}$ | $8 \times 017$ | $30+30$ | 833 | 435 |
| ${ }^{4} \mathrm{Kg}$ | $8 \times 018$ | $35 \cdot 35$ $40+40$ | 714 | 13.0 |
| $\begin{aligned} & \text { Regulation } \\ & 4 \% \end{aligned}$ | $8 \times 026$ <br> $8 \times 025$ | $40+40$ $45+45$ | 6.25 555 5 | - Dotitis |
|  | 8x033 | 40+40 50 | 5 500 | -uatic so |
|  | $8 \times 042$ | $55+55$ | 454 | 10tal E18 26 |
|  | $8 \times 028$ | 110 200 | 454 207 20 |  |
|  | $8 \times 029$ $8 \times 030$ | 220 240 | 2 2 2 08 |  |
| 625 va | $9 \times 017$ | $30+30$ | 10.41 |  |
| $140 \times 75 \mathrm{~mm}$ | $9 \times 018$ | $35+35$ | 892 |  |
| Regufation ${ }_{\text {a }}$ | $9 \times 026$ | $40+40$ | 781 | 10.13 |
|  | 990225 | $45+45$ $50+50$ |  | - Dider so |
|  | $9 \times 033$ $9 \times 042$ | $50+50$ $55+55$ | 662 568 568 | - watcz 79 |
|  | $9 \times 028$ | 110 | 568 |  |
|  | 98029 | 220 | ${ }_{2}^{284}$ |  |
|  | $9 \times 030$ | 240 | 260 |  |

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# 5 V to 25 V Switched Mode PSU For EPROM Blowing 

C.J. Jay, Bristol

The circuit shows the application of a Fairchild 78540 switched mode power supply chip used to generate a 25 V or 5 V (binary selectable) $\mathrm{V}_{\mathrm{pp}}$ input to a 2716 type EPROM. The supply was designed to be quite small and compact so that it could fit onto a single card EPROM programmer. All the necessary power input requirements were satisfied by a single 5 V $V_{\mathrm{cc}}$ input; this circuit will therefore eliminate the need for a transformer derived 25 V supply and the additional supply distribution on an already overcrowded microcomputer backplane.

The 78540 is designed into a 'step up' circuit configuration. The output is derived from pin 1 of the IC, the cathode of the internal charge pump diode D1, to a reservoir capacitor C3. When the internal transistor Q1 is turned on, current flows through inductor $\mathrm{L1}$ causing energy to be stored in the magnetic flux around the windings; the charge pump diode is reversed biased when Q1 is conducting. When Q1 turns off the magnetic flux collapses, inducing a positive voltage at node A. If this node voltage exceeds the voltage on the positive plate of capacitor C3, D1 will conduct and the capacitor will charge to a more positive potential. To regulate the output voltage it is necessary to control the switching of Q1. This is achieved by tapping Vout through a potential divider of R1 and PR1 to pin 10 of the 78540 . This negative feedback controls the on/off times (mark/space), and the frequency at which Q1 switches. Q1 is driven from an internal voltagecontrolled oscillator, which is in turn controlled by the negative feedback derived from Vour.

Q3 has been included in the potential divider to select the amount of feedback required to provide outputs of 25 or 5 V . When Q3 is turned on the feedback is reduced,

so $V_{\text {our }}$ will rise to +25 V . When off, feedback is increased and the output will fall to +5 V . To set the output voltage range it is necessary to adjust the multiturn cermet type trimpot, PR1. This should be done off load because high voltages can be generated if the feedback has been initially set up incorrectly. The CONTROL input may be CPUprogrammed via a TTL, latch or PIA; the two resistors R2 and R3 are chosen to enable Q3 to be driven hard on or hard off by TTL high or low level inputs.

Other components used in the design are a timing capacitor C 1 of $4 n 7$, a peak current limiting resistor of OR22 (R4) and a current limiting resistor R5, 180R. Capacitor C2 was included to aid smoothing of the switch ripple on the 25 V output. L1 is 34 turns of 24 swg wire on an RS RM6 ferrite core.

Regarding the performance, the circuit provides an excellent stabilised output of 25 V for loads requiring up to 75 mA . The +5 V supply does exhibit 500 mV of switching ripple, but superimposed on a mean 5 V DC level this will not violate the static input requirements of the $2716^{\prime} s V_{p p}$ input. The conversion efficiency of the entire supply was about $60 \%$.



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# SIGNAL LINE TESTER 

# If you're a PA person, here's an incredibly simple device to prevent embarrassment when all you can give them is the sound of silence. Design by Vivian Capel. 

This project came about as a result of a very unfortunate incident. The author was in charge of a large public-address system that had been temporarily installed to cater for a public meeting with an audience of several thousand. There were spare microphones, spare inputs on the mixer, and to make quite sure, a spare mixer under the bench. Very little really could go disastrously wrong - but it did! Part way through the main speech everything went dead. Calm and reasoned diagnosis was employed (it wasn't really, but I couldn't admit to blind panic). Finally, after what seemed an age of silence from the speaker and murmerings from the audience, the fault was revealed as a dead short across the audio cable between mixer and amplifier rack.

Thus the fault-warning unit here described was conceived, embarrassment being the mother of invention! The idea was to constantly monitor the condition of a signal cable; if it should go either short-circuit or open-circuit, an appropriate LED would immediately light up to indicate what had happened.

The device could be used not only for public address applications, but any situation where a vulnerable signal cable needs protection by constant monitoring. A security intercom or telephone link, for example, could be monitored to reveal a fault or tampering as soon as it occurred, and avoid the need for frequent testing.

## Requirements

To utilise the device successfully, the normal signal for which the cable is used must be AC. Furthermore there must be a DC path or load resistor at the far end of the cable to pass the small open-circuit mode sensing current. The input to the amplifiers or other equipment at the far end must be AC-coupled otherwise the input stages could be affected by the DC monitoring potentials. As a rule,
these conditions are met in most slave amplifiers by the input gain control and following coupling capacitor. Should the capacitor come first, a load resistor must be added across the input socket.


Fig. 1 Component overlay for the line checker. The line below PR1 may be replaced by a resistor - see text.

## PARTS LIST

| Resistors (all ${ }_{\text {d }} \mathbf{W}, 5 \%$ ) |  |
| :---: | :---: |
| R1 | 100k |
| R2 | 10k |
| Potentiometer |  |
| PR1 | 10k miniature horizontal preset |
| Capacitor | Ou 25 V axial electrolytic |

Semiconductors
Q1, 2 BC108
LED1, 23 mm red LED

## Miscellaneous

PCB (see Buylines); case and sockets (if built separate from audio equipment).

## BUYLINES

[^6]In considering the design, several features were deemed desirable. First, the value of the load terminating the line should not be critical. While false indications can be obtained under extreme load conditions with the circuit eventually evolved, there is a wide latitude in load values and no false alarms wil be obtained within the specified limits. The nominal load for which the circuit was designed is 10 k , but variations up to 20 k and down to under 1 k 0 can be tolerated. This will accommodate most applications, but other values could be obtained by changing the values of the three resistors from those given.

Second, the circuit must take very little current as it is active for the whole time the mixer is switched on. A current of 1 mA was stipulated as the maximum allowable in the quiescent mode. This meant that few active components could be used, and that they had to be non-conducting until a fault condition occurred.

Third, the unit should be as simple as possible; far too many electronic circuits at present are needlessly complicated. In this case simplicity was pursued not merely for its own sake but as a fundamental necessity for fault monitoring equipment. It has to have a high degree of reliability if it is to be depended upon, and every extra component is one more that could itself break down.

## Construction

The original circuit was built in to the mixer and powered from the mixer batteries, but construction and housing is by no means critical and almost any convenient form can be used. Input and output sockets can be standard jacks, Cannon XLR plugs or any suitable terminations appropriate for the equipment involved.

Before applying the battery voltage make sure that the variable preset PR1 is fully advanced so that maximum resistance is in circuit. If it should be fully the other way

## HOW IT WORKS

The circult dlagram is shown in Fig. 1: we will consider the operation of the open-circuit Indicator first. The emitter of Q1 ls taken to the junction of R2 and PR1 which have values of 10 k and 1 kO respectively, so the emitter is at a potentlal of one-tenth of the supply. The base of Q1 ls connected to the junction of R1 (100k) and the load, which is nominally 10k; hence lt too ls at one-tenth of the supply voltage. Therefore there is no forward blas on $Q 1$ as the base and emitter are at about the same potential, so no current passes through it and the LED in its collector circult.

If the load now goes open circult, the positive voltage on Q1 base rises since it is no longer tled down, so Q1 becomes
forward biased, and curreñt flows through LED1, illuminating It. Current limiting is provided by PR1 in the emitter circuit, which safeguards LED1.

The short clrcuit detector is built round Q2. The base of thls transistor is taken to the R2-PR1 junction, and Is at one-tenth of the supply; Q2 emitter is connected to the Junction of R1 and the load, and so it too has base and emitter at the same potential and is cut off. Should the load become short circuited, the emitter voltage drops to zero which means there is a poiltive blas on the base. Therefore Q2 conducts and LED2 lights to Indicate a short. Although there is no current-limiting resistor in series with LED2, the bias on the transistor can
be adjusted by PR1 to glve the correct current and desired illumination.

Note that neither fault condition affects the warning clrcult of the other. If the load is short-clrculted, Q1 base drops to $0 \mathbf{V}$ while its emitter is still positive, so it is driven even further Into cut-off. Similarly, If the load becomes open-circuit, the emitter voltage of Q2 rises above that of ita base.

The capacitor C1 blocks DC from the sending equipment, should there be any, from upsetting the operation of the warning circuits and vice versa. It also prevents any DC path in the output of the sending device from shunting the load and rendering the open-circult test ineffective.


Fig. 2 Circuit diagram of the signal line tester.
there will be no limiting resistance for LED1 and it could be destroyed. If preferred, the value can be split between a 500R preset and 560R fixed resistor for the sake of safety. Once set, the preset should not require re-adjustment, and so can be sealed with a spot of paint.

Connect a 10 k load resistor across the output socket. If one is already fitted across a switched jack socket, this will not be necessary. On applying the supply, both LEDs should remain off. Disconnect the load and LED1 should light up. Now short-circuit the output; LED1 should go out and LED2 illuminate.

It will be found that one LED is brighter than the other due to differences in the $\mathrm{h}_{\mathrm{fe}}$ of the transistors and tolerances of the resistors. Adjusting PR1 will produce equal brightness, so repeat the open-circuit and short-circuit tests and adjust PR1 each time until the desired illumination is achieved. Decreasing the value darkens LED2 and brightens LED1 and vice-versa.

Check that both LEDs are off with loads of 20k and 1 kO . If LED1 glimmers at 20 k increase the value of R1 to 120 k or even more if required. This may be necessary if Q1 has an exceptionally high $\mathrm{h}_{\mathrm{fe}}$.

## CAPACITORS

 180, 220, 270, 330, 390, 470, 560, 680, 820pF; 68, 82, 100, 120, 150, $2 n 7,3 n 3,3 n 9,4 n 713 p, 5 n 8,6 n 8,8 n 210 p$ CERAMIC Very small, 1.8, 2.2, 2.7, atc. up to in 8p each. 1n5, 2 n 2 , $3 n 3,4 n 7,6 n 8$ Ep; $10 \mathrm{n}, 22 \mathrm{ncp} 33 \mathrm{n}, 47 \mathrm{n} 7 \mathrm{p} ; 100 \mathrm{n} \mathrm{Ep}$.
 30n, 47n 7p. 282n, 1000 9p; 120n, 160n 11pi 180n, 220n 12p; 270n, 330n
 specing 2,2 35p
depth otocka. ELECTMOLYTICS NON-poler (for LS X-overs) BOV peat $2 \mu \mathrm{~F}$ 24p; 4 4 F
 6.8/40, 10/25, 22/10, 100: 10/40, 22/25, 47/10 11p; $47 / 25$ 125; $100 / 10$
 100/10, 19p;470/40; 1000/16 27p; $1000 / 26 \mathrm{~F} ; \mathrm{p} ; 1000 / 40$, 2200/16 44p; 1000/63 7tp; 2200/40,4700/18 73p.
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