

## MOSF=S MAK= MI CRAD =1 Brillant new design for peak performance

Glean up the airbuild our ion generator!

Hovel design techniques to secure your home or car

## POWER PACKED - by POWERTRAN

Powertran's black boxes are packed with punch. Not only are they superb kits to buy and build they really do the job! Imaginative and ingenious design goes hand in hand with top quality materials and outstanding performance capability. With their smart black styling the kits harmonise visually as well as musically.
Your can built each unit independantly fór its set task and then gradually increase your array until you have a complete bank of formidable controllable power.


Complete Kit - £49.90 + VAT


Complete Kit - $\mathbf{£ 4 9 . 5 0}+$ VAT


Complete Kit - £175.00 + VAT


Complete Kit - $\mathbf{£ 6 4 . 9 0}+$ VAT

MPA 200 is a low price, high power 100 W amplifier. Its smart styling, professional appearance and performance, make it one of our most popular designs. With adaptable inputs the mixer accepts a variety of sources yet straightforward construction makes it ideal for the first-time builder

CHROMATHEQUE 5000 - a 5-channel lighting system powerful enough for professional discos yet controllable for home-effects. Sound to light, strobe to music level, random or sequential effects - each channel can handle up to 500 W yet minimal wiring is needed with our unique single-board design.

ETI VOCODER - 14 channels, each with independent level control, for maximum versatility and intelligibility: Two input amplifiers - for speech/excitation - each with level control and tone control. The Vocoder is a powerful yet flexible machine that is interesting to build and thanks to our easy to follow construction manual, is within the capability of most enthusiasts.

SP2 200 twice the power with two of the reliable, durable and economic amps from the MPA200; fed by separate power supplies from a common toroidal transformer. Superb finish and quality components throughout - up to (even over!) the standard of high priced factory-built units.


DJ90 Stereo Mixer - this is a really versatile new mixer that enables the constructor DJ to produce a professional performance every time. There are two stereo inputs for magnetic cartridges, a stereo auxiliary input and mike input. Other 'plus' features are auto-panning for fast or slow, slider controls, multi-mixing, ducking, interrupt, input modulation, in short everything...the whole works - AND under $£ 100$ complete! (We have illustrated the DJ90 teamed in our own console with the Chromatheque and an SP2 200 and speakers.

Complete Kit - $£ 97.50+$ VAT


## WORLD LEADERS IN ELECTRONIC KITS

- Money Back Guarantee - If you are not completely satisfied with your

Powertran Kit return it in original condition withın 10 days for full refund.

- Free Soldering Practice Kit - To assist the beginner we will supply, on request with your first kit order, a free soldering practice kit with usefui tips and ilustrations.
- Component Packs - Most kits are available as separate packs le.g. PCB component sets, hardware'sets etc). Prices in our FREE catalogue.
- Ordering - Full ordering details, delivery service, and sales counter opening - outside back of this issue



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## FEATURES

## DIGEST

## 10. ELECTROMUSIC TECHNIQUES

Be informed, be entertained, be amused, be amazed - it's all in the news this month.

## TECH TIPS

.26
When they're not writing letters, our readers are designing circuits. Here's a few more that have been submitted.

DESIGNER'S NOTEBOOK .
36
Some merry MOSFET marvels, with a discussion of the advantages of VMOS and a few circuits for you to play with.
READ $\overline{\text { WRITE }}$
56
More missives from our readers about sound, speakers, System A, life, the Uhiverse and everything

PART 3
Tim Orr winds up this designer's delight with a few well-chosen building blocks.

BREADBOARDING SYSTEMS . . . . . . . 74
Put away the plywood, the solder and the $6^{\prime \prime}$ nails, there are better ways of prototyping circuits nowadays.


## PROJECTS

## NEGATIVE ION GENERATOR.

Spray some negative ions into the atmosphere and feel positive. You can build this project the way we did or experiment with the emitter design yourself.

## OSCILLOSCOPE PART 2

促 details for this amazing little gadget. A small piece of test gear with a big performance.
STYLUSTIMER
R. .41
Don't destroy your precious discs by neglecting your needle; build this gadget and you'll be certain when to change your stylus.


## 150W MOSFET AMPLIFIER

48 Here's the amplifier you've always promised yourself. Modern technology and brilliant design give a truly state-of-the-art performance.
## ROBOT MODULES:

DIGITALPWM .
66
Two for the price of one this month; first, an interface board for computer control of DC motors

## OPTICALSENSOR

and second, a cunning infra-red device that'll detect walls and a whole bunch of other stuff.

## LOGIC LOCK

79
Complicated enough to thwart Dr. Who's Adric, it'll certainly baffle your friendly neighbourhood burglar; yet it's very simple to build.

FOILPATTERNS

## INFORMATION

NEXTMONTH'S ETI17 BOOK SERVICE64
BINDERS ..... 23
PCB SERVICE ..... 82
ZXCOMPUTING SUBSCRIPTIONS ..... 84

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| 18n, 22n, 27n, 33n |  |
| 39n, 47n | 8 p |
| 39n, 56n | 12p |
| $82 \mathrm{n}, 100 \mathrm{n}$ | 11p |
| 100 V |  |
| 100n, 120n | 10 p |
| 150n, 180n | 12p |
| 220n, 270n 15p |  |
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## RAM for <br> 

## COMPUTER



Tolerance $5^{\circ} \mathrm{O}$.



















# Sinclair 2X81 Personal Comp the heart of a system that grows with you. 

1980 saw a genuine breakthrough the Sinclair ZX80, world's first complete personal computer for under £100. Not surprisingly, over 50,000 were sold.

In March 1981, the Sinclair lead increased dramatically. For just $£ 69.95$ the Sinclair ZX81 offers even more advanced facilities at an even lower price. Initially, even we were surprised by the demand - over 50,000 in the first 3 months!

Today, the Sinclair ZX81 is the heart of a computer system. You can add 16 -times more memory with the ZX RAM pack. The ZX Printer offers an unbeatable combination of performance and price. And the ZX Software library is growing every day.

## Lower price: higher capability

With the ZX81, it's still very simple to teach yourself computing, but the ZX81 packs even greater working capability than the ZX80.

It uses the same micro-processor, but incorporates a new, more powerful 8K BASIC ROM - the 'trained intelligence' of the computer. This chip works in decimals, handles logs and trig, allows you to plot graphs, and builds up animated displays.

And the ZX81 incorporates other operation refinements - the facility to load and save named programs on cassette, for example, and to drive the new ZX Printer


Every ZX81 comes with a comprehensive, specially-written manual - a complete course in BASIC programming, from first principles to complex programs.

## Kit: £49.s

## Higher specification, lower price -

 how's it done?Quite simply, by design. The ZX80 reduced the chips in a working computer from 40 or so, to 21 . The ZX81 reduces the 21 to 4 !

The secret lies in a totally new master chip. Designed by Sinclair and custom-built in Britain, this unique chip replaces 18 chips from the ZX80!

## New, improved specification

 - Z80A micro-processor - new faster version of the famous $Z 80$ chip, widely recognised as the best ever made.- Unique 'one-touch' key word entry: the ZX81 eliminates a great deal of tiresome typing. Key words (RUN, LIST, PRINT, etc.) have their own single-key entry.
- Unique syntax-check and report codes identify programming errors immediately
- Full range of mathematical and scientific functions accurate to eight decimal places.
- Graph-drawing and animateddisplay facilities.
- Multi-dimensional string and numerical arrays.
- Up to 26 FOR/NEXT loops.
- Randomise function - useful for games as well as serious applications. - Cassette LOAD and SAVE with named programs.
- 1K-byte RAM expandable to 16 K bytes with Sinclair RAM pack.
- Able to drive the new Sinclair printer.
- Advanced 4-chip design: microprocessor, ROM, RAM, plus master chip - unique, custom-built chip replacing 18 ZX 80 chips.
 E69.95


## Kit or built -it's up to you!

 You'll be surprised how easy the ZX81 kit is to build: just four chips to assemble (plus, of course the other discrete components) - a few hours' work with a fine-tipped soldering iron. And you may already have a suitable mains adaptor -700 mA at 9 VDC nominal unregulated (supplied with built version).Kit and built versions come complete with all leads to connect to your TV (colour or black and white) and cassette recorder.



Designed as a complete module to fit your Sinclair $\mathbf{Z X 8 0}$ or $\mathbf{Z X 8 1}$, the RAM pack simply plugs into the existing expansion port at the rear of the computer to multiply your data/program storage by 16 !

Use it for long and complex programs or as a personal database. Yet it costs as little as half the price of competitive additional memory.

With the RAM pack, you can also run some of the more sophisticated ZX Software - the Business \& Household management systems for example.


6 Kings Parade, Cambridge, Cambs., CB2 1SN. Tel: (0276) 66104 \& 21282.

## Available nowthe IX Printer for only £59..5

Designed exclusively for use with the ZX81 (and ZX80 with 8 K BASIC ROM), the printer offers full alphanumerics and highly sophisticated graphics.

A special feature is COPY, which prints out exactly what is on the whole TV screen without the need for further intructions.

At last you can have a hard copy of your program listings-particularly

## How to order your ZX81

BY PHONE - Access, Barclaycard or Trustcard holders can call 01-200 0200 for personal attention 24 hours a day, every day. BY FREEPOST - use the no-stampneeded coupon below. You can pay
useful when writing or editing programs.

And of course you can print out your results for permanent records or sending to a friend.

Printing speed is 50 characters per second, with 32 characters per line and 9 lines per vertical inch.

The ZXPrinter connects to the rear of your computer - using a stackable connector so you can plug in a RAM pack as well. A roll of paper ( 65 ft long $x 4$ in wide) is supplied, along with full instructions.
by cheque, postal order, Access, Barclaycard or Trustcard.
EITHER WAY - please allow up to 28 days for delivery. And there's a 14-day money-back option. We want you to be satisfied beyond doubt and we have no doubt that you will be.


# AS THINGS GET TOUGH 

As a man thinketh, so he will be This is a major breakthrough programme in human development disciplines which is destined to effect the lives of thousands of thinking men and women. It engages a remarkable new concept -

## A MULTI-DIMENSIONAL APPROACH TO HUMAN SUCCESS AND TOTAL ACHIEVEMENT

A must for the Listening Library of every thoughtful man and woman GROWTH and RENEWAL


## SELF-REALIZATION

A lesson in winning - Broadside 1 is not a psychological gimmick but a profound professional in-depth study into the attitudes and the qualities that go into the formation of the total winner. It is a programme based upon years of exhaustive research, using clinically controlled feedback studies, into the every present human desire for success and achievement as the basic motivating winning force that brings men and women alike to the forefront of our society. It is a unique and masterful attempt to define and to formulate into a controlled audio pack, the exact guide lines for creating the winner.

## The motivated need

Have you ever given thought as to why some people are more successful than others?
The secret of the successful lies in direct relation to their positive attitudes and their defined purpose control.

WHAT DO YOU DESIRE? UNFORTUNATELY, THIS PROGRAMME CAN ONLY OFFER TOTAL WINNING KNOWLEDGE, THE BASIC ELEMENT SO ABSOLUTELY NECESSARY FOR POSITIVE HUMAN DEVELOPMENT


## ACTION

Each unit of human life is composed greatly from dreams derived from the unconscious. He who has the ability and know-how to translate them into positive reality, will truly be placed amongst the great. Ana is Nin BROADSIDE 1 - THE INSTRUMENT OF HUMAN DEVELOPMENT

| To: Broadside Associates 2 The Spinney, North Gray, Kent. | GUARANTEE <br> I understand that if I am not fully satisfied, I can, within 15 days after despatch, return the complete programme and obtain a full refund. |
| :---: | :---: |
| Please send me the full Broadside 1 programme for which 1 enclose a cheque/postal order (crossed) for the sum of $£ 25$ (incl. VAT and Postage) made payable to BROADSIDE lallow a maximum 28 days for delivery) |  |
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| Postcode.............. County................ |  |

# Memotech's New Memory System for the ZX881 Itgrowsas youprogress 



## Memopak 16K Memory Extension - $\$ 39.96$ incl.VAT

It is a fact that the $Z \times 81$ has revolutionised home computing, and coupled with the new Memopak 16K it gives you a massive 16 K of Directly Addressable RAM, which is neither switched nor paged. With the addition of the Memopak 16K your ZX81's enlarged memory capacity will enable it to execute longer and more sophisticated programs, and to hold an extended database.
The 16 K and 64 K Memopaks come in attractive, customdesigned and engineered cases which fit snugly on to the back of the $\mathrm{ZX81}$, giving firm, wobble-free connections See below for ordering information
Coming Soon...
Memotech Mi-Fes Graphics Centronics interface and Sottware Drivers Memotech Digitising Tablet RS232 Interface


All these products are designed to fit 'piggy-back' fashion on to each other, and use the Sinclair power supply. WATCH THIS SPACE for further details. We regret we are as yet unable to accept orders or enquiries concerning these products - but we'll let you know as soon as they become available.



## Memopak 64K Memory Extension -\&F9.00 indVAT

The 64 K Memopak is a pack which extends the memory of the ZX81 by a further 56K, and together with the ZX81 gives a full 64 K , which is neither switched nor paged. and is directly addressable. The unit is user transparent and accepts basic commands such as 10 DIM A( 9000 ).

## BREAKDOWN OF MEMORY AREAS

0-8K . . Sinclair ROM
8-16K ... This section of memory switches in or out in 4 K blocks to leave space for memory mapping, holds its contents during cassette loads, allows communication between programmes, and can be used to run assembly language routines.
16-32K...This area can be used for basic programmes and assembly language routines.
$32-64 \mathrm{~K} \ldots 32 \mathrm{~K}$ of RAM memory for basic variables and large arrays.
With the Memopak 64K extension the ZX81 is transformed into a powerful computer, suitable for business, leisure and educational use, at a fraction of the cost of comparable systems.

## Unique 3 monthtrade-in offer!

When your programming needs have outgrown the capacity provided by 16 K RAM, and you find it necessary to further extend your ZX 81 's capacity, we will take back your 16 K Memopak and allow a discount of $£ 15.00$ against your purchase of our 64 K model.*
*We reserve the right to reject for discounting purposes, units which
$\qquad$ DATE

|  | Quantity | Price | Total |
| :--- | :--- | :--- | :--- |
| 16K RAM, Assembled |  | ³9.95 |  |
| 64K RAM, Assembled |  | £79:00 |  |
|  | Postage | $£ 2.00$ |  |

$\qquad$
NAME
ADDRESS

# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS:NEWS 

# DIGEST 

## Ace Catalogue

W/hoops - we forgot to mention Ace Mailtronix in the 'Buying Mail Order' article. This is a shame because they've got a good little catalogue with a wide range of components at reasonble prices - what's more the prices are VAT-inclusive ( $I$ hate pricing an order and then finding l've got to add on $15 \%$ ). Ace offer all the services in the survey except surplus components and books, and can be found at 3A Commercial Street, Batley, West Yorks, WF17 5H J.

## Power Tower

That's what Binatone are calling this hi-fi stack system, and what you have to do is guess the price of it. Nope, you're wrong - the whole lot costs $£ 150$. Honest. From the top down we've got a belt-drive record deck, three-waveband stereo tuner, stereo cassette deck with soft eject, LED meter and metal tape facility, 40 W total peak music power (hmmm...) stereo amp and matching two-way stereo speakers. The cabinet also has storage space for singles, albums and cassettes and at the price, Binatone are feeling quite pleased with themselves.


## Eye Of The Needle



## Robot Revival

Me're not ones to boast, but since - we published our report on Industrial Robots in September ' 81 things appear to be picking up a bit. The British robot population is now growing at more than $35 \%$ a year and has moved up to fifth in the world league table. Last year's total was approximately 700 . Even more encouraging is the attitude of the unions, which is surprisingly lacking in hostility. In fact Terry Duffy, AUEW president, has this to say of robots on the factory floor: "Failure to accept the challenge of new technology would, in my opinion, be to sabotage our national future. Robots are here to stay." How very refreshing to hear such sensible sentiments.

Another promising trend is the entry into the market of the major computer manufacturers who, until now, hadn't bothered to compete with the specialist firms. GEC, for example, is expected to augment its own in-house robotics facility through co-operation deals with Japanese companies (Japan still dominates the world robotics scene). With this sort of investment by big companies there seems to be no reason why Britain should'nt become a major supplier of industrial robots within the next two or three years.

## Master Minds

What have the following five in common: Jack Kilby, Henry Ford, Ottmar Mergenthaler, Ernest O. Lawrence and Max Tishler? Well, no, none of them have appeared on the Muppet Show, but we had something else in mind. They've all just been appointed to the US National Inventors' Hall of Fame, and the one we're really interested in is Jack Kilby. He's the Texas Instruments engineer who, back in 1958, invented the integrated circuit and hence fathered all the goodies we've got today. Kilby holds more than 50 patents and is still going strong as a consultant to Tl . Wonder what it feels like to be responsible for the state of the world's technology.

## Dial-A-Muzak

It's bad enough being assaulted by non-stop muzak in shops and hotels but now you won't be able to escape even when making phone calls. The Hong Kong Trade Development Council have sent us details of the Music Phone; as well as the usual modern features like autoredial and jack plug connection, this gadget has a 'music switch'. If someone wants to place you on hold without you hearing their discussion with a third party, this control plays soft music to you while you're waiting. The manufacturers, Yee King Enterprises Company, are producing 12,000 phones a month and exporting most of them to the USA and Europe; further evidence that it's all a fiendish oriental plot to subvert the West. Arrggh!


## Stamp On It

$j^{o}$ real reason for using this photograph in Digest except that it intrigued us. Apparently it's
some kind of tiny inductor thingy and since none of you have got soldering irons that small you won't want to know anything else about it, will you?



# NEWS:NEWS:NEWS:NEWS:NEWS:NEWS:NEWS 



## Submicron Sculpture

These remarkably neat and tiny patterns were produced using the dew Direct-Write Lithography System (VIS-1000) developed by Varian Associates of California. The system will produce highdensity, submicron circuits at a throughput of $\mathbf{1 0 - 1 5}$ four inch wafer levels per hour. The first picture is a resolution test pattern while the second is a gate array pattern - somewhat beautiful, is it not?

## New UD

Aaxell have brought out a new UD cassette with a considerably better performance than the previous one. High frequency saturation level, bias noise level and housing stability are said to be particularly good. Apparently it's due to the wonder ingredient, New PXGamma Ferric Oxide, which has fewer voids and more uniform particle size. The magnetic particle layer is also extremely uniform, causing modulation noise and DC noise to be minimised. The New UD cassettes will be on sale from early April. By the way, lads, just love the new TV ad.


## Making Connections

A new range of low cost moulded battery clips have been announced by Dau. The moulded connector ( a PP3 type is shown) offers long term reliability by overcoming corrosion problems. For further information contact Dau (UK) Ltd, 70-74 Barnham Road, Barnham, Bognor Regis, West Sussex PO220ES.


## Shorts

- Good news for kit builders the new Spring/Summer 82 catalogue from Heathkit is out with several additions to their already extensive range and a Spring Sale on many items.
- A word processing package is now available for the TRS-80 Model 1 and II that offers a professional range of facilities for a reasonable price. You'll need 48K, one disc drive, and $£ 79.00$ plus VAT and postage. "Newscript" is available from E.A. International, 8 High St., Meldreth, Royston, Herts SG8 6JV.
- Hewlett-Pckard are getting a bit carried a way with VLSI technology - they've just developed a chip set for mainframe-like performance which includes a 32-bit processor running off an 18 MHz clock, a $16 \mathrm{~K} \times 8$ RAM and a $16 \mathrm{~K} \times 40$ ROM chip. Jeez. .
- Made in Wales. . . . the TC-2011 $20^{\prime \prime}$ colour TV from Panasonic. The set features auto-search tuning, channel memory, a teak cabinet and 3 W audio output, and is available now from authorised dealers.
- Celdis of Reading can now supply the new Mostek MK4802 16K static RAM direct from stock. Organised as $2 \mathrm{~K} \times 8$ for ease of use, the chip is compatible with all the other members of Mostek's Bytewyde memory family.
- The latest BAEC newsletter has reached us; amongst other things it contains a catalogue survey, projects, book reviews and a nice write-up on the Breadboard exhibition. Thank you, gentlemen we're glad you enjoyed it too.
- Rapid Electronics are on the move - by the time you read this ther'll be at Hill Farm Industrial Estate, Boxted, Colchester, Essex This is just off the A12 and Rapid say callers will be most welcome.
- Winner of the recent "Office of 2000 AD" design competition was an attache case containing a data processor/transmitter/receiver; if you press a button the case describes itself and how it works through a built-in loudspeaker. The day my briefcase starts talking to me is the day I give up the Beaujolais.
- Still and moving pictures from one shop; the Fotovalue photographic franchise are to introduce Telefunken VHS video into their camera shops. Products for sale are a video recorder, four televisions, accessories (including a Teletext adaptor) and a colour camera.
- Good old Goonhilly (you remember, Telstar and all that) is to get a facelift. British Telecom are replacing the antenna ready for use with the intelsat system from the end of 1984. Cost is estimated at $£ 3$ million.
- Semiconductor Specialists are now stocking two of Raytheon's high-performance dual low-noise op-amps, the RC5532 and the RC5532A. Suitable for applications in high quality audio equipment and instrumentation circuits, these chips are an all-round improvement over standard items such as the 1458.
- Why send us a press release about Britain exporting Tigers to Africa, we wondered? Because Tigers are telephone management and accounting systems manufactured by Minster Automation, that's why. Nice to see some foreign cash flowing this way for a change.
- IMF Electronics have launched a couple of professional monitor loudspeakers and a studio monitor. They feature such esoteric delights as a "ferro-fluid damped tweeter" and are apparently rather good.
- KGB Micros Ltd are now selling an IEEE 488 interface bus for Superbrain for $£ 200$. With a name like that, how can you dare refuse to buyone?
- Build a better photodiode and the world will beat a path to your door. The BPW41D from Ferranti has a very high rejection of wavelengths below 700 nm and is ideal for infra-red remote control and data transmission systems.
- Mitsubishi - bless you - have recently succeeded in manufacturing an amorphous solar cell, with the world's highest energy conversion efficiency ( $8.5 \%$ ). My dictionary says amorphous means 'shapeless'; must be great fun trying to build them into equipment.



## West Hyde Wonders

We've just received the latest West Hyde catalogue; curiously they're always out of step with the seasons (this one's dated Winter 81-82) but who cares, there are lots of goodies inside. With boxes and racks to suit any application you can think of, and then some, this is definitely the first place to look if you're after an enclosure.


## Solar Sums

Drovided you don't want to do any calculations in the dark, Hanimex's new light-activated calculators dispense with the need for batteries. Even dim light is sufficient now that a new generation of very efficient light-sensitive cells have been developed, and Hanimex expect these calculators will become the leading solar calculators of the year. The SC852 has a list price of $£ 13.95$ while the 853 is $£ 10.95$.

## Who's Kidding Who?

Now this is very silly. In fact we thought it was a joke, except that the press release is dated March 1st, not April 1st. Anyway, BL Systems and ITR have announced the TARDIS - Time and Attendance Direct Input System which is claimed to be much more efficient than traditional shop floor clocking-on methods. It says here that the key feature of TARDIS is the use of TIME LORD clocks - honest! - and that the computer-controlled system cuts the work involved in payroll/attendance calculations by up to $75 \%$. Watch out, Doctor; the Time-andmotion Men are out to get you!


## In Coherent

nnova 90 lasers, developed by Coherent (UK) Ltd and to be exhibited at Electro-optics/Laser International 82, are an entirely new concept in ion laser design. The "Cool Disc" bore technology gives higher guaranteed power capability with longer life expectancy, while other design features mean a hands-off, trouble-free laser for both research and industry. Maybe we can get our hands on one and have a real game of Space Invaders.

## A Fine Pear

A pparently the microcomputer industry has some sort of preoccupation with fruit. We already have the Apple and Tangerine systems and shortly we'll have the PEAR II. This PAL-colour microcomputer is based on the ubiquitous 6502 microprocessor and is intended for the more advanced user who wants to choose from various programming languages. Typical options are BASIC, Pascal, FORTRAN or COBOL. The standard system has 32K of RAM, on-board expandable to 96 K using bank-select. Further expansion to suit your requirements is accomplished using plug-in cards. The PEAR II will be available around September/October of this year for £975 plus VAT, and further information can be obtained from Pearcom Ltd, 17 Nobel Square, Basildon, Essex SS13 1LP. Anyone for pomegranite?

## Dai-Electric

ospitals at Wrexham and Swansea are to have reliable fire and alarm monitoring systems fitted by the Welsh Health Technical Services Organisation. The Statiscan System is based on a single threecore cable and uses timedivision multiplexing to transmit hundreds of signals from the monitoring points to the central consoles. (Of course, you all know about TDM because we featured it in Notebook in April.) The system is produced by Static Systems Group of Wolverhampton and will no doubt be of great use when the home rule nuts run out of English holiday cottages to burn. -

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## mitexalinual

## TV BARGRAPH

Surely your television can be put to better use than as an aid for insomnia? The video game manufacturers pointed the way and we've published a couple of designs ourselves - but how about using the television as a research tool? Next month we do just that with our TV Bargraph project; a device that displays analogue voltages as a histogram (columns) on any domestic 625 -line set. The horizontal axis may be at the bottom of the screen (for positive-only inputs) or half-way up for AC signal displays, and the number of channels is userselectable. The basic unit has eight, while additional channel cards will give 16,32 (how about $1 / 3$ octave spectrum analysis over the audio band?), 64 and even 128 channels on a good TV. We could have used a video controller chip for this project, but you wouldn't have learnt much from it; the circuit uses standard logic ICs so you can see how the TV sync signals are generated. Don't wait for ITV2; get a new channel on your telly with the July ETI.

## MOSFET BRIDGING MODULE

Now you'd think most ordinary people would be more than happy with the power output of the MOSFET amplifier published this month. But we know better. Time and again it's been proved that ETI readers like to provoke their neighbours, weaken their foundations and generally make their presence felt with LOUD music; and who are we to argue? To forestall the inevitable requests for more power, next month we show you how a cheap, simple add-on circuit will enable you to bridge two ETI-5000 modules, giving a 300 W output and shattered windows in the living room. <br> \section*{\title{
LOOK OUT <br> \section*{\title{
LOOK OUT FOR THE JULY ISSUE FOR THE JULY ISSUE ON SALE JUNE 5th
}} ON SALE JUNE 5th
}}

Articles described here are in an advanced state of preparation. Articles described heres may dictate changes to the final contents.
However, circumstances

## VIDEO SYSTEMS

Next month we fearlessly take the lid off video recording literally. Taking time off from hi-fi, Stan Curtis has been unbolting all kinds of video recorders and finding out exactly what's inside and what it does. You never knew there was so much in it. Don't miss the July issue for the story even the video magazines couldn't bring you.

## AUDIOPHILE

We've been waiting to get our hands on one of these for a long time. Look at the sleek lines, the compact build, the elega nt black enclosure, the hint of mysteries within to enthrall you long into the night. (And if you haven't figured out that next month's article features the power amp rather than the admittedly lovely lady, then one of us has the wrong magazine). The long months of flattery, threats, bribery and begging have finally resulted in a Magnetic Field Power Amplifier - the Carver Cube - being delivered into the hot, eager hands of your editor (now stop that!). As Stan Curtis pointed out a year ago, this extraordinary amp is only 7 " on a side and can punch out an incredible 200 W per channel. In that article the technical merits of the design were examined; now Ron Harris can reveal all concerning the sound quality. It's in Audiophile next month - be there!


# TheMICRO PROFESSOR solvesthemystery ofmicro-processors. 

Micro-Professor is a low-cost Z80A based microcomputer which provides you with an interesting and inexpensive way to understand the world of microprocessors.
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# NEGATIVE ION GENERATOR 

# For readers who just have to find out for themselves what this subject is all about, this negative ion generator should provide a good basis for experiment. Design by Jonathan Scott. Development by Graeme Teesdale. 

The rise in popularity of negative ion generators, the claims made for them, and the attention they have received in newspapers and magazines recently has undoubtedly intrigued many readers with a technical background or interest. As the electronics associated with a negative ion generator is relatively simple, generally employing readily available components, this article describes how to build a unit that can be used as the basis for experiment.

## Design Of The Emitter Head

The object of the emitter head is to take in the HT, in our case about 3 kV , and produce a stream of negative ions flowing forwards into the room in which the generator is placed. The ions are produced by a very intense field gradient, which is induced by the high voltage and the geometry of the head assembly. This ion flow is a corona wind.

It is a basic principle of electrostatic physics that the field gradient is stronger in the immediate vicinity of a point projection, the gradient being greater when the point is sharper so most ion generators employ some combination of sharp projections and high voltage.

## Pointing The Way

If the points are spaced well away from other parts of the unit the ions will naturally repel themselves away from the region of emission. However, if the point or points are partially enclosed in the case of the device there may need to be either a chimney-
shaped assembly around the emitters or some sort of accelerator electrodes to help eject the ions from the emitter head. We didn't require an accelerator as the points protrude beyond the slot in the case.

Wherever there is ion production there will be ozone production. Ozone is corrosive as well as a strong antibacterial agent, and is poisonous in sufficient concentration. In order to keep it to a minimum, as low a voltage as possible should be used. Our project has been designed to give the lowest voltage compatible with adequate ion production. The design should be such as not to allow any arcing or serious breakdown; this is really only likely if you try using an "accelerator", as there will be no metal in close proximity to the emitter otherwise.

The best metal for the points which is easily obtainable is steel, preferably stainless. This is hard enough to hold an edge and will resist the effects of cathode stripping. The latter is undesirable both because the fine point will be eroded away, and also because the heavy metal ions which are ejected are undesirable agents in the air we breathe (stick to getting your minerals from cornflakes).

There is no shock hazard as the unit is not mains powered and there is a very large series resistance between the points and the multiplier output. At
most, there results something between a nip and a tickle if you touch the emitter points.

## Construction

First stage of construction is to assemble the components on the PCBs; commence with the inverter board. Insert the resistors, capacitors, IC and transistors before asembling the transformer to it. As usual, take care with the orientation of the diode, IC1 and the transistors. Next, wind the transformer - details are given in the box. The transformer employs a potcore and this can be held on to the PCB with a nylon bolt - do not use a metal bolt. Cut the transformer coil wires to length, scrape off the insulation and solder them in place. The TIP31C transistors, Q1 and Q2, do not actually require any heatsink, though they do get warm in operation.

The high voltage board may be assembled next. Take care with the orientation of the diodes. Stand the capacitors erect on the board so that they do not touch each other or you may have arc-over problems between these components.

Mount the appropriate components on the 'blinker' board


PARTS LIST

| Resistors (all $1 / 4 \mathrm{~W}, 5 \%$ ) |  |
| :--- | :--- |
| R1 | 100 k |
| R2 | 220R |
| R3,5 | 1k0 |
| R4 | 10 k |
| R6,7 | 4M7 |

Potentiometer
PR1 $\quad 47 \mathrm{k}$ miniature vertical preset
Capacitor

| Capacitors |  |
| :--- | :--- |
| C1 | 10u 16 V tantalum |
| C2 | 220p ceramic |
| C3 | 10n polycarbonate |
| C4.9 | 10n 1 kV ceramic |
| C10 | 3n3 or 4n7 polystyrene or |
|  | ceramic |

Semiconductors
IC1 555
Q1,2 TIP31C
Q3 BC107 or BC547
D1,8 1N4001
D2.7 1N4007 or similar ( 1 kV PIV)

## Miscellaneous

LP1 wire-ended neon bulb (no series resistor)
PCBs (see Buylines); potcore and former (FX2242 - see Buylines); coaxial DC jack socket; case ( $180 \times 110 \times 55 \mathrm{~mm}$, Vero ref. 75-2861D); five needles.
next, as you'll need this for a testing aid. It is important to watch the diode polarity here. The cathode of the diode goes to the pad marked with the 'ground' symbol. Note that the components are mounted on the copper side.

The emitter points are steel needles soldered directly to the PCB. The easiest method is to tape the needles, parallel and the correct distance apart, so they overhang the end of a wooden block etc. Support the board underneath, and touching, the overhanging needles and solder them in place before removing the tape. Since ions will be ejected from any sharp point we recommend you


The inverter and high voltage boards mounted side by side in the case, with the emitter needles protruding through the slot in the side of the case.
cut all the component leads on the high voltage board and then resolder them, using enough extra solder to give rounded solder blobs. (Make sure the same is true of the needle connections). This will prevent unwanted ion leakage.

The DC input socket we mounted on one side of the box. Exactly how the DC coaxial jack socket is wired will depend on how your plug pack output plug is wired. Some have the outer connector connected to positive, while others have it connected to the negative.

## Getting It Going

You will need a multimeter and a supply of between $9 \mathrm{~V} D C$ and 14 V
DC. Switch the meter to the current range to read 300 mA full scale or more, and connect it in series with the DC supply input. Switch the supply on and, assuming all is well, adjust PR1 on the inverter board for minimum current. This could be between about $220-280 \mathrm{~mA}$.

Run the unit for a few minutes, then switch off, discharge the rectifier capacitors and feel Q1 and Q2. One should not be markedly hotter than the other, otherwise you have adjusted PR1 incorrectly or you have a fault - most likely a transistor inserted incorrectly or a dry joint between the output of IC1 (pin 3) and the bases of Q1, Q2, or Q3.

Having confirmed everything


Fig. 2 Component overlay and wiring for the ion generator. The two-board design will allow for different physical layouts and connection pads are provided for experimentation with accelerators and off-board emitters.

The DC-to-AC inverter consisis of a 555 astable multivibrator, the output of which is used to drive two transistors operated in push-pull. The collectors of Q1 and Q2 switch current through each side of the transformer (T1) primary in turn. Diode D1 prevents any damage from a supply connected with reverse polarity. Capacitor C1 is a bypass. IC1 oscillates at around $25 \mathbf{k H z}$, determined by R1 and C2. The exact frequency is unimportant. The mark-to-space ratio of the output of IC1 (at pin 3) may be adjusted by PR1, which is connected in series with pin 7 of IC1.

The output of IC1 drives the base of Q1 directly, via R3 and R2. Q1 turns on when the output of IC1 goes high. Resistor R3 is there principally to limit the base current supplied to Q 1 , while R 2 serves to discharge the base emitter junction capacitance so that Q1 turns off quickly when the output of IC3 goes low.

When pin 3 of IC1 goes high, Q3 also turns on, preventing Q2 from turning on. When pin 3 of IC1 goes low, Q1 and Q3 turn off and Q2 will turn on as base bias will be supplied via R5. Thus current is alternatively switched through each side of the primary T1. The secondary provides a voltage step-up of $25: 1$. If the supply voltage is 12 V DC, then the peak-to-peak output from the secondary of T 1 will be 600 V .

The voltage-multiplier rectifier employs the well-known Cockcroft-Walton circuit, where the output of successive halfwave rectifiers is connected in series with the previous one. This circuit provides a multiplication of six times. Thus, with a 12 V DC supply, the output will be about -3.6 kV . With a 10 V DC supply (as can be obtained from a 9 V DC battery eliminator), about -3 kV is obtained. An output for an 'accelerator' is provided.

The high voltage output to the emitter head is taken via a $4 M 7$ resistor to ensure that only low short-circuit current occurs if the emitter head is accidentally contacted or excessively humid air causes 'flashover' from the emitter.

The blinker is simply a crude relaxation oscillator. When a charge builds up on the 'antenna' pad, it will charge C10. When the voltage on C10 reaches the breakdown voltage of LP1 (about 70 V ), the neon will conduct. This will discharge the capacitor, the voltage across it falling until it reaches the extinguishing voltage of the neon (about 30-40 V), which will then cease conduction. While the neon conducts, it will emit light, but as it discharges C10 fairly rapidly, all you will see is a brief flash from the neon. Diode D8 ensures only negative charges operate the blinker.

When the neon ceases conduction, the charge on C10 will build up again and the whole process will be repeated.
construct a negative ion generator and the electronics can readily serve as the basis for experimenting with different designs. Higher voltages are unnecessary - and are not usual in commercial designs - and can lead to problems with ozone generation, breakdown, etc. A connection is available on the high voltage board for
works as it should, and having adjusted PR1, assemble it all into the case and you can check its operation with the blinker.

Turn the ioniser on and grasp the blinker so that your thumb is in good contact with the pad marked by the 'ground' symbol. Hold the blinker such that the 'antenna' pad is about 10 mm
in front of the emitter. You should be able to count around one blink per second if all is well and this is a good 'benchmark' for successful operation when you experiment with different head designs and geometries.

## Notes On Experimentation

This project shows but one way to
supplying an 'accelerator' on an emitter head. It should be connected via a $4 M 7,1 / 4 W$ resistor. The accelerator voltage could be tapped off lower down the rectifier chain if desired - we suggest at the junction of C6 and C8.

The exact value of capacitors C 4 to C9 on the high voltage board is not important and may be any value between about 1 nF and 22 nF or so, but should not be lower than 1 nF . The voltage rating of these capacitors should not be less than 1000 V .

The DC supply should not be greater than 15 V or more turns be wound on the secondary of T1, else you may experience insulation breakdown within the transformer.

## BUYLINES

None of the electronic components for this project should cause any supply problems. The FX2242 is stocked by C.T. Electronics (Acton) Ltd. of $\mathbf{2 6 7 1 2 7 0}$ Acton Lane, London W4 5DG. Suitable steel needles can be obtained from your family sewing drawer! Failing that, any sewing accessories supplier can help you. The boards can be obtained from our PCB Service at the prices given on page 82.


The assembled blinker, which we tinned with solder to avoid sweaty finger marks and oxidation. The cathode of D8 is at the bottom.

## TRANSFORMER WINDING DETAILS

## Potcore: FX2242

Secondary: 125 turns of $\mathbf{0 . 2} \mathbf{~ m m}$ diameter enamelled copper wire.
Primary: 10 turns, centre-tapped, of $\mathbf{1 . 0} \mathbf{~ m m}$ diameter enamelled copper wire.

The secondary is wound on the potcore bobbin first. Wind it in five or six neat layers. Slip thin plastic sleeving over the start and finish leads so that the sleeving is held well inside the bobbin. As you finish winding each layer, insulate it with $1 \mathbf{~ m m}$ mylar sticky tape (if you can obtain it) or electrical insulation tape (a bit heavy, but it will do the job). Wind the next layer on the insulation of the previous layer, and so on until you finish winding. Wind several layers of insulation over the completed secondary. Leave the start and finish wires protruding from the different sides of the bobbin so that they exit via different slots of the assembled potcore.

Wind the primary over the secondary; it can be wound bifilar (two wires together, five turns, connect finish of one to start of other to provide centre tap) or in one winding - but don't forget the centre tap. Wind the primary so that its wires exit the potcore opposite the secondary wires.

In operation, if you have breakdown problems (arcing sounds inside the potcore) it means you have not wound or insulated your secondary carefully enough and you'll have to rewind the transformer.


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## TECH TIPS

## Surgeless 555 Clock

## H.B. Broughton, Bishop's Stortford

The CMOS version of the 555 has two major advantages over the bipolar type: a) it is a micropower device; b) it is largely free of the supply current surges generated by an ordinary 555 every time its output changes state. This is important because the surge can upset other circuits powered from the same supply, but the problem can be avoided by using the rather unusual configuration shown, saving 60p an oscillator over the CMOS answer to this difficulty!

Experiment has shown that the supply surge (typically 300 mA by 100 ns ) is roughly halved by using pin 3 (OUTPUT) of the 555 to charge/discharge the capacitor rther than pin 7 (DISCHARGE) and a resistor to $\mathrm{V}+$. It is reduced to less than $25 \%$ of its typical size by using pin 3 and wiring the capacitor to $V+$, and is then small enough to be removed by a 100 nF capacitor between the supply rails.

This gives the circuilt shown, a 'surgeless' clock with frequency $f=6 R C$

( R in ohms, C in microfarads), mark/space ratio of typically $3: 2$, and voltage swing of 1 to 11 V . These figures were found using a load impedance of 330 k ; the frequency is affected if load impedance is less than about $10 \times R$, but if low impedances are to be driven, pin 7 can be used and the load wired from pin 7 to power: alternatively, a 1 k 5 reistor can be taken from pin 7 to power and the load put between pin 7 and ground, though this reduces the available voltage swing at pin 7 .

With the component values shown, this clock runs at 2 kHz and produces a nice clean square wave with low rise times.

## Stylophone With Memory

J.R. Walker, Norwich

This single-octave 'stylophone' can store and play back over a minute of music (two in 'slow' mode). The keyboard was constructed by pushing drawing pins into a plastic box on to which a $C$ to C keyboard was drawn. The drawing pins are numbered one to twelve; this is just the order in which they are scanned and it doesn't matter what notes they actually are, since they are manually tuned by the presets PR1-12. LED1 indicates that no more memory is left, while SW2 allows you to stop in the middle of playback/record without affecting the tune. The unit is automatically reset when switching from play to record, and by leaving it on reset you can play tunes without affecting the one in memory. Like magnetic tape, recording completely wipes out what was previously recorded. To obtain the best results, record in 'slow' and play back in 'fast', since one finger playing is not a speedy operation!

In'fast' mode IC6 oscillates at about



10 Hz , causing IC1 and IC2 to scan the keyboard (each one clocks on by one alternately). IC4 is held off by IC3a. If the stylus is placed on one of the drawing pins, nothing will happen until the counters get to that pin. At this point they stop and IC4 turns on, producing a note. The pitch of the note depends on which preset is driving pin 7 of IC4. The note stays on until the probe is removed from the pin, whereupon everything carries on as before.

With each pulse of $1 \mathrm{C} 6,1 \mathrm{C} 7$ clocks on once, counting from 1 to 1024 in the memory. IC8 remembers if a note was played at that point. On playback all counters are reset and IC8 plays back the tune, taking 1024/10 or over 100 seconds to do so. Slow mode takes roughly twice as long.

The power rail ( 5 V at 200 mA ) should be well smoothed (say 1000 uF 3300 uF ) to preserve the memory if the supply flickers(memory is lost on powerdown). Although the circuit doesn't draw anything like 200 mA the capacity to deliver this current is important as IC4 and IC6 can draw heavily on the power rails.

# Solid State Scope Using LEDs 

## G. Durant, Selby

This 'scope' is completely solid state, with the absence of the cathode ray tube. The tube is replaced by a 10 by 10 matrix of LEDs which provide a smaller, more robust substitute. The $Y$ input is connected via a series resistor to IC1, an LM3914 which is an LED bargraph driver. The IC is used in the dot mode and therefore, if a waveform is applied to the $Y$ input, a single 'dot' will appear to move up and down with the voltage from the wave. Input sensitivity is adjusted via SW1, which brings different series resistors into circuit. The resistors are odd values and are made up by placing two or three in series. If $1 \%$ resistors are used, an accuracy of about $0.25 \%$ can be achieved.

The LEDs must be spaced 1 cm apart, both on the $X$ and $Y$ axis. The use of an LED matrix screen means that the scope can be very thin - in fact pocketsize. Also, an LED screen does not need
very high voltages to work it, so batteries could be used. The only problem is that at high frequencies the resolution is not as good as it could be, although the scope can be used nevertheless at frequencies up to 1 MHz .

The time base uses a crystalcontrolled CMOS clock running at 1 MHz . This is broken down into lower frequencies by a string of divide-by- 10 chips, in this case 4017 s . These frequencies go to a six-way selector switch which selects the timebase frequency. The time base is variable from 100 mS right down to 1 uS in steps of 10 . A 100 pF variable capacitor, marked 'freeze waveform' is used to fine-tune the main oscillator, so the waveform can be frozen.

A suitable power supply is also shown, but any supply may be used with an output voltage of about 12 V (not more than 15 V ). Even a 9 V battery could be used (eg PP9) as, in theory, only one LED is on at a time.

The scope can be expanded to use more LEDs, or to have a trigger device. For the beginner, however, the scope shown would be sophisticated enough to be of great service.


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The mechanical construction of voltage divider and preamp is shown in Fig. 1. The major component (and main constructional member) is the switch SW1 (a Makaswitch type). Between wafers 1 and 2 is fitted screening plate 2 (measurements as in Fig. 3) and between wafers 2 and 3 , screening plate 3 (dimensions in Fig. 4). The screening plates support the voltage divider board and are soldered to it (on the copper track). The preamplifier board forms the end of the switch assembly. The entire section is surrounded by screening plate 1 (dimensions in Fig. 1) which is screwed to the bottom of the case and to the front structure.

In order to achieve a good earth connection for the switch rotor a safety pin, mounted on screening plate 3 before assembly (Fig. 5) is used as an earth contact.

The component layouts for the voltage divider and preamplifier boards are shown in Figs. 6 and 7. Resistors R1, R2, R15, R17, R19 and R21 are soldered directly to the switch contacts. Resistor R15 passes through screening plate 2 (drill diameter 4 mm ). The two 3 mm holes are used for the (insulated!) passage of the connecting wires for switch contact 1 as well as for the connection to R15/R16. The same is true for R19 and screening plate 3.

The equipment is housed in an aluminium case; Fig. 6 shows the necessary drillings and cut-outs in the front panel. The screen cut-out ( $64 x$ 55 mm ) must be fitted with a 'light tube', to prevent the entry of unwanted light. In the prototype instrument a plastic box was used from a pack of screws, having the appropriate outside dimensions. Behind this cutout, a green film is fitted (from stationers) on which the measurement scale ( $6 \times 8$ squares of 7 mm side) has been drawn with Indian ink. The screen is fixed to the inside of the front panel using strips of foam draught excluder.

Figure 6 illustrates how the main board and screening plate 1 are fixed to the front section, using a bracket for each (M3 countersunk screw through the front section and the fixing bracket).


Fig. 1 Assembly of the voltage divider and preamp unit.


Fig. 2 SW1 contact wafer arrangement. Use in conjunction with the circuit diagram published last month.

The main board provides lengthwise stiffening of the case, since it is screwed at one end to the front section and at the other to the back panel (aluminium angle $10 \times 10 \times 1 \times$ 70 mm long). The front half of the main board also provides lateral screening of the input voltage divider.

There are nine 5 mm diameter
holes in the bottom of the case (mark and drill from the circuit board, after the components have been fitted) for the adjustment of trimmer capacitors CV1-9. There is also a countersunk hole to take the M3 screw used for fixing screening plate 1 (as in Fig. 1) and three other $M 4$ countersunk holes for securing the power supply board


Fig. 3 Screening plate 2.


Fig. 6 The front panel drilling details.


Fig. 7 The internal construction of the oscilloscope


Fig. 8 The wiring details for SW2.
(marked out from the board). Note that the screw which passes through the transformer core should be made of brass or plastic.

The cathode ray tube is passed through - and only held by - the draught-excluder-coated screening cylinder. The fixing of this cylinder is as shown in the photograph. The sheet brass supports are formed to the shape of the screening cylinder and softsoldered or glued with Araldite. Q16 is screwed to, and insulated from, the back panel of the case (use a mica washer, and test the insulation after fixing!).

The back panel has six holes as follows; two $\times 8 \mathrm{~mm}$ for the insulated phono sockets (for the source voltage), four $\times 3.2 \mathrm{~mm}$ for the main circuit board fixing bracket, the M3 studding (tube fixture) and transistor Q16.

## Switching On And

 Setting UpAn ammeter must be connected in the supply line for the initial operation. At switch-on, a current of about 850 mA (DC) should flow for a short time; this should fall within a few seconds to about 700 mA as the tube heater warms up and increases in resistance. The following checks and adjustments should be made in the order in which they are given.

Set the stabilised supply voltage (on C26) to 10 V , using PR7.

Check the $\mathrm{V}_{r \mathrm{E}}$ voltage on Q 18 or


Fig. 9 Overlay for the preamp.


Fig. 10 Component overlay for the voltage divider board.

## PARTS LIST




Q19. This should be a 25 kHz square wave with an amplitude of 20 V peak-to-peak and an overshoot of less than 10 V . Due to tolerances in the pot core, the frequency may vary from 25 kHz .

Check the tube heater voltage: a 12-13 V peak-to-peak square wave. The heater voltage cannot be measured with a simple multimeter! The relatively high frequency of 25 kHz will
cause false readings. Check the +7 V , $-6 \mathrm{~V},+150 \mathrm{~V}$ and -460 V (on C36) supplies. Set the working point of the X-output stage, with PR6, to give 6 mA in the 150 V line. Set the working point of the Y-output stage, with PR3, to give +100 V 'after' R78/79. Set the beam to sufficient brightness with PR8 (not too bright, or the trace will be blurred). Adjust the trace for optimum sharpness
in the middle of the screen with PR9 and PR10. Set the picture width with PR5. Rotate the tube so that the X-axis is exactly parallel to the drawn scale. Fix the tube in this position using adhesive tape. Turn the Y -position potentiometer to its mid-position, and adjust the trace to the middle of the screen using PR1 (on the preamplifier board).


Fig. 11 Component overlay for the power supply board.


Next month we conclude this project with the final test procedures and calibration details.
Fig. 12 Overlay for the main circuit board.
LANKING $\underbrace{+7 V-6 V}_{\text {TO PREAMPLIFIER }}$ calibration details.

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# DESIGNER'S NOTEBOOK 

# It's definitely a MOSFET month; we've shown you how to design big with our 150 W amp, and now Don Keighley provides a counterbalance with lots of smaller applications. 

Field effect transistors (FETs) are peculiar brutes. If you've used them you'll know what I mean - negative bias voltages, depletion layers, pinch-off voltages and so on, ad infinitum. If you haven't used a FET before, the theory is simple enough: a FET is essentially a doped-silicon resistor (Fig. 1), much like a normal carbon resistor. The doped-silicon, however, exhibits a change of resistance if an electric field through the resistor varies. The electric field depends on the voltage present at the gate of the FET (Fig. 2), so a change of gate voltage changes the current through (and hence the resistance across) the device. Essentially a FET forms a voltage controlled resistance. In the example shown in Fig. 2 (a P-channe! FET) a gate voltage of 0 V will produce a resistance of approximately 100 R and a gate voltage of 5 V will produce a 1 M 0 resistance. For an N -channel FET the opposite is true; a gate voltage of 0 V will give a resistance of $100 \mathrm{R},-5 \mathrm{~V}$ gives 1 M 0 . For low drainsource voltages and low drain-source currents, the resistance change is linearly related to the gate voltage.

FETs have two enormous advantages over bipolar transistors. First, the gate input resistance is very high, meaning that virtually no current needs to be drawn from preceding circuitry. Second, FETs can exhibit very fast switching speeds - they can be used quite easily up to frequencies of many megahertz.

## Problems, Problems

So, everything is fine - as long as you follow the rules. In low-power applications there is no reason why FETs can't be used anywhere a bipolar transistor can (they are, in fact, more versatile than bipolars - in low-power applications). But, therein lies the rub - power. It is very difficult (and expensive) to make a FET which can pass large currents: the main reason being the horizontal make-up of ordinary FETs. Bipolar transistors have vertical current flow and can pass larger currents


Fig. 1 A field effect transistor (FET) is a doped-silicon resistor, the resistance of which can be varied by changing the electric field through it.

Fig. 2 The symbol for a FET. Current through the FET and hence the resistance across it is controlled by the voltage at the gate.


Fig. 3 Cross-sections through a) a bipolar transistor; b) a field effect transistor.
because of it. Figure 3a shows the theoretical cross-section of a bipolar transistor and a similar cross-section of a FET is shown in Fig. 3b. Current flow in the bipolar is vertically upwards from collector to emitter and the large area through which the current passes allows large currents. FET current flow is from left to right (drain to source) and the small area of current flow means smaller currents than in a similar-sized bipolar transistor.

Recently, VMOS FETs have been manufactured which overcome the power problems normally associated with FETs. A typical VMOS FET cross-section is shown in Fig. 4. Current flow is now vertically upwards, from drain to source, in much the same way as in bipolar transistors. The larger chip area means large current. Hence we have transistors exhibiting all the advantages of FETs without the usual power limits. VMOS FETs also have some other very interesting advantages:

- low ON resistance - good for audio switching purposes.
- power amplification - as high as $10^{6}$.
- positive temperature coefficient on the ON resistance - as the temperature goes up the transistor passes less current, therefore remaining thermally stable.
- easily operated in parallel to increase overall current flow - due to the inherent thermal stability no 'current hogging' by one device occurs.

We'll see applications using these advantages shortly.
The equivalent circuits of a VMOS FET (such as the VN67AF) in its OFF and ON states, are shown in Fig. 5. The zener diode protects the transistor from over-voltage on the transistor gate - it is a feature on many VMOS FETs but not all! If a VMOS transistor does not have such a gate-protection zener diode, it must be handled as a CMOS IC. You must take care to avoid static build-up between connections.

In the VMOS FET's OFF state (gate is low), diode D1 is reverse-biased and no current can flow from drain to source. In the ON state the diode is effectively shorted by a $2 R 0$ resistor, allowing current flow from drain to source. With gate-voltages between $0 V$ and $+V$ the resistor value is within the range $2 R 0$ to $\infty$


Fig. 4 Cross-section through a VMOS FET. Current flow is vertical, as in a bipolar transistor.
Fig. 5 Equivalent diagrams of a VMOS FET a) in the OFF state; b) in the ON state.

Fig 6. A simple unidirectional audio switch formed by a single VMOS FET.

## Applications

Low ON resistances and high OFF resistances make VMOS FETs ideal for use in audio switching networks. Figure 6 shows a simple on/off audio switch controlled by the voltage on the transistor gate: +15 V turns the switch on and 0 V turns it off. Audio signals can only pass in one direction, from drain to source, but any audio voltage of about $-1 / 2 \mathrm{~V}$ to +5 V can be switched.

The extremely high gate-input resistance of VMOS FETs means that they can be switched by virtually any control method, such as CMOS, TTL, op-amps and so on. A four-channel audio multiplexer is shown in Fig. 7, which uses a bank of four VMOS FETs as input switches with the transistors being clocked


Fig. 7 Four VMOS FETs used in a four-channel audio multiplexer giving a time division multiplexed output signal.


Fig. 8 TIL gate logic can be used to control VMOS FETs but a gate pull-up resistor must be used to ensure that the FET gate reaches a high enough voltage to allow sufficient current flow through the FET.

Fig. 9 Simple audio siren. An astable oscillator provides drive to switch the VMOS FET on and off at an audible rate.
in turn by a 4017 decade counter. The fourth output of the 4017 is connected to the reset pin, giving a 1-2-3-4 count to control the VMOS FETs. As each FET is enabled by the 4017 counter the audio input at its drain is connected, via the source and a 10 k resistor, to the op amp.

If TTL logic is used to control VMOS FETs, a gate pull-up resistor must be inserted (Fig. 8) to ensure that the gate voltage is pulled up to +5 V when on - sufficient to give about 500 mA of current through the transistor. Figure 8 also shows the principle of VMOS current control through a load, in this case an indicator lamp. The load can, however, be virtually anything requiring current, eg relays, LEDs or loudspeakers.

Figure 9 shows the circuit of a simple siren using an astable (formed by CMOS gates), a VMOS FET and a loudspeaker. When the transistor is on, its drain to source resistance is about 3R0 so about 1 A (ie $V / R=11 / 11$ ) passes through the loudspeaker. The average current (assuming a $50 \%$ duty cycle from the astable) is therefore about 500 mA . Audio output power is thus about 2 W .

Paralleling two or more VMOS FETs in an output stage easily increases current-handling capacity. The siren circuit of Fig. 9 is redrawn in Fig. 10 with four paralleled output transistors. This more powerful siren will produce an output power in the region of 6 W . You can see that no ballasting resistors are needed (as you would require with a similar circuit using bipolar transistors) because of the positive temperature coefficient of the drain-to-source 'on' voltage. The explanation of parallel operation is very simple: if any one of the VMOS transistors begins to conduct a larger than average current it will tend to get warmer and so current flow will reduce.

## Linear Applications

So far we've only considered switching applications using VMOS FETs (ie on or off), but they can just as easily be operated in a linear mode (to act as voltage controlled resistors) in the same way as ordinary FETs.

Linear regulators in power supplies are easily constructed:


Fig. 10 Paralleling output VMOS FETs can be done simply because they are inherently thermally stable.

## FEATURE : Designer's Notebook

such a circuit is shown in Fig. 11. An op-amp compares the output voltage with a reference voltage derived from a zener diode and parallel variable resistance. The reference voltage is thus variable from 0 V to about 11 V . If the output voltage is less than the reference voltage, the op-amp increases the drive voltage to the VMOS FET, and vice versa, in a negative-feedback controlled loop.

Constant-current sources suitable for charging Nicad cells can be made easily using VMOS FETs, and a simple unregulated circuit is shown in Fig. 12. The current output is defined primarily by the gate voltage of the transistor by altering the ratio of the two resistors R1 and R2. By varying the gate voltage between 0 V and 5 V , a range of currents of approximately $0-250 \mathrm{~mA}$ will be obtained. Although the high output impedance of the transistor (relative to that of a bipolar) provides a level of current regula-


Fig. 11 A VMOS FET used in a linear voltage regulator. An op-amp is used in a negative feedback loop to provide the controlling gate voltage for the VMOS FET.

Fig. 12 Unregulated constant current source formed around a VMOS FET.


Fig. 13 Transistor Q2 holds the gate-to-source potential of the VMOS FET constant for any load. The current is therefore constant.

Fig. 14 A simple class $\mathbf{A}$ power amplifier.
tion, differing loads will produce differences in current flow.
The circuit of Fig. 13 overcomes this problem with a negative feedback loop formed by Q2. This transistor holds the gate-to-source potential of the VMOS FET constant for any load. Thus the current flow is constant whatever the load.

A Class A power amplifier can be constructed with a VMOS transistor and because of the inherent thermal stability of the FET, very few precautions need be taken with the circuit (Fig. 14). The high transistor input resistance allows very high value biasing resistors. Although obviously an audio power amplifier (the transistor load is a loudspeaker!) the circuit itself will operate up to the megahertz regions.

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## PORTABLE POWER?

After years of writing books on computers and awarding his famous 'White Elephants to the industry, Adam Osborne finally took the ultimate step and produced the Osborne I. Despite tairly reasonably priced well . . almost, and the main attraction seems as hardware goes, he get several hundred to be the fact that you gere thrown in tor free. pounds' worth of sottwa system up hill and down dale and generally put it through its paces, our reviewer will hopefully have recovered sufficiently to put pen to paper and report on his findings.

## SIMULATING FORTH

(4) The interest sparked off by our recent series on FORTH has been so great that even we were taken by surprise. As a result, novel month we'll be publishing F operations so BASIC simulator for Fould like to try out the those of you who would actually having to buy language - without actistem - can have a a version of it won't break any speed go. It certainly wont bextremely clow, but it records, in tact it is extremely with Reverse does allow you
Polish Notation

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## our next issue! <br> ARE YOU SECURE?

Or, to be more precise, are your programs? In our next issue we'll be taking a look at a rather clever method of program protection which actually allows your programs on any copied but then won't let them rue extreme! other system... frustrating in extremely We will also be publishing which guarantees ingenious Voting Loader which guaranost any perfect loading from cassettes at mind and speed. So, for your own peams and data, the security of your programs of our June make sure you secure issue.

Articles described here are in an advanced state of preparation but circumstances final contents.

# STYLUS TIMER 

Do you play your records with a smoothly-contoured, precisionengineered, highly-polished stylus - or a worn-out nail? Check your playing hours with the ETI Stylus Timer. Design and development by Phil Walker.


For modern styli and cartridge combinations the life of the stylus may run to many hundreds or even thousands of hours before replacement is necessary. The trouble is that even at five hours each and every day (which is quite a lot) it will take over six months to accumulate 1000 hours playing time. If you are like us you could easily forget whether you changed the old nail last week or last year, quite apart from knowing how long it has been used since then.

Don't worry, help is at hand - this device is designed to measure the total number of hours your stylus has been in use since you last changed it and give some indication of that measurement.

The device has six LEDs which, in the basic configuration, change every 200 hours. This could be used to indicate that a check on stylus condition should be carried out either at home or by your local dealer. When the last one comes on it will stay on until the device is reset (assuming the power is on).

As mentioned above, the basic
design allows for 200 hours per step, the last one occurring after 1000 hours. This can be modified to $400,500,1000$ or even 2000 hours per step giving replacement times of up to 10,000 hours for the very lightest equipment (or Scrooges), or 100 hours per step if your equipment is a little heavier than some or you want to keep your stylus in tip-top condition all the while.

In order to eliminate dependence on mains supplies when the equipment is not in use, the device contains a rechargeable battery which provides the microamp or so needed to keep the CMOS devices active. Also the LED display is turned off when not required to conserve battery power. To prevent accidents the reset facility is disabled when the device is on standby.

## Designs Discussed

The circuit uses standard CMOS integrated circuits for most functions in order to keep the standby power as small as possible. This enables us to use a PP3-sized rechargeable Ni-cad battery, ensuring that with intermittent use the device should operate almost
indefinitely. (In fact a normal dry-cell PP3 battery will give a very long life but may not like the charging current flowing into it via R7).

The power for the LED display and the timing signal for the logic are taken from the AC input. This is any 50 Hz voltage source giving between 12 and 20 V at about 50 mA . For preference this supply should be switched with the turntable or equipment mains supply.

There was much discussion in the office about the actual method of detecting stylus use. The first method we considered involved detecting the presence of a music signal from the pickup, in a similar manner to the Watchdog auto-switch-off project we published in October '77. However, if the signal was tapped off after the RIAA preamp we realised the project couldn't be built by readers who lacked the confidence to muck about inside their expensive commercial hifi. On the other hand, putting the project between the deck and the preamp would lead to the knotty design problem of not degrading the pickup performance. Thus we opted for a


Fig. 1 Circuit diagram of the ETI Stylus Timer. The various lettered links are used to select the timing period (see text).

## HOW IT WORKS

The 50 Hz power input is rectified by $B R 1$ and charges C2 via D1. Q2 and R7 form a simple voltage regulator using the battery $B 1$ as a reference. If there is no $A C$ input then D6 isolates the rectifier circuitry from Q2 and B1 supplies the very small bias current needed to keep the CMOS devices active via the base-emitter junction of Q2.

R4, D3 and ZD1 form a moderately stable voltage for the optical sensor and an input to the power detection circuitry IC1b. The output from the optical sensor (LED1 and Q1) is taken via $R 5$ and $R 6$ to IC1e either directly or via IC1c. This allows the circuit to operate with either an open or blocked light path as required.

Depending on the position of link A-B or A-C a 50 or 100 Hz signal will be applied to IC1f. The voltage of this signal is limited by R1, R2, D2 and ZD1 to prevent damage to IC1.

IC2 is connected such that it divides by $\mathbf{2 2 0}$ or $\mathbf{2 1 9}$ as determined by the input from IC1d. This is accomplished by the device loading its internal eight bit counter with the binary number on its inputs each time it reaches a count of zero. In this case the most significant seven bits are wired to $1101101 \mathrm{X}=218_{10}$ while the least significant bit ( $X$ ) is switched between 0 and 1. The output from this stage drives IC3, a dual decade divider. The $Q_{4}$ output from IC3a controls the division ration of IC2 as
outlined above. As the $Q_{4}$ output is only high for two clock periods out of 10 the effective division ratio of IC2 is:-
$8 / 10 \times 220+2 / 10 \times 219=219.8$
IC3b is used to divids by 2 in the standard circuit and then drives IC4 which does the rest of the division required (a factor of $2^{14}$ or 16384) to give a one cycle in 200 hours signal.

IC5 is a decade counter with 10 decoded outputs. Each output is high for one clock period of the 200 hour input signal (or longer if counting is suspended). Only the first six outputs are used to drive IC6 and the sixth output also inhibits IC3b to prevent further counting. IC6 is a hex buffer with three-state outputs which have a fairly high current sink capability and can be forced to a high impedence state by a signal on pin 4. This facility is used to prevent the LED display taking current while the AC supply is off.

The reset switch PB1 is connected in an unusual place so that it can only pull the input to IC1a low when the power sense circuit indicates that the AC supply is present. When operated, the reset circuit applies a high logic level to the reset inputs of ICs 3, 4 and 5 for about a second. IC2 is not reset and will cause an error of two or four seconds in the timing but in a hundred hours or so this is not significant.
minutes in 1000 hours.
The final counter (IC5) has 10 decoded outputs of which only the first six are used. These control IC6 and thus the display. When the sixth output of IC5 goes high it disables the counter chain causing the sixth LED to remain on indefinitely.

IC6 contains six inverting buffers which have three-state outputs. This facility is used to switch off the LEDs and conserve power.

A transistor (Q2) was used in the standby battery circuit so that when operating from the $A C$ input, the supply voltage to the ICs was little different to that when operating from battery alone. Also the LED voltage can be stabilised at the correct value. The configuration allows the battery to be trickle-charged from the same supply.



Fig. 2 Constructional details of our sensor. Using different sized jack plugs will prevent incorrect connection.
mechanical solution, but adventurous readers may care to adapt this project and the Watchdog circuit for their own needs. Note that we CANNOT give any technical advice if you do try it.

The circuit operates by detecting when the tone arm is away from its rest position and then allowing the rest of the circuit to count at 50 or 100 Hz . The 50 or 100 Hz is divided by about 72 million in order to drive the final counter at one pulse every 200 hours. The already decoded outputs of this device (IC5) are used by the output driver (IC6) to power the display. The division ratio actually used is 72,024 , 064 , this being:-

```
214}\times20\times(1/5\times219+4/5\times220
(IC4) (IC3)
(IC2)
```

The error between this figure and the theoretical $72,000,000$ is $0.033 \%$ or 20

## Construction

The construction of the main unit is straightforward, if a little fiddly on account of its small size. Assemble the components onto the PCB including the three links but excluding LED 2-7. Place the assembled PCB in the bottom of the box and align it over the wider spaced fixing holes with C2 next to the space for the battery. Mark the positions for the LEDs on the front panel and the jack sockets and PB1 on the back panel. Also mark a position for the power cable grommet. Drill all these holes in sizes to fit your components.

Wire up the switch and sockets. The common connection from R4 on the board should go to the sleeve connections on the jack sockets to


PARTS LIST

| Resistors (all $1 / 4 \mathrm{~W}, 5 \%$ ) | BR1 $50 \mathrm{~V}, 1$ A bridge rectifier |
| :---: | :---: |
| R1,2 100k | LED1 TIL32IR transmitter |
| R3 330R | LED2-5 2 mmgreen LED |
| R4 470R | LED6 $\quad 2 \mathrm{~mm}$ yellow LED |
| R5,8 47k | LED7 2 mm red LED |
| R6,9 4M7 | Miscellaneous |
| R7 10k | PB1 subminiature push-button |
| R10-15 680R | B1 8V4 PP3-size Ni-cad (or PP3 |
| Capacitors | - see (ext) |
| C1 10n ceramic | PCB (see $125 \times 65 \times 30 \mathrm{~mm}$ (Vero ref. $75-2682 \mathrm{~A}$ ); |
| C2 $\quad 100 \mathrm{u} 40 \mathrm{~V}$ axial electrolytic | case and 2.5 mm jack plugs and sockets; |
| Semiconductors | three pin DIN plug (if required to connect |
| IC1 40106B | to power unit); thin screened twin cable; |
| IC2 40103B | small grommet; $100 \times 55 \times 6 \mathrm{~mm}$ acrylic |
| IC3 4518B | sheet. |
| IC4 4020B |  |
| IC5 4017B | AC POWER UNIT (IF REQUIRED) |
| IC6 4502B | $12 \mathrm{~V}, 6 \mathrm{VA}$ mains transformer; 100 mA fuse |
| Q1 TIL78 IR receiver | and fuseholder; mains neon; case |
| Q2 BC182L | $125 \times 65 \times 50 \mathrm{~mm}$ (Vero ref. 75.2684 B ); |
| D14 ${ }^{\text {2 }}$ (N4148 | three pin DIN socket; three-core mains |
| ZD1 6V8400 mW zener | cable; grommet; solder tags. |

prevent accidental short circuits via the panel. The LEDs should now have their leads bent so that they will go into the board while the LED body protrudes through the panel. Finally connect the battery connector and the AC power lead. The latter should be a twin core screened cable terminated in a three pin DIN plug or similar to pick up the supply.

## The Sensor

The purpose of the sensor housing is to hold the emitter and receiver in line and exclude some of the ambient light. Our sensor was constructed from an offcut of black Perspex about $90 \times 55 \times 6 \mathrm{~mm}$. This was cut into two pieces ( $36 \times 55$ and $52 \times 55 \mathrm{~mm}$ ). A U-shaped slot was cut out of the longer side of the smaller piece; then a hole was drilled in the thickness of the material in both legs of the $U$ to take the optical devices and hold them in line. The back edge of the $U$ was slotted to take the screened wire from the phototransistor.

Three holes were drilled along the centre line of the other piece of Perspex; two to take wires and one to take a fixing screw. The underside of the base piece was channelled out using a rasp attachment in a hobbyist drill to conceal the wires.

The sensor device is mounted in the top hole to reduce the amount of ambient light reaching it. The screened wire from the phototransistor is run along the slot in the plastic and down the hole in the base. The slot can be filled with resin and painted when finished.

If a small three-way connector can be obtained this could be used in place of the two jacks and a single length of screened cable would suffice to connect the sensor.

## Power supply

The AC power supply is very simple and consists of a small mains transformer, fuse, neon indicator and three pin DIN socket mounted in a small box. Construction is very straightforward and, if the specified box is used, most small $6 \mathrm{~V} \cdot \mathrm{~A}$ transformers will fit onto the moulded pillars in the box, obviating the need for external screws.

## Use and Modifications

To use the stylus timer, the sensor should be positioned so that the tone arm interrupts the light beam when it is in the rest position. Make sure that it does not foul the arm at any time if you have any sort of automatic control.

If possible the AC power supply for the device should be obtained from your system. Anything from 12 to 15 V $A C$ may be used without modification.

## PROJECT : Stylus Timer

Up to 25 V may be used but R4 and R7 should then be 1 k 01 W and 27 k respectively. If the supply is greater than 25 V , one side of any available supply is earthed or if you prefer not to tamper with your system then use the simple mains power supply described.

With the sensor in position and a suitable $A C$ supply connected, press the reset button on the unit. The first green LED should light and stay alight for 200 hours of playing time, followed by the next LED until the red LED lights to indicate replacement overdue. If the power supply is switched off at any time the accumulated time is stored until the power is restored.

Other time intervals can be used in the device by changing the link positions. Changing A-B to A-C doubles the time period. Changing $\mathrm{G}-\mathrm{J}$ to $\mathrm{C}-\mathrm{K}$ or G-L increases the interval by $21 / 2$ or 5 respectively while changing it to $\mathrm{C}-\mathrm{H}$ halves it, although it will not stop on the last count as before. The link adjustments therefore give a range of 100 to 2000 hours per LED on the display - further reductions could be made by using pin 2 or 1 of IC 4 as output instead of pin 3 , giving $1 / 2$ or $1 / 4$ of the period. In some of these other positions the intervals between the lighting of each LED may not be as


Inside the Stylus Timer. Take care during construction as things are a little cramped.
regular as before (especially G-K). If it is desired to have counting enabled when the light path is obstructed then link D-F should be changed to D-E. We would like to thank Sonic Sound Audio, who live under our offices and kindly dismantled a rack hi-fi system so we could take our lead photograph.

## BUYLINES

None of the electronic bits and pieces should present any problems; try any of the component companies advertising in this issue. The PCB can be obtained from our PCB Service using the order form on page 82; for the Perspex you can try plastics suppliers, arts and crafts shops or DIY emporia.

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# 150WMOSFET AMPLIFER 

# Employing MOSFETs, this power amplifier features a 'no compromise' design from Dave Tilbrook and is rated to deliver 150 W RMS maximum; it features extremely low harmonic, transient and intermodulation distortion. This is achieved by overcoming the many basic problems encountered in the use of MOSFETs for audio amplification. 

The objective of this project is to provide a power amplifier module of the highest possible performance. Ideally the power amp should produce an amplified version of its input signal and contribute no sound of its own. In order to design a practical amplifier that will come as close as possible to this ideal, it is necessary to 'define' limits on the input signal characteristic and then ensure that the power amp exceeds these limits.

The problem of amplitude overload cannot be eliminated, since no practical power amplifier has access to infinite supply voltage. In order to overcome this problem, the ETI-5000 module has been designed to handle in excess of $\pm 50 \mathrm{~V}$ rails, giving it a conservative power rating of 100 W RMS into 8 ohms. The output stage has been designed so that the MOSFETs will not operate outside their safe operating area on any load in which the effective series resistance does not drop excessively below 8 ohms. Increasing the supply rails will increase the audio power output (up to 150 W RMS max.) but for normal use, we recommend sticking to $\pm 55 \mathrm{~V}$.

Similarly, since no power amp has an infinite slew rate or infinite frequency response, the input signal has been limited by a passive input filter. It can be easily demonstrated by experiment that the introduction of a passive filter that does not excessively affect the frequency response within the audio passband will not affect the sound of the input signal. This filter will define a maximum possible input slope. It is therefore only necessary to design the amplifier with a slew rate that exceeds this by a sufficient margin to ensure freedom from slew-induced distortion. Since the amplifier is operated below its slew rate limit, the

application of negative feedback will decrease distortion produced as a result of the signal slope approaching the slew rate (TIM).

## Pair Difference

Differential pairs have been used throughout the design to form not only the input stage but also the voltage gain stage. This ensures that the distortion characteristics of the input and voltage gain stages are low enough so that the open loop characteristics of the amplifier will be determined by the output stage. The improved frequency and phase linearity of the differential pair make it considerably easier to ensure that the amplifier meets the

Nyquist stability criterion. Another advantage of the differential pair is its relatively high supply rejection, a parameter which is often not given sufficient attention in power amp design.

Careful control of the feedback loop and the use of a passive filter/load on the output of the module, coupled with the design points mentioned above, have yielded an amplifier with particularly low dynamic distortion characteristics. An amplifier that has been designed with these objectives in mind will automatically have low THD and TID figures. The ETI-5000 is no exception, with a THD at 1 kHz and 10 W RMS of less than $0.001 \%$, rising


A general view showing the internal layout of the amplifier. Toroidal transformers can be used instead.

## SPECIFICATIONS

## Power output

100 W.RMS into 8 ohms
( $\pm 55 \mathrm{~V}$ supply)
(up to 150 W with suitable PSU)

## Frequency response

8 Hz to $20 \mathrm{kHz},+0-0.4 \mathrm{~dB}$
2.8 Hz to $65 \mathrm{kHz},+0-3 \mathrm{~dB}$

NOTE: These figures are determined solely by passive filters.

Input sensitivity
1 V RMS for 100 W output
Hum
-100 dB below full output (flat)

## Noise

-116 dB below full output (full, 20 kHz bandwidth)

## 2nd harmonic distortion

$<0.001 \%$ at 1 kHz
( $0.0007 \%$ on prototypes)
at 100 W output using a
$\pm 56 \mathrm{~V}$ supply rated at 4 A
continuous
$<0.003 \%$ at 10 kHz and 100 W
3rd harmonic distortion
$<0.0003 \%$ for all frequencies less than 10 kHz and all powers below clipping.

Total harmonic distortion
Determined by 2nd harmonic distortion (see above).

Intermodulation distortion
$<0.003 \%$ at 100 W
( 50 Hz and 7 kHz mixed 4:1)
Stability
Unconditional
slightly to around $0.003 \%$ at 10 kHz (top end distortion figures are a function of bias current). It should be remembered, however, that obtaining low THD figures should not be the prime objective of a good power amplifier design, but results from the reduction of dynamic distortion mechanisms.

## Tested And Trying?

The module has been tested exhaustively and all prototypes have performed with negligible differences.

When attempting to measure distortion figures as low as these, great care must be taken with the earthing arrangement to the test equipment. The
amplifier module will give its lowest distortion figures only when measured with respect to the correct earth. It may be necessary to remove the connection between mains earth and signal earth inside some distortion analysers. This problem will not arise when the amplifier is connected to a loudspeaker. This condition is not unique to the 5000 module, but will occur whenever an alternative earth path is provided to the output signal earth.

The sound is clean with no sign of the aggressive high frequency performance common to many transistor amplifiers. There are some amplifiers that give the subjective


Fig. 1 The basic voltage gain stage of the ETI-5000 MOSFET amplifier.
impression of being 'over smooth', ie the amplifier on first listening sounds clean and unobtrusive. Further listening test reveals, however, that these amplifiers lack detail, and complex sounds like a symphony orchestra tend to beome a single mass of sound rather than being rendered as single instruments. The ETI-5000 does not suffer from this problem!

## Carefully Does It

Particular care has been taken to minimise slew-rate limiting and harmonic distortions. An inspection of the 'How It Works' section will reveal the techniques employed.

Follow the suggested constructional method and no problems will be encountered.

## Construction - Module

The construction of the power amp module is not difficult since all the components are mounted on a single board. Since the design employs a fairly large amount of negative feedback, the board pattern is a critical factor in attaining the maximum theoretical performance. It would be virtually impossible to achieve the same performance if the board pattern were altered, without recourse to a distortion analyser with a sensitivity of at least $0.005 \%$ and a very good spectrum analyser. The board pattern shown ensures freedom from earth path interaction and therefore does not degrade the distortion performance of the design.

Commence construction by soldering all the resistors onto the circuit board. The OR22 ( 0.22 ohm ), 5 W source resistors in the output stage get warm if the amplifier is operated for extended periods at high power.


Fig. 2 Component overlay of the MOSFET amp module.


Fig. 3. This overlay shows the components which are mounted directly on the copper side of the PCB.

They should never get hot enough to burn the circuit board, since any fault capable of causing this much power dissipation should blow the supply fuses first. Nevertheless, it is good construction practice to space these resistors a few millimeters off the surface of the board. The $4.7 \mathrm{ohm}, 1 \mathrm{~W}$ resistor R29 should definitely be spaced off the board since it will overheat if a fault condition should cause oscillation of the amplifier at high frequencies. Do not mount the four 100 ohm resistors R21, R22, R23, R24 at this stage. These are mounted on the rear of the circuit
board and are best left until after the MOSFETs are mounted.

Solder the four fuse clips into the board next. Now mount all of the capacitors, with the exception of $\mathrm{C} 7,8$, 10, and 11. Once again, these mount on the rear of the board. Make sure the electrolytic capacitors C1, C5, C12 and C13 are inserted with the correct orientation as these are polarised components. Mount the 1 N914s and zener diodes, taking care to orient them correctly. Solder the trimpot RV1 into place and then the small-signal transistors, Q1, Q2 and Q13.

Next step is to mount the six voltage amp transistors, Q3 through Q8. These are situated on the board in two parallel rows, each row with three transistors. In the prototype modules, the heatsinks were constructed from two pieces of aluminium, as can be seen from the photographs. The transistors are mounted using 6BA bolts, each passing through a pair of transistors. This forms a very strong assembly which can then be soldered onto the board. Insulating mica or plastic washers should be used between the metal side of the
trasnsistors and the heatsink strip, using a small quantity of heatsink compound between each mating surface. When this transistor-heatsink assembly is completed, but before soldering it into the circuit board, check that each transistor is effectively insulated from

PARTS LIST

| Resistors (all $1 / 2 \mathrm{~W}, 5 \%$ ) |  |
| :--- | :--- |
| R1 | 470k |
| R2,11 | 47k |
| R3 | 10R |
| R4,16 | 1k0 |
| R5 | 4k7 |
| R6,20 | 47R |
| R7,10,13,18 | 270R |
| R8 | 10k |
| R9,14,19 | 22k |
| R12 | 3k3 |
| R15,21-24 | 100R |
| R17 | 39R |
| R25-28 | 0R22, 5 W |
| R29 | 4R7,1 $W$ |
| R30 | 1R0,1 $W$ |
| R31 | 150R |

Potentiometer
RV1 250R vertical trimpot
Capacitors
C1 $470 \mathrm{n}, 25 \mathrm{~V}$ PCB electrolytic C2 $\quad$ nopolyester
C3 330p ceramic or mica
C4,7,8 $\quad$ 470p ceramic or mica
C5 $\quad 100 \mathrm{u} 25 \mathrm{~V}$ PCB electrolytic
C6 10p ceramic or mica
C9 220n polyester
C10,11 47n polyester
C12,13 100u $63 \vee$ PCB electrolytic
C14 220p ceramic or mica

| Semiconductors |  |  |
| :--- | :--- | :---: |
| Q1,2,13 | BC550 |  |
| Q3,4,7,8 | BF470 |  |
| Q5,6 | BF469 |  |
| Q9,10 | 2SK134 |  |
| Q11,12 | 2SJ49 |  |
| D1,2,3,4 | 1N914 or similar |  |
| ZD1,ZD2 | 12V,400 mW zener |  |

## Miscellaneous

PCB; four fuse clips; two 3 A fuses; one plastic bobbin or similar former, 15 mm diameter; one metre of 0.8 mm dia. enamelled copper wire; two strips of 20 g aluminium,each 15 mm wide by 47 mm long (for voltage amp heatsink); heatsinks, case to suit.

Semiconductors
BR1 $200 \mathrm{~V}, 35$ A bridge rectifier
Capacitors
C1,2,3,4 10,000 uF, $80 \vee$ can
electrolytics
C5,6,7,8 100n polyester
C9 470n polyester
Transformers
T1,T2 two $\times 35 \mathrm{~V}$ secondaries

## Miscellaneous

SW1. . . illuminated rocker switch, 240 V AC rated; 1 off $2 A$ fuse and fuseholder, 1 off 3-pin DIN socket; 2 off 2 -way plastic terminal blocks; 2 off phono sockets; 2 off red and 2 off black heavy duty screw terminals; clamp grommet and sundry rubber grommets; hookup wire; nuts, bolts etc; heatsink/front panel, metalwork etc.
the heatsink. Using a multimeter on the resistance range, check for shorts between the centre lead (collector) of each transistor and the heatsink strip. Note that the bolts through the six transistors are automatically insulated from the metal rear of the transistor by the plastic body of the device so no additional insulation of the bolts should be necessary.

Before mounting the MOSFET output devices it is necessary to make the heatsink bracket. This is cut from a suitable aluminium extrusion. The board has been designed to suit extrusions with one of the sides at least 40 mm wide. The transistor mounting holes have been placed so that the heatsink brackets used in the ETI 300 W modules (April '80) are compatible, although there will be some unused holes.

The output assembly should now be checked for shorts. Remove the earthing bolt first (see overlay). The resistance between the case of each MOSFET and the bracket should be checked with a multimeter. If any device shows a short to the bracket it should be disassembled and the short found. Usually it is necessary to replace the TO-3 insulating washer as most faults of this type are the result of small metal burrs cutting through the washer when mounting the device.

Once the MOSFETs are mounted, the last passive components resistors R21, R22, R23 and R24 plus capacitors C7, C8, C10 and C11 can be mounted on the rear of the circuit board. These are positioned on the rear of the board so that lead length is kept as short as possible. Cut the leads just short enough to mount the components in place.

## Set-up Procedure

The recommended supply voltage for the modules is around $\pm 55 \mathrm{~V}$. With this voltage and reasonable supply regulation, the module will deliver around 100 W RMS into a nominal 8 ohm load.

First, re-check that the output devices are not shorted to the heatsink bracket. This is best done with the earthing bolt removed as mentioned earlier. If no shorts are found, replace the earthing bolt.

Do the same check for shorts between the six voltage amp transistor collectors and their heatsinks.

Check the polarity of all polarised components. It is often difficult to tell one end from the other on diodes since the markings are easily rubbed off. If in doubt, check these with a multimeter. Wind the wiper of the trimpot RV1 fully counterclockwise (least resistance). This ensures no bias is applied to the output stage. Now, remove the fuses from the board if they have been fitted and replace them with 10 ohm, $1 / 2$ W resistors.

The module can now be connected to a power supply.

Make sure that the power supply connections are sound, with good solder joints. If you have access to a current limited bench supply it is best to connect the module to this for the set-up and initial test. If you can do this, set the current limit to around 200 mA . Do not connect a load to the output of the module at this stage.

If the power is now turned on, the current through the two 10 ohm resistors replacing the fuses should be low. If these resistors start to smoke, this indicates a fault condition - turn the power off immediately.


Fig. 4 This graph shows the measured distortion versus frequency for two values of quiescent current in the output stage.

Fig. 5 The measured frequency response of the amplifier (single module). Roll-off points are defined by the input filter (low end) and output compensation network (high end).

the OV6 drop of Q3 and Q4, will cause a voltage drop of 1 V 2 across resistor R15, causing a current of 12 mA to flow in this resistor. This current is shared by Q3 and Q4 and causes a i 7 drop across resistors $R 7$ and
R18. Once again, the effective input impedance of Q5 and Q6 is in R18. Once again, the effective input impedance of Q5 and Q6 is in
parallel with the resistors, but in this case the value of $R 7$ and $R 18$ is very paraliel with the resistors, but in this case the value of R 7 and $R 18$ is very
much lower than the base impedances of Q5 and Q 6 , so this effect can be ignored in a DC analysis like this. The voltage across R7 and R18 minus the OV6 drop of Q5 and Q6 will cause $1 \mathrm{V3}$ to be dropped across 32 mA
through the resistor. Again Q5 and Q6 form a differential pair, and this through the resistor. Again Q5 and Q6 form a differential pair, and this
current is shared equally by the two transistors. The load for these devices is formed by a current mirror, Q7-Q8, that ensures the current through Q5 and Q6 will remain the same. Transistors Q4 and Q5 therefore form the main voltage gain section of the amplifier and have a typical emitterit when the output stage quiescent current has been set. Diodes D3, D4 and zeners ZD1, ZD2 protect the MOSFET output
devices from being overdriven, as described in the text. The RC-RL netdevices from being overdriven, as described in the text. The RC-RL net-
work on the output ensures that the amplifier has a correct load at all frework on the output ensures that the amplifier has a correct load al all wise result.

This very brief DC analysis of the circuit is intended to help the constructor rationalise the voltages quoted on the circuit diagram. The voitages totypes, and slight deviations from these should be expected. The input signal is fed to three RC filter sections formed by C1/R2,
R4/C2 and R5/C3. The C1/R2 fitter defines the low end 3 dB point of the amplifier at around 7 Hz , with an attenuation rate below this frequency of $6 \mathrm{~dB} / \mathrm{octave}$. The two sections R4/C2 and R5/C3 both have 3 dB points of around 30 kHz defining the top end response of the amplifier. This filter
limits the maximum signal slope to less than the slowest stage in the
amplifer. It also provides protection against
Transistor Q13 and the associated components R8, R12 and D1, D2 formaconstant current source (or sink), maintaining a final DC current through the differential pair Q1, Q2 of around 240 mA . Under no-signal
conditions this current is shared equally between Q1 and Q2. The conditions of current is shared equaly between Q1 and $Q 2$. The resistances of the transistors Q3, Q4, decreasing the effective load resistances of the differential pair to around 15k. The voltage drop across
R9 and R14, should therefore be 1 V 8 approximately. This voltage, minus

## HOW IT WORKS

devices，since the resistance of only several
centimetres of track will greatly decrese
their effectiveness．
In the 5000 these capacitors are
In the 5000 these capacitors are
mounted on the rear of the circuit board with one lead soldered directly to the drains
The gate resistors R21，R22，R23 and R24
 board，again soldered directly to the

 the bias preset RV1．The maximum gate to
source voltage of these MOSFETs is 14 V ；if this voltage is exceeded the MOSFET can be













 RV1；this acts as the bias voltage for the out－
 ple can never go more than 0V6 above the
output voltage，due to the 0V6 forward dosp ә8ер！ол әч！әכu！S zaZ jo dosp әsełjo＾




sənb！uчวə⿰ วsəчł If fo uo！peu！quos aч1


 overall feedback loop．

The most common method employed to
achieve this is to increase the value of the emitter resistors in the input stage．This reduces the voltage gain of the input stage， bandwidth．Consequently，in a power amplifier employing MOSFETs in the out－
put stage，the relatively small amount of
 the output devices，and the result is an
 gate－source capacitance is cured by the ad－
dition of the capacitors $C 7$ and $C 8$ shown in dition of the capacitors C7 and C8 shown in in the circuit and with the 100R resistors
R21，R22，R23 and R24，the only other com－ ponent required to ensure total stability of the output stage is the power R LC combina－
tion formed by R29，C 9，R30，L1 and the sup－

 stage will virtually see a short circuit since
the high frequency（ $>100 \mathrm{kHz}$ ）fourier
 The inductor 11 ensures that this does not happen by inserting a reactance that in－ R30 is placed in parallel with the induc－


The other two components of this net－



 The 220 nF capacitor is necessary since
 әч！u！uo！jed！ss！p ләмоd $48!4$ ач！to pueq

әp！noid $8 z y$ pue $\angle Z y$＇9zy＇szy siopsisay
 pue sa！！！due ayi fo כ！s！sajseseys sajsued


The final components needed to ensure
 Capacitors C12 and C13 are 100 uF elec－
trolytics that provide general supply
 where the MO：FET would tend to oscillate． For this reason capacitors C10 and C11
have been included also．These capacitors
must be positioned very near the output
the voltage on the bottoms of R5 and R6 will
be almost identical．If these two resistors
are made the same value，the current be made the same value，the current
are meath each resistor，and therefore the through each resistor，and here the the collectors of OB and $\mathrm{O4}$ will
current be identical．This ensures the Q3，Q4 pair
will operate symmetrically，even when a single－ended load is attached to the pair． Furthermore，since the collector of Q3
is connected directly to the base of O6，the is connected directly to the base of Q ，the
transistors $\mathrm{Q6}$ and $\mathrm{Q4}$ combine to form a push－pilt parr wh hery high gain in iont per－ In order to achieve good transient per－
formance when drivng the slightly capacsive toad of the outpul stage int
necessary to run a fairly large amount of necessary to tun a dirly lare amounh of
current through this stage，especially the final differential pair and the associated
current mirror．In the 5000 there is approx－ imately 16 mA through these transistors， and the average power dissipation is
thereforearound 0.8 W ．

These transistors will run fairly hot，ap－ proximately $60^{\circ}$ on the sman el ansink
shown，but the transistors are well inside their maximum ratings．The result is a
voltage amplifier stage of exceptional vorage amply very high gain．Coupled with a well－designed input differential amplifier
and a good ouput stage，the phase linearity and d good ouphts stage，the phase linearity
produced by this voltages stage is excellent， stability of the amplifier．
Since the output of the voltage gain
stage has been designed to have sufficiently
 two MOSFETs in parallel，the output stage
consists simply of the MOSFETS themselves．If a preset pot is inserted bet－ ween the collectors of of $\begin{aligned} & \text { and } \\ & \text { voltage amplifies stage of } i \text { ．} 1 \text { the voltage }\end{aligned}$ across this presel can be used as the bias
voltage for the output stage．This is shown

The gates of the output divices are con－ nected to either side of the presel via
resistors R21，R22，R23 and R24．As men－ tioned earlier，these resistors increase the time constant associated with the MOSEET
gate capacitance，reducing the frequency response of the output stage slightly but en－ Both N－channel and P－Channel
MOSFETs are used in this project．The im－ $\dot{d}=$
3
0
0
0令
年 han the negative feedback is reduced，and accept the consequent increase in distor－
tion．

When designing amplifiers intended，as this proiect is，or extremely low distortion，is designed for as low a distortion as possible． When the negative feedback is applied，tru－ E the buk of the open loop gain is provided
by the main voltage gain stage．The dif． ference amplifier will generally provide
some voltage gain but its main objective is 5
3
0 respect to its two inputs．In some power
amps the output stage is a common drain or source follower MOSFET desisn and conse－
quently has a voltage gain slightly less than E An analysis of the distortion
characteristics of the differential pair
reveal

 distortion in a bipolar transistor is due ex－
clusively to the exponential relationship
 voltage，the distortion generated by a dif－
ferential pair and a single transistor can be calculated by techniques of fourier analysis．

As well as having low distortion itself，a
differential voltage emplifier will enable
both outputs of the input differential pair


 amplifier，a problem that would otherwise
lead to increased distortion in the first

Figure 1 is a circuit diagram of the



 pair，however，must be converted into a single－ended stage suitable for driving the
output stage．This is the iob of the current
 shorted and therefore acts like a diode，but with the same characteristics as the base－
emitter junction of Q6．The bases of Q5 and emitter junction of Q6．The bases of Q5 and
Q6 are connected together and therefore 0
0 Q5 forms a cmirror image＇of the base－
emitter junction of 66 ，the vollage drop 0
0
0
0
 transistors are matched．Since the vollage
on the bases and the volatage drop arrosst he on the bases and the voltage drop across the
base－emitter junctions is almost identical，

Fig. 7 Interwiring diagram for the complete (stereo) amplifier.
NOTE THAT THERE IS NO DIRECT
CONNECTION BETWEEN CHASSIS AND OV
full power this will drop to around $\pm 50 \mathrm{~V}$. With a 10 V drop across the output devices the peak signal voltage before clipping is around 40 V , which gives 100 W into a eight ohm load. In reality, the voltage drop across the
MOSFETs is not as high as this since MOSFETs is not as high as this since
the module uses two devices in
parallel. The maximum output power of the prototype unit using the power supply shown was 112 W single channel and 105 W both channels driven (RMS).


Getting lt Coing if
Having satisfied yourself that all is
well, remove the fuses on
 your breath ... and switch on.
aınseəu 'ınכวo sıefsesip ou su!unss $\forall$
 up your modules then you can replace up your modules then you can replace listening tests. Before replacing the fuses allow sufficient time for the electrolytic capacitors to discharge. This will take several minutes.
 up procedure, your amplifier is ready for listening tests.

The top and bottom covers can be screwed in place once you've conformed all is well. We . . for these items as steel plates will react
 produce quite a loud hum

## O 1

 input signal earth line. The best way do thisen the chassis of the power amp人jddns samod әyl uo lu! od 10 วч7 pue In this way the power amp still has a valid earth reference at its input but the possibility of a hum loop is
 an effective shield to external electrical aq ueכ wәjqoad s! 47 Inq 'sajınos asiou
 chassis to the 0 V track at selected
places in the power amplifier. The

 effective open circuit to prevent the pəu!ןno əınрәวoud su!чนеә ә૫1

 numerous other power amps, and

 by the earthing configuration in the
 Lead Astray
lap to standard shielded cable for this application. The input wiring must be kept away from the 240 V wiring at the rear of the power switch. has to be AC-coupled to the 0 V line on each board for the reasons discussed earlier. This is done by soldering a 100 n capacitor on the rear of each board, immediately beneath R3. The earthing





 between this lug and the 0 V track

Follow the interwiring diagram
carefully and recheck at each stage.

of 10 amp hookup cable and the connection for the input earth is done at the input sockets. This is shown in
the circuit diagram.
. The 10 amp hook-up cable is used instead of the more usual shielded cable, since in this application the lower resistance of the hookup cable
results in better hum rejection.
告 fact that both the power amplifier and the preamplifier used to drive it must
be connected to the same chassis ground point via their power cables. If
 power amp are connected to the 0 V point on their respective power
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3

 amp, a closed circuit is formed. Any


 point, down the shielded cable at the power amp input to the 0 V point in the preamp and via the preamp chassis around the loop again. The presence of input earth will be seen as an input by
 The cure is to open-circuit this loop so
that hum current cannot flow in the

Interwiring
By far the biggest problem in the design and construction of any
desplifier is that of earthing. If
maximum performance is to be obtained from the ETI-5000 modules great care must be taken to ensure complete isolation of high current
earths from low current ones such as
 the large currents flowing in the speaker return earths, for example, will interact with the input and distortion
 from the electrol current signal earth amplifier will have degraded hum figures and may even be unstable. The board layout has been designed to overcome these problems through the use of a singlepoint earthing arrangement. Earth lines from the output devices and power earth
lines from the on-board electrolytic capacitors are kept separate until they reach the $0 V$ point on the circuit 뭉

The main input signal earth is the most critical. To overcome any problem the input earth is isolated from the 0 V track on the circuit board by the 10 ohm resistor R3, shown on
the circuit diagram. The input wiring to the circuit diagram. The input wiring to

# READ/WRITE 

Letters for this page should be addressed to Read/Write at our Charing Cross Road address.

Dear Sir,
I am writing to you about two separate matters. First, a friend of mine recently purchased a kit for the ETI System A preamp from lelgate, as recommended in Buylines. The screened cable supplied for the internal wiring was the kind which has a semiconducting layer between the screen and the inner conductor insulation. My friend did not know of such cable and, as a result, his preamp had several strange 'faults.' This could, of course, be blamed on my friend's inexperience, but I think that the same mistake could be made by other constructors. I leave the matter to your judgement, but respectfully suggest that you contact lelgate with the suggestion that they include a note in future kits making the special nature of the cable quite clear.

Second, I refer to the articles on Pickup Amplifier Design (ETI Jan. and Feb. 1982). Mr Tilbrook makes reference in the first article to a resonance which is "a function of both the mass and the compliance of the cantilever and suspension system," which occurs around 10 Hz . As far as I am aware, there is a resonance which occurs around 10 Hz , and it is a function of the cartridge compliance. However, the mass involved in this particular resonance is the mass of the whole cartridge plus the effective mass of the pickup arm (as 'seen' by the stylus tip). This mass is of the order of 5-10 grams. The effective mass of the cartridge cantilever and suspension system will be (at a guess!) 5-100 milligrams. This would lead to a resonance around 100 Hz according to Mr. Tilbrook's equation ( $F=(2 \pi \sqrt{ } \mathrm{mC})$, where $m$ is the mass of the cantilever and suspension system and C is its compliance).

I was also under the impression that the RIAA roll-off point around 50 Hz was defined by a time-constant of 3180 uS, and not 3150 uS, as published. Perhaps this was changed by RIAA at the same time as the 20 Hz roll-off was added?

Signetics data shows the input current of the NE5534 to be less than $2 u A$, typically $0.5 u A$. My own experiments have lead me to believe that the operation of a moving-magnet cartridge is not affected by a current of this order flowing through it. However, in a preamp which must suit all cartridges, a DC blocking capacitor
must be a good idea.
To sum up then, thanks for an article which treats the cartridge preamp very well indeed, despite some small confusion over resonances.
Yours faithfully,
S I Merrick,
Lancashire
David Tilbrook has been contacted about these points and we hope to be running a reply from him very soon.

## Dear Mr Harris,

I advertised in ETI for 18 months but have never received any editorial comment.

We are THE UK specialists in pocket computers. This is a very exciting field which has not been taken seriously. We started by selling the PC-1211 and recently the CASIO FX-702P and at last the new SHARP PC-1500. This really is a fantastic machine. We really look after our customers with hardware software and consumables. Our mailing list has grown and we are selling all over the world.

We have the only range of books on pocket computing in the UK. Our new book "POCKET COMPUTING MADE EASY" has sections on the PC-1211 - FX-720P and the new PC-1500. This is the most comprehensive book available on pocket computing. We are giving it FREE on all orders for the PC-1500. Our 1982 catalogue is being printed and we will send free copies to your readers.

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Give a small dedicated company a break. Mrs Thatcher will be grateful. Yours faithfully,
Barry Elkan,
Elkan Electronics,
28 Bury New Road,
Prestwich,
Manchester M25 8LD

## O.K. You've had a mention. So stop moaning!

Dear Sirs,
May I ask you to consider the following as the basis for a future article.

It is widely known that high sound levels can cause temporary, even
permanent hearing damage. However, although this fact is widely known it is not properly understood by the public. have not seen any 'in-depth' articles on the subject in the magazines that I take.

This seems to be a glaring omission when magazines such as yourselves devote pages and pages to the production of sound in ever-increasing variety and power. I am not
immediately involved in this subejct at the moment, but my colleagues are, and frequently bemoan the lack of understanding they encounter.

Would you consider publishing an article to clearly explain the hearing process and subsequently explain the effects of abnormal sound levels. This could be supported by details of audiometrics, possibly even a circuit (or project) of an audiometer.

When you consider that many of your readers are interested in the beauty of sound, from Bach to The Police, would it not be a great benefit to help them to continue to enjoy their pleasures for as long as possible, by not having those pleasures diluted by unnecessary hearing loss?

I hope you feel that this idea has sufficient merit to result in the printed word being published.
Yours faithfully,
G.M. Francis,

Ipswich
We have published a project for a SPL meter recently, but the point on high levels is well taken and we'll look at the idea for an article in more detail.

Dear Mr. Harris,
I am writing to say how delighted we are with Peter Freebrey's article on building the Wharfedale E7O speakers using our flatpack cabinet kit!

Just a point, though, - the article refers several times to "chipboard". Our cabinet kits are made from MDF board which is much finer grained, smoother and more consistent in texture and density than the highest density chipboard. Being ideal for speaker enclosures, this material is now being used by a number of up-market manufacturers (in spite of being more expensive than chipboard).

We found the April 82 issue of $E T I$ interesting and enjoyable.
Yours sincerely,
R.F.C. Stephens,

Managing Director,
Wilmslow Audio Ltd.
To be honest no-one here had ever heard of MDF before and even now we can't find out what 'MDF' stands for!

Never was any good at woodwork anyhow.

## STERIEO AMPLIFIER KIT


＊Featuring latest SGS／ATES TDA 200610 watt output IC＇s with in built thermal and short circuit protection．
－Mullard Stereo Preamplifier Module．
＂Attractive black vinyl finish cabinet， 9 ＂$\times 8 \% / 4 \times 3 \%$＂ （approx）．
－ $10+10$ Stareo converts to a 20 watt Disco amplifier． To complete you just supply connecting wire and solder． Features include din input sockets for ceramic cartridge． microphone，tape or tuner．Outputs－tape，speakers and headphores．By the press of a button it transforms into a 20 watt mono disco amplifier with twin deck mixing． The kit incorporates a Mullard LP1183 pre－amp module， plus power amp assembly kit and mains power supply． Also features 4 slider level controls，rotary bass and treble controls and 6 push button switches．Silver finish fascia with matching
knobs and contrasting
cabinet．Instructions cabinet．Instruction
available，price 50 p avarable，price 50 p ．
Supplied
FFEE with SPECIFICATIONS： SPECIFICATIONS：
Frequency response Frequency response
input sensitivity

Tone controls
Distortion
Mains supply

## £16－50

$+£ 2.90$ p\＆p． Suitable for 4 to 8 ohm speakers $40 \mathrm{~Hz}-20 \mathrm{KHz}$ P．U． 150 mV ．Aux． 200 mV Mic． 1.5 mV ．
Bass $\pm 12 \mathrm{db} @ 60 \mathrm{H}$ Treble $\pm 12 \mathrm{db} @ 10 \mathrm{KHz}$ Mains supply $\quad 220-250$ volts 50 Hz ． 8＇SPEAKER KIT Two $8^{\prime \prime}$ twin cone domestic speakers．$£ 4.75$ per stereo pair plus $£ 1.70$ p\＆p．when
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## PRAGTICALELEGTRONICS CAR

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 （ $6^{\prime \prime} \times 4^{\prime \prime}$ app．）available as a com．
plete kit．£2．50／pack $+\mathbf{£ 1 . 5 0}$ p\＆p．
£2．00 p\＆p．

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To boost your car radio or radio cassette to 15 W r．m．s．per channel．
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## 125W HICH POWER AMP MODULE

# кіт：£10－50 вицт：$£ 14-25$ <br> <br> $+£ 1.15 p \& p$ <br> <br> $+£ 1.15 p \& p$ £1．15 p\＆p． 

The power amp kit is a module for high power applicat－ ions－disco units，guitar amplifiers，public address systems and even high power domestic systerns．The unit sate in an open circuit condition．A large safety maroin exists by use of generously rated components，result，a high powered rugged unit．The PC board is back printed etched and ready to drill for ease of construction and the aluminium chassis is preformed and ready to use． Supplied with all parts，circuit diagrams and instructions． ACCESSORIES：Suitable mains power supply kit with transformer：$£ 7.50$ plus $£ 3.15$ p\＆p．
Suitable LS coupling electrolytic：$£ 1.00$ plus 25 p p\＆p．


Max．output power（RMS）： 125 W ．
Operating voltage（DC）： 50 ． Operating voltage（DC）： $50-80 \mathrm{max}$
Loads： 4.16 ohms． Loads： 4 ． 16 ohms．
Frequency response measured＠ 100 watts： $25 \mathrm{~Hz} \cdot 20 \mathrm{KHz}$
Sensitivity for 100 watts $400 \mathrm{mV} @ 47 \mathrm{~K}$ ． Sensitivity for 100 watts： $400 \mathrm{mV} @ 47 \mathrm{~K}$ ，
Typical T．H．D．$@ 50$ watts $40 \mathrm{~ms} \cdot 01 \%$ Typical T．H．O．＠ 50 watts， 4 ohms： $01 \%$
Dimensions： $205 \times 90$ and $190 \times 36 \mathrm{~mm}$ ．

## H－FI SPEAKERS <br> ATBARGAIN PRICES

## GOODMANS TWEETERS

8 ohrm soft dome radiator tweet
er（ $31 /{ }^{\prime \prime}$＂sq．）for use in up to 40 W
systems；with 2 element crossove
$£ 3.50$ each（ $\mathbf{p \& p} \mathrm{f} 1$ ）or $\mathbf{£ 5 . 9 5}$ pair（ $\mathbf{p \& p} \mathrm{f}$ ）
35 WATT MICRO 2－WAY SPEAKER SYSTEM Unit comprises one 50w（4＂app．）Audax soft dome tweeter HO100．And one $5^{\text {＂}}$ Audax bass／midrange 35 w driver HIFIIJSM． Complete with 2 eiement crossover． Total impedance of system 4 ohms
£7．95

## PE．STEREO TUNER KIT

This easy to build 3 band stereo AM／FM tuner kit is de－ signed in conjunction with Practical Electronics（July 81
issue）．For ease of construction and alignment it issue）．For ease of construction and alignment it incorp－ FEATURES：VHF，MW，LW Bands，interstation muting and AFC on VHF．Tuning meter．TWo back printed
PCB＇s．Ready made chassis and sale Aerial：AM．－ rod，FM－ 75 or 300 ohms．Stabalised power supply with＇ C ＇core mains transformer．All components supp lied are to P．E．strict specification．Front scale size： $101 / 2$ $\times 21 /{ }^{\prime \prime}$ approx．Complete with diagram and instructions．
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# ELECTROMUSIC TECHNIQUES PaRT 3 

## In the final part of this electronic extravaganza, Tim Orr tidies up the loose ends with details of ADSR and signal input circuits, plus controllers and sequencers.

The ADSR unit has become industry standard for generating envelope contours and for filter sweeps. Curtis produce an entirely voltage controlled device, the CEM3310(Fig.1). The A, D and $R$ inputs have an exponential sensitivity; these time constants will vary an octave for a 18 mV voltage change (or 60 mV per decade) at the input pins of the $I C$. The control voltages are externally attenuated by a resistor pair ( 30 k and 470R). The time constants are controllable over four decades, that is from 2 milliseconds to 20 seconds. The sustain level is variable from $0 \%$ to $100 \%$, again using voltage control ( 0 V to +10 V , linear law). The CEM 3310 is ideally suited to polyphonic systems where one set of ADSR pots will control 5 or 10 independent ADSR units, by use of voltage control. An ADSR unit that employs a separate pot for each parameter is shown in Fig. 2. Time constants from three milliseconds to two seconds are available. This unit can be used in monophonic synthesiser systems.

Fig. 1 (Right) A monolithic ADSR circuit using the Curtis chip. The A, $D$ and $R$ time constants are variable from 2 milliseconds to 20 seconds.
Fig. 2 (Below) An ADSR unit using discrete components (courtesy of Powertran Electronics).




Fig. 5 Typical monophonic synthesiser keyboard unit.

## Signal Input Circuits

Synthesisers are often used as signal processors, and so a variable gain input amplifier is needed (Fig. 3). This circuit has a high level high impedance input, which makes it suitable for guitars, plus a low level impedance input suitable for a low impedance microphone. A fuzz circuit has also been included so that severe harmonic distortion can be introduced to the signal. Another useful device is the envelope follower (Fig. 4). This is a fullwave rectifier followed by a lowpass filter. The envelope is often used to sweep a VCF or a VCO, to produce dramatic effects.

## Controllers

Most synthesisers are controlled from a musical keyboard. The keyboard has to generate and remember a pitch voltage $(+1 \mathrm{~V} / \mathrm{octave})$ plus a gate signal to indicate that a note is being pressed (Fig. 5). The pitch circuit is a stable current source that drives a resistor chain. A potential of 83.3 mV is set up across each 15 ohm resistor. When one note is pressed, the potential of that note is stored in a sample-and-hold unit. When the note is released the potential is retained. If more than one note is pressed, then the lower note is selected.

A sample-and-hold unit cannot retain its information for ever; it droops. A no-droop solution to pitch generation is to use a digital system, as in Fig. 6. The 74C922 is a keyboard encoder; it scans a keyboard and generates a digital code representing the last note pressed. Unfortunately it can only define 16 notes (the 74C923 can define 20) and so must be limited to rather small keyboards. The digital code is converted into a pitch voltage with an eight bit DAC, only using the most significant top four bits. Some keyboards have a dynamic feature, so that the faster you play the note the louder it sounds. This is done by timing the key transition, but it is very difficult to get a natural feel to the keyboard. Some keyboards are pressure sensitive; the harder you press down, the more pitch bend or vibrato you add to the VCO pitch. Other manual controllers such as pitch bend/vibrato wheels and joysticks are also employed (Fig. 7). The wheels are always mounted on the left hand side of the keyboard so that the musician can operate them with his left hand and play the music with his right. The pitch voltage from a

## FEATURE : Electronic Music Part 3



Fig. 6 A digital keyboard, in this case for a 16 -note range.



Fig. 8 Two portamento circuits; one linear (top), the other exponential (bottom).
keyboard is often processed by a portamento unit (Fig. 8). The pitch then glides from its last note to the next. Some people prefer exponential glides, others linear.

## Sequencers

Electronic music often has a background rhythm, which is generated by some sort of sequencer (Fig. 9). A sequencer generates a string of pitch voltages which change on the beat. These are used to control a VCO which is perhaps passed through an ADSR and VCA unit. The result is a rhythmic string of notes. In the circuit, the clock is externally generated, although


Fig. 9 A simple sequencer.
there is no reason why the sequencer should not have its own dedicated oscillator. The sequence length can be modified by connecting the reset line to any output; if you connect it to the ' 8 ' output the sequence length will be 8 . An LED indicates which decoded output is active. The pitch pots have been given a three octave range, which might make tuning rather difficult; $0.1 \%$ of the pot rotation equals 3.6 cents of pitch ( 100 cents $=1$ semitone). Either a smaller pitch range could be used or multiturn pots could be employed, but these are rather expensive. Some sequencers quantise the pitch into semitone steps so that $2.7 \%$ of the pot rotation would equal one semitone. During this $2.7 \%$ rotation, the output pitch remains fixed. This can be done by inserting the pitch voltage into an $A D C$, using the beat signal to start the conversion and taking the DAC output as the quantised pitch output voltage.

## Final Points

A modular synthesiser should have one or two simple audio mixer circuits (Fig.10,). These can be used to mix the final output signal or to send the signal to external treatments such as reverberation units, phasers or chorus units, and electronic or tape echoes.

A synthesiser relies heavily on stable and clean power supply rails. A good rule is never to dump large currents down the ground rail, as this is used as a voltage reference throughout the system. If you are going to turn lamps on and off, run them between the positive and negative rails. Ordinary 78XX and 79XX voltage regulators can be used, but if they get hot then their output voltage will drift and will give the system a long warm-up


Fig. 10 A simple audio mixer with four inputs.
time. A 723 regulator plus a power bypass transistor will give better temperature stability performance. Any hum on the supply rails will frequency modulate all the VCOs.

## Good Books

Musical Applications of Microprocessors - Hayden Book Company.
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# ROBOT MODULES 

## A brace of projects for our mobile this month; a digital PWM interface board for the motor controller so your computer can take your robot for a walk, and an optical sensor so it won't run over your cat/budgie/kids. Here's the first one. Design and development by Rory Holmes.

The digital pulse width modulator is constructed entirely from CMOS logic ICs to provide pulse control of two independent channels. Pulse width modulation can be used to control a variety of analogue functions; this design is offered to allow computer control of ETI's Dual Motor Switching Amplifier, featured in part 1.

Binary counters are used to count both the ON and the OFF periods of the mark-space cycle; thus the duty cycle is independent of the timing circuitry, being an exact ratio of two numbers.

The duty cycle data for either channel is loaded through an eight bit port common to both, and is addressed to a particular channel by the use of two strobe lines. When a strobe line is taken low the input data is latched into the associated control channel, which generates a pulse width corresponding to the input value. The pulse width will remain at this value after the data has changed and until its strobe line is activated again. The minimum strobe pulse required is 50 uS ; thus both motor speeds can be changed in about 100 us .

To suit the standard eight bit data bus of most microprocessors we have allowed seven bits for controlling the pulse width, with a further bit for setting the forward/reverse motor direction. Pulse widths can thus be obtained with a resolution of one part in 128 . With the modulation frequency set at 20 kHz (just above audibility), the pulse width can be increased from zero in 390 nS increments up the maxirnum of 50 uS ; this represents $100 \%$ duty cycle modulation ( $50 \mathrm{uS}=$ one cycle at 20 kHz ).

Timing signals for pulse generation can, of course, be implemented directly with software from the controlling microprocessor; however, this creates large overheads on processor time. Furthermore, any application software must be designed around these timing routines which can soon become a nightmare of interactive real-time programming


This photo shows the digital PWM board mounted above the motor driver board from Part 1 of the series.

Our design strategy has been to dedicate external hardware circuitry to the mundane and repetitive tasks, thus freeing the controlling processor for running the more sophisticated command programs.

Figure 2 shows the circuit diagrams of both channels with the accompanying description. The unit is assembled on a PCB which fits inside the existing motor driver amplifier case and runs off the same 12 V power source. A 15 -way D-type cannon socket mounted on the side of the diecast box provides input for the programming data.

## Construction

The PCB should be assembled as illustrated in the overlay diagram. We recommend soldering in the 14 links first, followed by the resistors and IC sockets. Veropins should be inserted at all the input and output terminals, 17 in all, and finally the capacitors can be soldered in. Before plugging in the ICs, connect a 12 V power source at the supply points shown and check the ground and positive rail voltages at all the IC sockets. If all is well, a short length of 11 way ribbon cable can be wired up to the eight input bits, ground, and the two strobe lines.


Fig. 1 Timing diagram for the digital pulse width modulator.


## HOW IT WORKS

Each of the two control channels operates in an identical fashion with some circuitry common for both. We shall describe the basic circuit action referring to the upper half of the circuit diagram in Figure 2. IC3 is the heart of this circuit, being an eight bit binary down counter with presettable data inputs; it is used to generate the variable pulse widths. The eight data bits presented at the preset inputs 10 to 17 are loaded into the counter when the synchronous load input on pin 9 is pulsed to logic low. If a clock is now fed to pin 7 the counter will start counting down to zero from this preset value. When zero is reached the previously high zero detect output on pin 14 goes low. In our configuration this zero output is used to gate the clock input via AND gate IC4a. Thus, as soon as zero is reached the clock is disabled, leaving pin 1 low and so allowing the zero output on pin 14 to remain low also. The counter IC 3 can only be restarted by another load pulse to pin 9, which presets the count start value and returns the zero output to logic high, thus enabling the clock for the down count. A look at the timing diagram of fig. 1 should clarify this sequence.

The 'zero detect' output on pin 14 directly provides the required PWM signal and is buffered by AND gate IC4b before driving the power switching stage. The load pulses are produced by the edge detector implemented using inverter gates IC5a and b. These negative-going pulses are very narrow, with a width set by differentiator C1/R2 of about 200 nS ; they are derived
from the negative-going edges of a square wave produced by the counter IC6. The load pulses for the other channel are generated in a similar fashion, but from the positive-going square wave edges. This ensures the $180^{\circ}$ relative phase offset of the two PWM signals, necessary for reducing the peak supply current.

This square wave determines the full period of each pulse width cycle and thus sets the constant frequency of the PWM output. The counter IC6 is a dual four-bit binary counter; the stages are wired in cascade for ripple counting, and the square wave is taken from the seventh output bit. A 2.5 MHz astable clock built around IC5e and $f$ drives both IC6 and the down counter IC3. IC6 will thus divide the master clock by 128, providing load pulses at a repetition frequency of 20 kHz which is the required modulation frequency. For a maximum duty cycle modulation of $100 \%$ the down counter must have a maximum start count of 128, giving a resolution of one part in 128. This is achieved by using only the first seven input bits of IC3 and taking the eighth to logic low. The puise width is thus variable from 0 to $\mathbf{5 0} \mathbf{u S}$ in $\mathbf{4 0 0} \mathbf{~ n S}$ increments. The eighth bit of the input port is used to set the fowardireverse direction of the motor.

The pulse width data for the preset inputs of counter IC 3 must be continuously available, since it is loaded afresh for each cycle on the negative-going edge of the asynchronous load pulse. The eight bit data port from the 'outside world' is thus latched
by IC2, a dual four bit hold-follow latch (the CMOS 4508). When the 'store' inputs on pins 2 and 14 are logic high the data appearing at the $Q$ outputs will follow the data on the inputs; when 'store' is taken low the current data is held internally and remains on the $\mathbf{Q}$ outputs to control the down counter.

The corresponding data input bits of each control channel on IC2 and 7 are wired together to form a common input port; data is thus altered for a particular motor channel by taking its associated store input to logic high. Strobing in new speed data is achieved using IC1a, a NOR gate. One input, the strobe line, is held normally high by $R 1$, and the other input is taken from the PWM output.

When the strobe line is now taken low the store input will only go high as the PWM output signal goes low. This arrangement ensures that any new data will be stable when it is loaded into the down counter; it also prevents the forward/reverse control from changing state while the motor driver transistors are turned on. Once the required data has been set up on the inputs, the data strobe line must be taken low for a minimum period of 50 uS to ensure the data has been latched. NOR gate IC1b is used to buffer the forward/reverse control line before it leaves the PCB. The 12 V power requirement is taken from the motor driver stage described in Part 1, with C7 providing smoothing and C4, 5, and 6 decoupling the fast switching pulses.


Fig. 3 Component overlay for the PWM board.

## Testing Times

With nothing connected to the ribbon cable inputs, the two pulse width modulation outputs should be continuously low and the two FWD/REV outputs should be high (you may need to briefly touch the strobe input wires to a ground terminal). These outputs may be observed with either a 12 V FSD meter or a scope. If a scope is used, the 2.5 MHz clock can also be checked; a square wave should be observed on pins 2 and 5 of IC4.

Pulse width control for each channel may now be verified. Wire bit seven of the ribbon cable to the positive terminal. When either strobe is now taken briefly to ground, the corresponding PWM output line will change to a square wave. The duty cycle is now $50 \%$, this ratio being 64 (bit 7) divided by 128 (full cycle), and
the meter will read 6 V . If a scope is being used the square wave pulse trains from each channel can be observed; they should be $180^{\circ}$ out of phase with each other (in the case of square waves this looks like inversion).

This process can be repeated, taking only bit 1 positive, to give the minimum pulse width of $1 / 128$ th of the full 50 uS cycle (about 390 nS ). This very small pulse cannot be detected by the meter but can be seen on an oscilloscope. Further bit combinations can be strobed in, to provide varying pulse widths in increments of 390 nS .

## Modifications

The eighth bit of the data input port is used to set the forward/reverse signal line. For applications that do not require a separate direction signal, ie a computer-controlled pulse generator,

PARTS LIST

## DIGITALPWM

Resistors (all $1 / 4$ W, 5\%)

| R1,6-14 | 220k |
| :--- | :--- |
| R2,3 | 4k7 |
| R4 | 1k0 |
| R5 | 3 k 9 |

Capacitors

| Capacitors |  |
| :--- | :--- |
| C1,2 | 100 p ceramic |
| C3 | 33p ceramic |
| C4,5 | 10 n ceramic |
| C6 | 100 n ceramic |
| C7 | 1000 u 16 V axial |
|  | electrolytic |


| Semiconductors |  |
| :--- | :--- |
| IC1 | 4001B |
| IC2,7 | 4508B |
| IC3,B | 40103 B |
| IC4 | 4018 |
| IC5 | 40498 |
| IC6 | 45208 |

Miscellaneous
PCB (see Buylines); Veropins.

## BUYLINES

## No problems at all here; most mail order

 companies advertising in this issue should be able to supply the components. See page 82 for the price of the $P C B$,this bit may be utilised for higher resolution pulse width control; the full eight bits give a resolution of one part in 256 . To implement the modification the following changes can be made.

The tracks to pins 1 and 2 and pins 12 and 13 of IC1 should be cut; this provides two unused NOR gates, whose input pins should be connected to the nearest ground track via insulated links. The tracks to pins 13 of both IC3 and IC8 should be cut; pin 13 of IC3 is now linked to pin 11 of IC2 and pin 13 of IC8 to pin 17 of IC7. Pin 7 of IC5 should be cut from its track and linked to pin 14 of IC6. The forward/reverse input line, bit 8, now becomes the most significant bit of the pulse width control.

The pulse width modulator now works in exactly the same way as before but with an output frequency of 10 kHz , exactly half the original value.

Once the board has been tested it can be mounted using brackets or adhesive PCB slots directly above the motor driver board in the diecast box. The two supply pins are wired to the existing 12 V input terminals, while the FWD/REV and PWM outputs are connected to the corresponding inputs of each motor driver channel. We used a 15 -way D-type Cannon socket bolted onto the case side for the eight bit data input. The two strobe lines and ground wire should also be wired to this connector.

ETI

# PROXIMITY DETECTOR <br> This project will endow your man of steel with infra-red vision; and it's not much bigger than a human eyeball. Alternatively you can use in applications such as batch counting. Design and development by Rory Holmes. <br>  

This project provides a very useful means of detecting the presence of anything by the reflection of infra-red light, and provides a direct digital output of object detection. The transmitter and receiver of the infrared beam are both mounted on the same miniaturised PCB, which is housed in a short length of aluminium tube for screening and protection. By the use of modulation and high power bursts of infra-ed at a very low duty cycle, a detection range of over a foot is achieved. The receiving photoamplifier is tuned to the same modulating frequency of 1 kHz , and thus provides good rejection of stray infrared interference. Bright lights will not affect the operation of the module.

The module features a wide supply voltage range, with an LED to indicate correct operation. A preset adjustment pot at the rear of the sensor allows the detection range to be preset at any distance between 1 and 35 cm .

## Construction

Although the PCB layout (Fig. 2) is quite dense, with several vertical mounting resistors, the assembly should be straightforward. The only component of note is PR1, a $3 / 4^{\prime \prime} 20$ turn rectangular cermet preset. These are readily available, though, and should fit the board exactly. The power transistor Q3 is mounted flat, with the metal side face down; likewise, observe the orientation of the other transistors. Photodiode D1 has a chamfered edge on one side; this is mounted to face the infra-red emitter LED2, so allowing the sensitive surface to face outwards. The infra-red LED should be mounted with the flat side facing away from the photodiode (the flat identifies the cathode). After assembly of the board it is important to mount a small guard between the infra-red emitter and detector, to prevent infra-red light
passing directly to the detector before it has been reflected. The guard should be a 7 mm square, cut from unetched PCB or a piece of aluminium. It can be stuck between the two diodes and directly in front of C 4 with a blob of superglue.

The board is mounted in a 55 mm length of aluminium tube, of internal diameter 27 mm or greater.

The diagram of Fig. 3 illustrates how a 6BA nut is soldered sideways onto the PCB track directly beneath the 3 -way connector socket. Holes are drilled in one end of the tube to mate up with the indicator and preset adjustment screw. A rectangular hole also needs to be cut, allowing access to the connector socket. A 6BA bolt can now be used to tighten the board against the tube end. The sensing end of the tube may be covered with anything that is transparent to infra-red (red filter plastic polarising sheet, or just clear plastic). If openings are cut for the emitter and detector then an aluminium disc could also be used. The disc should be cut to fit the tube and mounted flush against the small guard plate with epoxy. The sensor tube may be mounted with a circular clamp that tightens round the tube; this can be seen on the photographs of our prototype.

C2, the smoothing capacitor, is shown on the circuit diagram as a


100 uF 10 V tantalum electrolytic. This value was chosen to fit on the PCB and consequently limits the supply to 9 V maximum, although the circuitry is capable of operating up to 35 V . To allow higher supply voltages, change C 2 to 22 uF 35 V tantalum. An additional electrolytic of 100 uF 40 V should be mounted underneath the board and soldered to the same pads as C2.

The sensor is now ready for testing, and the three way connector plug should be wired to a suitable power source capable of providing 100 mA (this is for the benefit of the bulb, if used; the circuit itself only takes 20 mA ). A PP9 9 V battery is adequate. One of the test circuits illustrated in Fig. 4 should be adopted; if the LED arrangement is used, for example, the LED will be on when the sensor points into free space. Start with the preset fully anticlockwise; this gives minimum sensitivity and the sensors should not trigger at all.

Keeping the sensor pointed at freespace, the preset should be turned clockwise to increase the sensitivity until the LED just goes out. The preset should now be backed off until the LED just comes on again, thus setting the maximum range. Placing a hand about $12^{\prime \prime}$ in front of the sensor will now turn off the test LED, while striking


Fig. 1 Component overlay for the optical sensor. The photos overleaf show how small the unit is.

HOW IT WORKS
The proximity sensor works on the principle of transmitting a beam of modulated infrared light from the emitter diode LED2, and receiving reflections from objects passing in front of the beam with a photodiode detector D1. The circuit can be split into three distinct stages, the infra-ed transmitter, the photodiode amplifier, and a variable threshold comparator.

The transmitter provides 1 A peak current pulses for 10 uS through the infrared emitter diode, at a repetition rate of $1 \mathbf{k H z}$. Q1 is arranged as a constant current source to supply the base of Q2, and to charge up C1. As C1 charges up, the base voltage of Q2 rises until it reaches about 0V6 relative to ground. Q2 then turns on, so tuming on another constant current source formed by Q3 and LED1. This current source sets a temperature compensated voltage of about 1 V 5 across R3, thus defining a current of 1 A through the infra-red emitter LED2. After Q3 turns on, a negative pulse through C1 turns off Q2 again, thus restarting the oscillation cycle. The current pulse, determined by C 1 and R 2 is set at 10 uS . A 10 uS pulse every 1 mS is equivalent to a duty factor of 1:100, so that although 1 A peak pulses are generated, the average current required is only 10 mA . Capacitors C3 and C2 are there to provide power supply smoothing to decouple the fast current pulses.

The detector is built around IC1, a CA3240 dual op-amp. IC1a is configured as an inverting amplifier with a gain of -2 . It amplifies the infrared signal picked up by photo-diode D1. C4, which couples the diode signal to IC1a, acts as a high-pass filter in combination with the input impedance of the amplifier. Positive-going pulses of $\mathbf{1 0}$ uS duration are fed from the output, via rectifier D2, to a smoothing filter C5 and R9. This provides the signal voltage reference for the inverting input of comparator IC1b. A 2V7 reference, formed by R8 and ZD1, provides the biasing voltage for the photodiode through $\mathbf{R} 7$. It also provides the reference voltage for the nor inverting comparator input, set by potential divider PR1. R10 creates positive feedback round the comparator, to improve the switching and introduce a small amount of hysteresis. Thus, if a reflected light signal received due to the presence of an object rises above the threshold set by PR1, the comparator output will go into negative saturation. The comparator output is used to turn Q4 on or off, thus providing an open collector output for digital interfacing to logic circuits.

NOTE
D1 IS PHOTODIODE
01 IS 2N3819
02,4 ARE BC184L
03 is BD140

D2 IS 1 N4148
ZD1 IS 2 V 7400 mW ZENER
LED2 IS IS INFRA.RED LED
ED2 IS IS INFRA-RED LED


Fig. 2 Circuit diagram for the sensor.

PARTS LIST

| Resistors (all $1 / 4 \mathrm{~W}, 5 \%$ ) |  |
| :--- | :--- |
| R1 | 22k |
| R2 | 22R |
| R3 | 1R5 |
| R4,10 | 470k |
| R5,6 | 1M0 |
| R7 | 100k |
| R8,11 | 12k |
| R9 | 3M3 |

Potentiometers
PR1 10k 3/4" 20 turn cermet trimmer

| Capacitors |  |
| :--- | :--- |
| C1 | 68 n ceramic |
| C2 | 100 u 10 V tantalum |
| C3 | 10 n ceramic |
| C4 | 330 p ceramic |
| C5 | 47 n polycarbonate |

Semiconductors

| IC1 | CA3240 |
| :--- | :--- |
| Q1 | 2N3819 |
| Q2,4 | BC184L |
| Q3 | BD140 |
| D1 | Photo-diode (TIL100 or similar) |
| D2 | 1N4148 |
| LED1 | 3 mm red LED |
| LED2 | infrared LED (TIL38 or similar) |

## Miscellaneous

PCB (see Buylines) threeway PCB plug and socket (see Buylines); aluminium tube ( 27 mm diameter, $\mathbf{5 5} \mathbf{~ m m}$ long)


Fig. 4 Any of these test circuits may be used to check out the sensor.

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# BREADBOARDS 

# In the beginning there was the bird's nest; and engineers saw that it was bad. Since then a great many systems for prototyping and small scale production of electronic circuits have been developed, and Peter Green has been getting his hands on some of the latest ones. 

Breadboards have come a long way since the days when a prototype circuit was an impenetrable jungle of wires soldered to nails banged into a piece of wood. (Honest, it really was like that once!) When the plug-in type of breadboard arrived on the scene it was a great advance; components could be unplugged and swapped around with great ease if the circuit didn't work, and if you needed to build larger circuits you simply linked more boards together. However, connections still had to be made with loops of wire all over the place, and the boards were no use for small scale production; this had to be done by track cutting on Veroboard, which can get a bit tedious, or by designing a $P C B$, which can get expensive.

The new generation of breadboarding systems have been designed to overcome these disadvantages. They fall into two general categories; a 'wrap-and-solder' type where enamelled wire is wrapped around the component pins and then soldered to make the connection, and the IDC types. Insulation displacement connection involves forcing plastic-coated wire between tines of some sort which pierce the insulation and make an electrical connection to the conducting core without breaking the wire. Both types have the same attractive features - that is, the boards are low-profile with neat wiring and nounsightly loops to be pulled off accidentally. Furthermore, all three systems that we looked at come in standard board sizes and are capable of being plugged into a card frame, etc, and used in the same way as a normal PCB.

## Quick/Connect

Quick/Connect is an IDC system sold by Dage Eurosem, and many readers will have seen it on their stand at the Breadboard Exhibition last November. A handheld wiring pen is used to push 30 awg solid copper wire into two-pronged tines on the wiring side of the board. The tines pierce the insulation to make a gas-tight joint without soldering; up to two wires will fit into a single tine, giving the equivalent of four levels of wire-wrapping. Components are then inserted into the sockets which form the other end of the the tined pins.


The Quick/Connect wiring pen and bobbin.


This photo shows the underside of a Quick/Connect Eurocard. Interwiring of the components is done between the tines and a data bus, for example, is easily wired (left of board). At the top of the page you can see the component side of this board; ICs are especially suited to the system.


#### Abstract

The Quick/Connect pen is metal and plastic and consists of a tube which terminates in a guide channel/insertion head. Wire is fed down the tube from a bobbin held between offset metal prongs at the top of the pen. This bobbin is also made of metal and is quite large ( 4 cm in diameter); consequently the pen feels somewhat unbalanced and awkward and doesn't handle easily. At first the prongs held the bobbin so tightly that wiring up was heavy going, but flexing them outwards to give a looser fit cured the problem and made the pen almost effortless to use. The wire itself is available in a wide range of colours so that colourcoding may be employed on complicated boards.

Boards are available either fully populated with sockets or blank for the user to insert sockets as and where he requires. We were provided with a fully populated single Eurocard for this review, so we cannot comment on the ease or otherwise of socket insertion; but it would appear from the catalogue that only industrial users would be interested. A variety of expensive tools, including a press are required (unless you fancy your chances with a mallet), and the hobbyist wouldn't want that sort of outlay; however, in industry people might well prefer this capital outlay to paying for full boards with lots of pins they'll never use. A large range of standard boards either exist or are planned, including various Eurocard sizes and boards compatible with Apple, S-100, Dec Unibus and other microcomputer


 buses.Apart from the balance aspect, once the tension in the bobbin is correct the tool is very easy to use; very little pressure is required to force the wire between the tines. However, you need good light and lots of concentration because misalignment of the insertion head over the tines means you sever the wire instead of connecting it (as I found to my dismay on several occasions). When you want to cut the wire, at the end of a wiring run, you'll have to use side-cutters - no built-in cutting device is provided.

Component insertion depends on what you're actually trying to plug in, and is easiest for ICs - after all, this system is primarily designed for digital applications. The IC simply plugs
into the sockets on the component side of the board, where it is firmly held. The manufacturer recommends the use of a special tool but we found insertion fairly easy without one. The problems start when you want to use other types of component. Tantalum, ceramic and polycarbonate capacitors fitted OK, as did 3 mm LEDs and small signal transistors. We found that some $1 / 4 \mathrm{~W}$ resistors fitted while others had leads just a little too thick; 1 N4148s are OK but not the 1N400X series; while 5 mm LEDs, electrolytics, polyesters and anything in a T0220 package were much too big. The catalogue suggests that such components be soldered to the sockets (bye bye reuseability) or soldered to DIL 'component carriers' (ie header sockets) which are then plugged into place. However, we found a simpler solution; all you have to do is plug in ordinary DIL IC socket into the board sockets and then plug your large components into the IC socket. The photograph shows an example. This would be useful if you wanted to put a 7805 voltage regulator on a computer card, for example.

Quick/Connect is a system which will obviously have a lot of appeal to industrial users, as it offers easy, reliable wiring in development work and a low profile finished board which can be stacked in a card frame on a $1 / 2^{\prime \prime}$ pitch. However, it's unfortunate that the pen has the odd 'feel' to it (at least it did to me), and of course the price will not be any help to it as regards sales to the hobbyist market.

Distributed by: Dage Eurosem Ltd, Rabans Lane, Aylesbury, Bucks HP193RG.
Prices: Fully populated Eurocard $£ 59.00$ +VAT. Wiring pen $£ 10.00$ + VAT. Reel of wire(50ft) $£ 3.36$ +VAT.






Fig. 1 Working anticlockwise from the top left, we have the IDC principle, the Quick/Connect and Speedwire pins, and a diagram showing the Speedwire termination technique.

## Speedwire

This is so new that the sample we played with is one of only five preproduction sets in the country, and jealously guarded it was too! However, we were informed that the launch was to be at the All-Electronics Show here in London; this should have been a few days ago as you read this, so if you went you may have seen the system on the BICC-Vero stand.

Speedwire is a similar system to Quick/Connect but some features show the benefits of industrial hindsight. The pen and



Close-up of the Speedwire pen.
bobbin are plastic with a central metal guide tube, and consequently very light, while the bobbin itself is pivoted on the pen axis rather than off-centre, so that balance and handling are better.

The wiring tines are quite innovative, being a castellated tube - rather like the top of a castle turret, with slots cut on three sides but not the fourth (see the diagram). The pins are arranged in the board so that their slots are all orientated in the same direction. To daisy-chain terminals together, the tool is held so that you insert the wires through the pair of opposing slots, which make the IDC connection as usual. You work across the board in this manner, holding the tool at the same angle to the board, until you reach the endpoint of the signal chain. The tool is then rotated $90^{\circ}$ so that the wire is forced through the single slot on the final insertion; this makes the last IDC connection while the unslotted pin face opposite automatically severs the wire. Damn clever, these British.

The Speedwire sockets suffer from the same problem as Quick/Connect, namely that large component leads won't fit; but the same solutions apply and in any case, the Speedwire sockets are slightly larger(all the $1 / 4 \mathrm{~W}$ resistors fitted).

When Speedwire becomes available it will be sold both with fully populated boards (again, we saw a single Eurocard), and as drilled boards with fit-them-yourself pins. However, the process is easier than for Quick/Connect as only one tool is required, looking rather like a safety razor. You open it up and insert the pins (which are supplied on a strip like Soldercon sockets), then push them home into the required holes and snap off the carrier strip. The tool ensures that the pins are inserted to the correct depth, and we found it quite easy to use. This is obviously much more economical for the small-scale user, or the large-scale user with only a small amount of components perboard.

Initially there will be two kits available. The first contains a plain unpopulated Eurocard, contacts and insertion tool, wiring pen and spare wire, while the second has fully populated Eurocard, a wiring pen and spool of wire, spare wire spools and a pair of Speedwire cutters.

Although we didn't have our hands on it for more than two hours before it was whisked away again, we were impressed by the quality of this system. Indeed, once the kits are available we'd like to have another go with it(that's a hint, Mike!).

Distributed by: BICC - Vero Electronics (Retail Dept), Industrial Estate, Chandler's Ford, Eastleigh, Hants SO5 3ZR.
Prices: Not yet known. Apply to BICC-Vero for further information.


## Roadrunner

This is the sole 'wrap-and solder' system of the three mentioned in this article. The system is based around two components; a distribution pen for feeding out the wire, and plastic distribution strips which act as guide channels. In use, this method is quite different from the others under consideration; it is also well established, and was on show at Breadboard 81. The wire used is 36 swg quick soldering enamel (or QSE); the bobbins are only 2 cm in diameter and so the pen is smaller and lighter than the other types.

Before using the pen, the distribution strips must be secured to the wiring side of the board. The manufacturers supply two specially designed "Roadrunner" boards, a single height Eurocard and a double height Eurocard; however, the beauty of the system is that any type of board is suitable since connections are made directly to the component pins rather than to special sockets. For example, Veroboard, bare matrix board (no copper) or even a bit of scrap paxolin you've drilled a few holes in, can all be used if circumstances demand. The plastic strips are of two types; 2" press-fix for use on boards with 1.02 mm diameter holes on a 2.54 mm pitch matrix, and $6^{\prime \prime}$ lengths which must be glued in place with a contact adhesive. The photos show the castellated nature of the strips, which can hold a large number of wires while allowing easy routing to the component pins and maintaining a low profile.
How to position the Roadrunner distribution strips for high density (left) (right).


[^3]that will fit through the holes in the board, just like ordinary PCB construction. To use the system most effectively, a wiring schedule should be prepared; this will not only act as a point-topoint wiring guide but also encourages thought about the optimum arrangement of the components. Wire runs should be kept to a minimum for efficient operation.

With all the strips and components in place, wiring can begin. The protruding wire end (about 4 mm ) is bent over slightly to prevent it being pushed back up the guide tube, then inserted into the first hole and wrapped several times around the pin. The wire is routed into the strip through one of the cutouts, then along and out to the next connecting point where another wrap is made. When the first run is complete, press down on the wire against the board with the metal guide tube; this severs the wire. Feed out another 4 mm of wire, bend it over and wire the second run. Continue working through the schedule until all the connections have been made.


Fig. 3 Plan view of Roadrunner wiring. No more than than three pins should be wired from each break in the strip.

To complete the construction, all the connections are soldered using a high temperature iron and resin cored solder. The solder removes the insulating lacquer on the wire and makes good the joint. Note that a high temperature is very important here, otherwise the enamel will not burn off properly resulting in poor or nonexistent electrical contact. The bit temperature should be approximately $420^{\circ} \mathrm{C}$, and certainly higher than $400^{\circ} \mathrm{C}$. My iron wasn't really up to it, but Roadrunner will supply suitable types.

Although the fact that soldering is involved makes mistakes a little harder to correct than with IDC, it is possible. Incorrect wires should be cut at the component and left in place - pulling them out of a tightly packed loom could strip the enamel from adjacent wires and cause short circuits. If you haven't used IC sockets, a faulty IC can be cut away on the component side and a replacement soldered to the original leads.

To sum up, Roadrunner has many advantages. Any type of component or board can be utilised, and in digital circuits a very high packing density can be achieved. The complete boards have a very low profile and may be mounted in card frames on a $0.6^{\prime \prime}$ pitch. Furthermore, existing PCBs may be modified or repaired with the system, unlike the IDC systems.


Modifying a PCB using the Roadrunner system.
Distributed by: Roadrunner Electronic Products, 116 Blackdown Rural Industries, Haste Hill, Haslemere, Surrey GU27 3AY.
Prices: Euro Introkit $£ 15.99$ + VAT. Single Eurocard $£ 4.50$

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# COMBINATION LOCK 

## A dazzling digital device, deftly designed to defend your domicile from dangerous desperadoes. Design and development by Rory Holmes.

For almost every lock made there's a thief to match it, and for every set of keys there's always someone to lose them. Such problems of security can now be overcome thanks to ETI's new Logic Lock, where the only moving part is the door latch! The complete device including the keypad and control-box can be built for about $£ 10$ and incorporates some very useful features in the design, resulting in total simplicity of operation.

The keypad, pictured above, can be built by anyone using completely standard components. It provides a waterproof and very rugged touchsensitive input, which will last much longer than mechanical contact switches since there's virtually nothing to go wrong with it.

## Programmed Protection

A single switch on the control-box puts the lock into either 'program' or normal 'operation' mode. In 'program' mode new combination sequences of any length from one to 14 digits can be entered directly through the keypad. Any number of repeated digits may be used, and the complete sequence will be stored in CMOS memory. A change of combination can thus be effected in seconds. After programming, the lock is left in 'operate' mode where the 5101 CMOS memory consumes only 10 uA of standby current, allowing long operating life from a 6 V battery.

As soon as the first number is

entered the memory receives full power for a 25 second period, allowing the combination sequence to be entered. The front panel LED illuminates for each number received to provide operator feedback, and also gives low battery indication by remaining off when replacement is due. The keypad input has been fully debounced to eliminate false entry errors. When the last digit is tapped in, the solenoid door bolt will activate for a preset time of around six seconds allowing the door to be opened. The LED also comes on during this period to confirm entry of the correct code.

The Logic Lock has well over four million million combinations, and an alarm was therefore considered unnecessary. Your local illegal entry operative can tamper with this lock until the cows come home; it won't open. Even a PDP-11 hooked into the keypad cable would take nigh on 6000 years of computing power to crack the code!

An optically coupled solid state relay is used to switch power to the solenoid, again being cheaper and more reliable than its mechanical counterpart. The PCB will accommodate several optional output configurations, for switching either mains or DC solenoid door latches.

## Installation

Since the solenoid bolt mechanism is remote from the keypad and controlbox, the logic lock may be placed anywhere in the vicinity and is not restricted to door mounting, although this would be more conventional. Obviously, the important factor is mounting the keypad on the outside and the control-box on the inside the keypad could be mounted next to the bell push, allowing the ribbon cable to take the same route into the house as the bell wires. The control-box is screwed or stuck to the inside of the door and the keypad can be fixed to the outside using a strong epoxy. Alternatively the inside of the keypad can be filled with resin or Araldite and a long fixing bolt allowed to set in the resin. The keypad can then be bolted directly through the door. A hole drilled behind the keypad lets the ribbon cable pass through the door to the control-box. The wires for connecting up the solenoid latch, which are normally tacked across the door and pass across the hinges, should be connected up to the control box as shown in the wiring diagram.

The inclusion of an earth terminal for mains switching applications provides an extra degree of protection in the unlikely event of an opto-isolator fault.


Fig. 1 The complete circuit diagram for the ETI Logic Lock. A general-purpose output is provided; next month several application circuits are given to suit mains or DC solenoids.

HOW IT WORKS

Numerical input to the lock is achieved using IC1, an eight input priority encoder. When one of its input lines is taken high, the corresponding number allocated to this line appears in binary form on its output lines, pins 6,7, and 9 . An input key detect signal, GS, is also available on pin 14; this line goes high if any input line is activated.

The eight input lines are activated using a touch keypad. Resistors R1-8 keep the inputs normally low, and they are taken high when the skin resistance of a finger bridges the input pins to the central +6 V rail. Capacitors C1 to 8 debounce this switching action to provide a measure of stability and the key detect output is further debounced to give completely reliable operation of the sequencing logic.

The rest of the circuitry consists of a four-bit counter IC4, which addresses part of a $256 \times 4$ bit CMOS memory (IC3). The data output of the memory is compared with the three bit keyboard word using EXOR gates IC2a, $b$ and $c$, to provide an equal (logic low) or unequal (logical high) I signal for controlling the subsequent action.

All of this circuitry has two modes of operation. With the switch in position 1, the 'write' mode is selected for programming a new combination sequence; position 2 is
the 'operate' mode where the circuit waits for a valid combination sequence to activate the lock.

To explain the 'write' mode we shall assume that the counter IC4 is on count 1 (ie only output $A$ is at logic 1 ), and there are zeroes in all memory locations.

IC5b, a NAND Schmitt trigger, provides a debounced control signal from the GS line. GS is normally low, thus pin 2 of IC5b is low; also C14 is discharged via D5, so holding pin 1 low. Thus the output on pin 3 is normally logic high. When any input number is touched the GS line goes logic high, taking pin 2 of IC5b high and allowing C14 to charge up through R 17 with a 150 ms time constant.

Now, as long as the GS line is still high when the pin 1 voltage reaches the trigger threshold, the gate output will switch low. When the touch input is released, GS goes low, C14 discharges through D5, and the Schmitt trigger output returns high again.

The control output from pin 3 is taken to five different circuit points. The negative-going edge is differentiated by C15 and R18 to produce a brief negative pulse as an input number is activaled. During the 'write' mode this pulse is routed via SW1 to the write line of the memory, IC3 pin 20. As this pin is taken low the current
data on pins 11, 13 and 15 (corresponding to the input number) is recorded at the present address. The IN1 memory input on pin 9 is also taken directly from the control line and will thus always be zero when the 'write' pulse occurs. This data bit is used to indicate the end of a correct combination sequence during the 'operate' mode (a necessary marker, since any length of sequence is permissible). The control line is taken directly to the clock input on pin 15 of IC4 and also to the preset input A at pin 4. As the control line returns to logic high when the touch input is released, the address counter IC4 is clocked on by one count on the positive-going edge.

The cathode of D6 is also taken low by the control line, thus turning on Q2 via R19 to illuminate the front panel indicator LED1. This shows that an input has been received to give a measure of operator feedback. As the timing diagram of Fig. 2 indicates, the above recording process repeats for each number that is selected on the touch pad, with the corresponding binary values being stored sequentially in consecutive memory addresses.

After entering the required digits, the sequence is terminated by moving SW1 to position 2, the 'operate' mode. As the switch closes, one last write pulse is


Fig. 2 Timing diagram for the Logic Lock logic! It will repay careful study in conjunction with the circuit diagram and How It Works section.

Next month we conclude this project with the component overlay and application circuits.
generated as C12 charges up through R12 (C12 was previously discharged via R13). No touch inputs are activated so the INT memory input is held at logic 1 by the control line. When the logic 1 is written to memory the associated OUT1 output also goes high, activating the solenoid timer monostable built around IC5a. (The solenoid is energised by this monostable for both normal lock operation and at the end of a write sequence. Although the door must already be open during reprogramming, the solenoid provides a useful indication of successful reprogramming.)

The pin 5 input of IC 5 a is normally held low by R13, thus disabling this gate. However, after the brief write pulse caused by changing SW1, pin 5 is taken high via R12. Pin 6 was also held low by R10, but as the OUT4 data output goes from low to high, the previously discharged capacitor C11 takes pin 6 high. The gate output on pin 4 will thus go low for a period set by the R10-C11 time constant. This time period is about five seconds and is used to turn on both the front panel indicator (via D7), and the solenoid lock mechanism, thus allowing plenty of time to open the door.

The monostable signal turns on
transistor Q1, which applies current to the LED in an opto-isolator and allows isolated external power to be switched to the solenoid. (Transistor or thyristor optoisolators may be used).

When the monostable finishes its time period, the output returns high again. C13 differentiates this positive-going edge to provide a load pulse to the counter IC4. The preset $A$ input is held at logic 1 by the control line, so the counter will return to its first state, switching the OUT1 memory output back to logic low.

The lock has now been programmed with the desired sequence and remains in its resting state of count position 1. When a number is now entered, the negative-going pulse produced through C15 appears on pin 9 of the EXOR gate IC2d.

This gate is wired as an inverter and the positive-going output pulse is fed to the pin 12 input of NAND gate IC5c. The other input to the NAND gate comes from the three bit comparator at the junction of D2,3 and 4. These diodes (with R11) form a three input AND gate; thus, if the data from the input encoder wired to one side of the digital comparator has the same binary value as the data currently on the OUT memory outputs feeding the other side,
then the pin 13 input of IC5c will remain low via R11. However, if the two values are not equal, there will be a logic high output from at least one of the exclusive OR gates IC2a, b or $c$, taking the input of IC5c high via the corresponding diode. Thus if a wrong number is entered, the short positive pulse from IC2d will appear in inverted form on the pin 11 output of IC5c. IC5d further inverts this pulse before feeding it to the load input on pin 7 of counter IC4. Since the preset $A$ input of the counter is held low by the control line when this load pulse is received, the counter resets to zero. At the end of the wrong number input the control line goes high to clock the counter on to its count 1 resting state. Every wrong number entered will always return the counter back to the beginning of the sequence, regardless of where it occurs. As long as the correct numbers are entered in sequence, no reset pulses occur and the counter will be clocked on to the next number at the end of each entry.

After the last number in the sequence is entered, the counter will address the terminating location. The previously low OUT1 output goes high, triggering the solenoid timer monostable as described earlier and the door will now open.

# ETI PCB SERVICE 

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| THO (IKHz at 150M | 0.002\% |
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## ADVERTISEMENT INDEX

A. D. Electronics ..... 91
Ambit Int. ..... 58
Amral Ltd ..... 93
Audio Electronics ..... 28, 65
Audio Video Services ..... 91
Aura Sounds ..... 39
BICC Vero ..... 46
BI-Pak. ..... 16
BK Electronics ..... 46, 77
Black Star ..... 46
BNRS ..... 44
Boss Industrial Mouldings ..... 72
T. J. Brine ..... 83
Broadside Assoc ..... 8
C.S.S. ..... 92
Chiltmead ..... 71
Chordgate Ltd ..... 28
Clef Products ..... 78
Cambridge Kits ..... 78
Crimson Elektric ..... 84
Crofton Electronics ..... 78
Delta-Tech. ..... 84
Display Electronics ..... 34
E.D.A. Sparkrite ..... 35
Electronize Design ..... 91
Electrovalue ..... 88
Enfield Electronics ..... 15
Greenbank Electronics ..... 63
Greenweld ..... 89
Global Spec. Corp ..... 13
Flight Electronics. ..... 18
Happy Memories ..... 72
Henry's Radio ..... 91, 93
ILP. ..... 24, 25
L \& B Electronics ..... 38
Hy-Tek ..... 40
L.E.M. Services ..... 89
Lightning Elec. Comp ..... 92
Mawson Assoc ..... 78
Maplin ..... 100
Magenta Electronics ..... 88
Memotech ..... 9
Midwich Computers ..... 23
Namal Assoc. ..... 90
OTC Australia ..... 93
Parndon Electronics ..... 93
Powertran ..... 2,99
Rapid Electronics. ..... 11
Pantechnic ..... 89
R.T.V.C. ..... 57
Relay a Quip ..... 22
Redditch Electronics ..... 83
Silica Shop ..... 47
Sinclair Research ..... 6,7
Solidstate Security ..... 92
Swanley Electrical ..... 72
Technomatic ..... 28, 29
Tempus ..... 85
TK Electronics ..... 73
Watford Elec ..... 4, 5
West.Hyde Develp. ..... 78
Wilmslow Audio ..... 83
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