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Inside: computing today no 4

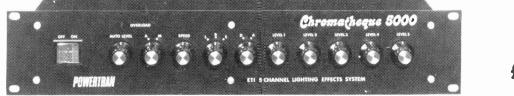
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TWONKY - A SCAMP Of A Composer Computer Speech Lessons Tape Slide Synchroniser Noise Reduction

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CHROMATHEQUE 5000 5 CHANNEL LIGHTING EFFECTS SYSTEM

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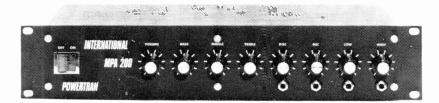


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This versatile system featured as a constructional article in ELECTRONICS TODAY INTERNATIONAL has 5 frequency channels with individual level controls on each channel. Control of the lights is comprehensive to say the least. You can run the unit as a straightforward sound to light or have it strobe all the lights at a speed dependent upon music level or front panel control or use the internal digital circuitry which produces some superb random and sequencing effects. Each channel handles up to 500W and as the kit is a single board design wiring is minimal and construction very straightforward.

Kit includes fully finished metalwork fibreglass PCB controls wire etc - Complete right down to the last nut and bolt

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TRANSCENDENT 2000 SINGLE BOARD SYNTHESIZER

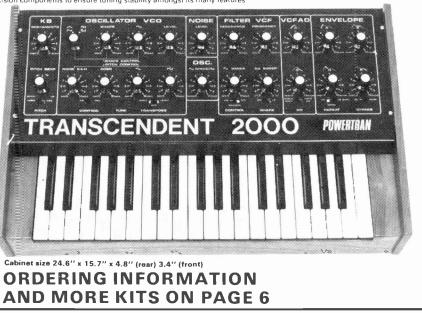
LIVE PERFORMANCE SYNTHESIZER DESIGNED BY CONSULTANT TIM ORR (FORMERLY SYNTHESIZER DESIGNER FOR EMS LIMITED) AND FEATURED AS A CONSTRUCTIONAL ARTICLE IN ELECTRONICS TODAY INTERNATIONAL. The TRANSCENDENT 2000 is a 3 octave instrument transposable 2 octaves up or down giving an effective 7 octave range. There is portamento, pitch bending, a VCO with shape and pitch modulation a VCF with both low and high pass outputs and a separate dynamic sweep control a noise generator and an ADSR envelope shaper. There is also a slow oscillator a new pitch detector. ADSR repeat, sample and hold, and special circuitry with precision components to ensure tuning stability amongst its many features.

The kit includes fully-finished metalwork-fully assembled solid teak calimet filter sweep pedal prufessional quality components fail resistors either 2 % metal oxide or 1/3% netal trainil) and it really is complete in right down to the last nut and built and last price of wref. There is even a 13A plug in the kit — you need buy absolutely nin more parts before bluquing in and making great music! Vitually all the components are on the one professional quality blengtase PCB printed with component loca tions. All the controls mount directly on the main board all connections to the board are made with connector plugs and contruction is so simple in can be built easily in a lew evenings by almost anyone capable of neat soldering! When finished you will possess as vither-size comparable in performance and quality with ready built units selling for between 1500 and ±700!

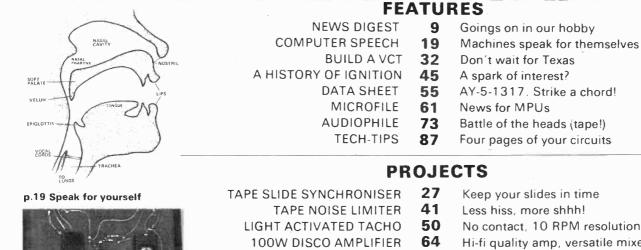
COMPLETE KIT ONLY £172.00 + VAT!

Comprehensive handbook supplied with all complete kits¹ This fully describes construction and tells you how to set up your synthesizer with nothing more elaborate than a multi-meter and a pair of ears¹

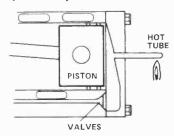








p.79 Scamp Tunes



p.45 Old Flame!

A

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	ABC	PUBLISHED BY Modmags Ltd.; 25-27 Oxford Street, London W1R 1RF DISTRIBUTED BY Argus Distribution Ltd. (British Isles) Gordon & Gotch Ltd. (Overseas) PRINTED BY QB Limited, Colchester
	Electronics Toda	y International is normally published on the first Friday of the month prior to the cover date

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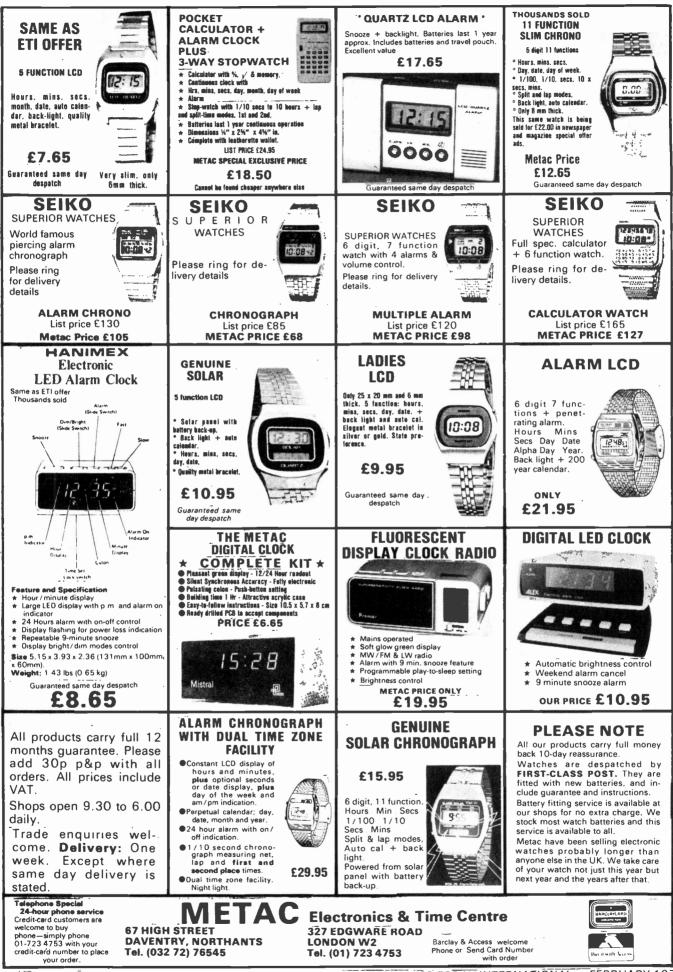
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TRANSFORMERS SALE OFFER S141 0235 240v primary 0-55v at 2Amp second £4.50* +£1.00 p&p.	72709 £0	0.20 uA741Č 72747	£0.55	76115 £1.25 NE555 £0.22 SL414A £1.80					
S142 0349 240v primary 0-20v at 2Amp second; £3.50° + £0.86 p&p.	DISPLAYS No. 1510. 707 LED	ZN 414 RADIO CHIP 75* OPTOELECTRONICS DISPLAYS 2ND QUALITY LED PAKS No. 1510 707 LED Display £0.70 No. 1511 747 LED Display £1.50							
COMPLETE AMPLIFIER KIT STA15. 15 watts per channel amplifier ki CONSISTS. 2×AL60 — 1×PA100	No. 1512. 727 Dual	No. 1511.747 LED Display E1.50 No. 1512.727 Dual LED Display £1.55 No. S123.10 × 2 Red £1 LED CLIPS No. 1508/125							
1×SPM80 — 1×2034 transformer - 2×coupling capacitors. £37.70 inc V.A.T. + 85p p&p.	 No. \$120125 8rig No. \$1212 Bright No. 1502125 Gre 	Red £0.09 en £0.12	S for £0.12 S for £0.12 No. 1508 /. 2 .2 .5 for £0.15 No. S139. Infra-red Emitter, Fairchild						
STA25. 25 watts per channel amplifier ki CONSISTS. 2×AL60 — 1×PA100 - 1×SPM120/45 — 1×2040 transforme — 1×reservoir capacitor — 2× col pling capacitors £41.45 inc V.A.T.+£1.16 p&p.	- No. 1503 .125 Yell r No. 15062 Yellow - No. S82. Clear .2 Ill	ow £0.12 £0.12	No. 1514 NORF No. S76 OCP7	1 5 for £1.00 Tubes ITT 5870 ST					
STA35. 35 watts per channel amplifier ki CONSISTS: 2 × AL80 — 1 × PA100 – 1 × SPM120 — 1 × 2041 transformer — reservoir capacitor — 2 × Coupling capac tors £48.45 inc. V.A.T.+£1.15 p&p.	S85 - 2 Off Post Of	RELAYS ffice relays 40p		£2.00 (including Data) Indicator Lamps 230 AC Colour (Red, Amber and) 25p each					
STA50. 50 watts per channel amplifier kir CONSISTS 2×AL120 – 1×PA200 – 1×SPM120/65 – 1×2041 transforme – 1 reservoir capacitor – 2×couplin	to take r Order No. 202	Y HOLDERS 6x HP7's 10p each	Approx. 200 Piec grated circuits, in Linear, Audio and	OTH I.C. PAK 28, Assorted fall-out inte- cluding: Logic, 74 series, d D.T.L. Many coded de- marked — you to identify.					
capacitors. £58.20 inc. V.A.T. + £1.10 p&p. STA125. 125 watts per channel amplifier	EX-G.P.O. MIC Order No. S51	CROSWITCHES 4 for 50p	Order No. 16	vices, but some unmarked — you to identify. Order No. 16223 £1.00 POWER SUPPLY					
kit. CONSISTS: 2×AL250 - 1×PA200 - 2×SPM120/65 - 2×2041 transformers - 1×reservoir capacitors - 2×coupling capacitors. £72.85 inc. V.A.T.+£1.25 p&p.	CABL	E CLIPS d single pin fixing 30p	Unused ex-equipm 30V. D.C. Outpu circuit diagram.	STABILIZER BOARD Unused ex-equipment stabilizer board. Input 30V. D.C. Output 20V. Complete with					
Sale Orders £0.50 PLUS any ile Advertisement. nail.	I.C. INSERTION EXTRACTION TOOL			6, Ware, Herts.					
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ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979



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POWERTRAN

PSI 4002 STUDIO MODEL



cabinet size 17.2" × 17.2" × 6.7"

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READ THE REVIEW IN SOUND INTERNATIONAL DEC. '78



T20 + 20 20W STEREO AMPLIFIER £33.10 + VAT

This kit, based upon a design published in Practical Wireless, uses a single printed circuit board and offers at very low cost, ease of construction and all the normal facilities found on quality amplifiers. A 30 watt version of this kit (T30 + 30) is also available for **£38.40** + VAT.

POWERTRAN SFMT TUNER £35.90 + VAT

This is a simple low cost design which can be constructed easily without special alignment equipment but which still gives a first-class output suitable for leading any of our very popular amplifiers or any other high quality audio equipment. A phase-locked-loop is used for stereo decoding and controls include switchable atc. switchable muting and push-button channel selection (adjustable by controls on the front panel). This unit matches well with the T20 + 20 and T30 + 30 amplifiers.

WWII TUNER £47.70 + VAT

This cost reduced model of our highly successful Wireless World FM Tuner kit was designed to complement the T20 + 20 and T30 + 30 amplifiers and the cabinet size, front panel format and electrical characteristics make this tuner compatible with either. Facilities included are pre-aligned front-end module, switchable afc, adjustable switchable muting. LED tuning indication and both continuous and push-button channel selection (adjustable by controls on the front panel).

COMPLETE KITS: Our complete kits really are complete. All of the projects shown on this page-are supplied with fully finished metalwork, ready assembled high quality teak veneer cabinet, cables, nuts, bolts, etc., and full instructions -- in fact everything!

All of the kits shown on this page are available as separate packs (except the Powertran SFMT Tuner) for those customers who wish to spread their purchase or perhaps make their own cabinets or metalwork. Prices are given in our FREE CATALOGUE.

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S2.50 (VAT inclusive) per kit.
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- PLUS all the following features too!
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- Value for money quality and performance comparable with ready-built amplifiers costing over £600!

DE LUXE EASY TO BUILD LINSLEY HOOD 75W STEREO AMPLIFIER £99.30 + VAT

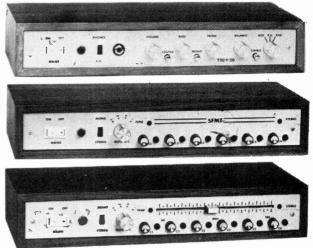
This easy to build version of our world-wide acclaimed 75W amplifier kit based upon circuit boards interconnected with gold plated contacts resulting in minimal wiring and construction delightfully straightforward. The design was published in Hi-Fi News and Record Review and features include rumble filter, variable scratch filter, versatile tone controls and tape monitoring whilst distortion is less than 0.01%.

WIRELESS WORLD FM TUNER £70.20 + VAT

A pre-aligned front-end module makes this Wireless World published design very simple to construct and adjust without special instruments. Features include an excellent a.m. rejection, push-button station selection as well as infinitely variable tuning and a phase locked loop stereo decoder incorporating active filters for "birdy" suppression.

LINSLEY-HOOD CASSETTE DECK £79.60 + VAT

This design, published in Wireless World, although straightforward and relatively low cost provides a very high standard of performance. There are separate record and replay amplifiers and switchable equalisation together with a choice of bias levets are also provided. The mechanism is the Goldring-Lenco CRV with electronic speed control.



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OWERTRAN ELECTRO

news digest



Light Emitting Damsels

FINDING a nice young lady on your desk first thing in the morning is a good way to start a day and, it happens, a nice way to start News Digest.

The lady this month is pictured with a new clock radio from Ingersoll Electronics. Featuring MW/FM/LW with a 12-hour LED display the clock incorpo-

rates the usual alarm, snooze features.

The model XK802 has pushbutton controls along the top of the unit allowing a fast and slow timeset facility. The clock is available from

most larger electrical stores and at a recommended price of £34.00, is good value.

Clubs From Newcastle

The formation of a new society, whose aim is to promote personal computing in the Newcastle area, should be of interest to some of our Northern friends.

Further details from Dr. W. G. Allen, Dept. of Elec. Eng. and Physical Elect.

Meetings usually comprise of a lecture, informal discussions and the demonstration of a particular

Newcastle upon Tyne Polytechnic.

Newcastle upon Tyne,

system.

Raspberries to EMI!!

A fruity tale this. I assure you we are not winding you up. Right? Read on. . . . A team of engineers has been disguising acceleration transducers, accelerometers which convert force into electrical energy - as Raspberries. And hanging them out on bushes.

The number of comments one could make at this point is truly staggering, so one will say nothing at all.

As anyone (?) knows picking raspberries is a sticky process. When ripe they are soft and fragile and liable to squash flat at the drop of a basket. Furthermore they hide beneath leaves upon brittle and lethal canes.

Automation is called for. Automation has arrived.

Only problem is the prototypes smashed the berries to a pulp in no time flat. Slim pickings, and this is where our men in white coats with the fake raspberries come in. Hang these little fakers out with the real berries and if the machine doesn't smash them to bits too, you know exactly what forces are being produced at the crucial parts, and can adjust your machinery accordingly. Clever eh?

EMI produce the transducers, called Entran, and the Scottish Institute of Agricultural Engineering are the loonies . engineers who produced the model raspberries and went out hanging them - full moon?)

The actual accelerometers are only 3.6mm square, and give out 1mV for every 'g' of acceleration they are subjected to. The false fruits are wired up to both magnetic and pen-recorders to give full details of the fall of the raspberries. (I didn't believe it first time either. . . .)

Projected Index

Now this is one of those ideas that someone should have thought of long ago, and now that someone has the rest of us must hang our heads in shame and wonder why we didn't

A very clever man called M. L. Scaife has compiled a complete index of all electronic projects appearing in all the relevent magazines from 1972-1977. Next year a second listing will appear which will bring the index com-

Some 2500 projects are listed, all with brief description where components where applicable, a list of how many components are

used in each - and what they are - method of construction PCB. Veroboard etc, and source. Our sister magazine ETI is naturally included as are all ETI Specials.

Subjects are sensibly grouped together to make browing easy and the listings clearly and precisely done

This tryly amazing piece of work costs only £1.50 a copy from the patient M. L. Scarfe at: Cen-tral Library, Northumberland Square, North Shields, Tyne and Wear. Recommended in the strongest possible terms to all who ever intend to build a project again.

Pray Tell Prestel

LONDON'S Portman Hotel has become the first hotel to feature Prestel viewdata as a permanrnt feature at the hotel. The set, a Baird 26 colour model has been specially installed in the lobby where guests can access pages of information.

General Manager, Michel Favre, was first off the mark with Prestel with the help of Radio Rentals Contracts executives who, only eighteen months earlier, gave the Portman the first of commercially installed 100 teletext receivers at the hotel.

Technically Speaking

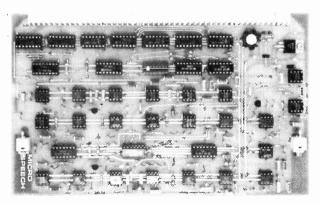
Computer Speech has come a long way since the Daleks first clanked their way across the TV screen. In the USA add-on devices for home systems (peripherals) can be purchased to make the small system talk in a reasonable - at least recognisable manner.

Microspeech is the first such unit to be released in the UK as far as we are aware. As you can see from the photo it is not a massive system at all. The program for the board converts typed in phonetic speech from the keyboard into sets of data which is then transmitted to the synthesiser.

There are nine parameters controlled by the unit which make possible manipulation of the frequency, amplitude and resonance of the final sound, and in this manner male speech can be reasonably imitated.

Microspeech also has an external input which enables it to produce 'talking instrument' effects from guitars and the like. The data coming out is converted back to audio form by an 8-bit digital-to-analogue (DAC) convertor using nine sample and hold circuits which can 'store' information for well over a minute.

Relatively little memory is used up by the system, and any system using BASIC in its langu-age can be adopted to operate with Microspeech. Costronics, 13 Pield Heath Avenue, Hillingdon, Middx.



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74154 100p 74155 90p 74156 90p 74157 70p 74159 190p 74160 100p 74161 100p 74161 100p	74LS193 14 74LS195 14 74LS195 14 74LS221 14 74LS240 17 74LS241 17 74LS242 17 74LS243 17	40p 40p 20p 40p 75p IR 75p IR 75p M 70p M	NTERFA	CE 100p 100p	LEDS 0.125 TIL32 I.R. TIL209 Red TIL211 Gr TIL212 Ye TIL216 Red	75p 13p 20p 25p 16p	TIL 78 0.2 TIL 220 Red TIL 222 Gr TIL 228 Red MV5491 TS Clips	70p 18p 18p 22p 120p 3p	All items are stocked in dept items are desp return.	normally hand stock	(a A low-cos to interfac	VDU : featured in P t memory-m e with all m	SYSTEM P.E. Nov/Dec 19 happed system hicro-compute	78) n designed er systems.
74164 120p 74165 130p 74166 140p 74167 200p 74170 240p 74172 720p 74173 120p 74173 93p	74LS251 14 74LS253 14 74LS257 12 74LS258 25 74LS259 16 74LS256 5 74LS273 13	70p 7 10p 7 10p 7 20p 7 50p 7 50p 7 50p 8	5150 5182 5324 5325 5451 5491/2 T26	230p 375p 375p 72p 96p 250p	DISPLAYS 3015F DL704 DL707 Red 707 Gr DL747 Red 747 Gr FND357	200p 140p 140p 225p 225p 120p	FND500 FND507 MAN3640 TiL311 TiL312/3 TiL321/2 TIL330 7750/60	120p 120p 175p 800p 110p 130p 140p 200p	Data Books on LINEARS, MEMO SISTORS ETC. AV	DRIES, TRAN-	Complete Ready-bui (Above prices	Kit ilt tested mo include VAT ar circui nooting serv	nd P&P and a free it design) /iCes available	£49 £69 Power Supply
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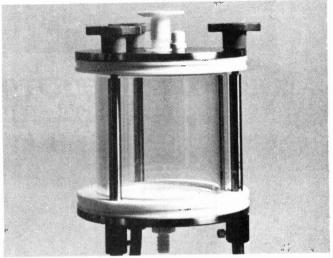
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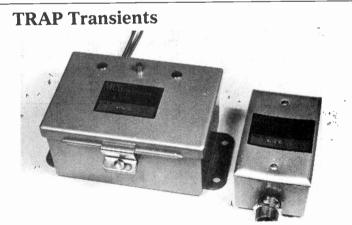
news digest



Technical Enquiry

WHEN this photo arrived on our desk it had been parted from the words which, probably, accompanied it when it entered our post

room. We have no idea what it is or does — any suggestions would be gratefully received.



A RANGE of mains transient absorbers is now available from Rhopoint Ltd, of Oxted, Surrey. Manufactured by MCG Electronics Inc, of USA, these transient absorbers are designed to protect electrical and electronic equipment from mains.borne high energy transients and spurious spikes, etc., on 120, 240 and 480 volts lines 50/60/400 Hz single phase and three phase, star or delta.

Operating in parallel with the mains supply, they control unwanted transients that appear on the mains supply to the user and are independent of the load drawn by the equipment from the supply. All voltages are clamped to just above the peak value of the mains and up to 480 joules of energy can be absorbed. The transients are clamped to a level where they are unable to cause interference or corruption, for example, of digital logic in difficult control areas. As such these transient absorbers are ideal for use in computer installations, process control instrumentation and with equipment where reliability of data information is critical and random equipment failure due to transients would be serious. Strategically placed MCG transient absorbers can protect this vulnerable equipment.

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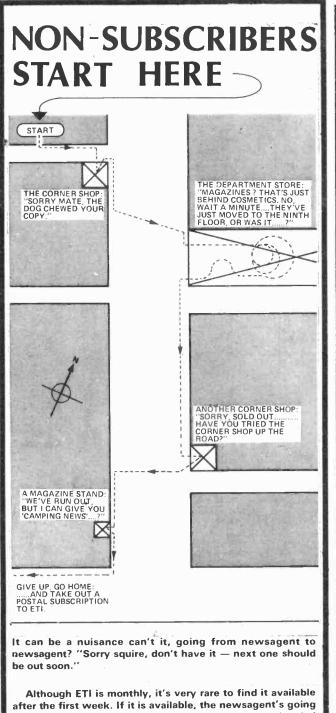
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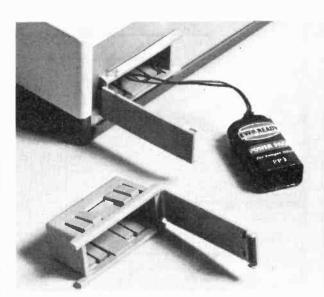
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news digest



Now Hear The Word of VERO

MOST battery powered equipment inevitably has numerous screws connecting the front panel to the circuit board, the circuit board chassis, the chassis to the case — now hear the word of Vero.

No longer is it necessary to dismantle a complete project to get to a poor exhausted battery, with their inspection-moulded battery housing you can provide access to the battery from outside the unit.

The holder accepts a 9V battery and may be easily fitted to a panel or enclosure with a thickness of 1,5 to 3mm. All that

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POLYESTER CAPACITORS: Axial lead type. (Values are in µ F). 400V: 0-001, 0-0015, 0-0022, 0-0033 7p; 0-0047, 0-0068, 0-01, 0-015, 0-018 9p; 0-022, 0-033.	AD149 70 BC307B 14 BF336 30 OC71* 25 ZTX326 40 2N3108 39 AD161* 42 BC308 13 BF394 22 OC72* 30 ZTX341 20 2N3442* 131
10p ; 0-047; 0-068 14p ; 0-1, 15p ; 0-15, 0-22, 22p ; 0-33, 0-47 39p ; 0-68 45p . 1609 ; 0-039; 0-15, 0-22 11p ; 0-33, 0-47 19p ; 0-68, 1-0 22p ; 1-6 29p ; 2-2 32p . 0.181 ; He ; 16009 ; 0-01; 0-015; 0 ; 0-02, 22p ; 0-68, 1-0 47 ; 48p . 0.181 ; He ; 16009 ; 0-01; 0-015; 0 ; 0-02, 22p ; 0-64, 747 ; 76p ; 0-1 38p ; 0-47 48p .	AD162* 42 BC327 15 BF594 40 0C74* 45 ZTX500 13 2N3613 20 AF106 70 BC328 13 BF595 38 0C75* 45 ZTX501 14 2N3614* 169
POLYESTER RADIAL LEAD (Values in µF). 250V:	AF114+ 30 BC33B 12 BFR39 25 OC76+ 36 ZTX502 19 2N3615+ 135 AF114+ 30 BC441+ 30 BFR40 25 OC79+ 76 ZTX502 19 2N3663+ 24 AF115+ 30 BC441+ 30 BFR40 25 OC79+ 76 ZTX502 15 2N3663+ 24 AF115+ 30 BC441+ 30 BFR40 25 OC79+ 76 ZTX504 25 2N3702 10
0-01, 0-015, 0-022, 0-027 5p; 0-033, 0-047, 0-068, 0-1 7p; 0-15 11p; 0-22, 0-33 CAPACITORS 13p; 0-47 15p; 0-68 18p; 1-0 24p; 1-5 27p; 2-2 31p. 1000pF / 350V 8p	AF110* 30 BC401* 30 BFR41 28 OC810* 28 ZTX531 25 2N3703 11
ELECTROLYTIC CAPACITORS: Axial lead type (Values are in #F). 400V. 10 40p; 47 68p; 250V 100 65p	AF121# 48 BC548 11 BFR81 28 OC83# 48 40250 # 85 2N3705 11
63 ¹ / ₂ ¹ / ₂ , 1.0, 1.5, 2.2, 2.5, 3.3, 4.7, 6.8, 8, 10, 15, 2.2, 8, 4.7, 15 , 0.4, 1p, 6.3, 1, 27 , p; 50 ¹ , 50, 100, 220, 259, 4700, 50 , 100, 66 <i>p</i> ; 40 ¹ , 22, 3.3, 7 p; 100, 12 p; 2200n300, 62 p; 47 00, 50 , 35 <i>y</i> ; 10 , 33, 7 p; 330, 470, 32 p; 1000, 49 p; 25 <i>y</i> ; 10, 22, 47, 6 p; 60 , 100, 160, 6 p; 42 0, 250, 13 p; 470, 640, 25 p; 1000, 47 0, 30 p; 30 , 47 0, 30 p; 30 , 47 0, 32 p; 1000, 42 p; 3 ² (1), 100, 12 , 4 ² (1), 5 (1), 10 , 10 , 10 , 10 , 16 , 10 , 15 , 10 ,	AF125+ 35 BC557 13 BFX29+ 26 OC122+ 48 40311+ 50 2N3707 10 AF126+ 50 BC557 13 BFX29+ 26 OC122+ 48 40311+ 50 2N3707 10 AF126+ 50 BC558 12 BFX81+ 130 OC123+ 48 40313+ 125 2N3708 10
27p; 1500, 30p; 2200, 41p; 3300, 58p; 4700, 68p; 1521, 410, 160, 180, 25p; 200, 250, 13p; 470, 640, 25p; 1000, 140, 68p; 1521, 100, 147, 68, 7p; 100, 125, 8p; 220, 330, 140; 470, 16p; 1000, 1500, 20p; 2200, 34p; 10V; 100, 6p; 640, 10p; 1000, 14p.	AF127* 35 BC559 20 BFX84* 24 OC139* 85 40315* 55 2N3709 10
TAG-END TYPE: 700: 200; 200; 220; 340; 100: 100; 50; 400; 100; 100; 100; 100; 100; 100; 10	AF178# 70 BCY34# 75 BFX86# 28 OC141# 85 40317# 52 2N3711 10 AF180# 70 BCY39# 80 BFX87# 23 OC170# 40 40319# 71 2N3771# 275
TANTALUM BEAD CAPACITOR'S POTENTIOMETERS (AB or EGEN) OPTO	AF186* 50 BCY40* 78 BFX88* 24 0C171* 40 40320* 56 2N3772* 170. AF239* 42 BCY42* 48 BFY18* 50 0C200* 48 40323* 60 2N3773* 288
35%: 0.1 μF, 0.22, 0.33, 0.47, 0.68. Carbon Track, ½W Log & ½W Linear values ELECTRONICS * 1-0, 2.2 μF, 3-3, 4-7, 6.8, 25 V: 1-5, 10 500Ω 1KΩ & 2KΩ (lin. only) Single gang EDs plus Clips 20V: 1-5, 16V: 10 μF 13p each. 500Ω 1KΩ & 2KΩ (lin. only) Single gang TIL209 Red 13p	AFZ11 128 BCY43 * 75 BFY50 * 20 OC201 * 75 40324 * 85 2N3819 22 ASY26 * 40 BCY58 * 22 BFY51 * 20 OC203 * 85 40326 * 52 2N3820 32
16V: 15. 22 25p. 47 35p. 100 50p. 5KΩ-2MΩ single gang 27p TiL211 Grn 17p	ASY27* 45 BCY59* 22 BFY52* 20 OC204* B5 40327* 62 2N3823* 65 ASY50* 95 BCY70* 15 BFY53* 28 SJE5039* 95 40347* 80 2N3824* 70 4 40347* 80 2N3824* 70
10V: 15, 22, 33, 20p; 100 35p. 6V: 47, 68, 100 30p; 3V: 100 20p. 5KΩ-2MΩ dual gang stereo 5KΩ-2MΩ dual gang stereo 70p 2" Red 18p 2" Red 18p 2" Red 18p 2" Red 18p	AST70* 95 BC771* 17 BF755* 45 ASZ21 60 BCY72* 17 BF764* 40 TIP29A 44 40360* 43 2N3903 18
MYLAR FILM CAPACITORS 100V: 0001 0002 0005 00145 5 SLIDER POTENTIOMETERS	BC107* 9 BCY78* 20 BFY71* 20 TP29B 56 40361* 45 273904 18 BC107B* 10 BC211 145 BSX20* 18 TP29C 60 40362* 48 2N3905 18 BC211 145 BSX20* 18 TP29C 60 40362* 48 2N3905 18
0-015, 0-02, 0-04, 0-05, 0-056 μF 7p 0.1 μF, 0-2 9p. 50V: 0.47 μF 12p 5KΩ-500KΩ single gang 70p LS400 0CP71 120p	BC108B* 12 BD115* 62 BSX29* 43 TIP30A 47 40407* 50 2N4037* 52
CERAMIC CAPACITORS 50V Self Stick Graduated Bezels 22p ORP61 85p	BC109* 9 BD123* 98 BSY95A* 25 TIP30C 65 40411* 285 2N4058* 17
Range: 0-5pF to 10,000pF 3p 3p QN5777 45p 0-015μ F, 0-022μ F, 0-033μ F 4p PRESET POTENTIOMETERS 7 Segment Displays	BC109C* 12 BD131* 45 BU205 190 TIP31A* 40 40467* 95 2N4062 13
0.047 μ F 4p; 0.1 μ F 6p, 0.1 W 50Ω – 5MΩ Miniature Vertical & TIL307 675 Forzontal 8p TIL307 675 SILVER MICA (Values in pF) 3-3, 4-7, 0.25W 100Ω – 3-3MΩ horiz, larger 10p TIL312 3° CA 105p	BC115 20 BD133* 43 E421 96 TIP31C* 66 40495* 90 2N4069 45. BC115 10 BD133* 43 E421 96 TIP32* 45 40603* 58 2N4236 145
Sitzver Mitca (values in pr) 3-3. 4-7. O-25W 100(13.3M(1) horr. larger 10p Til.313.3 3" CC 105p 6-8. 10.1 12.18.22. 33. 47. 50. 68. 75. 0-25W 200(2-4.7M(2) Vert. 10p Til.312.1 5" CA 115p 82. 85. 100.1 20. 150.220 9p each 100 Til.322.5" CC 115p	BC116 19 BD136* 37 MD8001* 158 TIP32A* 49 40636* 125 2N4286 20 BC117 20 BD137* 36 ME1120 25 TIP32B* 70 40673* 68 2N4289 20
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DENCO COILS BFC 5 chokes 91p 0A79 15 2A/400V 53 723±14 pin 45 LM3 DP' VALVE TYPE BFC 7 /18mH 91p 0A81 15 2A/600V 65 733± 99 LM3	08T 110 RC4136D 120 7403 14 7489 210 74182 90 54 28 192 130 11* 120 SAS560 240 7403 14 7490 33 74184 135 55 30 193 130
Range 1-5 B.Y.R.W 1FT 13/14/15/16/ 0A85 14 4A/100V 72 747C*14 pin 70 LM3 86p 17 74/C*14 pin 70 LM3	18H# 205 SAS570 240 7404 14 7491 75 74185 135 63 150 194 166 19S# 195 SG3402# 295 7405 18 7492 38 74188 275 73 46 195 136
6-7 B,Y,R 75p IFT 18/1.6 99p 0A91 7 4A/400V 79 753 8 pin 150 LM3 1-5 Green 92p 1FT 18/1.6 99p 0A95 8 4A/600V 105 810 159 LM3 IT 1-5 B Y B W 93 1FT 18/465 105p 0A900 8 4A/600V 105 810 159 LM3	27# 125 SL414A 275 7407 38 7494 78 74192 105 75 48 197 140 SN76003N 175 7408 17 7495 65 74193 105 75 40 221 96
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1μH. 4.7. 10. 22. 33 47. 100. 200 470 IN4003* 6 VM18 DIL 40 AY-1-6721/6 195 LM3 750, 1mH. 2.5. 5. 10 35p each IN4004/5* 6 VM18 DIL 40 AY-3-1015* 560 LM3	11N 145 SN 76115N 215 7413 30 74105 62 74198 150 90 60 245 270 82 125 SN 76131* 110 7414 51 74107 29 74279 110 91 104 247 190
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3 3 x 5 580 600 390 30 170 mmch AY-5-3507 * 415 MC1	304P 260 TAD100 150 7426 36 74122 46 75450 84 113 50 261 450 310P 149 TBA120S 70 7427 27 74123 48 75491 00 114 50 266 52
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Pin insertion tool 99p VARICAPS 1A100V 42 CA3018* 68 MC1 1A200V 47 CA3020 170 MC1	4954 395 5 0 0 2 10 11 250 7438 33 74142 269 02 16 132 95 293 128
VERO WIRING PEN★ MVAM2 135 14400V 52 CA3023 170 MC Plus Spool 325p MVAM115 14600V 70 CA302BA ± 80 MCC	7400 ± 150 18A810S 99 7441 74 74144 314 04 16 138 85 298 168 3340P ± 150 18A810S 99 7441 74 74144 314 04 16 138 85 298 168 3360P 170 7442 68 74145 65 05 200 139 85 324 240
Spare spool (wire) 80p * Combs 7p each BA102 25 5A100V 32 CA3035 240 MCC EEDDIC CLLI OPIDE BB104 40 7A300V 35 CA3043 110 MCC	401 70 .TBA9200 260 7443 115 74147 175 08 22 145 108 325 290 1780 205 TBA9900 395 7444 112 74148 109 09 22 147 170 326 294
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6 x 6 75p 90p 60p 8A800V 108 TIC45 25 CA3123E 200 NE5 6 x 12 130p 175p 1254100V 58 TIC45 25 CA3130+ 85 NE5	43K 210 11074CP* 199 7470 28 74161 92 26 48 61 98 308 66 44 185 11074CP* 52 7470 28 74163 92 27 28 162 138 373 180
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20p; 20 pin 2p; 22 pin 30p; 24 pin 30p; 16A400V 105 20p; 20 pin 2p; 22 pin 30p; 24 pin 30p; 16A400V 105 16A400V 105 16A400V 105 16A500V 155 16A500V 155 16A	00% 325 ZN414 90 7475 38 74167 198 33 39 166 226 379 215 61* 395 ZN414 90 7476 36 74177 625 37 39 170 288 384 86
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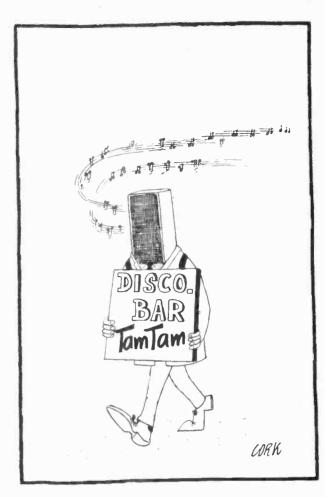
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100x75x40mm black 120x100x45mm black 120x100x45mm white

DIODE SCOOP!!!

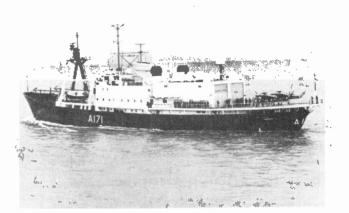
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news digest.



IMRC SATCOM For RN

. THE Royal Navy has had installed its first shipborne communications terminal for working via commercial maritime satellites. Supplied by International Marine Radio Company (IMRC) of Croydon, the terminal has been fitted into a Navy ice patrol vessel, HMS Endurance.

About a year ago the Navy, which had been watching the performance of the Marisat system, decided that there might be advantages in using commercial satellite communications for some of its non-strategic applications. The Navy sees the system being used, initially, on Naval auxiliary craft such as ice patrol vessels and perhaps, hydrographic survey ships. There are at present about 150 Marisat terminals on board merchant

ships — including one on the QE2. The terminal receives and transmits via retransmission from a satellite in synchronous orbit. That is, one which maintains its position over a particular point on the globe.

At present there are three such satellites, at 22,240 miles altitude over the Atlantic, Pacific and Indian Oceans. Corresponding shore stations are in Connecticut and California, with one in Japan serving the Indian Ocean satellite. These shore stations interconnect with the worldwide telephone/data and telex networks. Thus, a ship equipped with an appropriate terminal can exchange messages with any other telephone or telex user. Telex, voice, facsimile and data communications are also possible.

Cast Iron Seller



DAVID Griffin Ltd of Blandford. Dorset, has produced the Griffin Soldering kit, which includes a 25-watt soldering iron, a spare fine work bit, a reel of multi-core solder and a pair of tweezers. The

Griffin soldering iron is also available separately.

The expected retail price for the kit is £4.50 and £3.50 for the soldering iron.

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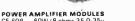
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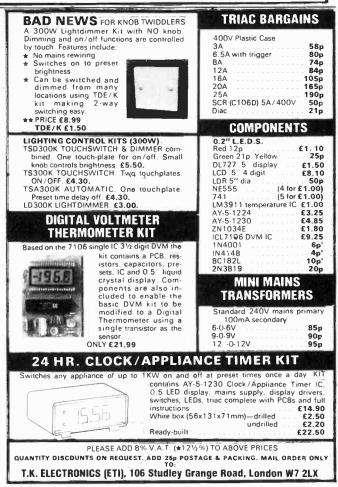
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FLECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979



COMPUTER SPEECH

Tim Orr takes a rest from his circuit mania this month to explain how speaking machines are moving off the TV screen and into the home computer market.

COMMUNICATION VIA SPEECH is a tremendously efficient way of transmitting information. A computer terminal with just a VDU or a hard copy printer compels the operator to be continually looking at the display. This limits the operator's freedom to do other jobs, such as controlling equipment, reading literature, typing, etc. If the computer had the option of being able to talk how much easier many operations would become. VDU's could also 'talk' their data and computer games could speak their instructions.

Computers have had this 'speech' option for many years, but as technology has improved, the size and cost of the equipment has been reduced to realistic proportions and the speech quality has got better. The microprocessor boom has helped this process and there are now several peripheral plug-ins that can be made to talk and even 'listen and understand'!

ROM For Improvement?

There are many methods by which a computer can generate speech. Some systems use a library of stored spoken text on a disc, just as the speaking clock does. Short phrases and individual words are sequentially selected by the computer programme and strung together to form the desired sentence.

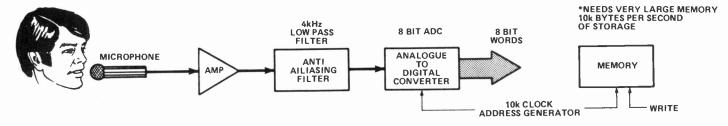
This technique is fine for some applications, where the set of phrases is small or where there will be no need to change them, because this means changing the disc. However, the unit is physically large and suffers from all the faults of any mechanical system.

An all electronic method of speech storage can be implemented using ROM's. Spoken words can be converted into a digital code (using an ADC), and programed into a ROM. Various words and phrases can then be selected by the computer and used to generate sentences by converting the reassembled data back into analogue information. This technique is the same in concept as the disc method, only the storage medium is electronic.

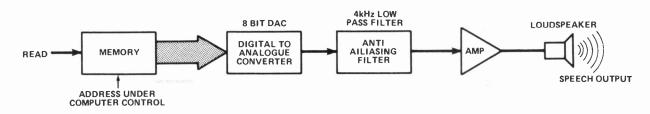
However, this type of storage would require enormous amounts of memory to generate short pieces of speech, because the unfortunate fact of life is that about 95% of the information stored by this method is redundant. The redundancy problem can be overcome by doing some special coding on the information. Linear predictive coding is one such technique, and this can result in very efficient ways of storing speech.

As A Rule

Yet another method of generating speech, which certainly gives the most versatile output (and is undoubtably the most complicated solution) is SPEECH SYN-THESIS BY RULE, using a speech synthesiser model controlled by data from the computer.



Block Diagram of the digital method of achieving voice storage.



ELECTRONICS TODAY INTERNATIONAL --- FEBRUARY 1979

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The phonetic code reads almost as if it were written in English (maybe someone will write a program to convert English to phonetic code?). Before discussing the speech program or the synthesiser it is desirable to explain just how human beings generate speech.

The Vocal Tract

Speech production has been studied for centuries and there have been many historical examples of 'mechanical talkers', that is mechanical models that can be manipulated so as to produce synthetic speech. These models generally have employed bellows, reeds and moveable acoustic resonators to synthesise the speech sounds and this is not too dissimilar from the real thing, the vocal tract, Fig. 1.

Air from the lungs is expelled through the vocal cords causing them to vibrate (when you breathe in the vocal cords don't vibrate — try it!). These vibrations produce a buzz which the speaker can control in pitch and volume. This buzz is coloured by a set of acoustic resonators known as the vocal tract.

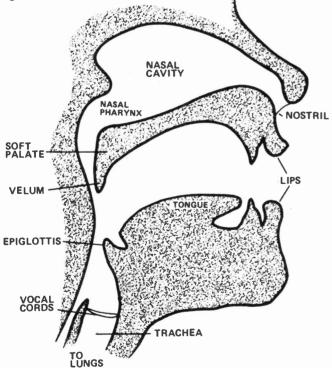
By opening and closing the mouth, by moving the tongue hump and by connecting or disconnecting the nasal cavity, the resonances of the tract can be manipulated so as to generate speech.

Take, for example some steady state vowels, AE as in HAD, EE as in HEED and OO as in WHO. Fig. 2 shows the acoustic frequency response for various vowels.

The operator types a phrase that is to be spoken. The phrase is spelled phonetically — it usually takes an operator a few hours to come to grips with the new way of spelling — and the computer converts the phrase into a series of parameters which control the speech synthesiser.

For example, the phrase 'Well, it can do with me' would be typed in as 'WEHL IHT KAAN DOO WIHTH MEE'.





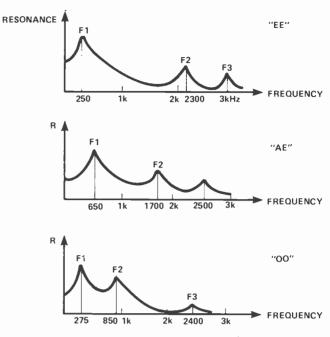


Fig 2. Acoustic response of some vowel sounds.

The first three peaks in the response, F1, 2, 3 are known as the first three formants. These are frequencies at which major resonances occur. For example, the 'OO' vowel has F1 and 2 close together at a low frequency and so the overall effect is a low frequency resonance. This is obtained by almost closing the mouth and pushing the tongue hump to the bridge of the mouth, whereas the 'AE' vowel is generated by opening the mouth and lowering the tongue hump.

Filter Vowels

It is possible to synthesise vowels by making an electronic model using active filters. If three band-pass filters (Q=5) are cascaded one after the other, set at frequencies of 660Hz, 1720Hz and 2410Hz and a saw-tooth wave form (100Hz) is injected into them, the resultant waveform will sound like the 'AE' vowel as in HAD.

A list of vowel resonances is given in Fig. 3. Note that they are for a typical MALE speaker.

À woman's voice is different in two respects. The resonances are about 10% higher because the vocal tract in women is about 10% smaller than that of a man.

Fig 3.	Listing o	f vowe	l resonances.
--------	-----------	--------	---------------

		FORMANT (ALL IN Hz)				
		F1	F2	F3		
H <u>EE</u> D	EE	270	2290	3010		
HID	1	390	1990	2550		
H <u>EA</u> D	Ε	530	1840	2480		
H <u>A</u> D	AE	660	1720	2410		
H <u>O</u> D	AH	730	1090	2440		
P <u>AW</u>	AW	570	840	2410		
н <u>оо</u> д	U	440	1020	2240		
wн <u>о</u>	00	300	870	2240		

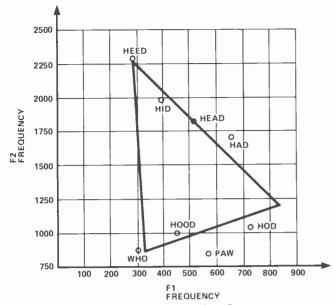


Fig 4. The vowel triangle!

Second, the pitch of the speech is perhaps an octave higher. These two effects characterise female speech as distinct from male.

Note that the formants 1 and 2 move over quite a wide range, but F3 doesn't move much at all.

However, including F3 in a model does help to improve the intelligibility. If we plot out F1 versus F2, we get what is called the 'vowel triangle', Fig. 4. Try gliding from the PAW vowel to the WHO vowel. The resulting

Say Through The Nose?

When the mouth is closed, virtually no sound comes out of it(!) However there is a secondary path via the nasal, cavity, which is available when the velum is open. The group of sounds generated via this route are known as NASALS. They include such sounds as 'M' as in MAN, 'n' as in NUT and 'ng' as in STING. The nasal cavity is virtually a static resonator and so all nasal sounds have an undynamic quality about them.

Vowels, dipthongs and nasals are all voiced sounds, that is they are all pitched, being generated by the vocal cords. There is a group of sounds called fricatives which are pitchless and are generated by blowing air between the teeth and lips. These sounds are the 'th', 'f', 's' and 'sh' noises and are very similar to bandpass filtered noise. 'Th' can be modelled by a bandpass filter at 8kHz whereas at the lowest frequency, 'sh' is modelled by a 2k5 Hz filter.

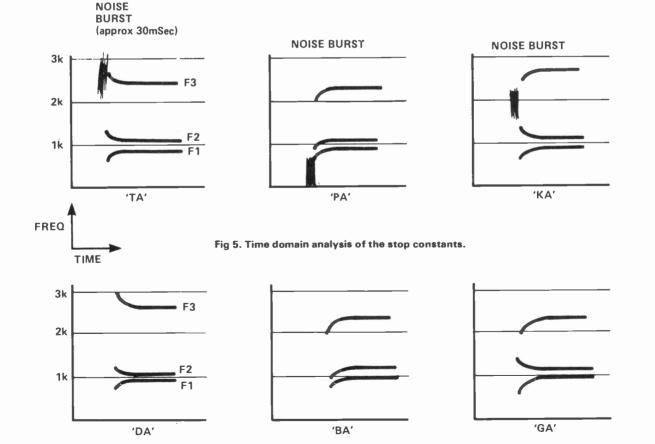
Constantly Talking

There are many other types of sounds but for the purposes of brevity we will consider only one more, the STOP CONSONANT. These sounds are characterised by a sudden opening of the mouth. This produces two effects.

One, there must be a period of silence (if only briefly), before the sound is generated.

Two, as the mouth opens, the formants rapidly move toward temporary target positions.

The stop consonants, 'T', 'P', 'K', 'D', 'B', 'G' are shown in Fig. 5. The vowel 'AH' has been used in this



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example and so the stop consonants are 'Ta', 'Pa', 'Ka', 'Da', 'Ba', 'Ga'. The first group are characterised by having a small noise burst which preceeds the opening of the mouth.

This burst only lasts for about 30 to 50 mS and it has a different resonant frequency for each of the examples. However, it is a very important phonetic element and does much to characterise the sound.

The lower group of stop consonants has no noise burst. This is the major difference between these two sets of sounds.

Verbal Circuits

Well, that's the end of the very rapid phonetics lecture, now for the electronics. The speech synthesiser must be able to model the vocal tract. It needs a voltage controlled oscillator, a noise generator, a controlled fricative formant, a controlled set of formants F1, 2, 3 and a nasal resonator. There are 9 parameters in this model which need controlling. These are:-

- AH amplitude of aspired sounds.
- AV amplitude of vowels sounds. _
- ΑF _ amplitude of fricative sounds.
- AN amplitude of nasal sounds.
- F-1 frequency of formant 1.
- F2 frequency of formant 2.
- F3 frequency of formant 3.
- Ff frequency of fricative formant.
- Fv frequency of oscillator.

The model is known as a serial 3 formant synthesiser with parallel fricative and nasal formants. The computer delivers data which is converted into 9 voltages which represent the 9 parameters.

It is entirely up to the computer to generate the

parameters correctly, the synthesiser merely does what it is told to do.

Speech Less Latches

The parameter generator is shown in Fig. 7. When the computer decides to deliver a frame of information it sends out an address and a data block. This address is unique to this peripheral device and is decoded by an address decoder inside the synthesiser. This decoded address generates a clock pulse which clocks a 12 bit latch

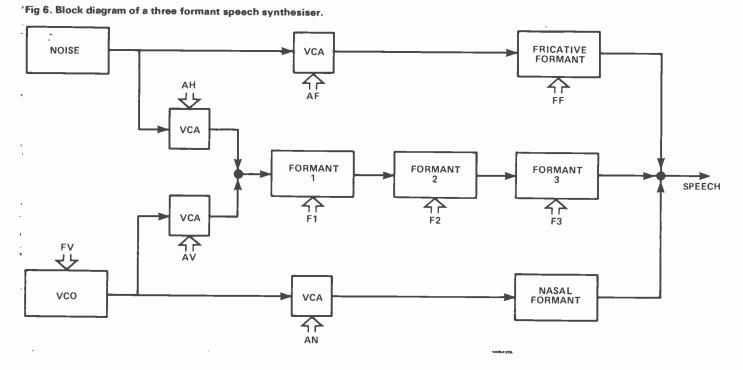
Four of these 12 bits of data are another address which decides which of the 9 parameters is being updated. The other 8 bits are data which drive an 8 bit DAC. The analogue output from this DAC is fed to a demultiplexer which drives 9 sample and hold units.

Thus the 8 bit data word is converted into a control voltage and is then steered by the 4 bit address into the correct sample and hold. The whole frame of 9 parameters is updated 50 times a second. This consumes only a small percentage of the computer time, and yet it allows the speech program to be run on a slower time scale without the steps between frames becoming noticeable.

Pitch In

The program was written so as to make the operator's job as easy as possible. There is a listing of about 50 phonemes which can be used to generate speech. Gaps can be typed in and changes to existing sentences can easily be implemented.

The pitch of the speech is controllable so that the correct pitch inflections can be used to stress various words. Also, an external sound source can be used in place of the VCO so that effects such as 'talking music' can be produced.



FEATURE: Computer Speech

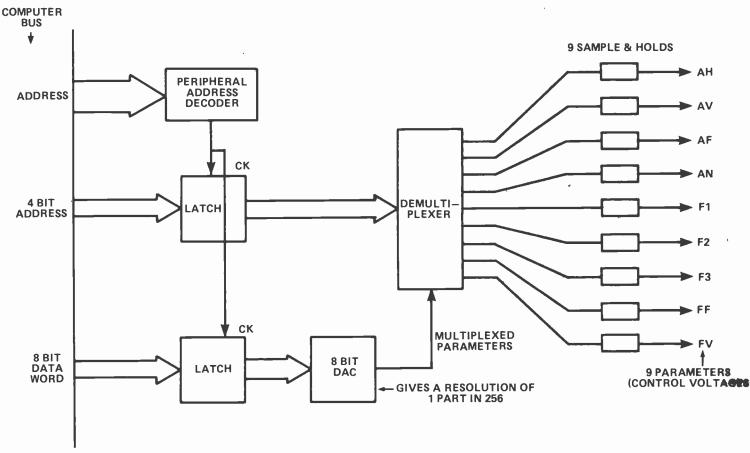


Fig 7. Block diagram of a parameter generator for a speech synthesis machine.

Resumé of Speech Products

The number of speech products that are being produced is rapidly increasing. Here is a list of some of them.

Texas Instruments have brought out a teaching aid called 'speak and spell'. This unit has an alphabetical keyboard plus display. The word that is typed in is spoken by a ROM that uses a linear predecive coding technique, enabling more than 200 words to be stored.

Federal Screw works make a speech synthesiser called Votrax. It generates speech by rule and it can be used as a computer peripheral or as a stand alone unit.

They also make a speech synthesiser which is a bit like a large pocket calculator, except that words are printed next to the buttons. This is intended as a limited talker for people with speech loss.

Telesensory systems make a 'talking' pocket calculator, a 'speaking chip set' and they are also working on a reading tool for the blind. This uses a little hand-held camera which converts the printed text into letters which are then converted into speech.

OVE III made by Fonema is a speech synthesiser similar to that described in this article. However, it uses lots of

parameters and the speech output can be better than the real thing!

Speech Lab made by Heuristics is a microprocessor peripheral. This device recognises the spoken word (after you have trained it to do so). The manufacturers claim real time operation and a 95% correct recognition rate.

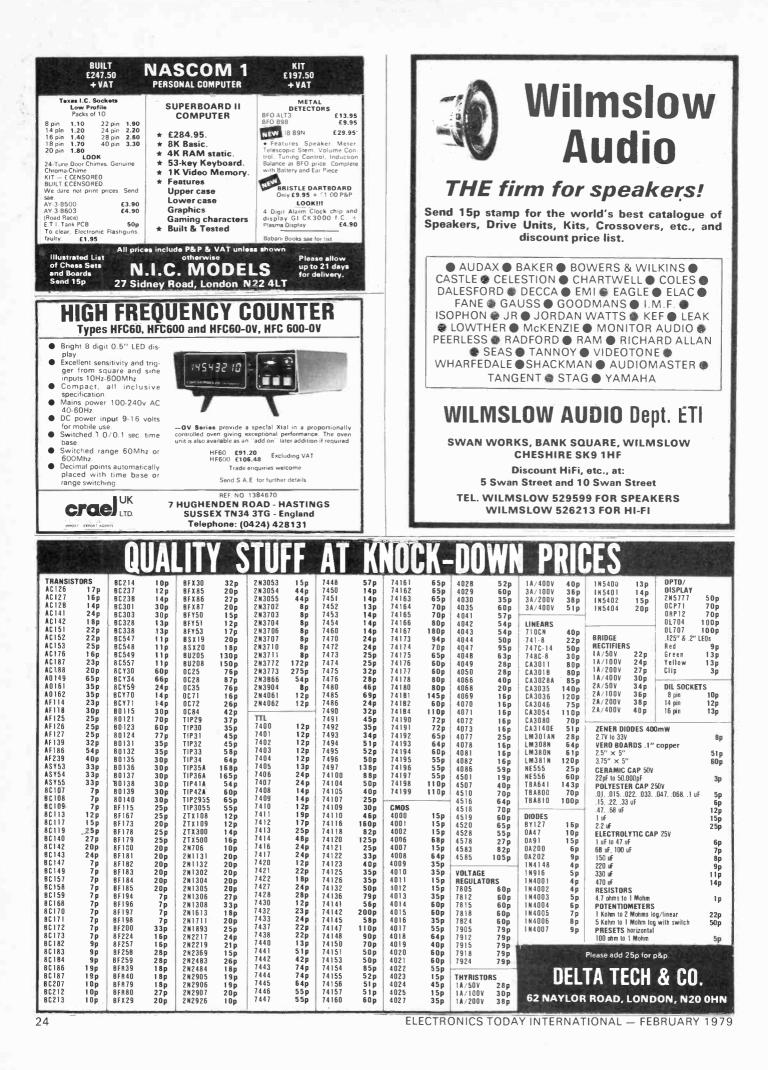
Computalker made by Computalker Consultants is a microprocessor peripheral speech synthesiser using the vocal tract analogue as described in this article.

Microspeech made by Richard Monkhouse and Tim Orr. A microprocessor peripheral speech synthesiser designed to run from 6800 orientated systems.

Vocoder and Vocoder 2000. The first commercially available channel vocoders for the music market manufactured by EMS. Enables normally inarticulate sounds to speak. (For example, talking pianos.)

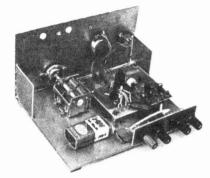
Vocaliser pedal made by Coloursound. A music product, not a Wah-wah pedal but a vowel pedal. Vowels available EE to AH to OO.

Dipthoniser made by Coloursound. Produces dipthong filter sweeps primarily for bass guitar. Sounds such as BOW, YEH, WAH and YAE are available.



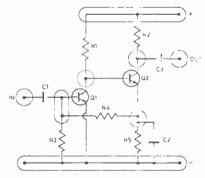
Hobby Electronics

Short Wave Receiver



Back in the bad-old-days when there were just valves, and they were expensive, you thought hard before adding another stage of amplification: first you tried to be clever. Our SW radio next month uses just two semiconductors yet will give surprisingly good performance over the range 55-25 MHz if used with a reasonable aerial.

Instant Circuit Layout



Do you have trouble getting your brain to translate a .circuit into a practical layout? If so you're in good company but next month join the elite by overcoming this. We give you practical advice on how to lay out components from practically any circuit diagram.

Scratch/rumble Filter

An add-on circuit to couple to an existing audio system, this project enables you to select the cut-off frequency of the system at both ends of the spectrum.



Radioactivity

Although most people shudder when they think of radioactivity, there's a lot more to it than fallout from nuclear bombs. Radioactivity is widely used in medicine and in industry and our article describes some of the uses and traces the history of its development.

Video Tape Recorders



The age of the Video Cassette Recorder has arrived and soon they'll be common. However, they are the most complex, sophisticated, pieces of engineering that have ever crossed the doormat in reasonable numbers. Next month we explain how they work and take a look at the different systems being offered.

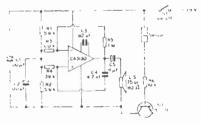
OST Rules, OK? Got the hang of Ohms Law? No problem but the world isn't yours yet — have you ever found a problem it can't cope with? The chances are that you have. However, there are two other approaches to help you solve the nasty ones: Superposition and Thevenin's Theorem. They may sound complex but in reality they make life simpler.

Sine / Square Wave Generator



An essential part of anybody's test gear and our project next month enables you to add one to your workbench at low cost.

Projects Using The CA3130



We publish one chapter from R. A. Penfolds '50 Circuits using the CA3130' (brought out by Babani) and mighty interesting they are too. The projects include an electronic organ, metronome, alarm and latching circuits.

Holograms



Today they are only a curiousity, shown as exhibits and as special effects but much work has been going on behind the scenes. Today's Holograms really make you wonder if you can believe your eyes.

The February issue will be on sale on January 12th

The items mentioned here are those planned for the next issue but circumstances may affect the actual content.

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TAPE SLIDE SYNCHRONISER

This must rate as one of the most requested projects of all time for us! This tape synchroniser uses a notched 100Hz tone to achieve its ends as neatly as possible.

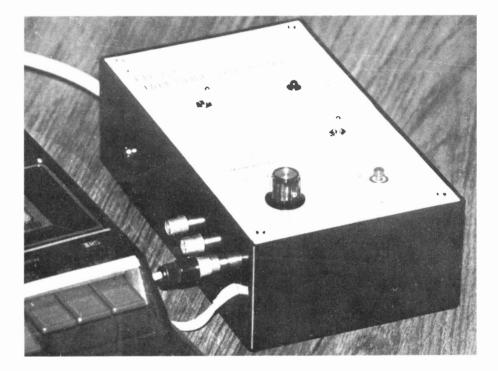
WHEN PUTTING on a slide show for your friends or a business meeting, it is usually necessary to have some commentary with it. If it is a one-time presentation this is no problem, but if the show is to be repeated or if you simply want to be able to recall good memories a couple of years later then a tape recording of the commentary is ideal. The problem now is to keep the slides changing in synchronization with the commentary, without having to record that obtrusive phrase 'change slide now' onto the tape.

This unit allows a control tone (100 Hz) to be recorded on the tape along with the normal voice recording; when replayed the tone will activiate a relay which will change the slide while a notch filter removes the tone so it is not heard through the speaker.

Construction

Assemble the PCB with the aid of the component overlay in Fig. 1. With the 240 V wiring it is better not to use pins but solder the wires directly onto the PCB. A covering of epoxy glue over the tracks leading to the transformer will help to prevent accidental contact.

We built the prototype into a large plastic box with the controls on the front panel and the tape recorder/amplifier connections on the rear. The wiring of the front panel is given in Fig. 3. We used an electret microphone insert mounted just behind the front panel. However the noise of the relay operating could be heard on the tape and therefore an external microphone is



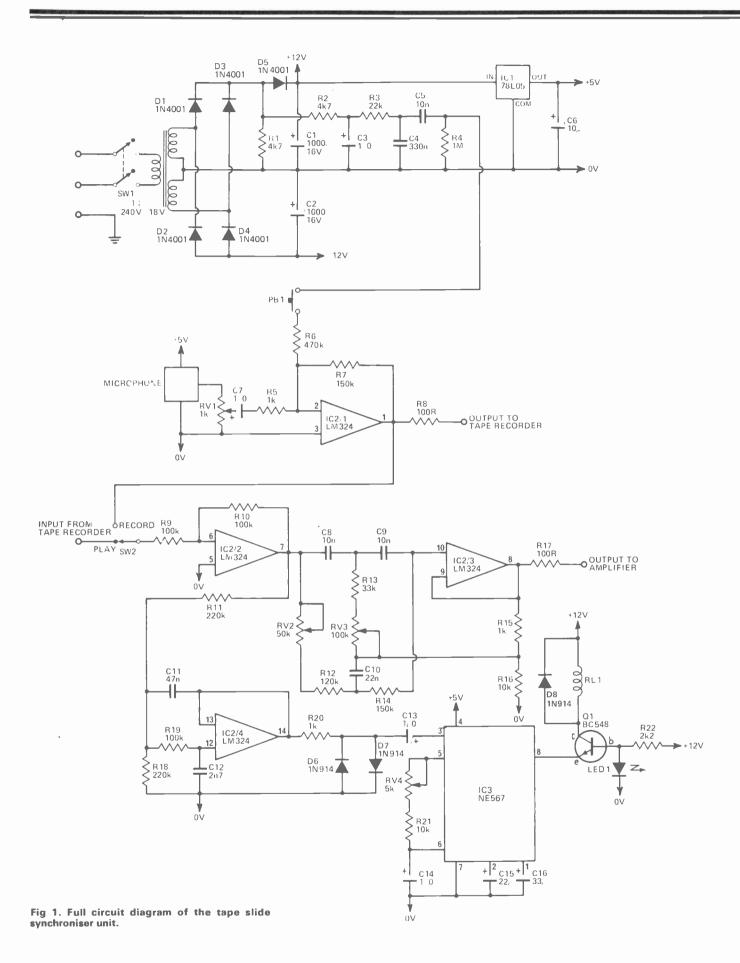
recommended. A socket can be mounted on the front panel in the microphone position.

Using the Unit

With this unit a separate amplifier/speaker system is needed. Also the slide projector must have a remote change button using normally open contacts. Connection has to be made between these contacts and the relay in the unit. Check that these wires are isolated from the 240 V mains and if not be very careful with the connections.

Connect the unit to the tape recorder and projector, assemble the slides in the correct order and switch on. With the record / playback switch in the record position and the recorder set to record, commence the commentary, changing slides with the button on the unit. The high level input on the recorder should be used and the microphone level pot set to give the correct recording level.

When playing back simply set the record / playback switch to playback and replay the tape.



PROJECT: Slide Synch

HOW IT WORKS

With this unit, unlike our previous design, we record a 100 Hz tone burst on the same channel as the speech whenever we require a slide to be changed. The tone is derived by full wave rectifying the output of the transformer and filtering out the harmonics by R2, 3/C3.4.

Pressing the slide change button mixes this tone with the output from the microphone which is amplified by IC2/1. This combined output is recorded on the tape.

In the record mode SW2 connects the output of IC2/1 to the buffer amplifier IC2/2. In the playback mode it connects the output from the tape recorder to the amplifier. The output of this amplifier is split into two paths. One of these is through a 100 Hz notch filter to IC2/3 effectively removing the 100 Hz tone without much change to the rest of the spectrum. This is used to drive an amplifier /speaker system.

The other path for the signal after IC2/2is via a low pass filter IC2/4. This removes frequencies above 150 Hz and has a response as shown in Fig. 2. When the 100 Hz tone occurs, this filter passes it, rejecting speech frequencies, and it is passed to IC3. This is a phase locked loop tone decoder and its output on pin 8 turns on when the correct frequency tone is received. The output stage of this IC is an open collector npn transistor which can sink but not source current. With no incoming tone this transistor will be off, preventing any emitter current in QI, hence turning it off also. The voltage on the base of Ql in this case will be set at 0V6 by LED1. When a tone occurs the output of the IC will saturate to about 0V6, forward biasing Q1, turning it on, and closing the relay. The current in R22 is now bypassed into the base of Q1, giving about 1V2 on the base. This is too low for the LED to conduct and it will go out.

The power supply is simply full wave rectified and filtered for IC2, and a 5V regulator is used for the PLL IC and the microphone amp.

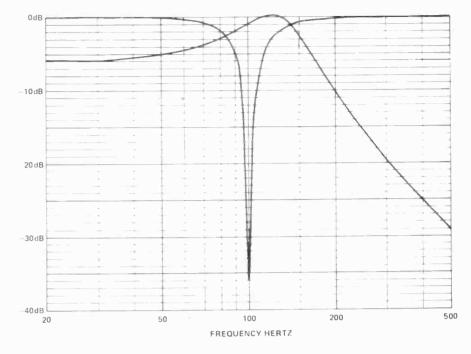
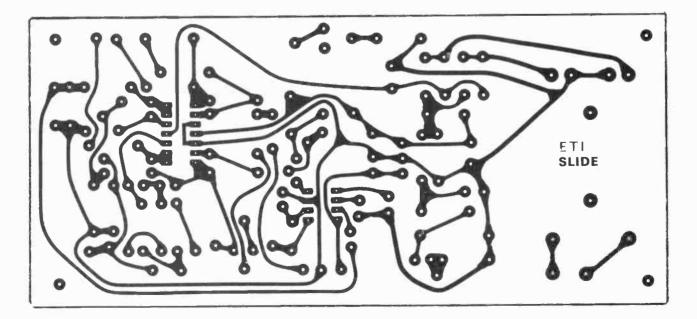


Fig 2. The frequency responses of the notch and low pass filter sections of the circuit.

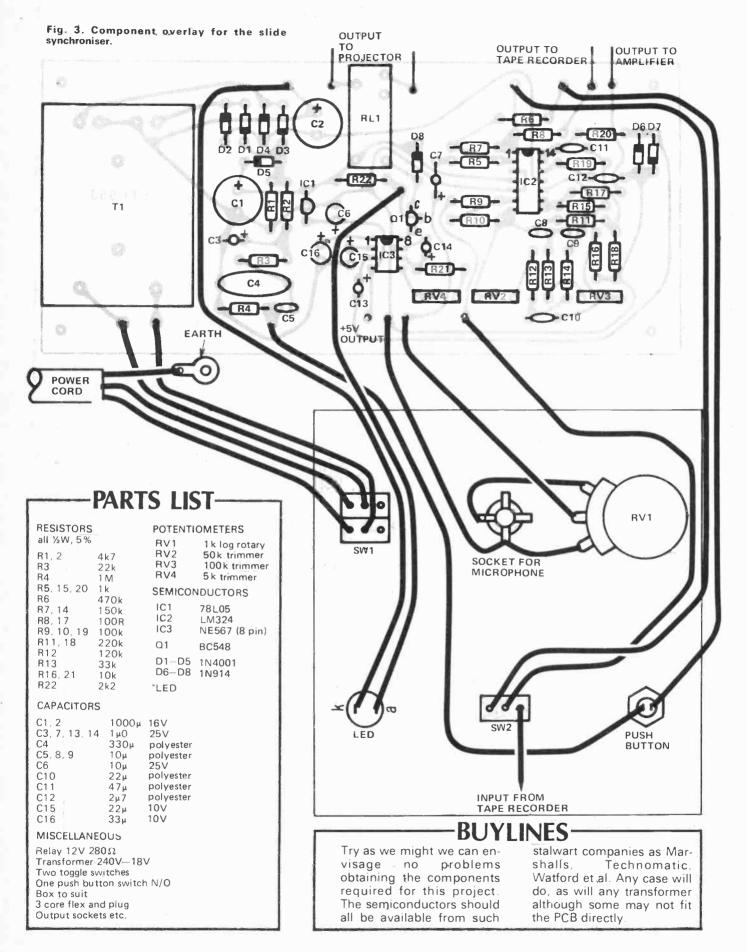
Adjustments

Set the unit up to record and with all trimpots at the centre of their travel and the microphone level at minimum, hold the slide change button down. Probably some 100 Hz signal will be heard on the output of the amplifier. Alternately adjust RV2 and RV3 to minimise this signal. It should be necessary to wind up the volume of the amplifier to finally adjust for a minimum level. The other adjustment is of the phase locked loop centre frequency. With the push button pressed slowly rotate RV4 until the relay either opens or closes. If it closes, continue to rotate it until it drops out then bring the pot back to the half way point. If the relay opened, reverse the rotation to find the other point at which it opens and leave RV4 midway between these two points.

Check the operation of the relay when pressing the button. There should be about half a second delay before it closes.



PROIECT: Slide Synch



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THE CIRCUIT SYMBOL of the voltage-current transactor (VCT) is shown in Fig. 1 with both voltage input and current output terminals floating. In the future it is expected that single chip VCTs (Ron Harris, ETI) will challenge the familiar op-amp as the universal linear circuit building block. At present, however, these have yet to emerge. In the meantime considerable familiarity with the VCT concept and with its circuit applications may be achieved by building a PCB version using readily available IC transistor arrays.

A single-ended VCT (C.A. Holt, "Electronic Circuits: Digital and Analog" p.788) is shown in Fig. 2. The floating output version of Fig. 3 corresponds to the circuit discussed before (J. E. Morris, ETI August 1977). In both figures the unfamiliar symbols (boxes) are intended to represent current mirrors. Ideally, the output from the high impedance current source(s) exactly equals the input current into the low impedance terminal (arrow-head). VCT operation is based upon these current mirrors.

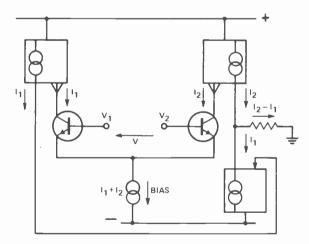


Fig. 2. Single-ended VCT (e.g. CA3080 operational transconductance amplifier.

No attempt will be made here to duplicate the earlier explanation of circuit operation which is expected to be reasonably clear from the diagrams (Figs. 2 and 3) anyway). The essential point is that the differential input voltage $V_1 - V_2$ leads to an imbalance in the currents flowing in the two halves of the symmetrical circuit and that this imbalance is translated into a load current I. The load is driven by constant current sources (high impedance) and the input impedance is high to minimise

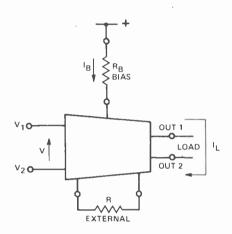


Fig. 1. VCT symbol and external connections.

input signal loading. With the system of Fig. 3, load current is given by

$$I_{L} = \frac{2}{3} Z (V_{1} - V_{2}) / R_{EXT}$$

up to the point where the bias current is exhausted, i.e. for $I_L < {}^2\!\!/_3 I_B$

Transistor Arrays

The original intention of the project described here was to build the current mirrors using perfectly matched transistor arrays in miniature flat IC packages. These were to be mounted on an alumina substrate with printed thick film interconnections in the circuit described in the earlier articles. As is often the case with electronics, however, the realities of the situation dictated a very different course.

In the first place both miniature package arrays and arrays of matched transistors were neither readily available nor acceptably priced! After some searching of the data books, we settled for the RCA arrays CA3084 and CA3086 on the basis of price and availability. (The pin diagrams for these are reproduced in Fig. 4). Not all of the components in these packages are used, in particular, the Darlington transistor D in the CA3084 is not employed in the VCT circuit.

The first point to be determined was the effectiveness of these transistor arrays in current mirror circuits. No claim is made for transistor matching in the CA3086 other than the obvious one of thermal matching. In the CA3084, Q_3 and Q_4 are obviously organized as current mirror outputs and Q_1 , Q_2 are described as a matched

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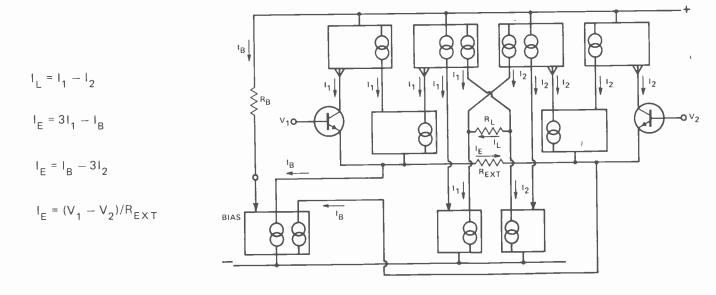


Fig. 3. VCT with floating input.

pair. The specifications on Q₁, Q₂ look impressive and those of Q₃, Q₄ seem rather inadequate (Fig. 4) but in fact for current mirror applications the reverse is true in both cases. To put these specifications into perspective, consider two similar base-emitter junctions where I_{E1}, I_{S1} exp (e V_{EB1}/kT) and I_{E2}, I_{S2} exp (e V_{EB2}/kT). Suppose these two junctions may be regarded as extremely well matched e.g. to ± 1 mV in V_{BE} carrying identical currents I_E. Substitution above leads to

 $I_{E} = I_{S1} \exp (eV_{EB}/kT)$ = $I_{S2} \exp (eV_{EB}/kT \exp (e 10^{-3}/kT))$

and if equal $V_{\mbox{\scriptsize EB}\mbox{'s}}$ are now specified for the current mirror application

$$E_{E2} = I_{S2} \exp (eV_{EB}/kT)$$

= $I_{S1} \exp (-e \ 10^{.3}/kT)$
= V_{EB}/kT
= $I_{E1} \exp - (e \ 10^{.3}/kT)$

ł

At room temperature, kT 1/40eV and I_{E2} 0.96 I_{E1}. So a \pm 1 mV matching in V_{BE} leads to a 4-5% error in a current mirror application. In this light, Q₃ – Q₄ seem to be reasonably matched for the purpose and Q₁ – Q₂ less so.

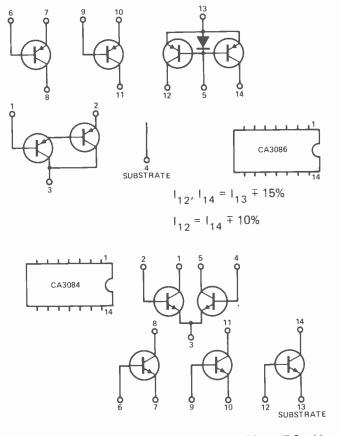
Clearly, the point is best resolved by direct measurement of current mirror performance using the arrays themselves.

Current Mirrors

As a first step, the transistors were checked for matching. For the CA3086, all transistors (except possibly the substrate transistor Q_5 whose measurements were later deemed to be suspect) were matched to within a 12 mV spread for a given current up to 500 μ A. This figure reduces to a low 1 mV range at 1 mA and increases again with increasing current to about 9 mV at 10 mA. (All measurements at V_{CE} = 3V.) It is only possible to

measure terminal characteristics of Q_1 and Q_2 in the CA3084 and from 10 μ A to 10 mA, V_{BE} values were matched to within 1 mV.

Fig. 4. IC transistor arrays — pin connections and CA3084 specifications. (S — substrate connection to most negative point).



 Q_1, Q_2 : for $I_7 = I_{10}, V_{BE1} = V_{BE2} \mp 6 \text{ mV}$ (all specs at $I_C = 100 \ \mu\text{A}$).

The performance of the CA3084 current mirror is shown in Fig. 5 and that of a more complex system in Fig. 6. Clearly, the extra components of the more complicated circuit (which are all subject to variations from the nominal device parameters), lead to increased discrepancies in the output current. On the other hand, the simple circuit (as found within the CA3084 chip, for example), provides output matching within specification although the absolute level is lower than expected.

With the CA3086 a slightly different measurement technique was employed (Figs. 7 and 8) where transistor gain was permitted to vary with V_{CE} . This accounts for the curvatures of the output characteristics in Fig. 7. In Fig. 8, the performance of the more complex system is seen to be clearly inadequate. (The transistors in these two diagrams with base and collector shorted together function as diodes, as does Q_4 in Fig. 6).

The results of this section led immediately to the decision to use only the basic type of current mirror. Both types were examined in the earlier article (ETI August, 1977) and the more complicated form is used in the prototype single chip VCT. The advantage of the complex circuit is that it performs better with low gain transistors but the typical h_{FE} figures of 100 and 40 (for the CA3086 and CA3084 respectively) are expected to be adequate. The problem with the system being developed here is that of poor matching and an elementary worst case analysis demonstrates the superiority of a minimal component count.

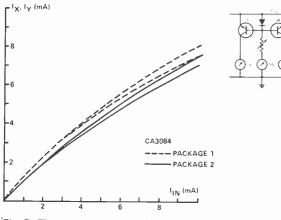


Fig. 5. Elementary current mirror — output matching test.

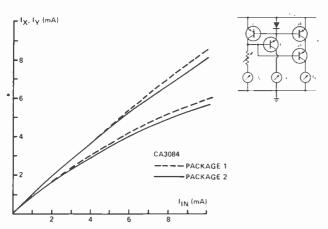


Fig. 6. Complex current mirror output matching test.

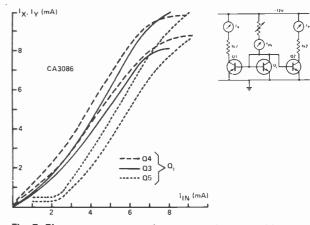


Fig. 7. Elementary current mirror - transistor matching.

Discrete VCT

The actual circuit employed is shown in Fig. 9 and differs markedly from the one discussed in the earlier articles. In the first place, the simple current mirror has been used throughout for reasons given above. Second, there is obviously no opportunity to provide current gain by utilising multiple emitter transistors since these are not provided in the arrays. (This is no disadvantage for the purpose of a familiarisation exercise.) The third discrepancy is apparent by comparison of Fig. 9 with Fig. 3. In recognition of device parameter variations and the asymmetry which these will necessary cause, the bias circuit has been split into two independent sources. In effect, this provides both bias and offset capabilities. Usually one would employ Darlingtons as the input transistors. This step would require an extra CA3086 and has been omitted.

A fifth difference lies with the elimination of any link between the input circuit current mirrors of the two sides. The circuit described in previous articles uses the complex current mirror with diodes shared between the two sides of the VCT. This set-up has been simulated

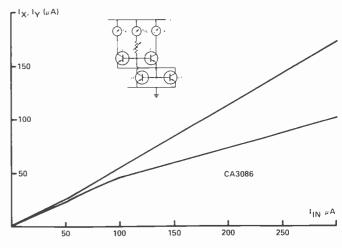


Fig. 8. Complex current mirror — output matching.

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FEATURE: VCT

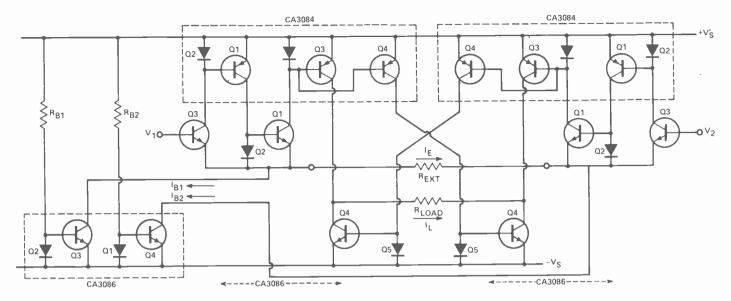
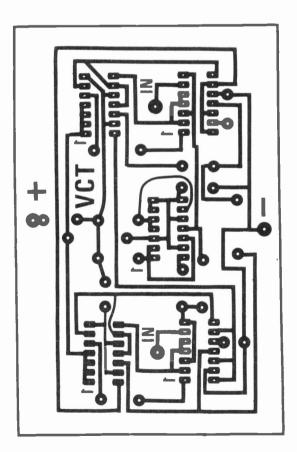


Fig. 9. Simplified VCT design employing IC transistor arrays.

(Fig. 10) and found to be ineffective as a means of compensation for bias imbalance between the two sides. If I_{IN2} is increased, for example, the base current of Q_2 and hence I_{02} increase with a compensating decrease in I_{01} . A link of this type is not possible with the simple mirror system adopted here, but would not have been employed with the more complicated circuit anyway.

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A printed circuit board layout is shown in Fig. 11. No claim is made with regard to the optimal quality of this layout but it seems satisfactory.



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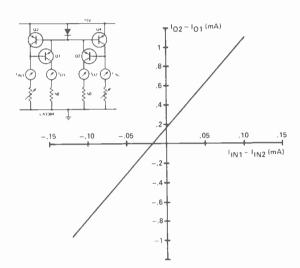


Fig. 10. Effect of linking current mirrors within the VCT.

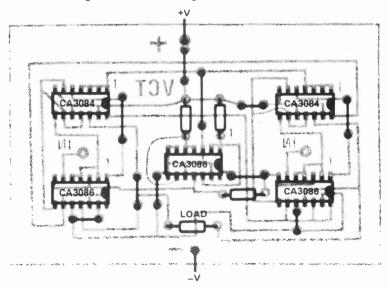


Fig. 11. PCB layout, showing external components, wire links, etc. — view from component side.

VCT Performance

It is not the function of this article to present an exhaustive survey of the circuit's performance in varied applications. Many of these have been proposed elsewhere (ETI, amongst others) and the reader is left to try these individually with his own discrete VCT. There are, however, a few pitfalls which warrant further discussion. Most of these may be classed as limitations of the non-ideal system.

Two VCTs were constructed and these are identified as 'a' and 'b' from here on. In general, they performed similarly, but there were some significant differences. Unless stated otherwise, below, supply voltages of ±10 volts were employed with $R_{EXT} = 1k$ and only current monitoring as loads. The first test was to establish bias current levels to achieve a null output. $R_{{\scriptscriptstyle B1}}$ was set to 4kwith each unit. For VCTa, $R_{B2} = 5k212$ and for VCTb, $R_{B2} = 5k552$ established zero output currents for $V_1 =$ $V_2 = 0V$. Drifts (of the order of 1 μ A for VCTa and 40 μ A for VCTb) were noted over the next few minutes and R_{B2} was finally set to 5k2 for VCTa and 5k6 for VCTb. In the test for common mode rejection (Fig. 12) the residual offset and the magnitude of short term drift effects are apparent (output levels must significantly exceed these drifts). As the supply voltages are approached, transistors begin to cut off and this may in turn lead to unpredictable effects depending on the relative parameters of the various devices. The lesson to avoid approaching the supply rails is clear. While the common-mode rejection ratio seems satisfactory for VCTa (Fig. 12), the curve for VCTb clearly indicates an asymmetry in the circuit, i.e. there is at least one transistor mismatched to its counterpart on the other side (this mismatch is most likely in a variation of gain with V_{CE}).

Fig. 13 shows the standard transfer characteristics. The slight variations in slope are due to R_{EXT} tolerances

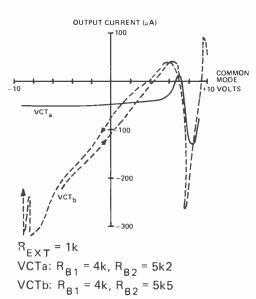


Fig. 13. Transfer characteristics.

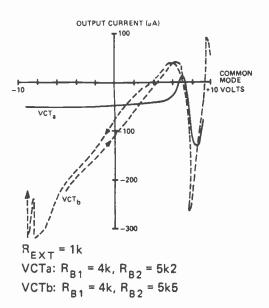


Fig. 12. Common mode signal transfer (V1 = V2).

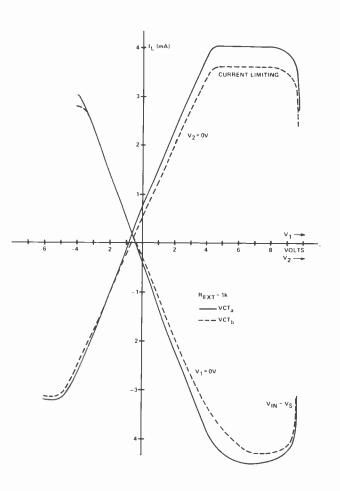


Fig. 14. Transfer characteristics with one end of the load grounded and with corresponding input grounded.

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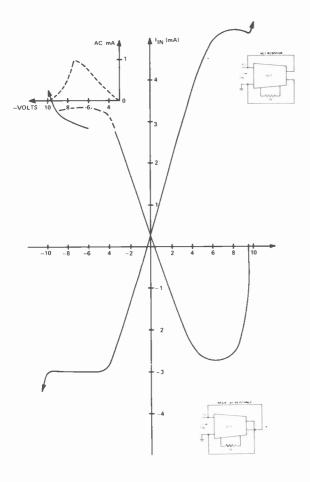
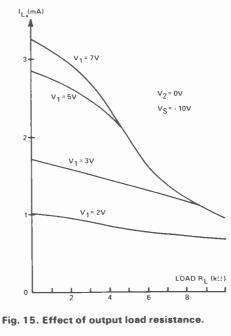


Fig. 16. VCT 'resistors' - positive and negative.

and the measured values (VCTa: 1mA/12V and 6mA/7.75V for R_{EXT} "10k, 1k exceed expectation slightly (c.f. 1mA/15V, 6mA/9V) due to a small current gain cuased by transistor mismatching. This effect also leads to small discrepancies from the expected current limit levels (e.g. $\frac{2}{3} \times 20V/4k$ and $\frac{2}{3} \times 20V/5k2$).

The results described in the preceding paragraph were obtained with one input grounded as a matter of convenience. There is a dramatic shift in the offset current when one end of the load is also grounded (Fig. 14) and when both these fixed points are switched to the other side of the VCT. It would seem that the concept of 'floating' input and output require re-examination.

It must be noted that while one might expect the two ends of the floating load to sit at approximately zero volts, it does not take a great deal of device variation to produce extreme deviations from this. In both the circuits built here, two output transistors (Q_4 , Q_5 of CA3086, see Fig. 9) were saturated at null output. (With different selection of devices, saturation of CA3084 Q_3 and Q_4 is equally likely). This creates no problems for the floating load unit high frequency or switching applications where performance will be down-graded by transsistor saturation. It does, however, mean that the output must be rezeroed if either end of the load is to be tied to a fixed potential as in Fig. 14.



The four constant current sources which comprise the output circuitry lead inevitably to saturation as soon as there is an imbalance between them. In many cases zeroing the output current aggrevates the problem. If device selection is contemplated, these are the transistors to consider first.

Variation of load current with load impedance (Fig. 15) suggests that the output impedance is about 40k substiantially below the 60-100 k range expected from the transistor specifications because of saturation. The differential small signal input resistance has been measured at about 35k which is approximately $h_{FE} R_{EXT}$. This figure would be increased by the use of Darlington input transistors.

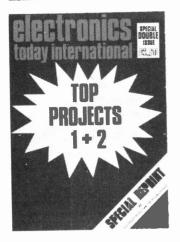
Applying Exotics

Up until this stage, none of the more exotic circuit applications has been discussed. A few remarks should be made, however, in closing. Ideally the output is a constant current and can be used to linearly charge a capacitor, e.g. to provide integration. The constant current sourcing is not perfect however and integrating applications will be limited to frequencies greater than $(2\pi R_{OUT}C)^{-1}$. A similar limitation will exist for gyrator performance.

A gyrator was built with the two VCTs but oscillated. The oscillation is believed to originate, however, with the use of inadequate power supplies — another point to note in investigating these circuits — rather than with that circuit itself. Gyrators operate on a negative immitance conversion principle so it is instructive to consider the resistance applications of the VCT in Fig. 16 where the terminal resistance case an oscillation region was identified (see inset). If the negative resistance circuit is examined, it clearly provides positive feedback if the driving source (V₂) impedance is not zero.

In closing I wish to acknowledge the assistance of Jock Howie and others with this project.





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TAPE NOISE LIMITER

Takes the hiss out of the quiet bits of your music, and does in a way which is simple yet effective, and is a replay only process so it will work on any tape!

DESPITE the small size, the performance obtainable from a cassette tape in a good recording deck is quite remarkable. In fact the latest top quality decks are so good that it is difficult to tell the difference between the recording and the original sound.

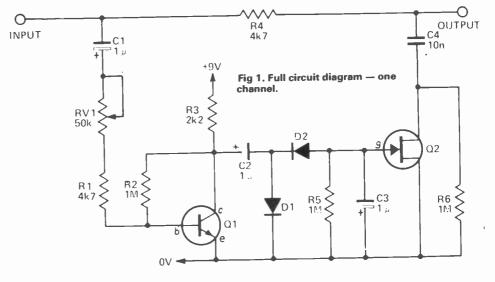
Unfortunately this is not true of the cheaper units — in which 'tape hiss' can be very prominent. Tape hiss is caused by random irregularities in a tape's surface coating. The effect is common to all tapes but some are marginally worse than others.

The annoying characteristic of tape hiss delayed the acceptance of cassette tape recorders in hi-fi systems for some years — until the advent of the Dolby system which was primarily developed as a cure for the phenomenon.

The Dolby system is often misunderstood — *it only works if the cassette tape itself has been recorded using the Dolby process* — and few commercially produced tapes are. Unless the tape cassette says specifically that it is Dolby processed then it's not! You can of course record your own tapes using Dolby if you own a Dolby machine.

Upper Limit

To overcome this limitation a number of cassette recorders are fitted with noise reduction circuitry which reduces the level of hiss on non-Dolby recordings. Most of these noise reducing circuits work by progressively reducing all high frequency signals when the output level falls below a preset minimum. Above that minimum level all sounds are allowed through because tape hiss cannot be heard once the sound



HOW IT WORKS

The circuit passes all frequencies (without attenuation) if the incoming signal is above a set minimum level. Signals below the preset minimum are progressively attenuated from 1 kHz upwards. The maximum attenuation of about 10 dB is applied at approx 10 kHz.

Resistor R4 and capacitor C4 form a filter in which Q2 is used as a variable resistor with the degree of resistance dependant on gate voltage. Thus, if the input voltage is at or near 0V then Q2 appears as a low resistance and C4 is in circuit. If on the other hand the input signal is higher than (say) four volts negative, Q2 has a very high resistance and C4 is effectively out of circuit.

The voltage applied to the gate of Q2 is that derived from Q1 — after rectification by D1 and D2. Transistor Q1 amplifies the input signal and with RV1 in minimum position, input signals above 10 mV or so will cause Q2 to be off.

Increasing RV1 raises the level below which high cut will occur. The change from full to zero cut occurs over a range of approx 5 dB input level change.

level is substantially louder than the hiss. This effect is called 'acoustic masking'.

The circuit described in this project is a simple but very effective unit which may be used with any cassette recorder which is connected to a hi-fi system.

The unit should preferably be connected between the cassette

recorder and the amplifier input using short lengths of screened cable and suitable connecting plugs. If you really know what you're doing it may be actually built into the tape recorder or amplifier. Alternatively it may be connected between the pre-amplifier and power amplifier on those units which are so separated (note that many apparently integral

PROJECT : Tape DNL

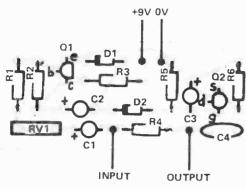


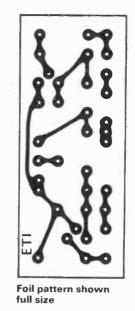
Fig 2. Above: Component overlay.

amplifiers still have 'pre-amp out' and 'power-amp in' connectors on the rear panel. These connectors are normally bridged by 'U' shaped links — which should be removed to enable this unit to be plugged in).

Construction

As with most projects in this series you can use either Veroboard or the special printed circuit board shown here.

Take the ususal precautions about inserting components the right way round — taking particular care with the field effect transistor Q2. Note that the cathode lead of the diodes



(shown as a horizontal bar on the circuit diagram) will be identified on the component by a black band or similar marking.

Unless the leads between this unit and the tape deck and amplifier are very short it is advisable to connect it via screened cable. Note that the OV line shown on the circuit is also the 'earthy' side of the input/output connections.

To set up the unit simply choose a

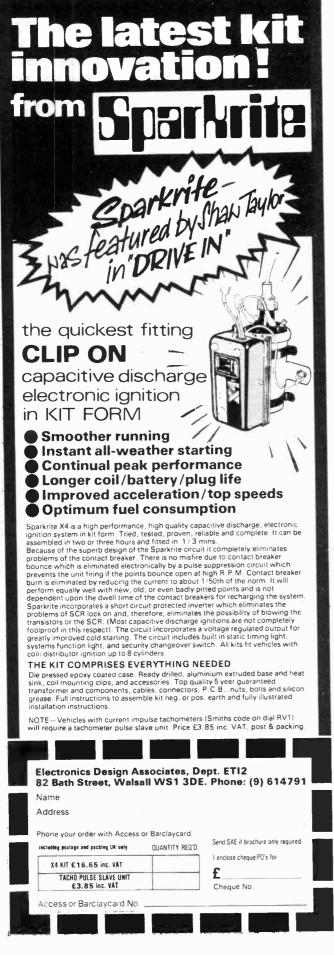
RTS LIST
W 5% 4k7 1M 2k2
R 50 k trimpot
1 uF 25 V 10n polyester
BC548 2N5459
1N914 JS and clip, PCB case.

recording with a longish quiet passage and then adjust RV1 for the best compromise between tape hiss reduction and minimum loss of high frequency programme content.

ETI

NOTE: If you listen only to hard rock — where there aren't any quiet passages — then this unit will be of little value to you. Its main effect is to reduce annoying tape hiss during otherwise quiet programme material.





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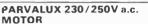
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A HISTORY OF CAR IGNITION

Ian Sinclair takes us back through the clouds of time (and exhaust!) to the beginnings of ignition, and sparks some interest on the way.

EVER SINCE the first motor car made its first coughing movements, designers have had the problem of ignition. Perhap's that's what encouraged the development of steam cars for so long, it's worth remembering that the Stanley Steamer held several speed records in its day, and was still being manufactured in the twenties.

The petrol engine still works in the same way as it did then. As the piston descends, a valve opens and lets the mixture of petrol vapour and air enter the cylinder. At about the end of this induction stroke, the valve closes and then the piston starts to rise, compressing the mixture (compression stroke). Near the top end of its travel, the mixture has to be fired - and that's the job of the ignition department. Firing the mixture is what provides the power, driving the piston down, and keeping things going. At the next upward movement of the piston another valve opens, letting the exhaust gases escape. This four-stroke scheme has survived pretty well unaltered in principle, though with many improvements in details. The ignition of the mixture is one of the rather important details which has changed quite a lot since the first four stroke petrol engines were tried out.

What A Gas!

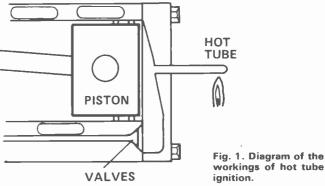
The first petrol engines used for ignition a scheme which had been quite acceptable for large gas engines. A small hole is drilled at the top of the cylinder (into the cylinder-head) and a lamp flame is allowed to burn close to the hole. This is easily done in a gas-engine by having a pilot-jet burning near the hole.

On the compression stroke, mixture is driven out of the hole, meets the flame, ignites, and the burning mixture blows back through the hole to ignite the rest of the mixture. Primitive, certainly, but quite effective for a large gas engine as long as you're not looking for high performance. The main problem here is that much of the mixture is lost, and it's very difficult to be sure that ignition won't be too soon.

Hot Tubes

1

For petrol engines, this was soon replaced by hottube ignition. As the name suggests, the end of the cylinder-head was formed into the shape of a sealed tube



workings of hot tube

the end of which was kept hot by a small blowlamp. The timing of the ignition still isn't under much control, but at least no mixture is lost, and the hot tube ignition was used on a lot of famous cars of the veteran period (before 1904).

Modern times start with electric ignition systems, and there are still plenty of cars running around with electrical ignition systems which would have been familiar to a mechanic seventy years ago. Oddly enough, it's not all that well understood so let's take a close look at it.

Highs And Lows

There are two parts to the ignition system, the LT and the HT (sparks). The LT circuit consists of the contact breaker and the primary winding of the ignition coil and the HT of the secondary winding of the coil, the distributor (which ensures that the spark goes to the correct plug) and the plugs themselves.

The contact breaker is a switch operated by a cam which runs at half of engine speed and has as many bumps (lobes) on it as there are cylinders in the engine. The spark occurs just as the switch contacts (the points) open, so that we can alter the timing of the spark by rotating the switch assembly slightly.

When the contact points are closed, current flows in the circuit through the primary of the coil and the points. The primary winding of the coil has a large inductance, and obeys exactly the same laws as any other large inductance-if we want the current through the coil to ►

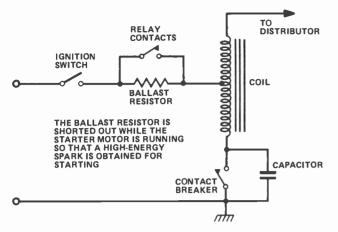


Fig 2. Simple electrical ignition

charge rapidly we need a high voltage; if we cause the current to charge rapidly, the coil will generate a very high voltage.

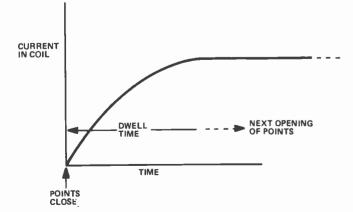
Make A Point Of It

When the points close, the current through the coil increases following the graph of Fig. 3. This time is called the dwell time, and the points must remain closed for long enough to give the current time to reach its final value. When the points open, the current is rapidly broken, causing the coil to generate a high voltage pulse from the collapsing magnet field.

This high voltage pulse is then stepped up by the transformer action of the secondary winding, giving about 20-25 kV to send a good spark cracking across the gap of the spark plug — we hope.

With such a simple system the life of the points can be rather short, and the spark at the plug low in voltage because of sparking at the points. The reason is that the voltage pulse which occurs whenever the points separate is enough to cause a spark at the points. This keeps some current flowing in the coil, so that the change is not so rapid as it should be. We can avoid these problems to some extent by connecting a capacitor (they still call it a condenser in garages) across the

Fig 3. Graph of current through coil.

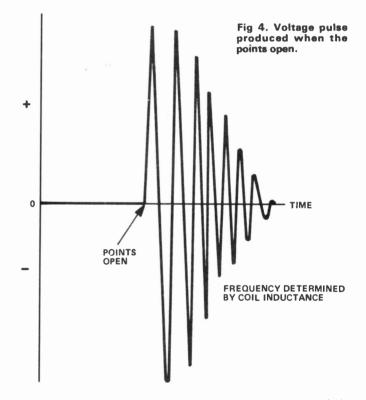


points. When the points open, the voltage pulse produced by the coil (Fig 4) starts charging the capacitor, giving the points time to open and so avoid the worst of the sparking. Because this also results in a more rapid charge of current, the spark at the plugs is very much better when a capacitor is used.

Bad Points

The whole system works very well indeed, and is remarkably reliable but suffers from two disadvantages. One is that a fast revving engine with a large number of cylinders may not permit enough dwell time to allow current to build up fully before the next opening of the points. The other problem is that there is still some sparking at the points, so that the contacts wear unevenly and have to be reset at intervals, and ultimately replaced.

Electronic ignition systems use the same coil and HT equipment, but a different method of obtaining a quick charge of current through the coil. Most modern systems



use capacitor discharge in which an inverter circuit is used to generate about 400 V DC to charge a capacitor.

When the points open the capacitor is discharged through the primary winding of the coil, and this voltage pulse is stepped up by the transformer action of the coil to provide the high voltage from firing the plugs. Because the capacitor can be recharged quickly, the dwell time that is needed is reduced, and because the contact points only have to cause a thyristor to fire, they need carry only a fairly small current and because they do not carry current to the coil, there is no voltage pulse across them.

The points still need periodic adjustment though, because the rubbing of the cam against the fibre peg which operates the points causes wear, altering the

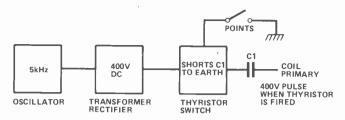


Fig 5. Typical modern system.

timing. To avoid such mechanical problems the cam can be replaced with a slotted cover rotating around an infra-red source (an LED), and so interrupting a beam which strikes a photocell. This then triggers the thyristor. Using this system, no adjustments are needed to compensate for mechanical wear until the gears on the shaft which drives the ignition system wear down—by which time the rest of the engine will have worn out anyway.

This is the system which enables car manufacturers to offer five year guarantees, and to promise 100,000 miles between adjustments of the ignition system.

Point Of No Return

Unfortunately, all this ingenuity does not ensure reliability, and electronic ignition systems have obtained a very bad name for causing accidents. The most common heart-stopper is that the ignition simply ceases—and if you're overtaking at the time it can be fatal. The other is completely erratic timing, with the engine knocking horribly and the sparks happening at any old time in the cycle—I limped home several miles like this once.

These problems can be solved, and car manufacturers who have gone over to electronic ignition have solved them. Nothing on earth, however, would persu-

Complete Ignition system. Note the position of critical components.

ade me to use an electronic ignition system unless each component was marked with a manufacturer's name and the ratings. A lot of DIY systems seem to use Brand X components—and that's asking for trouble.

The components which are critical are the inverter circuit, the transformer for the inverter, the chargedischarge capacitor, and the thyristor itself Fig 6. The inverter circuit must keep oscillating (though the frequency may change) even as the thyristor fires, shortcircuiting the output of the transformer. This is, in turn, possible only if the transformer is correctly designed for the job.

The charge-discharge capacitor has to provide large pulses of current, and must be rated to take much more than the normal 400 V to allow for surges. The thyristor must also be able to withstand the full voltage of the inverter, plus any surges, and to pass the pulses of current to the coil. A 1000 V 10 A thyristor may seem excessive, but is very much more reliable than the usual 600 V 1A component.

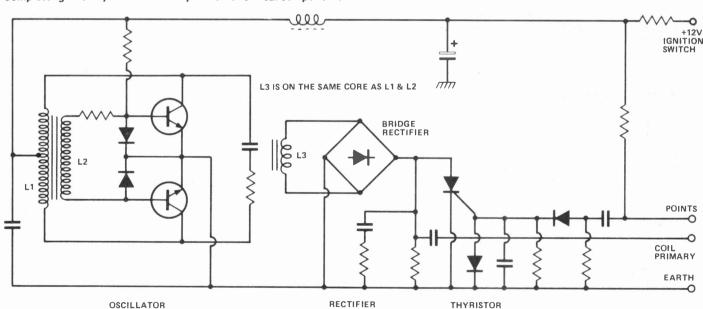
Good Points

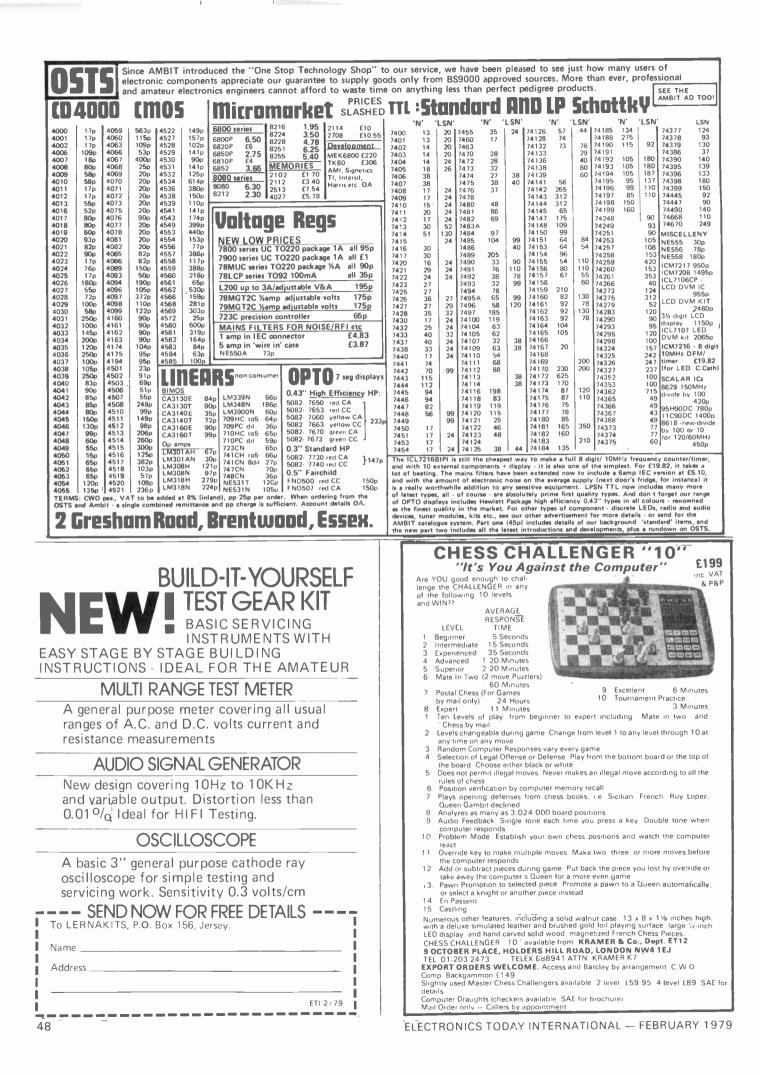
Many exaggerated claims are made for electronic ignition system, but the hard facts are that the main advantage is a longer time between ignition setting, particularly if the infra-red beam system is used. Cold starting can be better but only if the inverter uses a voltage regulator, which is rather rare.

Against this, reliability may be less, unless the whole circuit is built from top-grade military-specification components, rated to work at temperatures from well below freezing (you want it to start in the winter) to near boiling point (after it has stood out in the blazing sun for several hours).

However, there's little doubt that the well designed electronic ignition systems now being designed into cars by the manufacturers are quite definitely up to the job, with very great reliability and freedom from adjustment.

ET







Production of the new catalogue has been held up for a few weeks - since we have just been appointed as distributors for two of the most exciting ranges of radio components products yet : The Micrometals range of iron dust torroids cores and formers, and the OKI range of VLSI for digital frequency displays for receivers. formers, and the OKI range of VLSI for digital frequency displays for receivers. We apologize for any inconvenience, but these two ranges are really worth the wait, and include some products you will find hard to believe, like the MSM5523 IC, an IC with less than ten external components that gives AM frequency readout to 1kHz from LW to 39.999MHz, FM frequency readout in 100kHz steps - (all usual IF offsets programmable by diodes), a 24 hour format clock with 12 hour display, independent on and off timers, time signals on the hours, stopwatch facility and a sleep timer. This costs £14 with its timebase crystal, and makes all that has gone before an expensive and time wasting excercise. Rather like the way the Intersil ICM7216 has revolutionized the instrument counter market. (See the OSTS ad.) And those of you familiar with Amidon and IG dust torroids, favoured, in many new RF designs, will be pleased to know Ambit will be stocking a broad range of the Micrometals types for applications from EMI filters to RF PA stages. OKI frequency counter ICs: details in cat2 [A brief summary of some of our range of ICs:

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TERMS etc: CWO please, VAT on Ambit Items is generally 12%%, except where marked (*). Catalogue part 1:45p, part 2 50p all inclusive. Postage 25p per order, carriage on tuner kits £3. Phone Brentwood (0277) 216029/227050 9am-7pm. Callers welcome inc. Saturdays.



ELECTRONICS TODAY INTERNATIONAL -- FEBRUARY 1979

At last, DIY Hi Fi which looks as if it isn't.

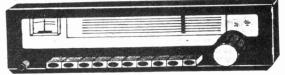
That's not to say it doesn't look like HiFi - just that it doesn't look like the usual sort of thing you have come to associate with DIY HiFi. The Mk3 outstrips and outperforms all British made HiFi tuners, and most imported ones too. Certainly at the price, there isn't one near it. But more than that, it looks superb . A small pic here would be an insult, so send an SAE for details on the kit that looks as if isn't. It's something else.....

Exceptionally high performance - exceptionally straightforward assembly Baseboard and plug-in construction. Future circuit developments will readily plug in, to keep the MkII at the forefront of technical achievement Various options and module line-ups possible to enable an installment approach the system

and now previewing the matching 60W/channel VMOS amplifier:

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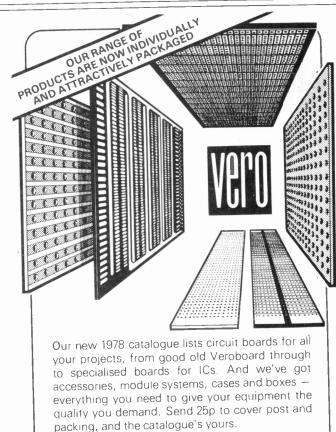
The PW Dorchester·LW,MW,SW,&FM stereo tuner



In much the same way as we have swept away the 'old technology' in frequency/timer counters - with the OKI and Intersil single IC counters, we now offer a single IC "All Ba radio tuner. Don't confuse this one chip radio with things like the ZN414 - for this is a genuine superhet receiver with a mechanical AM IF filter, and ceramic IF filters for FM. "All Band" genuine superhet receiver with a mechanical AM IF filter, and ceramic IF filters for FM. The AM section employs a balanced input mixer section, covering all broadcast bands - plus a BFO and MOSFET product decetor for SSB/CW - though at this price, the tuner is not intended as a "communications receiver" - although we know of many lesser designs that make that claim. The AM sensitivity is nevertheless better than 5uV, and FM sensitivity is 1.2uV for 30dB S/N. As a multiband broadcast superhet receiver, it is a unique constructor project that fulfills the requests we very frequently get for a general coverage circuit that sn't over complicated. The set has CA3089E FM performance, with mute etc., and a PLL store decoder with full plust tone filtering. isn't over complicated. The set has Choose I in performance, that index set, and 32 of 32

The case/cabinet with PSU, meter and mechanics etc $\pounds 2$ An SAE for full details please. See the feature article in Practical Wireless (Dec/Jan)

2 Gresham Road, Brentwood, Essex.



VERO ELECTRONICS LTD. RETAIL DEPT. Industrial Estate, Chandlers Ford, Hants. SO5 3ZR Telephone Chandlers Ford (04215) 2956

LIGHT ACTIVATED TACHOMETER

By using optical sensing this unit allows measurement of rotational speed without the need for actual contact!

THE USE OF a non-contact method of measuring RPM is not only convenient but sometimes the only method possible. Some motors used for model aircraft have a capacity of only 0.15cc yet run at speeds in the 25000 RPM region. The power required to turn a mechanical tacho would be many times the power of such a motor. Also on some machines there is no convenient place a normal tacho can be fitted.

Design Features

As the main application for this unit was to be outdoors it was decided that an LCD display would be preferable to an LED and more easy to read than an analogue meter. Unfortunately LCDs are not yet readily available, and nor are the ICs needed to drive them.

However the Intersil Evaluation kit which we have used in the past is fairly easy to get hold of, and so we based the design around this unit. This meant converting the pulses from the sensor into a voltage. This however has another benefit in that a greater resolution can be obtained more quickly. To have a resolution of 10 RPM with a two bladed propeller a sample time of three seconds would be necessary.

The use of the BPW34 photodiode in the photovoltaic mode, ie actually generating a voltage, simplifies the biasing otherwise needed.

Construction

All the electronic components are mounted on a single card with the exception of the photodiode. To save on real estate the main voltmeter IC is mounted under the display.

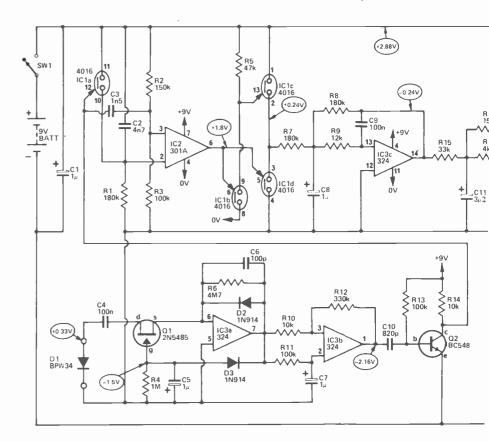
Initially, assemble all the components apart from the ICs and the

RPM range Low High Resolution Display

SPECIFICATION

Detection method

Power Battery life (216) 0 - 20000 . 10000 - 30000 10 RPM 12mm LCD reflected light 9V @4mA about 150 hours



PROJECT

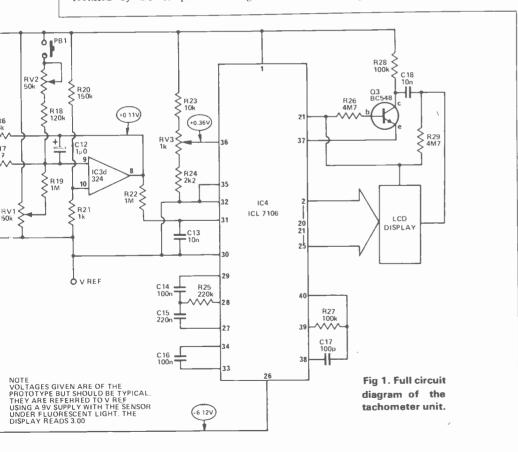


HOW IT WORKS

When using this unit to measure RPM, be the application a model aircraft motor or some other rotating object, the propeller or the white line (see operation section) gives rise to a changing light level. Dl which is a photo diode used in the photovoltaic mode, sees this light level and gives out a voltage proportional to the light. As this is only a small signal it has to be amplified before it can be used. This is done by IC3a. The transistor Q1 is included to provide some gain control allowing the unit to be used in differing light conditions without the need for any adjustment. The output of the amplifier is rectified by D3 to provide a negative

voltage on the gate of Q1. When the output of the amplifier is small the gate to source voltage will be near zero and the FET will appear as a low value resistor giving high gain to the amplifier. If the light change is such that the output of the amplifier is large, the rectified voltage on the gate of Q1 will cause the resistance of the FET to increase decreasing the amplifier gain. In this way the output of the amplifier is held relatively constant irrespective of the light level. Diode D2 is necessary to prevent the amplifier from saturating on the positive swing.

The output is then squared up by IC3b



R12 ensures that the output switches quickly. The output from this IC then triggers the monostable formed by Q2. What we have now is a pulse about $50\,\mu s$ long every time the propeller blade passes the light sensor. Before continuing, you may have

where the positive feedback provided by

noticed that besides the +9V and 0V we also have a line marked Vrcf. This is derived from IC4 which is a voltmeter chip and is a stable voltage of about 2.8 volts below the +9V line.

The output of the monostable (Q2) turns on IC1a for 50µs, discharging C2 which is then allowed to recharge to Vref. This voltage is compared (by 1C2) to the voltage set by R2 and R3. The output of IC2 is a negative pulse of about $900\mu s$. As it is on a stable voltage supply, variations in battery voltage will have very little effect on the output pulse width. Capacitor C3 is used to force the positive input of IC2 above the negative one for the 50 µs pulse ensuring that this time is not included in the output pulse. IC1b is used to invert this pulse and its output, and the output of IC2, control IC2c/IC2d. The output of 1C2c/IC2d is a positive pulse switching between Vref. and the +9V line.

This is then filtered by two 2 pole active low pass filters, IC3c and IC3d. As these have a cutoff frequency of around 10 Hz the output for most applications will be the DC voltage component only. This is measured by IC4 which is a complete voltmeter.

As offset voltages and currents can cause the output of the filters not to be exactly zero with no input, the positive input of IC3d is biased up about 30mV and then by injecting a current into the negative input (by R19 and RV1) correction can be made. For measuring RPMs above 20000 and below 30000 a current is injected into the negative input via R18 and this subtracts 10000 RPM from the reading.

-BUYLINES-

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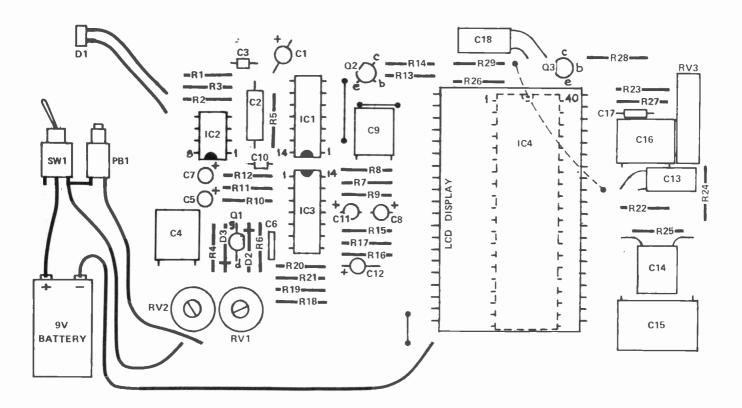
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The only awkward component here will be the BPW 34 photo diode. However a quick hunt through some catalogues showed us that Electrovalue sell the item at £1.73. evaluation kits should be available from people like Technomatic and Marshalls.

	PARTS	LIST —	
RESISTORS (all ¼ w 5% R1, 7, 8 R2, 20 R3, 11, 13, 27, 28 R4, 19, 22 R5 R6, 26, 29 R9 R10, 14, 23 R12 R15 R16 R17 R18 R21 R24 R25 POTENTIOMETERS RV1, 2 RV2	5) 180k 150k 100k 1M 47k 4M7 12k 10k 330k 33k 15k 4k7 120k 1k 2k2 220k 50k trimmer 1k trimmer 10 turn type	CAPACITORS C1, 5, 7, 8, 12 C2 C3 C4, 14, 16 9 C6, 17 C10 C11 C13, 18 C15 SEMICONDUCT IC1 IC2 IC3 IC4 Q1 Q2, 3 D1 D2, 3 MISCELLANEOU PCB, toggle sw display (evaluation	4016 301A 324 ICL 7106 2N5485 BC548 BPW34 1N914

Left: the BP34 photo-diode mounted on its lead. Shielding it from ambient conditions, in a tube for example, helps operation. Below: Fig 2 the overlay and wiring diagram. Note the D1 polarity is not important.



PROJECT: Light Tacho

display, taking care not to bridge between the tracks with solder. Also note that some of the capacitors have to be laid on their side to give a low height.

The ICs can now be added being careful to polarize them correctly. Due to the display being mounted over the main IC it is not posible to use a socket. A socket can be used for the display if desired however it will have to be modified by cutting it into two strips.

As there are no polarity marks on the display it is necessary to hold it at the light and look for the outline of the digits. A link for the decimal point should be added as shown in the diagram.

We mounted our unit in a metal box we made with the photodiode mounted about 25mm from the end of a 75mm long tube in front of the box. This narrows the field of view of the diode as well as giving a little more clearance

between high speed propellors and the fingers!

Calibration

Switch on the unit and cover the photodiode to prevent any light reaching it. Now adjust RV1 until the display reads zero.

Uncover the diode and point it at a fluorescent light. It will now give a reading and RV3 should be adjusted to indicate 3000 RPM.

Again cover the diode, then press the high range button and adjust RV2 to give a reading of -10000 RPM. Under fluorescent light it should read -7000 RPM

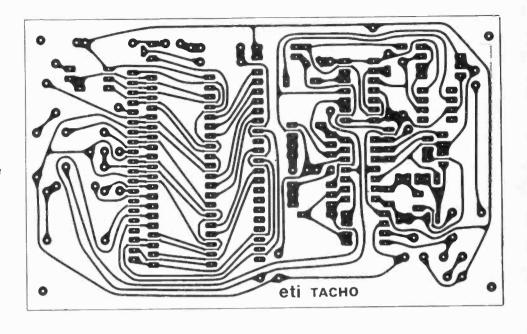
Operation

This unit relies on a changing light level for its operation. For use with a model aircraft, holding the unit near the propeller enables detection of the changes in the reflected light level. To measure the speed of other rotating equipment it may be necessary to paint a series of white lines to give the sensor something to 'see'.

However the unit cannot be used

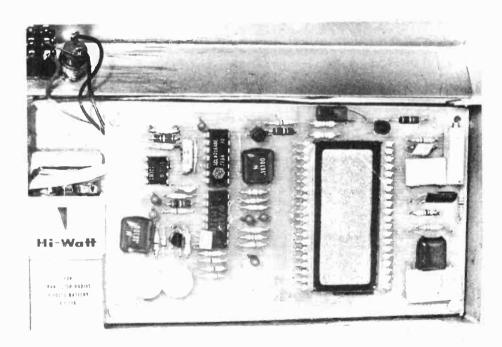
under fluorescent lights as it will see the 100 cycle flicker (see calibration section). In cases where this has to be done, and places where the ambient light is low, a small incandescent globe can be used to shine on the spot looked at by the sensor.

The unit, as described, is scaled to read up to 20000 RPM with a 10 RPM resolution, assuming two input pulses per revolution. If a different number of



Above: full size foil pattern for the tacho unit.

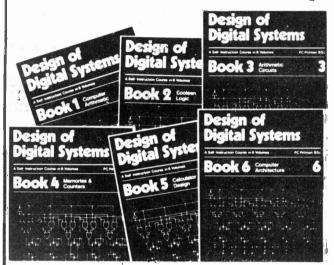
Below: An assembled pCB. Comparing this with the overlay shown opposite should help with construction.



input pulses is to be used, e.g. a three or four bladed propeller, the value of R1 can be changed. (R1 \approx 360k / number of pulses). The use of more than four pulses per revolution is not recommended on this range. If 2000 RPM is more than is needed for your application the value of R1 can be increased by a factor of 10; preferably with more than ten pulses per revolution.

Unlike a frequency meter, overranging this unit will cause the display to blank and greater resolution cannot be obtained simply by using a lower range. However an offset of a fixed number of RPM can be used as described in the 'How It Works' section. Using the values given, when the high range button is pressed, 10000 RPM must be added to the reading.

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data sheet

AY-5-1317A CHORD GENERATOR

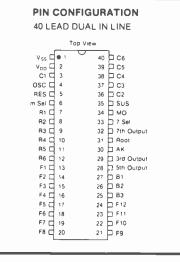
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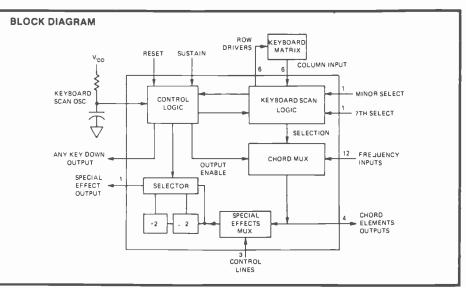
FEATURES

- ROOT, 3rd, 5th, 7th Chird Elements
- Additional output for special effects
- Sustain capability
 Top key priority
- Top key priority
 Self-contained os
- Self-contained oscillator circuit
 Operated with single pole single throw switch -
- Operated with single pole, single throw switch matrix

DESCRIPTION

The AY-5-1317A is a P-Channel MOS IC which accepts twelve basic frequencies (one full octave) and outputs the notes necessary to form Major, Minor and Seventh chords. This is the only known standard chord generator IC that performs these functions. The chord elements (ROOT, 3rd, 4th, 5th, 6th and 7th) can be multiplexed internally to perform special effects such as walking bass, rhythm arpegio, laternating bass, etc. The AY-5-1317A will operate in conjunction with and, through the KEY DOWN output, synchronize a rhythm generator such as the General Instrument AY-5-1315. The AY-5-1317A has a keyboard priority system with the C Major chord having the highest priority.





ELECTRICAL CHARACTERISTICS

Maximum Ratings*

 Standard Conditions (unless otherwise noted)

 $V_{DD} = -15V \pm 3V$ $V_{5S} = 0V$ (substrate voltage) Operating Temperature (T_A) = +25°C *Exceeding these ratings could cause permanent damage. Functional operation of this device at these conditions is not implied —operating ranges are specified below.

Characteristic	Sym	Min	Тур**	Max	Conditions
Input Logic Levels					
Logic 0	VIL	V _{DD}	- I	r -8.5	
Logic 1	VIH	-1.0V	_	+0.3V	
input Capacitance	CIN	- 1	- 1	10 pF	
Note Outputs					
Logic 0	ROFF	160KΩ	_	_	
Logic 1	RON		-	500Ω	
Row Drivers Output Impedance		- 1	750 Ω	-	$V_{DD} = -15V$
Control Input		10K Ω	_	1000K Ω	
Keyboard Row Input Impedance		24ΚΩ	-	100ΚΩ	
Keyboard Scan Frequency		_	25KHz		500 pF,750K,V _{DD} =15V

**Typical values are at +25°C and nominal voltages.

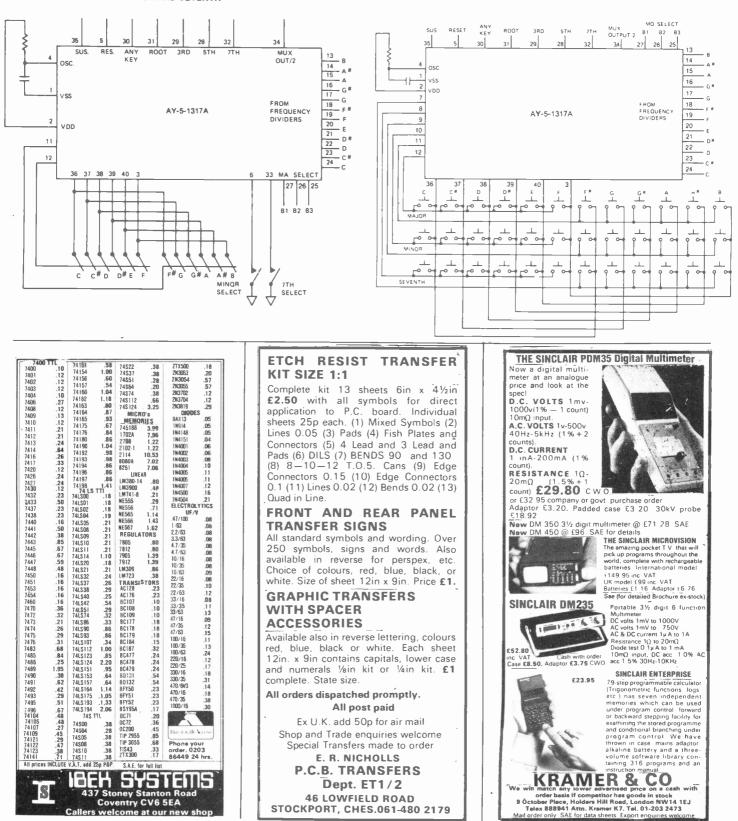
AY-5-1317A CHORD GENERATOR

-

Pin No.	Name (Symbol)	Function				
1	Ground (Vss)	Ground				
2	Power Supply (Vop)	Negative Supply				
3,36-40	Column Inputs (CI-C6)	Column inputs from Keyboard Matrix				
4	Oscillator Input (OSC)	R/C network connection for keyboard scan oscillator				
5	Reset (RES)	A logic '1' (ground) will reset the keyboard scanner, and the memorized key				
6	Minor Select (m Sel)	A Ground on this line changes the 3rd output from Major to Minor				
7-12	Row Outputs (R1-R6)	Row outputs to Keyboard Matrix				
13-24	Frequency Inputs (F1-F12)	These are the input lines for the 12 frequencies (one full octave B thru C) used to generate the chords.				
25-27	Control Inputs (B3-B1)	These 3 lines will be internally latched and decoded to select either the ROOT, 3rd, 4th, 5th, 6th, or 7th frequency as the special effect output.				
		B1 B2 B3 Selection				
		0 0 0 No change from last selection.				
		0 0 1 ROOT				
		0 1 0 5th				
		0 1 1 3rd				
1		1 1 1 7th				
		1 1 0 4th				
		1 0 1 6th				
28	5th Output (5th)	This line will output the 5th frequency element of the selected chord.				
29	3rd Output (3rd)	This line will output the 3rd frequency element of the selected chord. Minor 3rd will be provided if a Minor chord is selected. Major 3rd will be provided if a Major or 7th chord are selected.				
30	Any Key Down (AK)	This line goes to a logic '1' whenever a chord selection key is depressed.				
31	Root Output (Root)	This line will output the ROOT frequency element of the selected chord.				
32	7th Output (7th)	This line will output the 7th frequency element of the selected chord if a 7th chord is selected otherwise the output is logic '0' (voltage).				
33	7th Select (7 Sel)	A ground on this line turns the 7th output on.				
34	Special Effect Output (MO)	This line will output one of the six frequency elements as programmed by the control lines B1-B3. The 7th chord ele- ment frequency will be provided independently of the chord selection.				
35	Sustain (SUS)	A logic '1' on this line will activate the memory circuit which memorizes the last key played.				

FREQUENCY OUTPUTS							
Chord Selection	Root	3rd Minor	3rd Major	4th	5th	6th	7th
С	C (÷2)	D# (÷2)	E (÷2)	F (÷2)	G (÷2)	A (÷2)	A # (÷2)
C #	C#(÷2)	E (÷2)	F (÷2)	F#(÷2)	G#(÷2)	A# (÷2)	B (:2
D	D (÷2)	F (÷2)	F#(÷2)	G (÷2)	A (÷2)	B (÷2)	C (÷1
D#	D#(÷2)	F#(÷2)	G (÷2)	G # (÷ 2)	A # (÷2)	C (÷1)	C # (÷ 1
E	E (÷2)	G (÷2)	G # (÷2)	A (÷2)	B (÷2)	C#(÷1)	D (÷1
F	F (÷2)	G # (÷2)	A (÷2)	A # (÷2)	C (÷1)	D (÷1)	D#(÷1)
F #	F# (÷4)	A (÷4)	A # (÷4)	B (÷4)	C # (÷ 2)	D#(÷2)	E (÷2)
G	G (÷4)	A# (÷4)	B (÷4)	C (÷2)	D (÷2)	E. (÷2)	F (÷2)
G #	G# (÷4)	B (÷4)	C (÷2)	C # (÷ 2)	D#(÷2)	F (÷2)	F#(÷2
A	A (÷4)	C (÷2)	C# (÷2)	D (÷2)	E (÷2)	F#(÷2)	G (÷2
A#	A# (÷4)	C # (÷2)	D (÷2)	D#(÷2)	F (÷2)	G (÷2)	G#(÷2
в	B (÷4)	D (÷2)	D#(÷2)	E (÷2)	F#(÷2)	G # (÷2)	A (÷2

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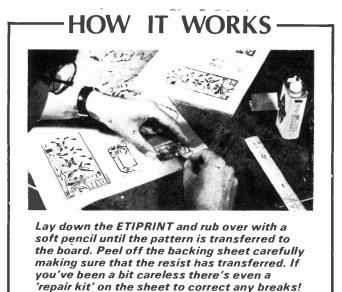
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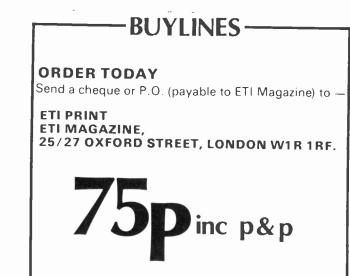
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ETIPRINTS

ETIPRINTS are a fast new aid for producing high quality printed circuit boards. Each ETIPRINTS sheet contains a set of etch resistant rub down transfers of the printed circuit board designs for several of our projects. ETIPRINTS are made from our original artwork ensuring a neat and accurate board. We thought ETIPRINTS were such a good idea that we have patented the system (patent numbers 1445171 and 1445172).





ELECTRONICS TODAY INTERNATIONAL -

-PARTS LIST-

Shown below is the listing for the last years ETIPRINTS. Earlier sheets are available, ring Tim Salmon for details.

003	Race Track Game Hammer Throw Freezer Alarm	Jan 78 Jan 78 Dec 77
004	Metal Locator Mk II Ultrasonic Tx / Rx 5 Watt Stereo Amp (mod	Feb 78 Feb 78 ified)
	Metronome Shutter Time	Jan 77 Feb 78 Feb 78
005	Op-Amp Supply Frequency Shifter LCD Panelmeter Light Dimmer (3 times)	Mar 78
006	CMOS Switched Preamp From Experimenters P.S.U. 555 Boards (twice	Electronics Tomorrow
007	Star Trek Radio CD Ignition CCD Phaser White Line Follower	May 78 May 78 May 78 April 78
008	Tank Battle Helping Hand	May 78
009	AM / FM Radio Bridge Oscillator CMOS Stars & Dots	June 78
010	Bench Amplifier Freezer Alarm Marker Generator LED Dice Watchdog (2 PCBs) Stars & Dots PSU	Project Book Six
011	Noise Generator General Preamp Flash Trigger Compander Active Crossover (2 PCBs)	Project Book Six
012	Disco Lightshow Stereo Simulator Digital Thermometer	Project Book Six
013	Amplifier Module Amplifier PSU Equaliser Equaliser PSU	Project Book Six
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015	UFO Detector Torch Finder (twice) Etiwet (twice)	July 78 July 78 Aug 78
016	Stac Timer Xhatch Gen Wheel of Fortune	Sept 78
017	Complex Sound Gen Tele Bell Extender Power Bulge	Oct 78
018	RF Power Meter Proximity Switch Audio Oscillator (2)	Oct 78 Oct 78 Nov 78
019	Car Alarm (2) Wine Temp (2) Curve Tracer	Dec 78 Dec 78 Dec 78
020	Digital Tacho Module Digital Dial	Jan 79 Jan 79 Jan 79 Jan 79

59

OBITUARY: John Miller-Kirkpatrick

I AM probably the worst person to write about John Kirkpatrick: I liked him too much to be objective. But I *am* going to write about him because John died on December 12th, 1978. I don't remember how old he was; you rarely do know how old good friends are but he was 30 or 31 — it doesn't matter which for it's far too young. John leaves behind a wife, Jane, and two young daughters.

When I first met John I can't quite remember, but it was probably shortly after he had developed the first digital clock that I'd ever seen. This would have been in 1971 or 1972 — way before the chips that make these things so simple today.

The circuit comprised a mass of TTL and the whole thing had been worked out from first principles.

Yes, John was one of the few people I know who could work from first principles.

It was also John who introduced me to ETI - amagazine I'd hardly heard of at the time. When I became editor of this magazine it was natural that the first person I contacted to write for me was John. He, of course, wrote Electronics Tomorrow for us, a series which we carried from mid-1973 until illness prevented him doing it some three months ago.

John's main business was Bywood Electronics which was one of the first companies to bring the new high-technology chips to the general public. It was John who designed System 68 which, although it suffered from a few teething troubles, was miles and away the first DIY computer ever described.

John's latest venture was the Scrumpi Series – we reviewed the Mk 3 not too long ago. To emphasise how close John was to me and the magazine we had to get an outsider to review it for objectivity.

John Miller was very nearly a genius but above all he was a damned good chap: I don't think there was an ounce of badness in him. I will miss him but our hobby will miss him as well.

Halvor Moorshead Editorial Director

microfile

GEORGE DAVIES IS INNOCENT, innocent that is of any blame for my last minute summons to talk to the Croydon branch of the British Computer Society. Mr Davies is chairman of said society and having found himself with no speaker on the subject of Home Computing, drafted myself and another speaker to fill the gap.

My talk was concerned with a descripton of the various items that one has to hang around an MPU in order to produce a system, power supply, input, output, RAM, ROM, control circuitry etc, using an Mk14 as an example of a minimum system and working up to an outline of the likes of the Triton, PET, and TRS 80.

The other speaker, John Sanderson, described a project that he has been working on for some time, a system aimed at the educational market.

A modular concept has been adopted in a system that allows a number of pupil "work stations" to be connected to the main processor card (based on a GI micro). Each work station consists of a VDU, cassette recorder and pair of head phones.

The system is flexible to say the least and a full description of it would take up far more spare time than I

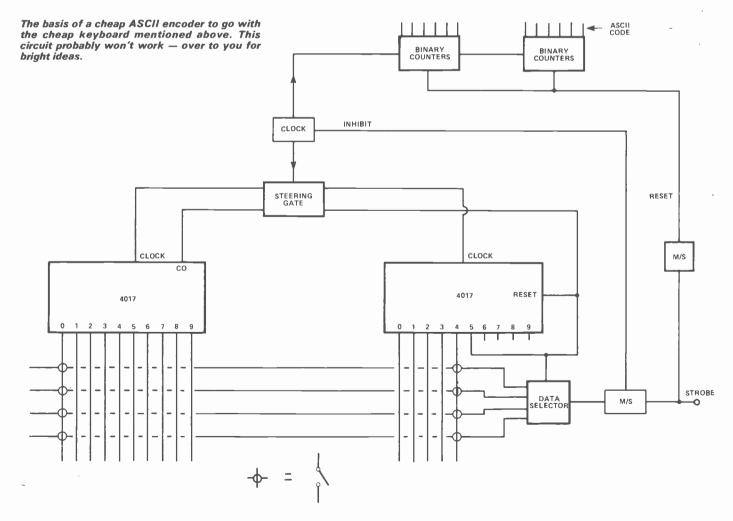
have available here — I shall, however, be reporting in more detail soon. One part of the hardware grabbed my attention immediately however — the keyboard.

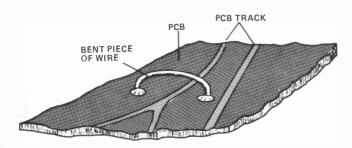
This was of very simple construction a formed A 60 station (15 \times 4 matrix) input terminal at a cost of about ±5.00.

Construction of the keyboard was kept as simple and straightforward as possible by forming the keys from a piece of bent wire as shown in the diagram. Although this sounds like a recipe for trouble, the design has proven itself over the past few months. To make the keyboard more attractive a layer of film, with appropriate ledgenos can be placed over the pcb to keep the cost right down, housing for the terminal is provided by a picture frame.

The finished product is both attractive, robust and above all cheap.

As it stands at present a certain amount of software is necessary to decode the output of the board but this set me thinking along the lines of a keyboard with the same system for providing the keys but with additional, hopefully simple, circuitry to produce an ASCII output with strobe — for more attractive.





How to make a cheap keyboard using only a PCB and bent wire.

My deadline for these words was almost upon me so the diagram below is very much a first attempt at such a scheme and J'm hoping some of you will take the basic idea and see if something can be made of it.

The idea is that clock pulses will, via the steering gate be fed to two 4017 ICs such that these devices form a 15 bit shift register, placing a logic '1' on each of the 15 'vertical' lines of the keyboard matrix. The horizontal lines are taken to a four input data selector that selects the output from one of the four ''horizontal'' lines and feds this to a mono stable.

The line selected will initially be the top row of switches but at the end of a row scan will advance to the second.

The output from the data selector will be low at all times unless a key has been pressed when it will trigger

the monostable. This will stop the clock for a short time. The output of 7Hz has also been fed to a couple of binary counters, the outputs of which will form a unique code corresponding to the key that has been pressed.

If the keyboard layout and markings have been chosen carefully. This binary code will be the ASCII code for the key pressed.

As the first monostable returns to its stable state it triggers a second device to reset all the counter stages.

In the time available I've not been able to put any detail in the design but hopefully the data selector and steering gate could be easily implemented. Roll over might be a problem — but over to you — If you've any thoughts on this scheme please let me know.

Micro digital of 25 Brunswick Street, Liverpool, L2 OBJ have been busy over the past few months, expanding the range of products available in their Computing Store.

They have one of the largest ranges of literature available in the country at present, all on display in the shop. Microdigital are also producing blank, high quality, cassette tapes and coding forms for MPU work.

Paying them a visit or sending an SAE for their latest catalogue could bring to light that book, component, whatever — that you've been searching high and low for.

STEVENSON Electronic Components VEROBOARDS Size in Otin Otin Veropins-	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	FALS LS95 65p LS123 56p LS125 40p LS00 16p LS126 40p LS01 16p LS132 66p LS01 16p LS136 36p LS03 16p LS138 54p LS03 16p LS139 50p LS04 16p LS153 50p LS13 30p LS155 80p LS13 30p LS156 80p LS14 70p LS157 45p LS20 16p LS154 90p LS33 16p LS157 45p LS20 16p LS164 90p LS30 16p LS174 60p LS32 24p LS175 60p
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Available with secondaries of: 6 · 0 · 6, 9 · 0 · 9 and 12 · 0 · 12. 92p each CRYSTALS WIRE ENDED TYPE Freq. MHz 0.100 380p 4.000 250p 12.000 250p 0.300 380p 5.000 250p 18.000 300p 1.000 320p 6.000 250p 20.000 300p	OPTO LEDs 0.125in. 0.2in. Red TiL209 TiL220 9p Green TiL211 TiL221 13p Yellow TiL213 TiL223 13p DispLAYS DL704 0.3 in CC 130p DL707 0.3 in CA 130p FND500 0.5 in CC 100p	7454 14p 74170 125p 7473 25p 74174 68p 7474 25p 74174 68p 7475 32p 74190 72p 746 28p 74191 72p 7485 70p 74192 64p 7489 145p 74193 64p 7490 32p 74196 55p 7492 35p 74197 55p FULL DETAILS INCATALOGUE 4029 60p 4001 15p 4042 54p 4002 15p 4042 54p
2.000 320p 8.000 250p 32.000 300p 3.276 250p 10.000 250p 48.000 300p LOUDSPEAKERS Sömm dia. 8 ohms 70p 64mm dia. 8 ohms 75p 70mm dia. 8 ohms 75p 70mm dia. 8 ohms 100p 70mm dia. 80 ohms 110p	Carbon film resistors. High stability, low noise 5%. E12 series. 4.7ohms to 10M. Any mix. each 100+ 1000+ 0.25W 1p 0.9p 0.8p 0.5W 1.5p 1.2p 1p Special development packs consisting of 10 of each value from 4.7 ohms to 1 Megohm (650 res.) 0.5W £7.50. 0.25W £5.70 HERE ARE JUST A FEW OF THE CAPACITORS	4002 15p 4046 100p 4007 15p 4049 28p 4011 15p 4050 28p 4013 35p 4066 40p 4015 60p 4068 20p 4016 35p 4066 20p 4017 50p 4008 20p 4018 65p 4075 16p 4023 15p 4093 48p 4024 45p 4510 70p 4026 95p 4511 70p 4028 52p 4520 65p SKTS 65p 65p 65p
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100 W DISCO MIXER AMPLIFIER

Designed by Richard Becker of Powertran this unit can deliver any way you want, and for less than £50!

Build it as a disco amp — it provides four inputs and three tone controls as well as 100W RMS.

Build the power amps for home use — check the spec — for low cost high quality sound.

NOT LONG AGO we published a super-fi 200W amplifier for the most demanding professional and domestic applications and this circuit is being highly acclaimed (by those who know about amplifiers!)

See for example the review in 'Sound International' December 1978 issue. However, exotic circuitry does not come at a low price.

This project does!

For less than £50 you can build a rugged general purpose high power amplifier complete with built in adaptable mixer. Using the newest of Motorola's extra strong power transistors this design pushes out 100 watts (genuine RMS type) and a bit to spare into 8 ohms. Overload protection is built-in and distortion is less than 0.1% right up to clipping level.

Mixing It

The mixer takes a wide range of inputs such as disc, microphone, guitar or just about anything you fancy as the sensitivities of the buffered input stages can be simply changed. There are three tone controls — bass, middle and treble each having a range of 15dB boost and 15dB cut and also a master volume control.

Mechanically the design is simplicity in the extreme with the absolute minimum of wiring. The power transistors fit onto the power amplifier board so there are no wires to give stability problems and all the controls mount directly onto the mixer board. Even the input jacks are soldered to the board! All the components are cheap and with the possible exception of the power transistors and transformer readily available.

These can all be obtained from Powertran who are supplying this project as a complete kit which incudes fully finished metalwork to give the professional finishing touches.

Construction

Assemble the printed <u>circuit boards</u> following the overlays. On the power amplifier board sandwich the cooling bracket between the power transistors and the circuit board as shown in the drawing not forgetting to smear silicon grease onto the mica washers. Fitting Q104 is easier accomplished after the bracket is in position. Smear some grease on this too before sitting it in the hole in the bracket

Even when there is no signal, Q105 is dissipating over 500 mW so get rid of the heat from this with a cooling clip pressed onto it. Wind L1 onto R128 with 10 turns of 25g wire before fitting to the board. The wire supplied in the kits is self fluxing polyurethane covered and can be soldered directly to the board. Before fitting any components to the mixer board press in pins, from the component side of the board at the 16 points marked x. These are for connecting to the jack sockets but do not fit them yet. Now fit all the components taking particular care to solder the potentiometers squarely on the board and when complete secure to the front panel with the potentiometer nuts. Remove all but three spacer washers from the jack sockets and bending their tags to fit over the pins on the board screw to the panel and solder in position.

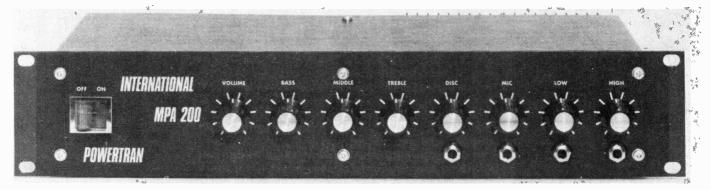
Press capacitors C115, 116 into their mounting clips and connect the rectifier diodes and C113, 114 across them as shown in the diagram then complete the mechanical assembly and wiring noting that ALL ground (OV) connections are made to a stack of solder tags fitted to the chassis near the power supply capacitors.

Testing and Setting Up

Without F2, 3 fitted check the power supply. Being off-load the voltage on each rail will be nearer 54 volts than 50 volts. Switch off and discharge the capacitors. No fireworks by using screwdrivers please! Use a resistor of about 100R. Fit the fuses, turn down the master volume control, turn RV101 to its midway position and turn RV102 fully clockwise.

Turn on and set the voltage between the can of Q111 and the amplifier output terminal to 33mV with RV 102. This corresponds to a

PROJECT



SPECIFICATION

SPECIFICATION (power amplituer) Power output: 112 into 8 ohms Harmonic distortion: 0.07% at 1KHz, 8R at clipping level. Frequency response: (3dB) 10Hz — 30KHz Damping factor: 100 Sensitivity: 0.775V (0dBm) for 100W into 8Ω Hum & Noise: —99 dB Input impedance: 22k

SPECIFICATION (mixer)

input DISC disc equalization sensitivity 3mV input impedance 47K

input MIC flat response sensitivity 1mV input impedance 1K input LOW flat response sensitivity 10mV input impedance 10K

input HIGH flat response sensitivity 100mV input impedance 100K

Bass control + 15dB — 15dB at 30Hz Middle control + 15dB — 15dB at 1KHz Treble Control + 15dB — 15dB at 15KHz

By simple component changes all four inputs can have flat response for any sensitivity between 1mV and 100mV

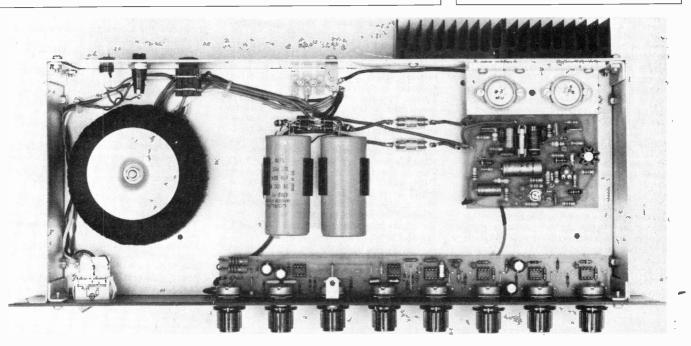
current of 100mA in the output stage. Adjust RV101 for zero off-set voltage at the output terminal. Make re-measurements of these voltages for about 10 minutes or until they stop changing whilst the amplifier is becoming thermally stabilized.

Switch off, fit the cover and your amplifier is now ready for use.

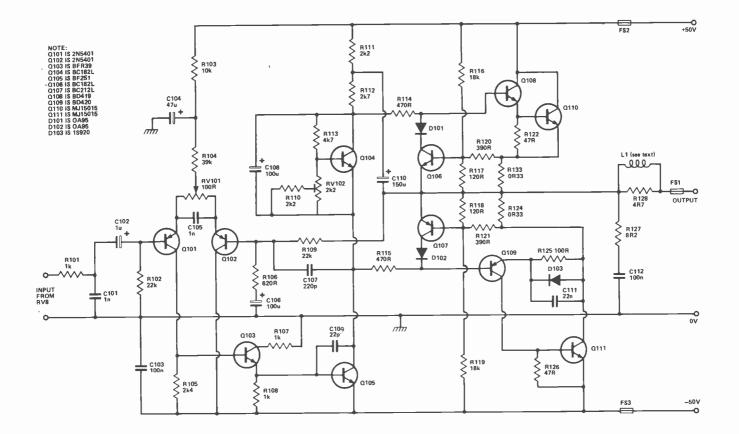
BUYLINES

A complete kit of parts for this project, including all metalwork, nuts, bolts, PCBs and components will be available from Powertran Electronics, Portway Industrial Estate, Andover, Hants SP10 3NM for £49.90+VAT. The PCBs will be available only from them as they are their design.

In addition the parts for both the mixer and power amp boards are available separately at a cost of $\pounds 10.40$ and $\pounds 10.60$ all inc. respectively.



ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979



HOW IT WORKS

Power amplifier.

1

To achieve reliable high power delivery at low cost 'Power Base Technology' type power transistors are the obvious choice, offering an excellent safe operating area at a very favourable price. One such device is the well known and readily available 2N3773 which can be used in this design, however Motorola have recently introduced the MJ15015 which will not only handle more power (180 watts) but is cheaper too (only about £1.50). These are driven by Q108, 109 which supply the base current for the output transistors without loading heavily the voltage amplifying stage of Q105. The combination of R125, D103, C111 is used to simulate the input impedance of a power transistor to make similar the impedances at the bases of Q108, 109 so as to increase the symmetry of the output stage which is necessary to achieve low distortion. R122, 126 improve the switching times of the output transistors by removing charge carriers from their bases. This is necessary for smooth transfer in the cross-over region i.e. when the signal changes from positive (delivered by Q110) to negative (accepted by Q111). Bias for the output stage is provided by Q104, the volt-age across which is adjusted by RV 102. For thermal stability of quiescent current this transistor is in thermal contact with the cooling bracket. C108 is an AC bypass.

R123, 124 are the resistors which sense the current in the output stage, the voltage across these will be the voltage across TR104 less the voltage of the three baseemitter junctions of TR108, 110, 111 and the junction of D103.

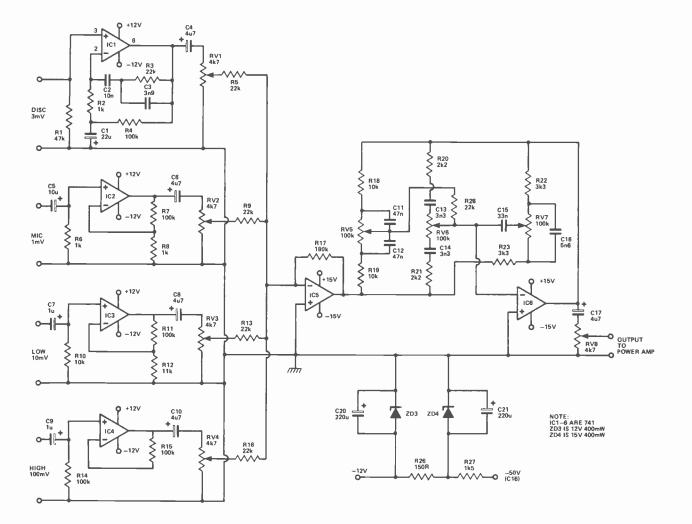
Protection against overload is provided by Q106, 107 with current sensing by R117, 120, 123 and R118, 121, 124 and voltage sensing by R116, 117 and R118, 119. R115 limits the current drawn from the load through Q107, 105 during overload. R114 restores symmetry for positive going signals. However, the presence of R114, 115 can, under heavy load conditions, lead to voltages which can turn on the base collector junctions of the protection transistors introducing a discontinuity into the transfer characteristic of the amplifier (that's a posh way of saying distortion!) This is prevented by germanium diodes D101, 102. C110 is a bootstrap capacitor which increases the effective impedance seen at the collector of Q105, thereby increasing the gain of that stage which takes the signal from differential pair Q101, 102, via the emmiter follower buffer Q103. RV101 is used to adjust the output off-set voltage to zero. The overall voltage gain of the amplifier is determined by R106, 109 and is about 36 corresponding to 0dBm (0.775V) for full power. R101, C101 are an input filter to remove RF interference and prevent overload by transients. Frequency compensation and stabilization is performed by C105, 107, 109, 112, R127, 128, and L1.

Power supply

For economy the supply to the amplifier is unregulated. D104-107 form a full wave rectifier filtered by C115, 116. C113, 114 remove high frequency transients from the power rails. A toroidal transformer is used because the stray magnetic field is very low thereby reducing the hum introduced into the system. The mixer is supplied by zener diode regulators fed from the 50V power rails. Two stages of regulation are used to prevent low frequency feedback to the input stages. Because of the bass boost of the disc equalization characteristic this is important otherwise low frequency instability of the system could result.

Mixer

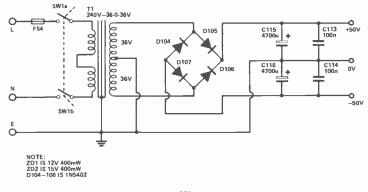
The actual mixing is carried out by RV1-4, R5, 9, 13, 16, 16 and IC5 but before that the inputs are buffered by IC1-4 stages. IC1 stage is RIAA equalized for use with a magnetic pick-up but if this facility is not required it can be built with flat equalization for another purpose such as a guitar pick-up (10 or 15 mV sensitivity being suit-

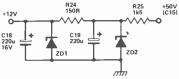


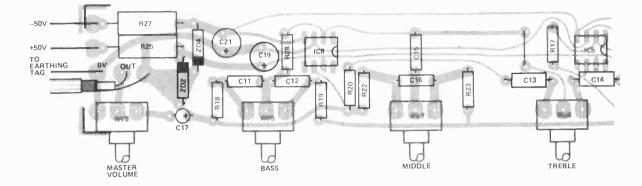
Above left: the full circuit for the power amplifier section of the mixer amp. This builds onto its own PCB and can be employed to good advantage in other systems. Above right: the mixer circuit itself. Note the provision of a third-midtone control circuit which will be found to be useful in disco applications. Below right: power supply circuit for the whole unit.

able for most pick-ups). For 15 mV sensitivity omit C3, R4, use wire links in place of C1, 2, use 15K, 18K, 100K for R1, 2, 3 respectively and replace link A with a lu tantalum capacitor. All the buffer stages produce an output of 100mV for their rated input. Changing the resistor values alters the sensitivity for example IC3 stage could also be built for 15mV sensitivity, 15K, 18K, 100K then being used for R10, 12, 11 respectively, provision is also made for disc equalisation components on the IC2 stage.

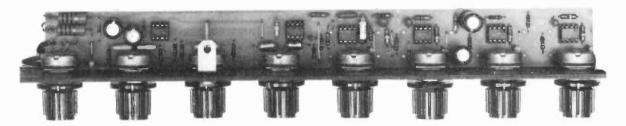
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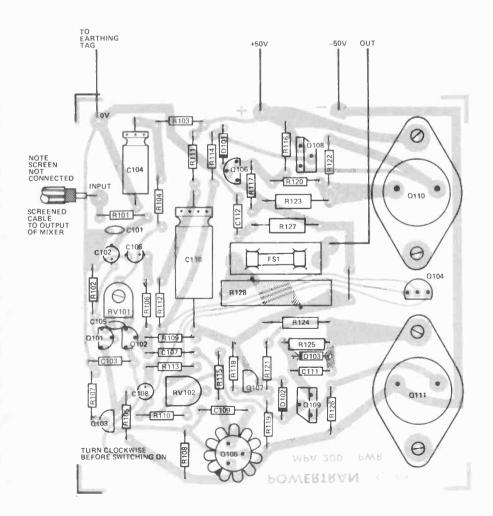






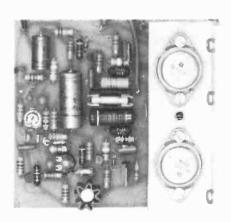
Above: the somewhat protracted overlay for the mixer board of the amplifier unit. This fixes onto the back of the controls to make construction easier. Below: the main power amplifier overlay. Read the setting-up procedures carefully before turning this on!



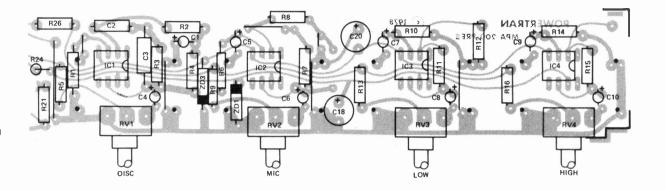


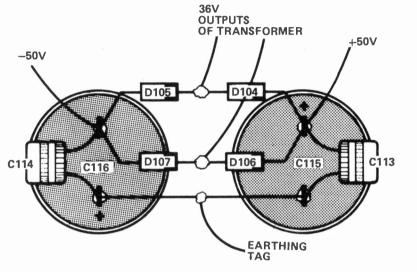
Component overlays for the boards within the unit. The mixer board is shown above — unfortunately in two pieces—(we're not fond of that kind of centre fold-out!) to make the components clear.

Below left is the 100W amplifier board. Take care when fitting the power transistors to their heatsinks, follow the diagram opposite. Note that RV102 should be set fully clockwise before switch on.

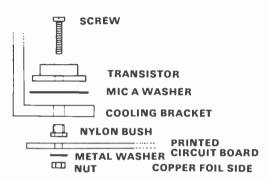


PROJECT: Disco Amp





Left: the method of mounting the PSU components around the large reservoir capacitors C116 and C117. Below: fitting the power transistors to the heatsink.



PARTS LIST

RESISTORS ¼W R1 R2,6,8, R3,5,9,13, 16,28 R4,7,11,14,15 R10,18,19, R12 R17 R20,21 R22,23 R24,26 R25,27	/ 5% Carbon Film 47k 1k 22k 100k 10k 11k 180k 2k2 3k3 150R 1k5(1W)
RESISTORS ¼W	5% Carbon Film
R101,107,108	1k
R102,109	22k
R103	10k
R104	39k
R105	2k4
R106	620R
R110,111	2k2
R112	2k7
R113	4k7
R114,115	470R
R116,119	18k
R117,118	120R
R120,121	390R
R122,126	47R
R123,124	0R33 (2½W)
R125	100R
R127	8R2 1W
R128	4R7 2W

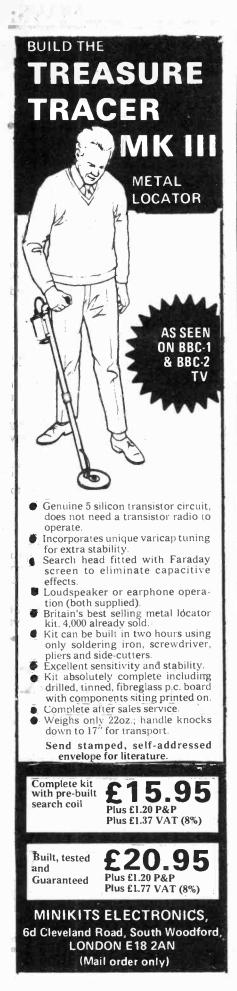
CAPACITORS C1 C2 C3 C4,6,8,10,17 C5 C7,9 C11,12 C13,14 C15 C16 C18,19,20,21 C101,105 C102 C103,112, 114 C104 C106,108 C107 C109 C110 C111 C115,116,	22u 16V tantalum 10n polyester 3n9 polystyrene $4\mu7$ 16V tantalum 10 μ 16V tantalum 1 μ 16V tantalum 47n polyester 3n3 polystyrene 220 μ 16V electrolytic 1n ceramic 1 μ 16V tantalum 100n polyester 47μ 63V electrolytic 100 μ 3V tantalum 200p polystyrene 22p 100V polystyrene 150 μ 63V electrolytic 22n polyester 4700μ 63V electrolytic
SEMICONDUCT	ORS
IC1—6	741
ZD1,3	12V 400mW
ZD2,4	15V 400mW
101,102	2N5401
103	BFR39
104,106	BC182L
105	BF257

107 108 109 110,111 D101,102 D103 D104-107	BC212L BD419 BD420 MJ15015 or 2N3773 OA95 1S920 1N5402 or BY254			
FUSES F1 F2,3 F4 POTENTIOMETE RV1-4,8 RV5-7 RV5-7 RV101 RV102	4A fast 3A fast 1A5 anti surge RS 4k7 log 100k 1in 100R pre-set 2k2 pre-set			
TRANSFORMER T1 0-117V — 234V to 36V-0-36V with electrostatic screen				
MISCELLANEOU	S			

Power transistor mounting bracket, two heat sinks 3in x 3in x 1in, TO5 cooling clip, six IC sockets, five ¼in mono jack sockets, two chassis mounting fuse holders, PCB mounting fuse holder, panel mounting mains fuse holder, illuminated mains switch DPDT, eight knobs, fibre glass ready drilled PCB's, metalwork and cabinet to suit, two capacitor clips, cable clamp, nuts, bolts, brackets, cable etc.

ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979

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1979 GOODIES7.409N 10p, EB-100, 7460N 10p, EB-1007.4109N 15p, E12-100, 74155 35p, MinOrder 10 of one type – 100+ POA p / p 20pPINER SLIDER POTS 47K Log Track 70 mmOverall 85mm, Singles 20p, £15-100, Doubles50p, £40-100, Min Order 10, 100+ POA, p / p2p,MAINS TRANSFORMER250v Prim 0-10v-18v 2 amp £1 00 + 50pp/p, Octal Cable fitting plug, 20 way, 20p, Cablemounting socket, 20 way, 20p p / p 20p.74540 25p, 74564 30p, MC1488L 75p,MC1489A175p 20p p /pTRIMPOTS 50(1 T05 20p, 1000 Cermet 20p, 1000 Painton PCB 20p, 2000 ditto 20p, 2K ditto 20p, 2K Heitrim 20p, 5K PCB 20p, 11Mkkeleton min. vert 12p / p 20p.CANNON D-TYPESOnly ones left. 15 waysocket 50p, 37 way plug 80p. 50 way socketE1 20.5 Oway wire wrap socket E1 30. 25 waynbbon plugs 90p. P / P 20p.NEW SN76477 sound generator IC (train-plane, explosion, phaser gun etc.) with data.2:50 + 20p P /PTT 74 SERIES7400 12p 7401 12p 7402 15p7401 12p 7412 12p 7441 45p7410 13p 7412 18p 7444 45p7410 13p 7412 18p 7444 45p7453 15p 7472 75p 7460 15p7464 34p 7417 25p 7480 35p7465 30p 7485 30p 7485 35p7466 30p 7481 30p 7441 30p7412 24p 7417 25p 7460 15p7412 34p 7412 25p 7438 30p 7441 30p7412 34p 7415 20p, 7415 30p7412 34p 7415 20p, 7415 30p7412 34p 741		-				
PIHER SLIDER POTS 47K Log Track 70 mm Overall 85mm, Singles 20p, E15-100, Doubles 50p, E40-100, Min Order 10, 100 + POA, p/p 20p MAINS TRANSFORMER 250v, Frim 0-10v-188 2 amp E1.00 + 50p p/p. Octal Cable fitting plug, 20 way, 20p, Cable mounting socket, 20 way, 20p p/p 20p. 74540 25p, 74564 30p, MC1488L 75p, MC1489AL 75p 20p p/p TRIMPOTS 50(1705 20p, 1000) Cermet 20p, 100() Painton PCB 20p, 2000 ditto 20p, 2500 ditto 20p, 5000 ditto 20p, 1K ditto 20p, 2500 ditto 20p, 2K Heitirm 20p, 5K PCB 20p, 1M skeleton min, vert. 12p p/p 20p. CANNON D-TYPES. Only ones left. 15 way socket 50p, 37 way plug 80p, 50 way socket E1 20, 50 way wire wrap socket E1 30, 25 way ribbon plugs 90p, P/P 20p. NEW SN76477 sound generator IC (train, plane, explosion, phaser gun etc.) with data. E2:0 + 20p P/P TTL 74 SERIES 7400 12p 7401 12p 7402 15p 7404 14p 7407 30p 7409 16p 7410 13p 7412 18p 7414 45p 7416 24p 7417 25p 7420 15p 7427 30p 7428 32p 7430 15p 7432 56p 7438 30p 7442 50p 7445 14p 7467 30p 7489 16p 7446 13p 7412 18p 7414 45p 7446 30p 7449 30p 7491 15p 7447 30p 7448 32p 7430 15p 7448 30p 7449 30p 7491 15p 7448 30p 7449 30p 7491 15p 7448 30p 7449 30p 7491 15p 7446 30p 7449 30p 7491 15p 7446 30p 7446 35p 7497 E150 7419 50p 74154 E1.10 74185 95p 7412 34b 74150 90p 74151 50p 7415 60p 74165 25p 7497 E150 74166 E1.00 74165 20p 74155 80p 74188 E1.35 75450 35p 7660 35p 74198 E1.30 74278 E1.20 74284 E3.60 74368 E1.35 75450 35p 7665 50 9p 74198 E1.30 74278 E1.20 74284 E3.60 74368 E1.35 75450 35p 76650 50p P/P 20p. SUPERSAVER 1 cassette recorder motor 9v Speed governed, brand new. fantastic value. 95p p/p 20p. SUPERSAVER 1 Cassette recorder motor 9v Speed governed, brand new. fantastic value. 95p p/p 20p. SUPERSAVER 3 ICL P S.U. 12v 1 8A (7 5v- 15v) in maker's carton E10 p/p E2. MEMORIES 2708 E6-85. 2102 (Signetics) E1. 1702A E2.95, 2513 (upper case) £4 65 Mostek MK4012N (1024 x 1), few only, 68p, p/p 20p. SUBERSAVER 3 ICL P S.U. 12v 1 8A (7 5v- 15v) in maker's carton E10 p/p E2. MEMORIES 2708 E6-85. 2102 (Signetics) E1. 1702A E2.95, 2513 (Upp	7409N 10p, £8-100, 7460N 10p, £8-10 74109N 15p, £12-100, 74155 35p, M	00 in				
250v Prim O-10v-18v 2 amp £1.00 + 50p p/p. Octal Cable fitting plug. 20 way. 20p. Chassis mounting plug. 20 way. 20p. Cable mounting socket. 20 way. 20p.p/p 20p. 74540 25p. 74564 30p. MC1488L 75p. MC1489A175p 20p.p/p TRIMPOTS 50() T05 20p. 100() Cermet 20p. 100Q Painton PCB 20p. 200() ditto 20p. 250() ditto 20p. 500() ditto 20p. 1K ditto 20p. 250() ditto 20p. 2K Helitrim 20p. 5K PCB 20p. 1M skeleton min. vert. 12p.p/p 20p. CANNON D-TYPES . Only ones left. 15 way socket 50p. 37 way plug 80p. 50 way socket £1.20. 50 way wire wrap socket £1.30. 25 way ribbon plugs 90p. P/P 20p. NEW SN76477 sound generator IC (train. plane, explosion. phaser gun etc.) with data. £2.50 + 20p.P/P TTL 74 SERIES 7400 12p. 7401 12p. 7402 15p. 7447 43p. 7441 18p. 7441 45p. 7416 24p. 7417 25p. 7420 15p. 7427 30p. 7428 32p. 7430 15p. 7432 26p. 7438 30p. 7442 50p. 7445 42p. 7475 30p. 7485 35p. 7485 30p. 7496 55p. 7497 £150 7485 430 7496 55p. 7497 £150 7410 30p. 7416 20p. 7416 30p. 7415 40p. 7416 20p. 7415 30p. 7416 2100. 74165 5110. 74155 80p. 7416 30p. 7445 5110. 74155 80p. 7416 7410 7416 20p. 7415 30p. 7415 40p. 7416 20p. 74163 90p. 7415 40p. 7416 2100. 74185 90p. 7415 40p. 7416 2100. 74185 90p. 7415 40p. 7416 2100. 74185 90p. 7415 400. 74182 90p. 74185 90p. 74198 £1.30. 74279 £1.20. 74284 £3.60 74198 £1.30. 74279 £1.20. 7428 £3.50 74198 £1.	PIHER SLIDER POTS 47K Log Track 70 m Overall 85mm, Singles 20p, £15-100, Doub 50p, £40-100; Min Order 10, 100 + POA, p	PIHER SLIDER POTS 47K Log Track 70 mm Overall 85mm, Singles 20p, £15-100, Doubles 50p, £40-100, Min Order 10, 100 + POA, p/p 20p MAINS TRANSFORMER 250v Prim 0-10v-18v 2 amp £1.00 + 50p p/p. Octal Cable fitting plug, 20 way, 20p. Chassis mounting plug, 20 way, 20p. Chassis mounting plug, 20 way, 20p. 74S40 25p, 74S64 30p. MC1488L 75p. MC1489AL 75p 20p p/p. TRIMPOTS 50Ω T05 20p, 100Ω Cermet 20p. 100Ω Painton PCB 20p, 200Ω ditto 20p. 250Ω ditto 20p, 2K Helitrim 20p, 5K PCB 20p. 1M skeleton min. vert. 12p p/p 20p. CANNON D-TYPES. Only ones left: 15 way socket 50p, 37 way plug 80p. 50 way socket £1.20. 05 way socket £1.30. 25 way				
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100Ω Painton PCB 20p, 200Ω ditto 20p, 2500 ditto 20p, 2K Helitrim 20p, 5K PCB 20p. 1M skeleton min, vert. 12p p/p 20p. CANNON D-TYPES. Only ones left: 15 way socket 50p, 37 way plug 80p, 50 way socket £1.20, 50 way wire wrap socket £1.30, 25 way ribbon plugs 90p. P/P 20p. NEW SN76477 sound generator IC (train plane, explosion, phaser gun etc.) with data. £2.50 + 20p P/P TTL 74 SERIES 7400 12p 7401 12p 7402 15p 7443 14p 7407 30p 7409 16p 7414 14p 7407 30p 7409 16p 7416 24p 7417 25p 7420 15p 7427 30p 7428 32p 7430 15p 7427 30p 7428 32p 7430 15p 7427 30p 7428 32p 7430 15p 7428 26p 7438 30p 7442 50p 7451 15p 7472 75p 7460 15p 7445 30p 7496 55p 7497 £1.50 74107 30p 74165 £1.10 74185 85p 74153 70p 74154 £1.10 74185 80p 74153 60p 7496 55p 7497 £1.50 74193 48p 74150 90p 74151 60p 74153 70p 74154 £1.10 74185 80p 74157 60p 74165 90p 74151 60p 74158 60p 7496 55p 7497 £1.50 74198 £1.30 74279 £1.20 7428 43.60 74198 £1.30 74279 £1.20 7428 43.60 74198 £1.30 74279 £1.20 7428 43.60 74198 £1.30 7429 90p 74185 90p 74156 £1.30 74279 £1.20 74198 £1.30 74279 £1.20 7428 43.60 74198 £1.30 74279 £1.20 74198 £1.30 74279 £1.20 7428 43.60 74198 £1.30 74279 £1.20 74198 £1.30 74279 £1.20 7428 43.60 74368 £1.35 75450 35p 76660 50p P/P 20p. SUPERSAVER 1 cassette recorder motor 9v Speed governed, brand new, fantastic value. 95p p/p 20p. SUPERSAVER 2 Hybrid Systems DAC 371-8 (8-bit) DIL packaged + data. ideal MPU users. 5brand new £2 (fraction of original cost) p/p 20p. SUPERSAVER 3 ICL P.S.U. 12v 1.8A (7.5v- 15v) in maker's carton £10 p/p £2. MEMORIES 2708 £6-85. 2102 (Signetics) £1. 1702A £2 95. 2513 (upper case) £4 65. Mostek MK4012N (1024 x 1), few only. 68p. p/p 20p. SUBMIN. TOGGLES (C ¹ 8 K USA) spco extended toggle (1.25 inch) super baulity 75p Standard submin. toggle dpco 80p. p/ p 20p. SUBMIN. TOG GLES (C ¹ 8 K USA) spco extended toggle (1.25 inch) super ba 236 for p.20, 25.11 (UD14 128 bit static shift register 65.01, S1 2-0.12 50m A sub- miniature transformer £1.35, 5LT01 (green phoshor) £4. suitable clock IC £3 25. TM 5314 £2 9	74S40 25p, 74S64 30p, MC1488L 75					
CANNON D-TYPES. Only ones left. 15 way socket 50p, 37 way plug 80p. 50 way socket £1.20.50 way wire wrap socket £1.30.25 way ribbon plugs 90p. P / P 20p. NEW SN76477 sound generator IC (train, plane, explosion, phaser gun etc.) with data. £2.50 + 20p P / P TTL 74 SERIES 7400 12p 7401 12p 7402 15p 7443 14p 7407 30p 7409 16p 7410 13p 7412 18p 7414 45p 7416 24p 7417 25p 7420 15p 7422 25p 7438 30p 7442 50p 7451 15p 7472 76p 7460 15p 7443 26p 7438 30p 7442 50p 7451 15p 7472 76p 7460 15p 7445 80p 7490 30p 7481 80p 7495 60p 7496 55p 7497 £1.50 74107 30p 74195 90p 74121 25p 74133 48p 74150 90p 74121 25p 74133 70p 74154 £1.10 74155 80p 74165 £1.10 74185 gp 74165 £1.10 74185 gp 74185 60p 7496 35p 7466 15p 74184 £1.00 74165 £1.10 74186 25.50 74190 £1.00 74165 £1.10 74186 £2.50 74190 £1.00 74165 £1.10 74186 £2.50 74190 £1.00 74165 gp 74151 60p 74168 £1.35 75450 35p 76660 50p P/P 20p SUPERSAVER 1 cassette recorder motor 9v Speed governed, brand new, fantastic value, 95p p / p 20p. SUPERSAVER 3 ICL P.S.U. 12v 1.8A (7.5v- 15v) in maker's carton £10 p / p £2. MEMORIES 2708 £6-85. 2102 (Signetics) £1, 1702A £2.95. 2513 (upper case) £4.65 Mostek MK4012N (1024 x 1), few only, 68p, p / p 20p. SUPERSAVER 3 ICL P.S.U. 12v 1.8A (7.5v- 15v) in maker's carton £10 p / p £2. MEMORIES 2708 £6-85. 2102 (Signetics) £1, 1702A £2.95. 2513 (upper case) £4.65 Mostek MK4012N (1024 x 1), few only, 68p, p / p 20p. SUBMIN. TOGGLES (C & K, USA) spco extended toggle (1.25 inch) superb quality 75p Standard submin. toggle dpco 80p. p / p 20p. SUBMIN. TOGGLES (C & K, USA) spco extended toggle (1.25 inch) superb quality 75p Standard submin. toggle dpco 80p. p / p 20p. SUBMIN. TOGGLES (C & K, USA) spco extended toggle (1.25 inch) superb quality 75p Standard submin. toggle dpco 80p. p / p 20p. SUBMIN. TOGGLES (C & K, USA) spco extended toggle (1.25 inch) superb quality 75p Standard submin. toggle dpco 80p. p / p 20p. SUBMIN. TOGGLES (C & K, USA) spco extended toggle (1.25 inch) superb quality 75p Standard submin. toggle	1000 Painton PCB 20p, 2000 ditto 20p, 250 ditto 20p, 5000 ditto 20p, 1K ditto 20p, ditto 20p, 2K Helitrim 20p, 5K PCB 20p,					
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THE REAL PROPERTY AND A RE	L. B. ELECTRONICS					

audiophile.

Seconds away — round one. Two well known tape recorder manufacturers locked in 🤌 combat! Ron Harris presides over the trial by transconductance.

I RECEIVED an Xmas card this morning. No, it wasn't my first ever you irreverent lot, but it was my first and last from Strathearn Audio. Later the very same day a press statement arrived on my ever receptive desk. I quote:

It is with a great deal of regret that I have to announce the closure of Strathearn Audio. Basically the reasons of this closure can be put into one sentence: the treasury was unwilling to consider providing additional funding until Autumn 1979 - but which time our proposed tie-up with Aiwa would have been in effect." Unauote.

And so effective December 31, 1978, our natimised hi-fi company ceases to be, five years and £9,000,000 later. One question oh ye powers that be - WHY NOW?

Strathearn's past has been one of huge losses, bad press and inadequately researched products. Through all this the government stood by them, while loud indeed the wolves did howl for blood.

In the past months, however, all has begun to change. Their record deck SM2000 and 21000 speaker system are very fine pieces of work. Export orders were growing, and by the end of '79 they could probably have been paying their way. At last.

Surely more cash should be made available - advertise the stuff don't kill it — with the excellent original thought evident in the products Strathearn had a future in the hi-fi market, and as we've already lost over £9,00,000 tax money it seems sheer lunacy to 'cut losses' at the first sign that those losses are about to end.

There is hope that the 21000 can be saved from all this and marketed separately. I hope so. In the meanwhile if there is anyone reading this out there connected with this decision, I say again WHY NOW?

Sorry I'll Leave That again!

DUE to circumstances never entirely within my control I've had to leave the description of Sonys TAE88 FET pre-amp until next issue. Before any of you write in accusing me of whiling away the month with wine, soft music and Felicity Kendal, Lassure it ain't true - if it had been '(Ah . . . what a thought . . .) I most definitely would not be here now, and neither would the Sony review.

Since I didn't spend the month with you-know-who, and there is no TAE88 review - let's call it guits eh? (I think I lose on this deal by a factor of about six million to one.)

Gloves on, Record Amps Away

MUST be the silly season again. Below I reproduce word for word two releases which arrived at ETI on the same day in the same post on the same subject -Actilinear.

There is a dispute. Revox v Tandberg. Read all about it here, folks. No comments from me, take your pick and make up your own minds . . .

accor

ACTILINEAR or "old wine in new bottles"

COMPETITION IS HOTTING up and not only against products from the Far East. Even among the "European alternatives" aggressively formulated headlines and the big treatment in advertisements and sales leaflets clamour for attention. Indeed, more and more arguments are being taken over by competitors, even word for word — which not only shows lack of imagination, but confuses the issue, at least for a time.

But it's a different matter when circuit arrangements that have been common property for years are trotted out as a new system and elevated to the status of main support for an advertising campaign.

What we are talking about is the "invention" published by a Norwegian manufacturer (and even put forward for patenting) known as ACTILINEAR. It is perfectly understandable if even test engineers don't

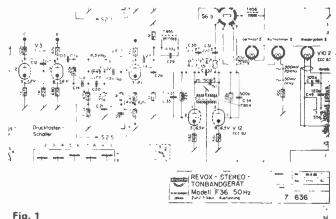


Fig. 1

Transconductance converter with filter coupling in valve technique. Recording amplifier of the REVOX F36 magnetic tape recorder. Circuit diagram drawn 26.2.62. 4

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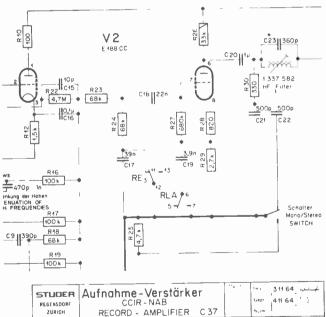
know every detail of the circuit by heart. After all, they've got other things to do besides concentrating on the subject of voltage-current conversion (transconductance converter) in the output stage of a recording amplifier.

Nor will a filter for decoupling the HF bias make them sit up too sharply, because they've been around for too long as well. A very long time, in fact. To be exact, at least since 1962 when valves were in use and since 1965 in transistorised circuits. We have no intention of making any assertions, for the fact is that the old REVOX F36 tape recorder (Fig. 1), the studio machines STUDER C37 (Fig. 2) and STUDER A62 (Fig. 3) used separate circuits for an equalising -reamplifier, a transconductance converter and a direct supply to the recording head via a filter circuit, with all the known advantages of such an arrangement.

These are well-proven techniques of long standing and are, for instance, still used in the latest REVOX B77 (Fig. 4). Interestingly enough, that machine too has an overload margin of approximately 20dB and a filter system prevents the bias oscillator voltage from interfering with the wanted signal.

Of course, we have no objections whatsoever if other manufacturers use these circuit details, which we had 16 years ago and did not consider worth partenting, since in the meantime they have become common property.

On the contrary, we assume that we are not the only ones who are prepared for sharpened competition, which benefits us all - as long as it is fair competition.



REGENSDORF ZURICH	Aufnahme - Verstärker CCIR - NAB RECORD - AMPLIFIER C 37	Gepr 41164 :
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Fig. 2

Transconductance converter with filter coupling in valve technique. Recording amplifier of STUDER C37 magnetic recorder. Circuit diagram drawn on 3.11.64.

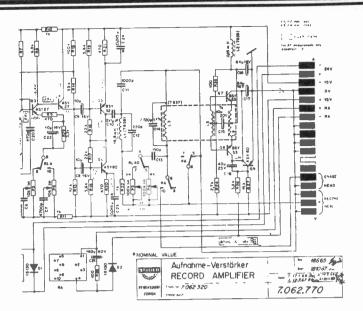


Fig. 3

Transconductance converter with symmetrical output stage, driven active generator and filter coupling in transistorised technique. Recording amplifier of the STUDER A62 magnetic tape recorder. Circuit diagram drawn on 18.6.65.

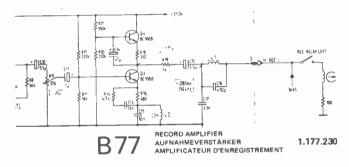


Fig. 4

Transconductance converter with free active generator and filter coupling. Recording amplifier of magnetic tape recorder B77.

the tandberg

Deeper Into Tandberg Actilinear

A SWISS MANUFACTURER of reel to reel tape recorders has released a "press information" claiming that the new Tandberg "Actilinear" Recording System, used in our tape recorders TD 20A and TCD 340A has been known to the industry for several years. This we believe is based on an imcomplete understanding of the circuitry. We are, therefore, issuing the following information which will help clarify the matter.

Figure 1 shows the schematic diagram of the Tandberg Actilinear Recording Amplifier chain which consists of three modules, an Equalizer, a Transconductance Converter and a Filter module. A more detailed explanation of the recording chain is given in a technical article "A New Recording System" by Senior Engineer Mr. Herman Lia, Dept. of Magnetic Research and

Development, Tandbergs Radiofabrikk A/S (printed in Audio Magazine, USA, July 1978), which also describes the ability of the Actilinear Recording System to be adjusted to fully exploit the potential of the new high coercivity tapes, such as the new metal particle tapes.

The claim is made that the principle of Transconductance has been well known for quite some time. This is, of course, a fact. Transconductance is the principle action of every transistor. Transconductance amplifiers have been a well known means of converting voltage to current for guite some years. Tandberg has employed such amplifiers before in their instrumentation recorder TIR 100/115 and in their professional portable audio tape recorder Arrivox-Tandberg.

There are, however, an almost infinite number of ways to design transconductance amplifiers, or voltageto-current converters, of which very few satisfy all the requirements of an optimum tape recording amplifier.

It is the Tranberg application of the transconductance principle which is of interest in the Actilinear System, and which is one of the distinguishing characteristics of the system. In Actilinear, we have used transconductance in such a manner as to create a high output impedance which is symmetrical. This gives minimum even harmonic distortion, which is not only an audible improvement for the consumer, but is also clearly and measureably superior to other applications of the transconductance principle.

Third World.

When it is so well known that 3rd harmonic distortion is inherent in all tape recording due to the tape characteristics themselves, it is of principal interest to eliminate other distortion components which will degrade the audible/measurable performance of a tape recorder. This is, of course, one advantage of Actilinear and clearly differentiates it from other known systems.

Figure 2 shows the wiring diagram for the Actilinear transconductance module, and Figure 3 shows a ''similar" circuit in another well known reel to reel tape recorder. Common for both circuits is that Q2 is used as a constant current source and there is no feed back loop from the output so that the linearity of the recording signal is only determined by the linearity of Q1 and Q2. However, the difference lies in the fact that the

Actiliner System (Fig. 2) is made symmetrical regarding

output impedance, as the collector of Q1 is connected to the collector of Q2. Thereby, the circuit produces minimum even harmonic distortion. In fig. 3 the point "R" is unsymmetrical because the output impedance varies as the output conductance hae of Q1 varies, and as h_{ne} will be different for the positive and negative amplitudes, even harmonic distortion will be produced.

Measurements of the TD 20A and the machine employing circuitry in Figure 3 show that the Tandberg TD 20A gives significantly higher output level in the very

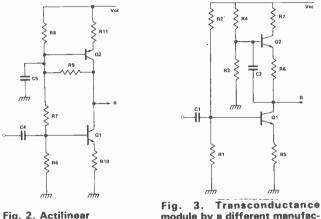


Fig. 2. Actilinear

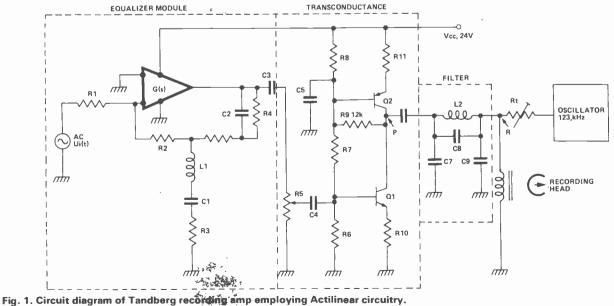
module by a different manufacturer.

critical segment of the frequency range between 1 kHz and 10 kHz at the same measured distortion level, and consequently gives a better performance.

We suggest that the public before jumping to conclusions about similarity between circuits, realize that we are talking about second and higher order effects in a circuit which is supposed to handle large amplitude audio signals and bias voltage applied to the output at the same time.

In disucssing such matters, we consider a comparison of small-signal equivalents of the various possible applications of transconductance to be a sub-optimal exercise, and of no value.

Our solution is, in so far as is known, unique in the tape recorder industry, can be adjusted to optimally exploit metal particle tape and gives audibly superior results with conventional tape formulations. ETI



y of filler



Ladies LCD Watch



.... and don't you ever say we don't listen to you again! Ever since we first did a gentlemans watch, we have been dealing with a constant never ending stream of requests for a ladies model. Well at long last we can claim to have done something about it!

It wasn't easy arranging this sort of price on a product this good — but ETIs done it again! The watch is small enough to look good on the prettiest wrist, and accurate enough to satisfy the most fastidious. Normal display shows time of course, with both date and seconds available on a push of a button. A backlight is also included.

Battery life should be greatly in excess of a year, and the bracelet is a smart stainless steel.



Inclusive of VAT and Postage

An example of this watch can be seen and examined in our reception at our Oxford Street offices.

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To: Ladies LCD Watch Offer ETI Magazine 25-27 Oxford Street London W1R 1RF
Please find enclosed my cheque/PO for £9.95 (made payable to ETI Magazine) for a ladies LCD watch
Name
Address
Please allow 14 days for delivery.

ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979

MARKET PLACE Digital Alarm | LCD



Size: 105mm wide 115mm deep x 55 mm high.

THIS IS THE THIRD digital alarm clock that we are offering (we regret the earlier versions are no longer available). We have sold thousands and thousands of these and our buying power enables us to offer a first rate branded product at a really excellent price.

The Hanimex HC-1100 is designed for mains operation only (240V/50Hz) with a 12 hour display, AM/PM and Alarm Set indicators incorporated in the large display. A switch on the top controls a Dim/Bright display function.

Setting up both the time and alarm is simplicity itself as buttons are provided for both fast and slow setting and there's no problem about knocking these accidentally as a 'locking' switch is provided under the clock. A 9-minute 'snooze' switch is located at the top.



Inclusive of VAT and Postage

An example of this clock can be seen and examined in our reception at our Oxford Street offices.

Τn· **Hanimex Alarm Offer** ETI Magazine ARM 25-27 Öxford Street London W1R 1RF Please find enclosed my cheque PO for £8.95 (payable to 4 ETI Magazine) for a Hanimex Digital Alarm Clock. Name GITA Adress Please allow 28 days for delivery

ELECTRONICS TODAY INTERNATIONAL --- FEBRUARY 1979



New low price!

The enormous numbers involved in ETI offers has enabled us to arrange a real bargain - a full spec LCD watch with adjustable metal bracelet for under half the going rate.

This watch gives continuous display of hours and minutes press the button once and you'll get the date (American style). After a couple of seconds the display automatically reverts to time but if you press again you'll get a continuous seconds display.

Press another button and you get a back light, enabling you to see the display in the dark. Setting, or resetting is simplicity itself and a 'hold' facility allows you to set the watch spot on. The accuracy is magnificent, as with all the current range of digital watches and battery life is well in excess of a year.



(Inclusive of VAT and Postage)

An example of this watch can be seen and examined in our reception at our Oxford Street offices.

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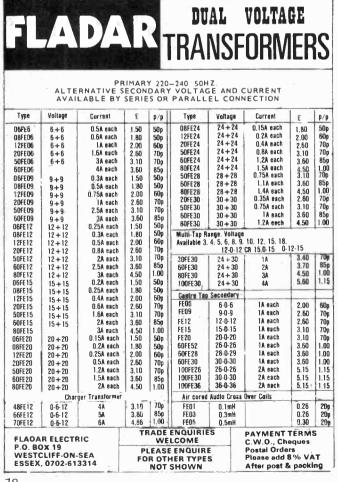
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Please find enclosed my cheque/PO for £8.95 (made payable to ETI Magazine) for my LCD Digital Watch.

Name Address

Please allow 14 days for delivery

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7400	10p	7460 120	74137 900	74195 50p	4055 130p	CA 3140	60p LM 3909 N	65p TBA 480 Q	200p
7401	10p		74138 100p		4056 120p	LF 356	80p MC 1310 P	140p TBA 520 Q	200p
7402	10p		74141 50p		4060 100p	LF 357	80p MC 1312 P	150p TBA 530 Q	200p
7403	10p			74198 100p	4066 35 p	LM 211 H	250p MC 1314 P	190p TBA 540	200p
7404	12p		74143 2700		4069 12p	LM 300 TR5	170p MC 1315 P	230p TBA 550 Q	250p
7405	12p	7475 25			4070 12 p	LM 301 AN	30p MK 50398	650p TBA 560 C	250p
7406	25p	7476 25			4071 12p	LM 304	200p MM 5314	380p TBA 641 A12	2 250p
7407	25p	7480 40	1		4072 12p	LM 307N	65p MM 5316	480p TBA 700	180p
7408	12p	7481 85			4081 12p	LM 308 TO5	100p NE 529 K	150p TBA 720 Q	225p
7409	120	7482 75				LM 308 DIL	100p NE 555	25p TBA 750 Q	200p
7410	12p	7483 75				LM 309 K	100p NE 556	90p TBA 800	80p
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7438	20p	74109 25	74177 50p		CA 3088 190	LIVI /48	40p TAA 790	350p XR 4136	150p
7440	12p	74118 75	74178 75		CA 3089 160		100p TAD 100	150p XR 4202	150p
7441	45p	74120 80	74179 120		CA 3090AQ360	Do 1111450	100p TAD 110	130p XR 4212	150p
7442	40p	74121 25	74180 90	4030 30 p	CA 3123 E 130	LIVI 3000	75p TBA 120 S	60p XR 4739	150p
7443	60p	74122 35	74181 130	4032 80p	CA 3130 100	LM 3900	55p TBA 120 T	85p ZN 414	100p
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ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979

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TWONKY

May Hadley has designed an MPU music box that plays random tunes to the rules laid down by a compositional algorithm.

EVER SINCE THE computer was invented, whenever that was, there have been people who have sought to apply it in previously untouched fields. Doubtless the same will happen with the microprocessor to a much greater extent because of its vastly lower cost and wider circle of users. Certainly the amateur constructor can do far more than simply make miniature computers. Twonky is one such application in the field of computer music.

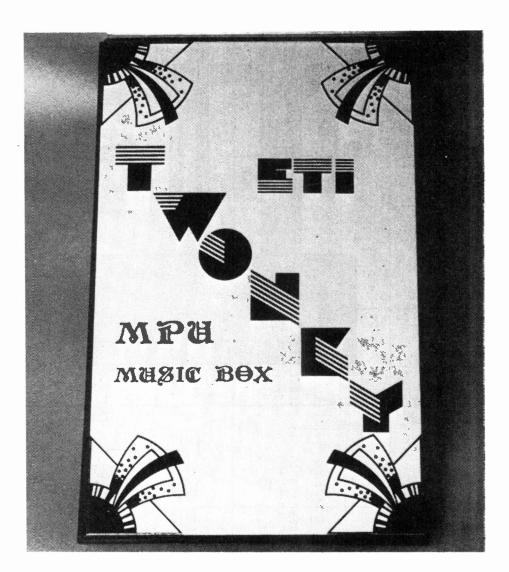
Macro Music

Music was first applied to computers in the late '50s. Machines of that vintage were often fitted with loudspeakers monitoring a register or address bit, to aid in software and hardware fault tracing. Cunning programmers soon realised how to make such computers play tunes when no-one was around to stop them, and so computer music was born. It grew rapidly.

One of its earliest exponents was Professor Lejaren Hiller of Illinois University who together with his colleague Prof. Leonard M. Isaacson conducted a series of studies which are described in their book 'Experimental Music' (McGraw Hill 1959). They began by using the computer to test the classical compositional rules of species counterpoint, developed in the seventeenth century by J. J. Fux and taught to music students ever since. A program was written which would generate random notes, test them against the rules and insert them where a suitable match was found. Though this sounds simple enough, it took several years to do, as the 'rules' were by no means complete: many things were assumed as being obvious by the musical theorists which had to be explicitly stated for the computer.

Suite Illiac

By this time, the original aim, which was to test the compositional



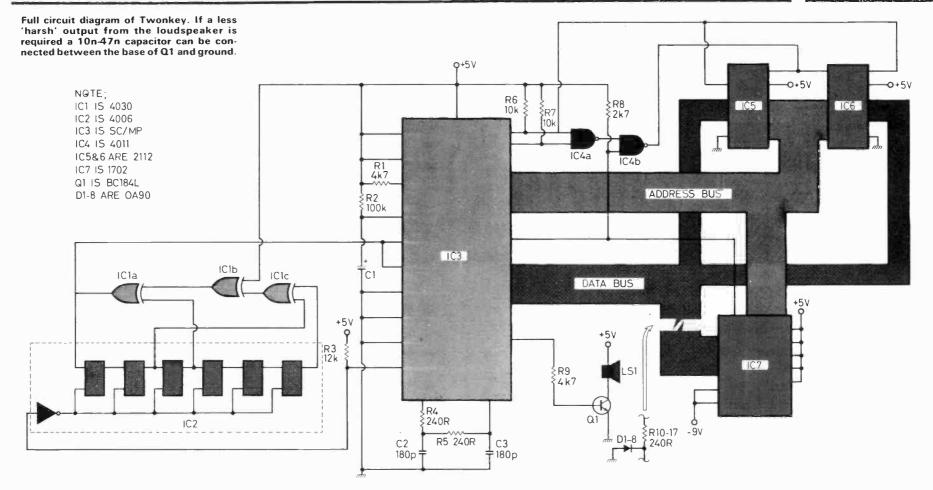
rules in question, had become secondary to the fun of using the computer to generate new music.

Other styles and principles, ranging from the sixteenth to the twentieth centuries, were applied in something of a mixture, and the result served up as the 'Illiac Suite for String Quartet' (named after the famous ILLIAC IV computer on which they were composed.) This proved rather disappointing, sounding almost a parody of twentieth century chamber music.

Other workers, such as Professor J. K. Randall of Princeton University, developed slightly different lines of approach, including the one used by Twonky. Prof. Randall's work 'Prelude to Mudgett' may be heard on disc (Nonesuch 71245), and is a typical example of this style and approach.

While this effort was going into composition and stylistic analysis,

PROJECT: Twonky



-HOW IT WORKS ~ HARDWARE

The National Semiconductor SC/MP is a simple, cheap, 8-bit processor designed for use in minimal systems; to this end it has an on-chip clock generator and I/O facilities, and needs no bus buffers in small systems. The instruction set is not large, but contains such useful features as a wide range of addressing modes and the capability of double-indexed memory references.

Internally, the chip has seven main registers; an 8-bit accumulator, an 8-bit status register, four 16-bit pointer or index registers (one of which is dedicated as the program counter), an 8-bit extension register. All memory references (including jumps) are via an index register; the second byte of each memory reference instruction is a displacement which is added to the index register and NWDS, and NRDS high, to prevent spurious memory enables while the MPU outputs are in the high impedance mode between memory accesses.

Components R5, R8, C2, and C3 set the processor clock frequency at about 4MHz. R5 can be made variable to act as a tuning control, but must be between 100 ohms and 2kilohms. The MPU is reset on power-up by R3 and C1, and the first instruction is fetched from location OOIH.

IC6 and IC7 form a PRBS generator. An 18-stage shift register, clocked by the NADS strobe from the MPU has exclusive OR feedback arranged around it such that it will produce a stream of bits in a repeating sequence 2^{18} -1 (262,143) bits long. Within this overall sequence, the bit stream is random, other people were engaged in turning the computer into a new musical instrument, a 'super synthesiser' (although this work was begun before Dr Moog invented the voltage controlled analogue synthesiser). Several programs have been developed; TEMPO by Glough and Sosman on an IBM 360/44, MUSIGOL at the University of Virginia, and the most widely used, MUSIC 4 (and its derivatives MUSIC's 4B, 4BF, and 5) at Bell labs and Princeton.

This is a program, mainly in FORTRAN IV but with some assembly language sections, which play tunes monophonically using squarewaves. The compositional algorithm (due to Prof. Randall) is based on two simply observations:-

1. Every tune has at least one highest note

2. Every tune can be split into two subtunes at least one not long, which may then themselves be regarded as tunes.

To compose a tune using these rules, we assume also that each tune only has one highest note, and that each subtune is half the length of the tune. We take a given note as the highest note in the whole tune and

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may be in the range -127 to 127. If the displacement has the value -128 the contents of the extension register are used as the displacement to be added (doubly indexed memory reference). There is, however, no explicit subroutine call instruction.

The status register contains carry, overflow, and interrupt enable flags, two sense input bits, and three user definable flags. These last five are taken to package pins to provide limited 1/O capability. Twonky uses the sense-B input to read random bits from the pseudo-random binary sequence generator (PRBS generator) and the flags to drive the audio amplifier. Out of the total of 46 instructions, only 17 are used and these are shown in fig. 1 along with the status register bit allocation.

The program itself is 256 bytes long and lives in a 1702A EPROM at addresses OOOH to OFFH. 256 bytes of RAM in the shape of 2, 2112-A4 256 x 4 bit static chips are provided at addresses 100H to 1FFH. Address lines AO to A7 are common to 1C2, 3, and 4, while A8 is taken to the CE input of 1C2 to enable it at the correct range of addresses. Note that IC2 will be enabled by any memory access, read or write, in the correct address range. If a faulty program goes berserk and tries to write to ROM, two devices will be enabled onto the data bus at the same time. This might be fatal, were it not for R9-R16 which prevent a short circuit. Additionally, in conjunction with D1-D8, they prevent negative voltages from the PMOS ROM appearing at the inputs of the other, NMOS, devices on the bus.

The RAM is enabled by the signal from pin 11 of 1C5, which will be low (RAM enabled) when A8 is high and either NWDS (not write data strobe) or NRDS (not read data strobe) is low. The resistors R7, 8, and 18 tie A8, i.e. the probability of the next bit at any point in the sequence being a one is constant at O.5. This random sequence is fed to sense-B on the MPU and is used by the software to provide the random element in each tune.

Also, since the sense -B input is not sampled by the MPU internal logic during the NADS strobe time, the random bit will always be read unambiguously.

The power to IC6 and IC7 is not switched; they are CMOS devices which when not being clocked draw only about a microamp. This is necessary, as should the shift register be in the all zero state on switch on, the generator will stick and produce a continuous stream of zeros. Logic could be incorporated to force ones into the register on switch-on, but unless it was very devious, would result in the dame sequence of pseudo-random bits (and hence tunes) occuring every time.

The audio output is taken from the MPU flag O output and amplified by Q1 to drive the speaker. A line level output may also be taken from flag 1 or 2 if desired.

There are two types of SC/MP processor available; this circuit uses the NMOS variety, which is cheaper, faster, uses less power and needs only +5V and ground. The older PMOS type can be used, but not all the control signals are the same, and so the circuitry around IC5 will need to be altered. The pitch will also be about an octave lower. Owners of SC/MP development systems, such as the introkit, MK14, or Scrumpi will be able to hook up a PRBS generator and loudspeaker to their systems with little trouble, and to relocate the code as appropriate. For further details on the SC/MP chip the data sheet, Nat. Semi pub. No. 426305290-OO1B (!) may be consulted.

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Hex dump of the

PROM program for

the Twonky composer

generates musical sounds as a series of digital samples which are fed to a D/A converter, usually via the intermediate medium of magtape. Sounds are described in terms of instruments, which are routine that use stored tables of sinewayes, exponentials, ramps and other waveforms to generate complex sound sources. These are coupled via filter, reverberations, stereo position and other modules into an 'orchestra,' which outputs the final sound onto tape. The music to be played is input in the form of note cards. These punched cards carry such details as pitch, rate of rise and fall of the envelopes, start time, and other, user-defined parameters.

One Hundred 'seconds

In the early days, it took as much as 50 to 100 seconds of computer time to generate a second of music, but with modern machines, synthesis can take place in real time or faster. The program is not, however, suitable for live performance use. The result of such programs can be most impressive, particularly in the hands of a skilled 'player.' Certainly, they are far more flexible and versatile than analogue synthesisers. They have the particular merit that if, for example, 96 oscillators are needed, the function OSCIL is merely called 96 times. This uses more processor time, but does not need any additional hardware.

MUSIC 4B, together with analogue sound synthesisers, is described in Hubert S. Howe's book 'Electronic Music Synthesis.' The field of digital sound synthesis is certainly an exciting one, but is somewhat beyond the reach of the amateur, although with powerful 16 bit machines such as the LSI 11 and TMS9900 becoming cheaper, it may not remain so for long.

A Little Micro Music

Twonky is a composing machine which also incorporates software to

assign it randomly to one or other of the subtunes. The highest note in the other subtune must be lower than that in the first: we assume it is the next note down whatever scale we are using. However, each subtune may now itself be regarded as a new tune, provided it is at least two notes long. Hence in each first-level subtune, we take the highest note and assign it randomly to one or other of the second-level subtunes. adding the next lowest note in our scale as the highest note in the other. By repeating the process, we double the number of known notes in our tune (each of which is the highest note of some subtune) and increase the number of pitches by one for each level of splitting we indulge in. This process can hence be described as a random tree.

Seventh Level

In Twonky, seven levels of division are used to generate 128 subtunes each one note long, with a total range of 8 pitches (one octave of the scale of C major). The random decision at each level is produced by a hardware random number generator.

The rhythmic element in each tune is produced by selecting one of a small number of rhythm units or bars on a random basis and fitting the notes of the tune to that bar. The melodic algorithm weights the distribution of notes binorally, thus there are 2 F s (one of each octave). 7 G s 21 A s, 35 B s, 35 C s, 21 D s, and 7 E s. The tonic or key-note C occurs most frequently, lending a definite key to the melody. However, it is usual for the dominant G also to occur frequently, which it does not do. This gives all Twonky's compositions a unique and unusual style, somewhat like Mediaeval music (nothing to do with the use of a SC/MP MPU) this is enhanced by the ready tone of the square wave output.

0000 08 04 01 36 04 FE 32 C4 01 CA 00 C4 00 36 C4 00 0010 32 C6 01 C4 80 31 C4 80 01 C4 01 35 C1 00 C9 80 0020 01 F4 02 02 01 C9 80 06 D4 20 98 0A C1 80 F4 01 0030 02 C9 80 01 90 10 01 F4 FE 02 01 C1 80 F4 01 02 0040 C9 80 01 F4 02 02 98 05 01 C5 02 90 CF C4 00 32 0050 F4 F9 02 98 06 F4 07 02 32 90 B6 C4 EF 32 35 C4 0060 01 37 C4 FF 33 06 D4 20 9C 06 C4 97 31 3D 90 F5 0070 06 D4 20 9C 06 C4 9B 31 3D 90 08 C4 9F 31 3D C4 0080 9F 31 3D 06 D4 20 9C 06 C4 9B 31 3D 90 D7 C4 9F 0090 31 3D C4 9F 31 3D 90 CD C4 01 90 06 C4 02 90 02 00A0 C4 03 35 C7 FF 01 C2 80 CB 00 01 F4 08 02 01 C2 00B0 80 01 C4 00 35 19 F4 FF 02 9C FA 01 CB 01 33 98 00C0 04 33 C7 FF 3D 33 C3 01 31 C4 07 07 C4 0F 8F 00 00D0 C3 00 F4 FF 02 9C FB C4 00 07 C3 00 F4 FF 02 9C 00E0 FB 31 F4 FF 02 9C E1 8F 3A C7 02 33 9C D7 35 3D 00F0 32 35 3C 44 48 51 5B 67 FE F0 D6 BD B3 A0 8F 7F

PROGRAM LISTING

00 01 03 04 06	08 C4 36 C4 32	01 FE	START	NOP LDI XPAH LDI XPAL	l PTR 1 FEH PTR 2	Dummy instruction — not executed Store 1 in location 510	83 84 86 88 88	06 D4 9C C4 31	20 06 9B	SECPART	CSA ANI JNZ LDI XPAL	00100000B RHYTH3 9BH PTR 1 PTR 1
07	C4	01		LDI	1		8B 8C	3D 90	D7		XPPC JMP	PTR 1 NXNOTE
09	OA C4	00 00		STO LDI	PTR 2+0 0	Clear cycle counter (No. of levels down	8E	C4	9F	RHYTH3	LDI	9FH
0B 0D	36	00		XPAH	PTR 2	decision tree)	90	31			XPAL	PTR 1
0Ē	C4	00		LDI	0	Each location in top half of memory is	91	3D			XPPC	PTR 1
10	32			XPAL	PTR 2	written to two locations starting at	92	C4	9F		LDI	9FH
						bottom of RAM with random incre- ment. Repeated 7 times	94 95	31 3D			XPAL XPPC	PTR 1 PTR 1
11	C6	01	OUTLOOP	LD	PTR 2 + 1	Increment cycle counter	96	90	CD		JMP	NXNOTE
11 13	C4	80	OUTLOOF	LDI	80H	Set PTR $1 = 180H = 384$ (bottom of top	98	C4	01	WRNOTE	LDI	1
15	31	00		XPAL	PTR 1	half of RAM)	9A	90	06		JMP	GO ,
16	C4	80		LDI		Extension register = -128	, 9C	C4	02		LDI	2
18	01	_		XAE		PTR 1 points to location being read, and	9E	90	02		JMP	GO
19	C4	01		LDI XPAH	l PTR l	is stepped upwards. EXT contains displacemtn to location being written	AO	C4	03		LDI	3
1B 1C	35 C1	00	INLOOP	LD	PTR1+0	displacement to location being mitten	A2	35		GO	XPAH	PTR 1
10	C9	80	meoor	STO	PTR 1-128	Take contents of (PTR 1) and store 1 in	A3	C7	FF		LD@	PTR 3-1
20	01	•••		XAE		(PTR 1 + EXT) and in $(PTR 1 + EXT + 2)$	A5	01	~~		XAE	DED 0 100
21	F4	02		ADI	2		A6 A8	C2 CB	80 00		LD STO	PTR 2-128 PTR 3 + 0
23	02			CCL			AA		00		XAE	FIKSTU
24 25	01 C9	80		XAE STO	PTR 1-128		AB		08		ADI	8
2.5	06	00		CSA	F I K 1-120	Input random bit and either:		02			CCL	
28	D4	20		ANI	0010000B = 32		AE		~ ~		XAE	DTD 0 100
2A	98	0A		JZ	INCLOW		AF	C2 01	80		LD XAE	PTR 2-128
2C	Cl	80		LD	PTR 1-128	Increment $PTR 1 + EXT + 2$) or:	B1 B2	C4	00		LDI	0
2E 30	F4 02	01		ADI CCL	1		B4	35	00		XPAH	PTR 1
31	C9	80		STO	PTR 1-128		B5	19	S10	1	S10	
33	01	00		XAE			B6	F4	FF		ADI	-1
34	90	10		JMP	EXTEST		B8 B9	02 9C	FA		CCL JNZ	S101
36	01	FF	INCLOW	XAE	0	Increment PTR 1 + EXT)	BB	9C	гА		XAE	5101
37 39	F4 02	FE		ADI CCL	2	·	BC		01		STO	PTR 3 + 1
39 3A	01			XAE			BE	33			XPAL	PTR 3
3B	ĊÌ	80		LD	PTR 1-128		BF	98	04		JZ	PLAY
3D	F4	01		ADI	1		C1 C2	33 C7	FF		XPAL LD@	PTR 3 PTR 3-1
3F	02	00		CCL	DTD 1 199		C4	3D	гг		XPPC	PTR 3-1 PTR 1
40 42	C9 01	80		STO XAE	PTR 1-128		Č5	33		PLAY	XPAL	PTR 3
43	F4	02		ADI	2	Add 2 to EXT for next pass	C6	C3	01		LD	PTR 3 + 1
45	02			CCL		to prove the second second second	C8	31	07	POS	XPAL	PTR 1
46	98	05	EXTEST	JZ	PTEST	If EXT = 0 go to PTEST; 1 pass through	C9 CB	C4 07	07		LDI CAS	00000111B
48	01	00		XAE		memory completed, else add 2 to PTR 1 and loop back to INLOOP	CC		OF		LDI	15
49 4B	C5 90	02 CF		LD@ JMP	PTR 1 + 2 INLOOP		ČĔ		00		DLY	0
4D	C4	00	PTEST	LDI	0	If cycle counter $PTR 2$ = 7 then go to	D0	C3	00		LD	PTR 3 + 0
4F	32			XPAL	PTR 2	note $PTR 2 = 0$)	D2		FF	ADII	ADI CCL	-1
50	F4	F9		ADI	-7		D4 D5	9C	FB		JNZ	ADII
52	02	00		CCL	NOTE		D7				LDI	00000000B
53 55	98 F4	06 07		JZ ADI	NOTE 7	else go to OUTLOOP	D9	07			CAS	
57	02	07		CCL		-	DA			1.010	LD	PTR $3+0$
58	32			XPAL	PTR 2		DC	F4 02	FF	ADI2	ADI	-1
59	90	B6	NOTE	JMP	OUTLOOP	PTR 2 points to pitch table	DE		FB		CCL JNZ	AD12
5B 5D	C4 32	EF	NOTE	LDI XPAL	EFH PTR 2	PTR was zero)	EI	31	1.0		XPAL	PTR 1
5E	35			XPAH	PTR 1	Clear high byte of PTR 1 - return address	E2	F4	FF		ADI	-1
02						/	E4	02			CCL	

Input random bit and either:

Write middle sized note by subroutine at 9BH, on return go to NXNOTE

or write 2 short notes by 2 calls to subroutine at 9FH

Go to NXNOTE for next note from RAM

Subroutine WRNOTE. Entry point determines note length divisor. This is loaded to high byte of pointer containing return address as this is always zero)

Load pitch code from RAM, use as index for table of pitches. Replace code with actual pitch from table. Pitch code stays in extension register

Add 8 to pitch code and use as index to duration table \cdot contains number of cycles for 0.362 secs at each pitch)

Note length to EXT ready for division divisor is fetched from pointer 1 high byte which is zeroed at the same time

Number of cycles divided by 2, 4, or 8 depending on subroutine entry point to give the 3 different note lengths

Note length is stored in RAM location immediately above corresponding pitch if end of RAM · 100H) has been reached, go to play else decrement PTR and return

Load note length · PTR points to low byte of current note) and store in PTR 1

Load bit pattern to set flag bits in stat reg. Output high delay to make half cycles of audio waveform same length

Load pitch from RAM • number of times round this delay loop for a half cycle at desired frequency)

Clear flag bits Takes audio output low Load pitch from RAM Delay loop for negative half cycle

Decrement cycle counter. If not zero, go to POS for another cycle

5 6 FL 6	1 37 2 C4 1	01 FF		LDI XPAH LDI XPAL	l PTR 3 FFH PTR 3	will be stored here) Set PTR 3= 511 = 1FFH · top of RAM)	E5 E7 E9 EB	9C 8F C7 33	E1 3A 02	JNZ DLY LD@	POS 58 PTR 3+2	Move	note gap note cou R 3/200H	inter to	next note play 'next note)	
-CTRO	5 06 5 D4 2 8 9C 0	20 06	XNOTE	CSA ANI JNZ	00100000B RHYTH1	Start of note length writing loop input random bit and <i>either</i> .	EC EE EF	9C 35 3D	D7	XPAL JNZ XPAH XPPC	PTR 3 PLAY PTR 1 PTR 1				or another tune!	
	C 31 D 3D E 90 1	97 F5		LDI XPAL XPPC JMP	97H PTR 1 PTR 1 NXNOTE	Write long note by subroutine at 97H, on return, jump back to NXNOTE	F0 F1 F2 F3	32 35 3C 44		DEFB DEFB DEFB DEFB	50 53 60 68	F E D C	350.875 332.005 294.985 261.645	HZ HZ s HZ	Pitches at 4MHZ	
7 7 7 7 7 7 7 7	D4 2 9C 0	, RI 20 06 9B	HYTHI	CSA ANI JNZ LDI	00100000B RHYTH2 9BH	or input random bit and <i>either</i> : Write middle sized note by subroutine at 9BH, on return go to SECPART	F4 F5 F6 F7	48 51 5B 67		DEFB DEFB DEFB DEFB	72 81 91 103	B A G F	247.645 221.045 197.47 175.07	HZ HZ		
77777777777777777777777777777777777777	31 3D 90 (3 C4 9 31 5 3D		HYTH2	XPAL XPPC JMP LDI XPAL XPPC	PTR 1 PTR 1 SECPART 9FH PTR 1 PTR 1	or write 2 short notes by 2 calls to subroutine at 9FH	F8 F9 FA FB FC	FE F0 D6 BD B3		DEFB DEFB DEFB DEFB DEFB	254 240 214 189 179	0.362 S 0.361 S 0.363 S 0.361 S 0.361 S	ECS ECS ECS ECS ECS		n of long note at	
	31	9F		LDI XPAL XPPC	9FH PTR 1 PTR 1		FD FE FF	A0 8F 71		DEFB DEFB DEFB	160 143 127	0.362 S 0.362 S 0.363 S	ECS			

-HOW IT WORKS'~SOFTWARE

The programme itself falls naturally into four parts, which are shown in the four flowcharts. Of these, three (START, NOTE and WRNOTE) write the tune, and one (PLAY) plays it. Before describing the operation of each in more detail, a couple of notes are relevant.

— enclosing an expression in brackets turns it from a number into an address. Thus 510 is a number, but (510) means 'the contents of location 510'.

— all variables used in the flowcharts are actual machine registers except for the dummy variables A and B in WRNOTE. Of these, B is introduced only to improve readibility, while A is an argument passed to this subroutine from the main program. It is implemented in object code by calls to 3 different addresses for its 3 possible values.

START

This is the program section which implements the random decision tree to select the pitches used in the tune. In this section the notes are numbered from 1 to 8 (highest to lowest). The code for each note consists of two bytes, one for pitch and one for duration, which occupy consecutive locations. Pitches are always in even-numbered locations.

On reset, a 1 is written to the last note pitch location (510), and the loop counter PTR 2 is reset. The program then enters a loop in which each note in the top half of RAM,

starting at the bottom and going up, is written to two successive note locations, starting at the bottom. of RAM and going up. The writing address catches up with the read address at location 510, which is written to 508 and back into 510. At each step one or other of the two locations is incremented, depending on the state of the random number generator.

Thus after one complete pass through this loop, our tune, which started out as one note — a one — in location 510, is now twice as long and has two notes, a one and a two, randomly arranged in locations 508 and 510. So far so good. We now repeat this loop a total of seven times, each time doubling the number of notes written, until the memory is full (128 notes). We will then have 8 different note numbers or pitches. In fact, what we have done is identical to the decision tree method in the text (try it yourself with pencil and paper).

This section occupies addresses 00H to 60H. PTR 2 is the loop counter which goes from one to seven. On reaching seven, the program branches to NOTE. Within the section, PTR 1 points to the location being read, and EXT contains the displacement from this address to that of the location being written into.

SC/MP fanatics may notice that a separate read-increment-write instruction sequence is used (at 2CH to 33H and at 3BH to 41H) instead of the increment and load single instruction. This is because the ILD instruction does not allow doubly-indexed addressing to be used. This is not made very clear in the databook, and had to be found out the hard way!

NOTE

NOTE is the program section concerned with writing the rhythm of the tune. It has three different note lengths to play with, of relative values 4, 2, and 1. Each bar or rhythm unit can be one of 4, 2 + 2, 2 + 1 + 1 + 1, 1 + 1 1 + 2, or 1 + 1 + 1 + 1, determined by random decisions. The flowchart for this section is more or less self explanatory. The notes of different lengths are written by calls to the subroutine WRNOTE. This has three different entry points (98H, 9CG, AOH) which determine the length of note (long, medium or short). There is no explicit test for leaving the loop in this section as this is done in WRNOTE.

WR NOTE

On being called, this section reads the value of pitch code from RAM (starting at location 510 and going downwards) and uses it as an index to the table of pitches at locations FOH to F7H. The pitch obtained from this table is then stored in the same RAM location from which was read its code. Thus 3 will be replaced by 3CH, 8 by 67H etc. These pitches represent the length of a half cycle at the desired frequency in multiples of the time taken to go round the delay loops in PLAY.

By adding 8 to the pitch code the table of durations (F8H to FFH) is accessed in the same way. The duration is then divided by 2, 4 or 8 to give the required note length in terms of a number of cycles at its particular frequency. This number is then stored in the RAM location immediately above its corresponding pitch. WRNOTE then tests for the last note in the tune (PTR 3=255); if the last note has not been reached, control is returned tio NOTE, otherwise control passes to PLAY.

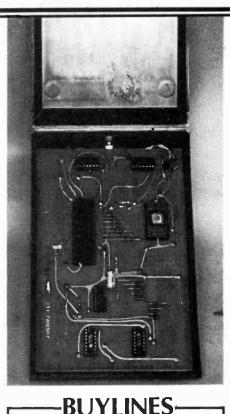
PLAY

This section is quite simple, consisting of two delay loops for pitch, and counters for duration and number of notes played. For each note in turn, the duration is first loaded to PTR 1. The pitch is loaded to the accumulator and the output taken high. The output remains high while the accumulator is decremented and tested for equality to zero. This gives a delay dependent on the initial pitch value. When zero is reached, the output is taken low and the pitch again loaded and decremented to zero. At the end of the second half cycle PTR 1, the duration counter, is decremented and tested for quality to zero. If not zero, another cycle of the same note is produced, otherwise the next note is played, after the end of tune test (PTR 3=512). When the end of the tune is reached, control returns to START to write and play a new tune.

PROJECT: Twonky

Construction is quite straightforward. Sockets should be used for all IC's and normal MOS handling precautions taken. Begin by installing all through board links and testing them for continuity. Then add the resistors, capacitors, and discrete semiconductors. IC 5 may be fitted and the memory decoding checked. IC 6 & 7 should be added next, and the production of random bits at IC 7 pin 6 as pin 3 is clocked by shorting it to ground verified. Finally, add the LSI chips and switch on. Music should greet your ears within about 0.25 secs. Gaps of about this length occur every 128 notes as a new tune is written. The circuit meets all timing requirements with the 1702A only up to 3.5 MHz. Most 1702As will work happily at 4MHz, but the odd one may not. Reducing the clock frequency should effect a cure.

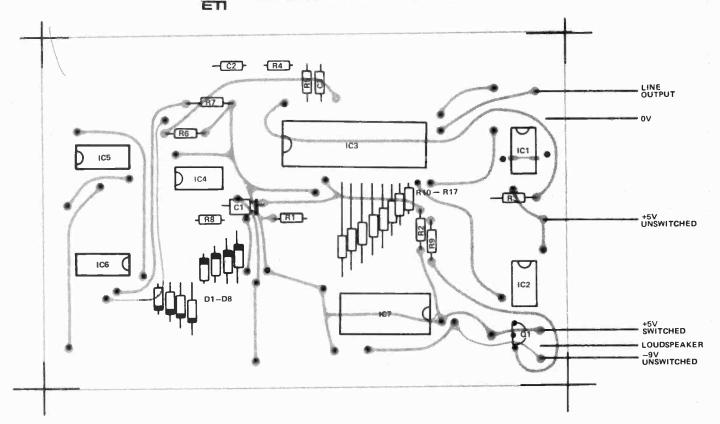
The PCB is single Eurocard size (100 \times 160 mm) and will fit in one of the larger size veroboxes, which are designed for this standard. Batteries, either 4 \times 1.5V + 1 \times 9V dry cells or the equivalent nicads, will then fit under the circuit board, or the PCB may be left uncased. The only major problem which may arise is getting the EPROM programmed. Several firms offering such a service advertise on the pages of ETI and one of these should be able to help.



Marshalls, see their advert in this issue for addresses, will be supplying an EPROM with the Twonky program burned in. They will also be able to supply all the other parts for this project except the PCB which will be available from Tamtronix, Ramar, Crofton etc. Photograph showing Twonky mounted in the larger sized Vero flip top case. The speaker and batteries are mounted under the PCB. The case is not very deep and a 'shallow' speaker must be used if Twonky is to be built in this case.

٢	PAR	rs list—
	R2 R3 R4, 5; 10-17	4k7 100k 12k
	CAPACITORS C1 C2,3	4u7 16V electrolytic 180p 16V ceramic
	IC4 IC5, 6 IC7	ORS 4030 4006 SC/MP 4011 2112 1702 BC184L 0A90
	MISCELLANEO	US
	PCB, loudspea clips.	iker, case batteries and

Component overlay for the ETI Twonky. The wire link that is visible on the photo or the prototype's PCB has been replaced with a foil track.





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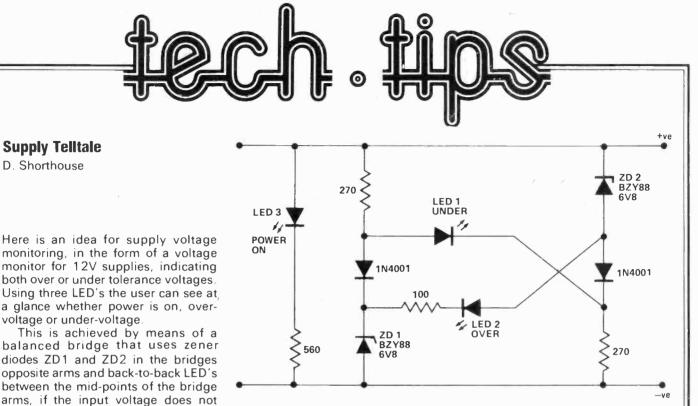
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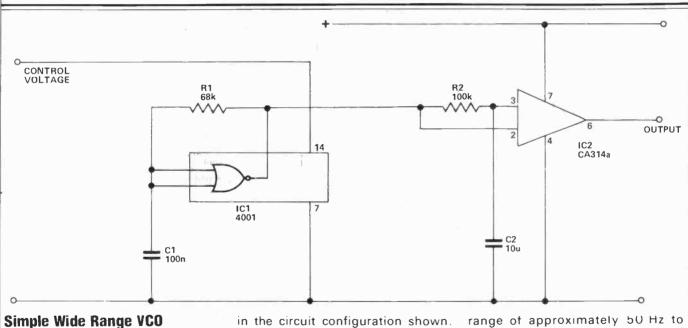
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the input voltage increases to the extent that at the junction of ZD2, it exceeds the zener voltage of ZD1, plus the LED voltage of 1.6V, then

LED2 is turned on, with resistor 100R limiting the current through the LED. Note total drain of unit is about 50 mA.



A. J. Richardson

Any section of ICI can be used but all unused inputs must be taken to ground.

exceed the two zener breakdown

voltages $(2 \times 6V8 = 13V6)$, LED1

lights but above 13V6 LED1 becomes

reverse biased and remains off when

This circuit takes advantage of the fact that CMOS gates readily oscillate

in the circuit configuration shown. The control voltage, which ideally is in the range 1V5 to 3V5, is applied to the power supply connection of ICI. IC2 is used to square up and buffer the output of ICI and can be operated from any suitable voltage rail. With the values shown a frequency range of approximately 50 Hz to 20 kHz is obtained with almost equal mark to space ratio, but if this is unimportant the lower end can be extended down to approximately 1 Hz. Other frequency ranges can be obtained with suitable values of R1 and C1.

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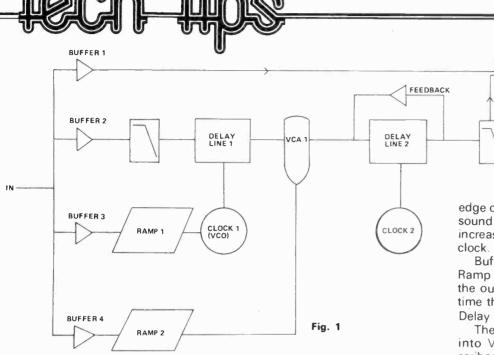
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Readers' Circuits



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S. Giles

Object:

To create an audio signal which is increased in pitch by up to one octave from the original, and sustain it for the duration of the original.

Applications:

(a) electric guitar (b) synthesisers with one VCO

(c) human voice???

Description of Block Diagram (fig. 1)

The input signal is buffered into four

separate paths which are dealt with as follows

Buffer 1 output is unprocessed and will apear at the mixer as original signal

Buffer 2 output is low pass filtered and fed to the input of Delay Line 1. This should consist of as many TDA 1022's that are required to create a one octave pitch change when clocked by a VCO which is modulated by Ramp 1

Buffer 3 output is converted into Ramp 1 for modulating Delay Line 1 clock to produce the pitch change. The ramp is triggered by the leading edge of each note played on the input sound source, and should be set up to increase the frequency of Delay Line 1

OUT

Buffer 4 output is converted into Ramp 2 and should be set up to hold the output of VCA 1 for slightly less time than the pitch change is held in Delay Line 1

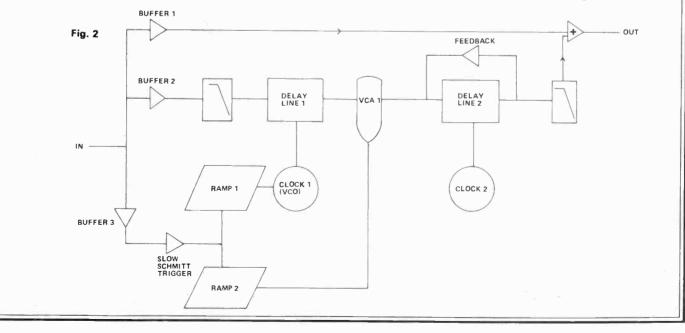
The output of Delay Line 1 is fed into VCA 1 and controlled as described above.

The output of VCA 1 is fed into Delay Line 2 which is wired as a reverberator with it's feedback set very high, but not over-loading

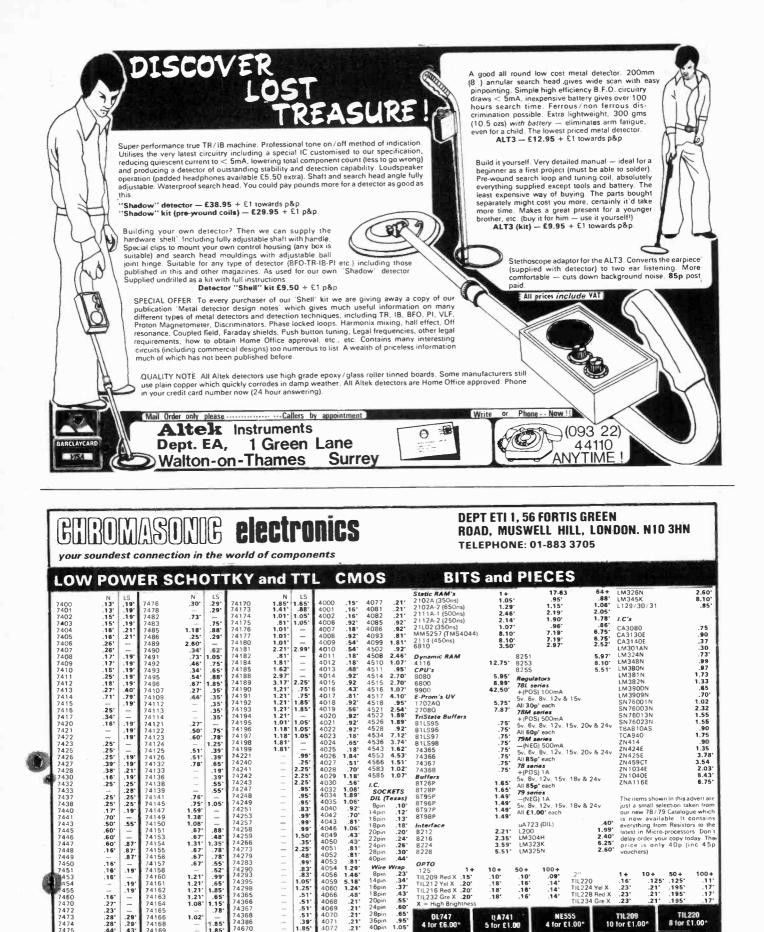
The output of Delay Line 2 is low pass filtered and mixed with the original unprocessed input from Buffer 1 in the required proportions.

Alternative Approach (fig. 2)

This should enable Ramps 1 and 2 to be triggered more than once during each note thus sustaining the signal which is changed in pitch even longer. Only three input buffers are required in this arrangement.



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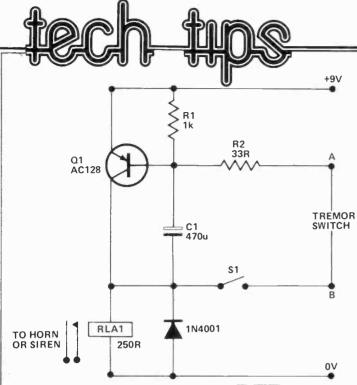
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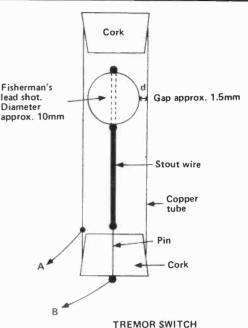
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P. Mann

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Capacitance Measurement

W. Winder

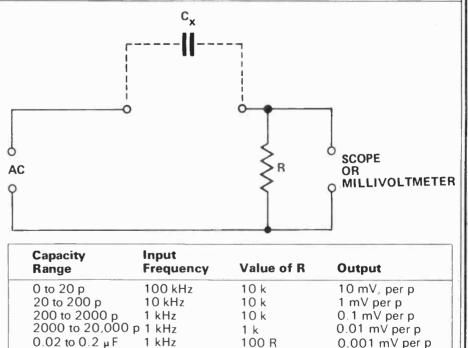
Few amateurs have a reliable method for measuring small capacitors. They may have a 50 Hz bridge, but the reactance of 10 pfs. at 50 Hz is some 320 megohms, which can well be of the same order as the bridge insulation, which leads to indeterminate and incorrect results. However if one has an A.F. signal generator and a measuring oscilloscope (or a.c. millivoltmeter), one can measure down to 2 or 3 pfs. with guite as good an accuracy as more complicated methods using square wave generators and diode pumps. The following very simple circuit is all that is necessary

As long as the reactance of the capacitor is several times larger than the resistance of R, the output voltage will be directly proportional to the capacitance of C. By supplying a 1.6 volt input signal, the mathematics are simplified, and the output measurements are as the table given below.

The input wave form should be

approximately $20-30\mu A$, so low that no on-off switch has been incorporated. SW1 isolates the tremor switch for riding. R2 prevents high current flowing on discharge of the capacitor which was found to weld the lead shot to the copper tube. Design of the tremor switch I think is up to the constructor and the bits and pieces he or she has available. Sensitivity lies solely on the construction of this, (weight of shot, gap and length of wire).

My siren consists of a NE555 design from ETI Jan '77 and an LM380 power amp.



fairly good, as any harmonics present are exaggerated by the capacitor, and the shape of the output waveform can be anything but a pretty sine wave. However it has to be a poor signal generator that does this.

ELECTROVALUE BUYING GUIDE

With this advertisement we complete our series in which we have presented a wide and useful cross-section of our most demanded lines. We hope it will have encouraged you to have purchased from us and sample our service. We go to great effort to make this as efficient and as personal as possible. NOW WE ANNOUNCE PRODUCTION OF 120-PAGE CATALOGUE NO. 10. Completely revised, enlarged and better than ever - AND IT'S YOURS FOR THE ASKING - FREE.

I.Cs/Opto/Displays

INTEGRATED CIR TTL 7400 series 7400 7401 7402 7403 7404 7405 7407 7408 7409 7410 7410 7413 7414 7410 7413 7414 7420 7430 7440 7442 7443 7444 7444 7447 7450	CUITS 14p 14p 14p 14p 14p 22p 18p 14p 22p 14p 14p 14p 54p 60p 60p 70p 14p	4024 4025 4026 4027 4028 4029 4030 4041 4042 4043 4044 4049 4050 4069 4050 4069 4050 4069 4070 4071 4072 4081 4082
7450 7451 7453 7454 7460 7470 7472 7473 7474 7475 7476 7476 7480 7482 7483 7485	14p 14p 14p 24p 24p 23p 45p 32p 41p 61p 58p 74p	4510 4511 4514 4518 4520 4543 4583
7485 7490 7491 7492 7493 7494 7495 74100 74104 74104 74107 74121 74123 74141 74151 74151 74154 74190 74191 74192 74193	74p 27p 40p 46p 66p 57p 66p 73p 73p 27p 27p 54p 94p 94p 94p 94p	723C5 723C14 741C5 723C14 741C8 741C8 741C8 741C8 741C8 741C7 7815 7905 7815 7905 7815 7912 CA313C CA313C CA314C LM308f LM308f LM308f
CMOS (All buffered protected types) 4000 4001 4002 4006 4007 4008 4009 4010 4011 4011 4011 4013 4014 4015 4016 4017 4018 4019 4021 4022 4023	1 and 19p 19p 96p 96p 54p 54p 19p 19p 40p 96p 1.14 40p 88p 64p 96p 88p 88p 88p 88p 88p 88p 88p 8	NE555V NE555V S041E S041P S042P S5668 SAJ131 SAJ205 SAJ410 SAS211 SAS201 SAS500 SAS500 SAS500 SAS500 SN7600 SN7600 SN7600 SN7600 SN7600 SN7600 SN7600 SN7600

hro		isp
4024 4025 4026 4027 4028 4029 4030 4041 4042 4043 4044 4046 4049 4050 4060 4060 4060 4069 4070 4071 4071 4081 4081	90p 19p 1.75 58p 95p 1.23 54p 80p 93p 1.28 48p 48p 48p 1.33 19p 19p 19p 19p	TAA761A TAA861A TAA2761A TAA2761A TAA2761A TBA120S TBA400 TBA80 TBA80 TBA80 TBA80 TBA80 TBA80 TBA80 TBA80 TBA9747A TB8145BB TCA311A TCA335A TCA345A TCA36D TCA360 TCA965 UAA170 UAA180
4510 4511 4514 4516 4518 4520 4543 4583	1.28 1.36 3.30 1.37 1.25 1.25 1.25 1.30 1.28	OPTO-ELEC Photosensit BPW32 BPW33 BPW34 BPX48 BPX48 BPX63 BPX91
I.C.s of alternative ture may be supplie 723C14 741C5 723C14 741C5 741C5 741C4 741C5 741C14 748C8 7107 7805 7812 7815 7905 7912 7915 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 CA3140 S042E S042F S07 S07 S07 S07 S07 S07 S07 S07 S07 S07		S dar Cells SC1 0.4/x2 SC2 0.4/x8 @1KW/m ²) Photo sensi BPX81 BPY61/2 BPY61/2 BPY61/3 BPY62/2 BPY62/3 BP101 Light depen 'MKY7C38E LD35A yello LD35A yello LD35A yello LD35A yello LD55C red = LD55C red = LD55A yello LD55A yello LD56A yello LD56A yello LD56A yello LD471 greer LD461 0.11 LD468 BxLD LD471 greer LD481 yello LD481 sello D478 gros 5.1. Small for 2.9 LED Driv UAA180 see 7-SEGMEN common ano LED 0.3'' with A120 red

761A 49 p	Incandescent 5V with RH dec
861A 47p	pt
865A 58p	3015F/BM8, 8mA/seg. 2.25
2761A 70p 4761A 1.04	3015F/BM15 15mA/seg
120S 67p	2.25
400 1.84	Incandescent + 1 with RH
B80 97p B10 97p	dec. pt 3015G/BM8, 8mA/seg
B1O 97p B2O 97p	2.25
0747A 62p	
145BB 62p	DISPLAY MOUNTING
105 1.25 311A 53p	HARDWARE 1750N04 4 × 0 3" digits
311A 53p 335A 65p	4.90N
345A 1.16	1750N06 6 × 0.3" digits
780 3.15	7.86N
965 1.23	1752N04 4 × 1" digits 7.30N
170 1.60 180 1.60	
1.00	OPTO COUPLER
O-ELECTRONIC	CNY 17 £2.24
tosensitive diodes	LIQUID CRYSTAL
/32 2.64 /33 4.32	DISPLAYS
/34 1.98	Field-effect types 4 to 17V
4B 3.93	rms operation 40-pin DIL package, 1.3" row spacing.
63 2.09	(IC 4543 suitable for driver)
91 2.70	Data sheet available
or Cells 0.4Vx22mA 2.40	L914, 3½ digit £13.75N L920, 4 digit £13.15N
0.4Vx22mA 2.40 0.4Vx80mA 3.15	L920, 4 digit £13.15N
(W/m ²)	
to sensitive transistors	POLARISED FILTERS Circularly polarised to elimin-
81 79 p	ate glare from bright metal
61/2 3.69	parts
61/3 3.86 62/2 89 p	PNF21 2" × 1" 44p PNF31 3" × 1" 60p
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01 85p	
t dependent resistor	
t dependent resistor (7C38E 70p	BREAD BOARDS
t emitting diodes	(PB Products)
OA red 19p	S-DeC 3.50 T-DeC 4.50 µ-DeC-A 4.65 µ-DeC-B 6.99
5A yellow 23p 7A green 23p	Four-pack (4 × S-DeC) 7.50
	μ-DeC-A 4.65 μ-DeC-B 6.99 Four-pack (4 × S-DeC) 7.50° DeCstor (2 × S-DeC) 4.29°
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5A yellow 26p 6C yellow # 42p	pack of 10 of 1 colour 45p EXPERIMENT GUIDES
7A* 26n	(zero VAT)
7 green # 42p 5 1mm dia not 2 9mm	PR121 (basic clastrian) 4 50
* 5 1mm dia not 2 9mm	PB121 (basic electricity) 1.50 PB122 (R, C, L, semi's) 1.77
e # extra-bright	PB123 (Bridges, pot'r ccts) 90p
42IR 72p	PB124 (L, C, R∾) 2.40
61 IR 94p 61 0.11" mat'x 23p	PB125 (Active circuits) 4.20 I C Carriers for T-µDeC-A with
68 Byl D 461 2 40	sockets
71 areen 0.1" 32p	DIL PB062 1.92
31 yellow 0 1'' 32 p	10-lead T05 PB072 1.80 Without sockets
38 8×LD4B1 2.40	DIL BB061 99p
al mountings	10-lead T05 BB071 90p
e for 5.1mm 3p Il for 2.9mm 3p	*Cannot be repeated
- F	
Drivers UAA170.	
1BO see ICs	
GMENT DISPLAYS	MOTOROLA
<i>mon anode</i> 0.3'' with LH dec	Microprocessor
0.3'' with LH dec	Microprocessor
3072 red 1.50 3052 green 1.80 3082 green 1.80	D2, Evaluation Kit (for the M.6B0
30B2 yellow 1.80	microcomputer)
	£175.87 (N) + V A T
1" with RH dec pt Dired 2.95N	

F/BM15 15mA/seg 2.25 Idescent + 1 with RH G/BM8, 8mA/seg 2.25 LAY MOUNTING LAY MOULT DWARE NO4 4 × 0 3" digits 4.90N N06 6 × 0.3" digits 7.86N NO4 4 × 1" digits 7.30N COUPLER £2.24 ID CRYSTAL LATS effect types 4 to 17V operation 40-pin DIL ge, 1,3" row spacing, i43 suitable for driver) sheet available , 3½ digit £13.75N , 4 digit £13.15N RISED FILTERS arly polarised to elimin-lare from bright metal | 2'' × 1'' | 3'' × 1'' | 6'' × 1'' 44p 60p £1.20 D BOARDS roducts) :3.50 T-DeC 4.50 :A.4.65 µ-DeC-8 6.99 back (4 × S-DeC) 7.50° or (2 × S-DeC) 4.29° 3 plugs. various colours if 10 of 1 colour 45p RIMENT GUIDES (A T) oducts) 3.50 ATI (basic electricity) **1.50** (R, C, L, semi's) **1.77** (Bridges, pot'r ccts) **90p** (L, C, R & a c) **2.40** (Active circuits) **4.20** triers for T-µDeC-A with PB062 1.92 PB072 1.80 TOS sockets BB061 99p

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SECTION FOUR Oct. '78 Resistors, Pots, Knobs, etc.

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Complete price list free on request

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	(ZERO RATED FOR VAT)					
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No	TITLE	,				
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3



Readers' Circuits

Micro-Digi Car Clock

D. lan

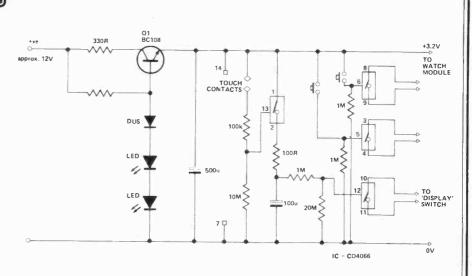
With the availability of economical LCD wristwatches has come a surplus of very cheap LED types which, with a little ingenuity, are eminently suitable for a permanent display installation; one obvious use is a cheap digital car clock.

The majority of these timepieces use two silver oxide cells in series to give 3.2 volts; current consumption, with the display on, is rarely more than 30 mA, easily provided by a simple stabiliser circuit.

Remove the back of the watch-case and discard the cells; the contacts of one cell holder are shorted together and the 3V2 supply soldered, noting polarity, to the two remaining contacts: with the 'display on' switch shorted out the result is a highly accurate mini-clock with negligable current drain as long as the vehicle is in regular use; even 35 mA will eventually flatten a car battery that receives no charge. Most simple LED watches have a brass tag, bearing on the metal case, as a common terminal to the various controls, these generally being spring loaded pins pressed, as required, into contact with clips on the perimeter of the module. These connections can be extended to panel mounting push switches, allowing the unit to be housed in a suitable box.

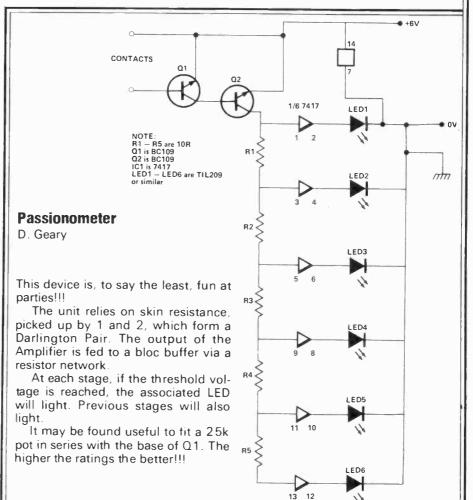
If the car is used infrequently it is prudent to arrange for the display to automatically extinguish at the end of a fixed amount of time; this also implies the simplest possible 'on' switch to minimise loss of attention when driving. One half of a CD4066 quad bilateral switch is connected as a touch-operated monostable and wired, as shown, across the LED display switch: C and R may be selected for a shorter or longer time period, those specified will enable the display for about 15 minutes. The remaining two sections of the 4066 are used to control the other functions, set time, etc., of the watch module.

Note that, in the stabiliser section, LED's are deliberately used to provide the reference voltage at the base of T1



since they 'zener' at appreciably smaller currents than a normal zener diode; total current of the stabiliser

and clock (display off) is about 2 mA — the smallest car battery should be able to supply this for about a year!



ELECTRONICS TODAY INTERNATIONAL -- FEBRUARY 1979

Preamplifier

HY5

HY30

15 Watts into 8Q

HY50 25 Watts into 8Ω

HY120 60 Watts into 80

HY200

120 Watts into 8Ω

HY400

240 Watts into 4O

POWER **SUPPLIES**

The HY5 is a mono hybrid amplifier ideally suited for all applications. All common input functions, (mag Cartridge, tuner, etc.)) are catered for internally, the desired function is achieved either by a multi-way switch or direct connection to the appropriate pins. The internal volume and tone circuits, merely require connecting to external potentiometers (not included). The HY5 is compatible with all L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is supplied with each pre-amplifier in single pack — Multi-function equalization — Low noise — Low distortion — High overload — two simply combined for stereo. **APPLICATIONS:** HI-Fi — Mixers — Disco — Guitar and Organ — Public address. **SPECIFICATIONS:**

15 - 240 Watts!

SPECIFICATIONS: INPUTS Magnetic Pick-up,3mV: Ceramic Pick-up 30mV: Tuner 100mV; Microphone: 10mV; Auxiliary 3:100mV; input impedance 47kl; at 1kHz OUTPUTS Tape 100mV; Main output 500mV R.M.S. [ACTIVE TONE CONTROLS Treble ± 12dB at 10kHz; Bass ± at 100Hz :DISTORTION: 0.1% at 1kHz; Signal/Noise Ratio 68dB. OVERLOAD: 38dB on Magnetic Pick-up. SUPPLY VOLTAGE ± 16:50V Price 56.27 + 78p VAT. P&P free. MV5 monuting baset B1 428 ± 16:50 VAT. P&P free.

HY5 mounting board B1 48p + 6p VAT P&P free

The HY30 is an exciting New kit from LL.P., it features a virtually indestructible I.C. with short circuit and thermal protection. The kit consists of I.C., heatsink, P.C. board, 4 resistors, 6 capacitors, mounting kit, together with easy to follow construction and operating instructions. This amplifier is ideally suited to the beginnet in audio who wishes to use the most up-to-date technology available. **FEATURES**: Complete kit — Low Distortion — Short, Open and Thermal Protection — Easy to Build. **APPLICATIONS**: Updating audio equipment — Guitar practice amplifier — Test amplifier — Audio costilator.

oscillator SPECIFICATIONS: OUTPUT POWER 15W R M.S. into BQ DISTORTION 0.1% at 15W INPUT SENSITIVITY 500mV FREQUENCY RESPONSE 10Hz-16kHz -- 3dB SUPPLY VOLTAGE ± 18V Price £6.27 + 78p VAT. P&P free.

The HY50 leads I.L.P.'s total integration approach to power amplifier design. The amplifier features an International of the statistic to the statistic product to power amplified resign. The amplified resulties an integral heatistic together with the simplicity of no external components. During the past three years the amplifier has been refined to the extent that it must be one of the most reliable and robust High Fidelity modules in the World. FEATURES: Low Distortion — Integral Heatsink — Only five connections — 7 Amp output transistors — No external components APPLICATIONS: Medium Power Hi-Fi systems — Low power disco — Guitar amplifier SPECIFICATIONS: INPUT SENSITIVITY 500mV.

OUTPUT POWER 25W RMS in 8Q LOAD IMPEDANCE 4-16Q DISTORTION 0.04% at 25W at

1kHz. SIGNAL/NOISE RATIO 75d8 FREQUENCY RESPONSE 10Hz-45kHz -- 3d8 SUPPLY VOLTAGE ± 25V SIZE 105.50 25mm Price £8.18 + £1.02 VAT. P&P free.

The HY120 is the baby of I.L.P.'s new high power range, designed to meet the most exacting requirements including load line and thermal protection, this amplifier sets a new standard in modular

design. FEATURES: Very low distortion — Integral Heatsink — Load line protection — Thermal protection — Five connections — No external components. APPLICATIONS: Hi-F — High quality disco — Public address — Monitor amplifier — Guitar and

organ. SPECIFICATIONS:

NPUT SENSITIVITY 500mV OUTPUT POWER 60W RMS into B() LOAD IMPEDANCE 4-16() DISTORTION 0.04% at 60W at T kHz. SIGNAL/NOISE RATIO 90dB. FREQUENCY RESPONSE 10Hz-45kHz —3dB. SUPPLY VOLTAGE #35V.

Price £19.01 + £1.52 VAT. P&P free.

The HY200, now improved to give an output of 120 Watts, has been designed to stand the most rugged conditions, such as disco or group while still retaining true Hi-Fi performance . FEATURES: Thermal shutdown — Very low distortion — Loadiline protection — Integral Healsink

TEATMORES: International owner of very low distortion -- Loaditine protection -- Integral Healsink --No Lextendat components. APPLICATIONS: Hi-Fi -- Disco. -- Monitor -- Power Slave -- Industrial -- Public address SPECIFICATIONS: INPUT SENSITIVITY 500mV. OUTPUT POWER 120W RMS into B() LOAD IMPEDANCE 4-16().DISTORTION 0.05% at 100W at

SIGNAL/NOISE RATIO 9648. FREQUENCY RESPONSE 10Hz-45kHz -- 348. SUPPLY VOLTAGE -- 45V

SIZE 114 x 100 x 85mm. Price £27.99 + £2.24 VAT. P&P free.

The HY400 is L.P.⁵ "Big Daddy" of the range producing 240W into 4Ω! It has been designed for high nower disco or public address applications. If the amplifier is to be used at continuous high power levels a cooling fan is recommended. The amplifier includes all the qualities of the rest of the family to lead the market as a true high power hi-fidelity power module. FEATURES: Thermal shutdown — Very low distortion — Load line protection — No external

components APPLICATIONS: Public address - Disco - Power slave - Industrial

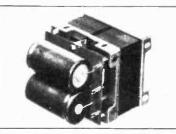
SPECIFICATIONS: OUR address - Disco - Power stave - Industrial. SPECIFICATIONS: OUTPUT POWER 240W RMS into 412 LOAD IMPEDANCE 4-1612 DISTORTION 0.1% at 240W at 1 kHz

Please Supply.

SIGNAL/NOISE RATIO 94dB. FREQUENCY RESPONSE 10Hz-45kHz - 3dB. SUPPLY VOLTAGE

INPUT SENSITIVITY 500mV_SIZE 114 x 100 x 85mm Price £38.61 + £3.09 VAT. P&P free,

PSU36 suitable for two HY30's £6.44 + 81p VAT PSU50 suitable for two HY50's £8.18 + £1.02 VAT PSU70 suitable for two HY120's £14.58 + £1 17 VAT PSU90 suitable for one HY200 £15.19 + £1.21 VAT PSU180 suitable for two HY2000's or one HY400 £25.42 + £2.03 VAT



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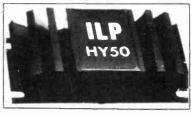
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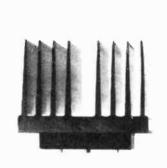
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AND EASY FAST AND EASY AND EAS AND EASY FAST E. F. Britain's Best eadboard Buys! SVE Best Buy Lektrokit Super Strip SS2 ND EASY Only £11.05 inc. p & p and VAT -ST Super Strip accepts all DIP's-as many as nine 14-pin at a time-and/or TO-5's and discrete components. With interconnections of any solid wire up to 20 AWG. And no AZD soldering. Super Strip has 840 contact points, combining a power/signal distribution system with a matrix of 640 contacts in groups of 5. Distribution system has 8 bus-bars, each with 25 contact points. ц Ś Lektrokit Breadboards FAST AND and Bus Strips From £3.25 inc p & p and VAT The modular, solderless system! Breadboards that link together for any size, any configuration. With pitch of 0.1" to accept all IC's. Just take each component, choose its hole and push it in. BREADBOARDS **BUS STRIPS** Model Contacts Price Price, each Model 264L 640 £8.32 212R £1.78 248L 480 £6.65 209R £1.62 234L 340 £5.75 206R £1.45 2171 170 £3 25 (All prices include p & p and VAT) ACE 23F Lektrokit All-Circuit Evaluators Seven ACE models from £12.53 -all prices inc p & p and VAT Just plug in components and make connections with ordinary 22-gauge solid wire. No soldering. Build any working project complete as fast as you could lay out a circuit diagram before 5 ACE 200-K (728 contacts: £12.53) and ACE 201K (1,032 contacts: £16.75) come in kit form To Lektrokit Limited, London Road, Reading, Berks, RG6 1AZ Tel. Reading (0734) 669116/7. Please send me the name of my nearest Lektrokit dealer-plus FREE catalogue. EASY Please supply the following (list items required) Lektrokit's policy is the right product, whatever the project, at the right price And it's backed by a nationwide network I enclose P.O./cheque for £. of retailers Send for the name of the dealer nearest (Allow 28 days for delivery. All prices above include packing, postage and VAT). you---plus a FREE full-colour catalogue. Name T Write to:- LEKTROKIT LTD., London Address Road, Reading, Berks. RG6 1AZ. Or send coupon. ETI 4

COMPLETES

CIRCUIT

ELECTRONICS TODAY INTERNATIONAL - FEBRUARY 1979

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Common anode 0.3 7 seg displays Toshiba type TLR303 65p E T s similar to 2N3819 18p 3N140 Mosfets 50p M203 Dual Matched Pairs Mosfets Single Gate per F E T 40p Intel 1024 bit MOS Rams 95 Mullard 56113 Tnple Varicap Diode 35p MC1310 Streeo Dacoder I C s E1 20 CD4051 CMOS S0p 500v 600m& Brdge Resc (ar equip) 25p 1N4002 100V 14 Diodes 4p 14005 800v 1A Diodes 7p E H T SIL Rec 15kv 2 5mA 15mm x 5mm 30p 7812 12v 14 Plastic V. Regis 95p MAN3 3mm IED 95p 27413 C5 Alpha-numerical Displays with data 275 ORP61 Mullard, new boxed, 30p Special offer SGS TBABOO ICS, 10 for E5 00 741 8-pin 6 for E1 NE555 27p each

MICROPHONES. EM506 Condenser Mikes. Uni-directional FET. Amp. Dual imped., 50K/600ahms. 30-18KHz, on/off świtch, E1100 Miniature Tie Pin Condenser mike 1K. imp omn-directional, uses hearing aid battery (supplied) E4.95 Grundig Electrel Inserts with built-in FET. Preamp E1.50 Crystal Mike Inserts 37mm 45p. Electret Condenser Mikes 1K() Imp with std. Jack Plug E2.85 Cassette Condenser Mikes 1K() E3 and 3.5 Jack Plugs C2.85 Standard Cassette Mikes 200 ohm Imped with 2.5 and 3.5 Jack Plugs E1.20

MORSE KEYS - Hi-speed Type, all metal. £2 25 Plastic morse

TOSHIBA L.E.D.s. --- TLG113 0 2" green 16p TLG115 0 2" green, diff lens 17p TLG1070 2" green flat top 17p TLR120 02" clear infra red 20p

NEW LOW COST MULTIMETERS - KRT100 - 1,0000 P V 1 000 volts AC/DC 150MA (max) D C current 0-100K Resistance Range Selector Switch (4.65

KRT101 Model. Same ranges above but range selection by test prod insertion £3 75

MOTORS, 1.5 to 6v DC Model 20p .115v AC min. 3.R.P.M. with Gearbox 30p .240y AC Synch Motor 1 / 5th R.P.M. 65p .240v AC Synch. Motor 1 / 24th R.P.M. 65p. Crouzet .115v AC 4.R.P.M. Motors, new 95p. 12v DC 5-pole 35p.

BOXES. Black A B S Plastic with brass inserts and lid, 75 x 56 x 35mm 40p 95 x 71 x 35mm 49p 115 x 95 x 16mm 57p

TOOLS. Radio pliers, 5in, insulated handles £1.40 Diagonal side cutters, 5in, insulated handles £1.40

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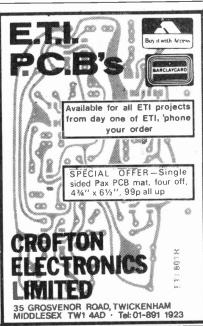
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MC1327P0 2.50 SL3046 2.60 TAA310 3.11 MC1330P 1.25 SN7603SN 2.50 TAA320 2.60 MC1339P 3.95 SN7603SN 2.15 TAA320 2.60 MC1349 3.35 SN76013N 1.50 TAA435 3.44	TBA2B1 4.70 TBA560C0 3.1 TBA350A 3.00 TBA570 2.0 TBA396 1.80 TBA570 2.1	0 TBA9200 3.50 0 TBA950-2 5.40 10 TBA950-2X 5.04		Single Ended Electrolytic 16V 10uF 0.11 each 22uF 0.11 each 47uF 0.11 each	150pF 0.18 each 180pF 0.18 each 220pF 0.18 each 330pF 0.36 each
MCx351P 1.25 817601300 2.15 TAA550 0.56 MC1352P 1.45 81760231 1.50 TAA5508 0.46	TBA4406 3.95 TBA641811 5.0 TBA4800 2.50 TBA673 5.0	15 TBA9900 3.75 TCA160 5.10		100uF 0.11 each 220uF 0.14 each 470uF 0.28 each 1000uF 0.32 each	470pF 0.36 each 680pF 0.36 each 820pF 0.36 each 1000pF 0.42 each
BRIDGE RECTIFIERS	PRICE CURRENT VOLT 0.25 80mA 30V	AGE CASE CO	IDER IDE PRICE 8/30 0.22	63V 1uF 0.11 each 2 2uF 0.11 each 4 7uF 0.11 each 10uF 0.13 each	2200pF 0.50 each 8200pF 1.55 each 10000pF 1.62 each Multi-sectional Electrolytic
1A 100V B1/100 1A 200V B1/200 1A 200V B1/400 1A 400V B1/400 1A 800V B1/400	0.26 80mA 80V 0.28 80mA 200V 0.30 1A 50V	T018 S0 T018 S0 T0220 \$1	8/60 0.24 8/200 0.25 /50 0.25	22uF 0.16 each 47uF 0.22 each 100uF 0.32 each 220uF 0.46 each	30V 2500/2500uF 1.94 eacl 70V 2500/2500uF 2.70 eacl 300V 150/150/100uF 2.81 eacl 350V 200/300uF 2.46 eacl
2A 50V B2/50 2A 100V B2/100 2A 200V B2/200	0.40 1A 200V 0.45 1A 400V 0.55 3A 50V	T0220 S1 T0220 S1 T0220 S3	/200 0.28 /400 0.35 /50 0.25	Double Ended Electrolytic 10V 22uF 0.14 each 100uF 0.14 each 22uF 20uF 0.17 each 0.17 each	200/200/100/32uF 3.89 esci 150/100/100/100/ 150uF 4.75 esci
2A 400v B2/400 2A 800v 82/800 3A 200v 83/200 3A 600v 83/600	0.65 3A 1009 0.70 3A 2009 0.70 3A 4009 0.80 4A 509	T0228 S3 T0229 S3 T0220 S4	/100 0.26 /200 0.30 /400 0.40 /50 0.35	220uF 0.17 each 470uF 0.36 each 1000uF 0.40 each	400/400uF 4.76 sect 300/300/150/100/ 50uF 6.18 dact
4A 100V B4/100 4A 400V B4/400 6A 50V B6/50 6A 200V B6/200 6A 400V B6/400 10A 400V B6/400 10A 400V B1/400 25A 50V B25/50 25A 20V B25/200 25A 400V B25/400	0.90 44 100 0.95 4A 200 0.95 4A 200 1.10 5A 400 1.20 7A 50 2.50 7A 100 4.00 7A 200 4.20 7A 400 4.30 8A 400	T0220 S4 T0220 S4 T0220 S5 T0220 S7 T0226 S7 T0220 S7 T0220 S7 T0220 S7	/100 0.40 /200 0.50 /400 0.55 /400 0.55 /50 0.45 /100 0.50 /200 0.55 /400 0.60 /400 0.70	PANEL METERS Size 2 × 2 × 1 % , requires 1 % cut-out ORDER F.S.O. CODE ImA M1 500mA M500	ORDERING All items offered subject to availability. Payment with order please add 20p for p.p. with orders under E5 orderwise carriage free. No other charges – just sen- prices shown. Enquines please send s a e. Governmen departments and educational institutions just sene official order. Mail order only Export orders welcome
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