

TRANSCENDENT 2000 SINGLE BOARD SYNTHESIZER

LIVE PERFORMANCE SYNTHESIZER DESIGNED BY CONSULTANT TIM ORR (FORMERLY SYNTHESIZER DESIGNER FOR EMS LIMITED) AND FEATURED AS A CONSTRUCTIONAL ARTICLE IN ELECTRONICS TODAY INTERNATIONAL.
The TRANSCENDENT 2000 is a 3 octave instrument transposable 2 octaves up or down giving an effective 7 octave range. There is portamento, pitch bending, a VCO with shape and pitch modulation, a VCF with both low and high pass outputs and a separate dynamic sweep control, a noise generator and an ADSR envelope shaper. There is also a slow oscillator, a new pitch detector, ADSR repeat, sample and hold, and special circuitry with precision components to ensure tuning stability amongst its many features

The kit includes fully finished metalwork, solid teak cabinet, filter sweep pedal, professional quality components (all resistors either 2% metal oxide or ½% metal film!) and it really is complete — right down to the last nut and bolt and last piece of wire! There is even a 13A plug in the kit — you need buy absolutely no more parts before plugging in and making great music! Virtually all the components are on the one professional quality fibre glass PCB printed with component locations. All the controls mount directly on the main board, all connections to the board are made with connector plugs and construction is so simple it can be built eavily in a few evenings by almost anyone capable of neat soldering! When finished you will possess a synthesizer comparable in performance and quality with ready built units selling for between £500 and £700!





Due to the fantastic success in the launching of this superbnew kit, instead of £186.50 we are able to continue the special introductory offer of £172.00 \pm VATI

200 + 200 watt Amplifier

400W rms continuous — 800W peak! 0.03% THD at FULL power! PLUS all the following features too!

- Each channel totally independent with its own stabilised power supply driven by custom designed TOROIDAL transformers!
- Inherent reliability monster heat sinks for cool running at the hottest venues electronic open and short circuit protection!
- Ultra low feedback (an incredible low 14dB overall!), super high slewing rate (20V/µs), 200W rms continuous to 4 ohm from EACH channel, input sensitivity 0.775V (0dB)
- * Professional quality components, sturdy 19 rack mounting chassis complete with sleeve and feet
- Easy to build plenty of working space with ready access to all components, minimal wiring, extensive instruction suitable for both experience constructors and newcomers to electronics
- Value for money quality and performance comparable with ready-built amplifiers costing over £600!



PSI 4001 SLAVE MODEL

PSI 4002 STUDIO MODEL



PSI 4001 COMPLETE KIT ONLY £187.50 + VAT **PSI 4002 COMPLETE KIT ONLY £196.90**

The kits shown on this page are available as separate packs. Prices are given in our FREE CATALOGUE

PRICE STABILITY: Order with confidence irrespective of any price changes we will honour all prices in this advertisement until November 30th, 1978 if ETI October 1978 issue is mentioned with your order. Errors and VAT rate changes

excluded EXPORT ORDERS: No VAT Postage charged at actual cost plus 50p handling

and documentation

U.K. ORDERS. Subject to 12% surcharge for VAT* (i.e. add 1/4 to the price). No charge is made for carriage, * or at current rate if changed

SECURICOR DELIVERY: For this optional service (U.K. mainland only) add

£2 50 (VAT inclusive) per kit

SALES COUNTER: If you prefer to collect your kit from the factory, call at
Sales Counter (at rear of factory) Open 9 a m -4 30 p m Monday-Thursday

OUR CATALOGUE IS FREE! WRITE OR PHONE NOW!

MTAMUUMA PSI 4002

PORTWAY INDUSTRIAL ESTATE ANDOVER, HANTS SP10 3NM

ANDOVER (STD 0264) 64455



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COMPUTING TODAY: NEW MAGAZINE FOR SMALL SYSTEMS. FIRST ISSUE INSIDE.

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High quality audio modules for Stereo and mono

S450

STEREO FM TUNER Fitted with phase lock-loop £22 · 30



FREQUENCY RANGE	88-108 Mhz
SENSITIVITY	3 0 µV
BANDWIDTH	250 kHz
SPURIOUS REJECTION	50 dB
SELECTIVITY ± 400 kHz	55 dB
AUDIO OUTPUT (22 5 kHz devi	ation) 100 mV
STEREO SEPARATION	30 dB
SUPPLY REQUIREMENTS	20 to 30V (90mA max)
AERIAL IMPEDANCE	75 ohms
DIMENSIONS	240mm × 110mm × 32mm

The 450 Tuner provides instant programme selection at the touch of a button ensuring accurate tuning of 4 pre-selected stations, any of which may be altered as often as you choose, simply by changing the settings of the pre-set controls. Features include FET input stage. Vari-Cap diode tuning. Switched AFC LED Styreo indicator.

Stereo 30 COMPLETE AUDIO CHASSIS

£18 · 95 + 40 p&p + 121% VAT



OUTPUT POWER	7 Watts RMS
LOAD IMPEDANCE	8 ohms
TOTAL HARMONIC DISTORTION	Less than .5% (Typically .3%)
FREQUENCY RESPONSE	50 Hz to 20 kHz ± 3dBs
TONE CONTROL RANGE	± 12 dBs at 100Hz and 10kHz
SENSITIVITY	190 mV for full output
INPUT IMPEDANCE	1 M ohms
TRANSFORMER REQUIREMENTS	22 V.A.C. rated at 1A
DIMENSIONS (Less controls and panel)	200mm × 130mm × 33mm

The Stereo 30 comprises a complete stereo pre-amplifier, power amplifiers and power supply. This, with only the addition of a transformer or overwind will produce a high quality audio unit suitable for use with a wide range of inputs i.e. high quality ceramic pick-up, stereo tuner, stereo tape deck etc. Simple to install, capable of producing really first class results, this unit is supplied with full instructions, black front panel, knobs, main switch, fuse and fuse holder and universal mounting brackets.

25w

AL60

AUDIO AMPLIETER MODULE 25 Watts RMS

£4 .55 + 35p p&p + 121% VAT

25 Watts RMS
30-50 V
8-16 ohms
Less than 1% (Typically 06%
20 Hz to 30 kHz × 2 dBs
280 mV for full output
90°C
103mm × 64mm × 15mm

This high quality audio amplifier module is for use in audio equipment and stereo amplifiers and provides output powers up to 25 RMS with distortion levels below 0.1%.

AL80

AUDIO AMPLIFIER MODULE £7·15*



OUTPUT POWER	35 Watts RMS
SUPPLY	40-60 V
LOAD IMPEDANCE	8-16 ohms
TOTAL HARMONIC DISTORTION	Less than .1% (Typicalty .06%
FREQUENCY RESPONSE	20 Hz to 30 kHz × 2 dBs
SENSITIVITY	280 mV for full output
MAX. HEAT SINK TEMPERATURE	90°C
DIMENSIONS	103mm × 64mm × 15mm

The AL80 is similar in design to the AL60 above and is of the same high quality but provides output powers up to 35W with distortion levels below 0.1%.

AL250

POWER AMPLIFIER



£17 .25* + 40p p&p + 8% VAT

125 Watts RMS continuous OPFRATING VOLTAGE 4-16 ohms FREQUENCY RESPONSE 25 Hz 20 kHz measured at 100 Watts SENSITIVITY FOR 100 WATTS INPUT IMPEDANCE
TOTAL HARMONIC DISTORTION
50 WATTS into 4 ohms
50 WATTS into 8 ohms 33 K ohms

This unit, designated AL250, is a power amplifier providing an output of up to 125W RMS, into a 4 ohm load.

AL30A

£3.75



125W R.M.S.

MAXIMUM SUPPLY VOLTAGE 10 Watts RMS POWER OUTPUT for 2% THD TOTAL HARMONIC DISTORTION Less than ·25% 8-16 ohms INPLIT IMPEDANCE 100 K ohms 50 Hz-25 kHz ± 3 dBs 75 mV for full output 74mm × 63mm × 28mm

+ 121 % VAT These low cost 5 and 10 watt modules offer the utmost in reliability and performance, whilst being compact in size

SPM80

STABILISED POWER SUPPLY £4 · 25 + 35p p&p + 121% VAT



INPUT A.C. VOLTAGE	33-40V
OUTPUT D.C. VOLTAGE	33 V nominal
OUTPUT CURRENT	10 mA-1 · 5 amps.
OVERLOAD CURRENT	1.7 amps approx.
DIMENSIONS	105mm × 63mm × 30mm

Less than 1% (Typically 07%) 100 mV/100 K ohms For an 100 mV/100 K ohms output 3.5 mV/50 K ohms 250 mV

Better than 65 dBs (All inputs) Better than 26 dBs (All inputs)

Within ± 1 dB from 20 Hz to 20 kHz ± 15 dBs at 75 Hz + 10-20 dBs at 15 kHz

Designed to power two AL60s at 15 Watts per channel simultaneously. Circuit Techniques include full short circuit protection.

EQUALISATION

FREQUENCY RESPONSE

BASS CONTROL RANGE TREBLE CONTROL RANGE SIGNAL/NOISE RATIO

TOTAL HARMONIC DISTORTION

SENSITIVITY 1. TAPE
INPUTS 2. RADIO TUNER
3. MAGNETIC P.U.

PA100



£15 · 80

20 to 40 V 300 · 90 · 33mm (less controls) DIMENSIONS A top quality stereo pre-amplifier and tone control unit, the PA100 provides a comprehensive solution to the front end regulrements of stereo amplifiers or audio units. The six push bulton selector switch gives a choice of inputs together with two filters for high and low frequencies.

INPUT OVERLOAD

MPA30

MAGNETIC CARTRIDGE PRE-AMPLIFIER



Enjoy the quality of a magnetic cartridge with your existing ceramic equipment using the MPA 30 which is a high quality preamplifier enabling magnetic cartridges to be used where facilities exist for the use of ceramic cartridges only.

3.5 mV for 100 mV output Within ± 1 dB from 20 Hz to 20 kHz 50 K ohms EQUALISATION INPUT IMPEDANCE 18 to 30 V—re earth 110 × 50 × 25mm (inc DIN socket) DIMENSIONS

PA12

STEREO PRE-AMPLIFIER

£7:10 + 30p p&p + 12; % VAT

PRE-ARTILITIES
The PA12 Stereo PreAmplifier chassis is designed and recommended for use with the AL 20/30 Audio Amplifier Modules, the PS12 power supply and the TS38 Transformer. Features include on/off volume, Balance, Bass Table controls. Complete with tape output.

and Treble controls. Complete with	tape output.
FREQUENCY RESPONSE	20 Hz-20 kHz (-3dB)
BASS CONTROL	± 12 dB at 60 Hz
TREBLE CONTROL	± 14 dB at 10 kHz
INPUT IMPEDANCE	1 Meg. ohm
INPUT SENSITIVITY	300 mV
CROSSTALK	60 dB
SIGNAL/NOISE RATIO	-65 dB
OVERLOAD FACTOR	± 20 dB
TAPE OUTPUT IMPEDANCE	25 K ohms
DIMENSIONS	152mm × 84mm × 25mm

PS12 POWER SUPPLY

Designed for use with the AL30A S.450 and MPA30 in conjunction with transformer TS38.
INPUT VOLTAGE OUTPUT VOLTAGE OUTPUT CURRENT 800mA 60mm × 43mm × 06-1

800mA + 35p p&p 60mm × 43mm × 26mm + 12½% VAT

GE 100 NINE CHANNEL MONO-GRAPHIC EQUALIZER

The GE100 has nine 1 octave adjustments using integrated circuit active filters. Boost and Cut limits are \pm 12dB. Max. Voltage handling 2 V RMS, T.H.D., 0.65%, input impedence 100N. Output impedence less than 10 K. Frequency response 20 Hz – 20 KHz (3dB). The nine gain controls are centred at 50, 100, 200. 400, 800, 1,600, 3,200, 6,400 and 12,800 Hz. The suggested gain controls are 10 K LIN sliders (not 3 Spp pages suggested gain controls are 10 K LIN sliders (not 3 Spp pages supplied with the module) See Paks S31 and 16192.

SG30 POWER SUPPLY BOARD for GE100 15-0-15 VOLT £5-50 + 121/2 WAT + 35p pap

SIREN ALARM MODULE

American Police screamer powered from any 12 volt supply into 4 or 8 ohm speaker. Ideal for car burglar alarm, freezer breakdown and other security purposes. Order No. S15. No. BP124. Only £3: 50 + 8% VAT + 25p p&p

MA60 HI-FI AMPLIFIER KIT

Build you own top quality amplifier, save yourself pounds. The MA60 kit comprises the following BI-kits modules, 2 × AL60 amps, 1 × PA100 pre-amp, 1 × SPM80 stab. power supply, 1 × BMT80 transf. giving 17 waits RMS per-channel STEREO. All modules covered by the BI-PAK satisfaction or money back guarantee. Details of the above modules are in this ad.

Price £32·00 + 121% VAT + £2p p&p.

TC60 KIT

A beautifully designed genuine TEAK WOOD veneered cabinet to put the professional touches to your home built amplifier. Full set of parts incl. Front & Back Panels, Knobs, Chassis, Fuses, Sockets, Noen, etc. Ideal for the MA60. Size: 425mm × 290mm ×

95mm. Price £19·95 + 12¦% VAT + 86p p&p

TRANSFORMERS

T538 For use with S.450 AL30A MPA30
Order No. 2036 Price: £3 20 + 55p p&p + 12;% VAT
2036 For use with Stereo 30
BM 500 For use with AL60 SPM80
Order No. 2034 Price: £5·40 + 86p p&p + 12;% VAT
BM 7500 For use with AL50 SPM80
Order No. 2034 Price: £6·35 + £1·10 p&p + 12;% VAT
Order No. 2035



DEPT. 11, P.O. Box 6, Ware, E.T.1. Herts.

SEMICONDUCTORS - COMPONER

CERAMIC PAK

Containing a range of first quality minia-ture ceramic capacitors.

6160 — 24 — 3 of each value 22pf. 27pf. 3pf. 39pf. 47pf. 68pf. 82pf 60p* 6161 — 24 — 3 of each value 100pf. 20pf. 150pf. 180pf. 220pf. 270pf. 120pf, 150pf, 180pf, 220pf, 270pf, 30pf, 30pf, 30pd, 30pd 30pd 30pd 50pf, 16162 - 24 pd 30 each value - 470pf, 560pf, 680pf, 820pf, 1000pf, 15000pf, 2200pf, 2200pf, 3300pf, 80pf, 16163 - 21 pd 300pf, 4700pf, 5800pf, -01uf, -015uf, -022uf, -033uf, -047uf, -015uf, -022uf, -033uf, -047uf, -015uf, -022uf, -05pf, -05uf, -05uf,

ELECTROLYTIC PAKS

A range of paks each containing 18 first quality, mixed value miniature electrolytics.

16201 — values from —47mFD — 10mFD 60p 16202 — values from 10mFD — 100mFD 16202 — values from 100mFD — 100mFD 16203 values from 100mFD — 680mF0 60p°

CARBON RESISTOR PAKS

These paks contain a range of Carbon Resistors assorted into the following

groups.	
16213 - 60 mixed 1/w 100 ohms - 8	20
ohms 60	Dρ.
16214 - 60 mixed 16w 1K phms - 8.	ŹK.
ohms 60	Ъ,
16215 - 60 mixed 1/sw 10K ohms - 8	3K
ohms 60	ЭÞ.
16216 - 60 mixed 1/w 100K ohms	
820K ohms 60	οp°
16217 - 40 mixed 1/2 w 100 ohms - 8	20
	Op'
16218 — 40 mixed ½w 1K ohms — 8.	
	ΟÞ,
16219 — 40 mixed 1/2w 10K ohms — 8	
	ΟÞ,
16220 - 40 mixed 1/2 w 100K ohms	
	ΟÞ,
16230 — 60 mixed 1/w 1 Meg. — 10 N	۸eg
	0p
16231 — 40 mixed ½w 1 Meg — 10 N	
	Op'
16231 — 40 mixed ½w 1 Meg — 10 N	
ohms 6	Oρ'

COMPONENT DAKC

PAKS
16164 — 200 Resistor mixed value approx (Count by weight) 60p*
16165 - 150 Capacitors mixed value
approx (Count by weight) 60p*
16166 — 50 Precision resistors Mixed values
values 60p 16167 — 1/sw resistors mixed values 80
60p°
16168 — 5 pieces assorted ferrite rods
60p*
16169 — 2 Tuning gangs MW/LW VHF
60p.
16170 — 1 Pack wire 50 meters, assorted
colours, single strand 60p
16171 — 10 Reed switches 60p*
161 — 3 Micro switches 60p* 16173 — 15 Assorted pots 60p*
16174 — 5 Metal jack sockets 3 x 3 5 mm 2
x standard switch types 60a
16175 — 30 Paper condensers — mixed
values 60m²
16176 - 20 Electrolytics, trans, types
60p*
16177 — 1 Pack assorted hardware —
Nuts/bolts, gromets, etc. 60p
16178 - 5 Mains slide siwtches, assorted
60p* 16179 — 20 Assorted tag strips and panels
60p
16180 — 15 Assorted control knobs 60p
16181 - 3 Rotary wave change switches
60p
16182 - 2 Relays 6-24v operating 60p*
16183 — 1 Pak copper laminate approx.
200 sq inches 60p
16184 15 Assorted fuses 100mA-5 amp
60p 16185 — 50 metres PVC sleeving assorted
size and colours 60p
312 8110 CO10013 90p

METAL FOIL CAPACITOR PAK

Containing 50 inetal foil Capacitor — like Mullard C280 series Mixed values ranging from —01uf-2 2uf Complete with identification sheet. 0 / N 16204 £1.20*

SLIDER PAKS

1619D - 6 Slider potentiometers mixed	d
values 60p	
16191 — 6 Slider potentiometers, all 470	0
ohm 60p	
16192 — 6 Slider potentiometers, all 10	k
lin 60p	
16193 — 6 Stider potentiometers, all 22K	1
in 60p	'n
16194 — 6 Stider potentiometers, all 47K	1
in 60p	٠.
16195 - 6 Slider potentiometers, all 47	K

BRAND NEW - FULLY GUARANTEED

Туре	Price	Туре	Price	Туре	Price	Туре	Price	Type	Price	Туре	Price
AC126	€0.18	8C108C	£0.10	BC479	€0.20	BFX87	£0.22	TIP41B	€0.51	2N3646	£0.09
AC127	€0.18	BC109A	€0.08	BC547	'£0.12	BFX88	€0.22	TIP41C	€0.53	2N3702	80.03
AC128	£0.18	BC109B	£0.09	BC548	'£0.12	BFX90	'£0.55	TIP42A	€0.53	2N3703	.£0.08
AC128K	€0.26	BC109C	€0.10	BC549	10.12	BFY50	€0.16	TIP42B	€0.55	2N3704	'£0.07
AC132	£0.20	BC147	£0.08	BC550		BFY51	€0.16	TIP42C	£0.57	2N3705	'£0.07
AC134	£0.20	BC148	40.08	BC556	'£0.14	8FY52		TIP2955	£0.65	2N3705	£0.07
					'£0.14	BIP19	£0.16	TIP3055		2N3700	
AC137	€0.20	BC149	£0.08	BC557	'£0.13	BIP20	£0.38	TIS43	£0.50	2N3707 2N3708	80.03°
AC141	£0.22	BC157 BC158	'£0.10	BC558	'£0.12	BIP19/20	£0.38	TIS90	£0.24		£0.07
AC141K	£0.30		'E0.10	BC559	'£ 0.14	BIF 19720			'£0.22	2N3708A	£0.07
AC142	£0.20	BC159	'£0.10	BD115	£0.50	BOYOU	£0.80	UT46	£0.22	2N3709	'£0.07
AC176	£0.18	BC167	'£0.12	BD116	£0.80	BRY39	£0.45	ZTX107	'£0.10	2N3710	£0.07
AC176K	£0.26	BC168	'E0.12	BD121	£0.65	BU105	£1.40	ZTX108	£0.10	2N3711	*£0.07
AC178	€0.25	BC169	E 0.12	BD124	£0.70	BU105/02		ZTX109	£0.10	2N3819	£0.20
AC179	£0.25	BC169C	'£0.12	BD131	£0.38	BU204	£1.70	ZTX300	£0.12	2N3820	£0.35
AC180	£0.20	BC170	'£0.10	BD132	€0.40	BU205	£1.40	ZTX500	'£0.14	2N3821	£0.60
AC180K	€0.28	BC171	'£0,10	BD131	/132MP	BU 208 / 02		2N1613	€0.20	2N3823	€0.60
AC181	€0.20	BC172	'£0.10		€0.85	E1222	€0,38	2N1711	£0.20	2N4058	'£0.12
AC181K	£0.28	BC173	'£0.12	BD133	€0.40	MJE2955	£0.98	2N1889	£0.45	2N4059	'£0,14
AC187	£0.18	BC177	€0.16	BD135	£0.38	MJE3055		2N189D	£0.45	2N4060	'£0.14
AC187K	£0.20	BC178	£0.16	BD136	€0.35	MJE3440		2N1893	£0.30	2N4061	'£0.12
AC188	£0.18	BC179	£0.16	BD137	£0.35	MP8113	€0.52	2N2147	£0.75	2N4062	'£0.12
AC188K	£0.20	BC180	€0.25	BD138	£0.40	MPF102	€0.35	2N2148	£0.70	2N42B4	'£0.18
AD140	£0.60	BC181	'£0.25	BD139	£0.36	MPF104	£0.38	2N2160	£1.00	2N4285	'£0.18
AD142	£0.85	BC182L	'£0.10	BD140	£0.36	MPF105	£0.38	2N2192	£0.38	2N4286	'£0.18
AD1'43	£0.75	BC183	'£0.10	BD139	/140Mp	MPSA05	'£0.30	2N2193	£0.38	2N4287	'£0.18
AD149	£0:60	BC183L	'£0.10		€0.80	MPSA06	'£0.30	2N2194	£0.38	2N4288	'£0.18
AD161	€0.42	BC184L	£0.60	BD155	€0.80	MPSA55	'£0.28	2N2217	£0.22	2N4289	'£0.18
AD162	€0.42	BC207	€0.11	BD175	€0.60	MPSA56	'£0.28	2N2218	£0.22	2N4290	'£0.18
AD161/16	1MP	BC208	'£0,11	BD176	€0.60	0022	£1.50	2N2218A	£0.20	2N4291	'£0.18
	£0.35	BC209	'£0.12	B0177	€0.68	OC23	£1.50	2N2219	€0.20	2N4292	'£0.18
AF114	£0.21	BC212	'£0.11	BD178		0C24	£1.35	2N2219A	€0.24	2N4293	'E0.18
AF115	£0.21	BC212L	'£0.11	BD179	€0.75	OC25	£1.00	2N2904	€0.18	2N4921	'£0.55
AF116	€0.21	BC213	'€0.11	BD201	/202MP	OC26	€1.00	2N2904A	£0.21	2N4923	'£0.65
AF117	€0.21	BC213L	'£0.11		€1.70	OC28	£0.80	2N2905	€0.18	2N5135	'£0.10
AF118	€0.40	BC214	'£0.12	BD203	€0.80	CC29	€0.95	2N2905A	€0.20	2N5136	'£0.10
AF124	€0.30	BC214L	'£0.12	80204	€0.80	OC35	€0.90	2N2906	£0.16	2N5138	'£0.10
AF125	€0.30	BC237	'£0.16		/204MP	OC36	€0.90	2N29D6A	£0.19	2N5194	€0.56
AF126	€0.30	BC238	'£0.16		£1.70	OC70	€0.24	2N29D7	€0.20	2N5245	€0.40
AF127	£0.32	BC251	*£0.15	BDY20	€0.80	OC71	€0.15	2N2907A	€0.22	2N5294	£0.34
AF139	€0.35	BC251A	'£0.16	BOX77	€0.90	TIC44	'£0.29	2N296G	£0.09	2N5296	£0.56
AF180	€0.40	BC301	€0.28	BF457	€0.37	TIC45	'£0.35	2N2926Y		2N5457	£0,32
AF181	£0.60	BC302	€0.28	8F458	€0.37	TIP29A	€0.40	2N29260	'£0.08	2N5458	£0.32
AF186	€0.58	BC303	£0.28	BF459	£0.38	TIP29B	£0.52		*60.08	2N5459	£0.35
AF239	€0.35	BC304	£0.38	BF594	£0.30	TIP29C	£0.50	2N2926B		2N5551	'£0.36
AL102	€1.20	BC327	'£0.16	BF596	£0.30	TIP30A	£0.50	2N3053	£0.16	2N6027	£0.39
AL103	£1.20	BC328	£0.15	BFR39	€0.24	TIP308	€0.60	2N3054	£0.40	2N6121	£0.70
AU104	£1.18	BC337	'£0.15	BFR40	*£0.25	TIP30C	€0.60	2N3055	€0.40	2N6122	
AU110	£1.00	BC338	£0.15	BFR79	£0.25	TIP30C	£0.45	2N3414	£0.40	2140122	€0.70
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BC107A	£0.08	BC441	£0.30	BFX29	€0.28	TIP316	£0.47	2N3416	£0.16		
BC107A	£0.08					TIP31C		2N3416 2N3417	£0.29		
	£0.09	BC460	£0.38	BFX30 BFX84	€0.30	TIP32B	€0.49 €0.51	2N3614	£1.00		
BC107C	£0.08	BC461 BC477	£0.38	BFX85	£0.22	TIP326	£0.51	2N3614 2N3615	£1.00		
BC108A						TIP41A	€0.49	2N3615 2N3616			
BC1088	£0.09	BC478	£0.20	BFX86	£0.25	TIF4TA	EU.49	4113010	£1.05		
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7401	£0.11	7410	€0.12	7442	£0.54	7483	£0.70	7494	€0.75	74123	€0.46
7402	€0.11	7411	€0.17	7446	£0.65	7484	£0.88	7495	€0.50	74141	€0.55
7403	£0.11	7412	€0.20	7447	£0.60	7485	£0.88	7496	£0.62	74154	€0.95
7404	€0.11	7413	€0.24	7448	€0.56	7486	£0.25	74100	£0.92	74180	£0.84
7405	€0.11	7414	€0.50	7475	£0.29	7489	£1.05	74110	€0.45	74181	€1.60
7406	£0.26	7416	€0.16	7480	€0.44	7490	£0.32	74118	€0.80	74190	£1.00
7407	€0.26	7417	€0.26	7481	£0.85	7491	£0.64	74119	£1.18	74198	£1.45
7408	£0.13	7440	€0.12			7492	€0.37	74121	€0.24	74199	£1,45

CMOS ICs

Type CD4000 CD4001 CD4002 CD4006 CD4007 CD4008	Price 0.15 0.18 0.18 0.98 0.18 0.98	CD4017 CD4018	Price 0.20 0.52 0.98 0.50 0.98 1.00	Type CD4022 CD4023 CD4024 CD4025 CD4026 CD4027	Price 0.90 0.20 0.80 0.20 1.70 0.60	CD4031 CD4035 CD4037 CD4040 CD4041 CD4042	Price 2,20 1,30 0,95 0,95 0,82 0,82	Type CD4046 C04047 CD4049 CD4050 C04054 CD4055	Price 1.30 1.10 0.55 0.55 1.10 1.40	Type CD4071 CD4072 CD4081 CD4082 CD4510 CO4511	Pric 0.2: 0.2: 0.2: 0.2: 1.3: 1.6:
		CD4018 CD4019 CD402D				CD4042 CD4043 CD4044					

LINEAR ICs

Туре	Price	Туре	Price	Туре	Price	Type	Price:	Type	Price	Туре	Price
CA3011	€1.05	CA3090'	£4.25	NE550"	€0.95	LM320-15v	£1.50	72710	€0.30	SN76110	£1.50
CA3014	€1.70	CA3123*	£1.90	NE5500'	NLA	LM320-24v	£1.50	μA711C'	€0.32	SN76115'	€1.90
CA20181	€0.75	LM301*	€0.39	NE555	£0.32	LM380-14P	€0.85	72711	£0.32	SN76660*	€0.75
CA3020	£1.70	LM304	€2.00	NE556	€0.82	LM381	€1.55	µ A723C	€0.45	SL414A	£1.95
CA3028	£1.02	FW308.	£1.60	NE561*	£3.95	LM3900N1	£0.65	72723	€0.45	TAA550B	€0.35
CA3035*	£1.70	LM309K	€1.50	NE562B*	€3.95	MC724P	£1.50	μA741C'	€0.24	TAA621A	£2.00
CA3036	£1.35	LM320-5v	£1.50	NES65A	£1.75	MC1303L*	£1.48	72741	€0.24	TAA661A	£1.65
CA3042*	£1.50	LM320-12v	£1.50	NE566'	£1.50	MC1304P*	£1.90	741P*	€0.20	TAD1001	£1.30
CA3043*	€1.85	MC1456G*	£1.40	NE567	€1.80	MC1310P*	€0.95	4A747C	€0.70	TBA5400°	€2.20
CA3046	€0.80	MC1466L	£4.50	9A702C*	€0.46	MC1312PQ*	£1.90	72747	£0.79	TBA641B*	£2.25
CA3052	£1.60	MC145698	£2.95	72702	€0.46	MC1330P*	€1.20	# A748°	€0.35	TBA800"	£0.80
CA3054	£1.35	MC1496G*	£0.98	#A703A*	€0.25	MC1350°	£1.20	72748	£0.35	TBA810S	£1.05
CA3075	€1.50	NE536'	£3.50	# A709C	€0.25	MC1351P	£1.20	748F	£0.35	TBA820"	£0.80
CA3081	£1.50	NE515A	£3.50	72709	£0.46	MC1352P*	£1.40	SN76013N1	€1.75	TBA920Q*	£3.40
CA3089	€2.10	NE540'	€1.50	709P	€0.25	μΑ710C	£0.40	SN76023N1	£1.75	TCA2705*	£2.20

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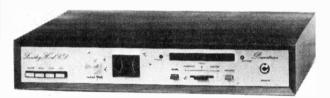
DE LUXE EASY TO BUILD LINSLEY-HOOD 75W AMPLIFIER £99.30 + VAT

This easy to build version of our world-wide acclaimed 75W amplifier kit based upon circuit boards interconnected with gold plated contacts resulting in minimal wiring and construction delightfully straightforward. The design was published in Hi-Fi News and Record Review and features include rumble filter, variable scratch filter, versatile tone controls and tape monitoring whilst distortion is less than 0.01%.

WIRELESS WORLD FM TUNER £70.20 + VAT

A pre-aligned front-end module makes this Wireless World published design very simple to construct and adjust without special instruments. Features include an excellent a.m. rejection, push-button station selection as well as infinitely variable tuning and a phase locked loop stereo decoder incorporating active filters for "birdy" suppression.





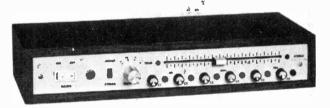
LINSLEY-HOOD CASSETTE DECK £79.60 + VAT

This design, published in Wireless World, although straightforward and relatively low cost provides a very high standard of performance. There are separate reduct and replay amplifiers and switchable equalisation together with a choice of bias levels are also provided. The mechanism is the Goldring-Lenco CRV with electronic speed control.

T20 + 20 AMPLIFIER £33.10 + VAT

This kit, based upon a design published in Practical Wireless, uses a single printed circuit board and offers at very low cost, ease of construction and all the normal facilities found on quality amplifiers, A 30 watt version of this kit (T30 + 30) is also available for £38.40 + VAT. 14 to





WWII TUNER £47.70 + VAT

This cost reduced model of our highly successful Wireless World FM Tuner kit was designed to complement the T20 + 20 and T30 + 30 amplifiers and the cabinet size, front panel format and electrical characteristics make this tuner compatible with either. Facilities included are pre-aligned front-end module, switchable afc. adjustable switchable muting. LED tuning indication and both continuous and push-button channel selection (adjustable by controls on the front panel).

POWERTRAN SFMT TUNER £35.90 + VAT

This is a simple low cost design which can be constructed easily without special alignment rims is a simple low cost design which can be constructed easily without special alignment equipment but which still gives a first class output suitable for feeding any of our very popular amplifiers or any other high quality audio equipment. A phase-locked-loop is used for stereo decoding and controls include switchable atc, switchable muting and push-button channel selection (adjustable by controls on the front panel). This unit matches well with the T20 + 20 and T30 + 30 amplifiers.



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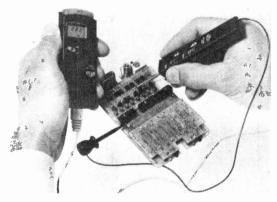
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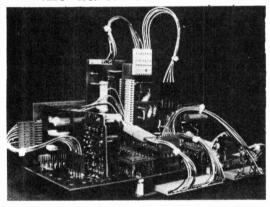
news digest

dmm(digital midget meter?)



Guinness take note — the world's smallest DMM it seems. Made by Heuer Time Ltd it measures just $4^{\prime\prime} \times 1.6^{\prime\prime} \times 0.5^{\prime\prime}$ (100 x 40 x 20mm to you Euro-people) with a probe which is $4^{\prime\prime} \times 0.8^{\prime\prime} \times 0.5^{\prime\prime}$ (you mm lot can work that out yourselves). Volts Ohms and Amps either DC or AC can be accommodated between 2V-1kV, 2mA-2A and 2k-20M although not necessarily in that order. AC measurement is true RMS. Display is 3½ digit LCD. Input 7 10M +Price around £240. Address: Heuer Ltd, Argyle House, 29/31 Euston Road, London.

the dalek connection



This lot looks like it could give Dr Who a few sleepless nights does it not? It's easy to imagine it lumbering across a smoke-circled hill and intoning "Take me to your leader. . . ."

Perhaps fortunately for the human race it is simply a noteworthy new connection system from Pressac Ltd. The PCB mounting plugs and sockets can be got at from either direction, and cable and chassis mounting assemblies are also available. Spacing can be either 2.5mm or 50mm and up to 40 ways are possible.

5.0mm and up to 40 ways are possible.

Pressac Ltd, Acton Grove, Long Easton, Nottingham NG10 1FW.

eye of the tornado?

Britain and NATO's new aircraft the MRCA Tornado is to be fitted — in its inceptor role — with a Visual Augmentation System developed by Marconi. The system presents the crew with a television picture of what lies ahead of their machine using a newly-developed low-light TV camera system.

Based on an existing Marconi design for a low light camera, the equipment produces an image at ranges far in excess of what the unaided eye can manage, and in light conditions anywhere from daylight to starlight.

Every little helps. . . .

on the face of it

It had to come. Someone somewhere had to go produce an ANALOGUE digital watch. And here we have it. Must confess it looks very nice too. Texas get the credit/blame or whatever.

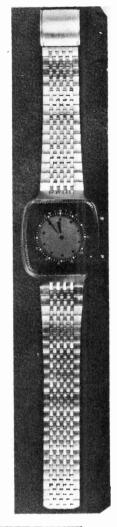
The display is beautiful. 120 segments are used to produce the illusion of the dial. On normal LCD's up to half the area is used for contacts to the segments, which would mean that with 121 contacts to provide the display would have to be pretty big — a clock yes, but no watch. Texas have gotten this by multiplexing the drive to the segments, which allows 90% of the area to be freed for

The chip is I²L — and this is unusual. I²L is not normally employed in LCD units because of the problem of driving the highly capacitive elements. Bipolar drivers are used to avoid this, and are designed to drive the large capacitance with a (relatively) large 150 μ A initial current for about 100 μ S and to provide the 100 nA 'sustan' current thereafter.

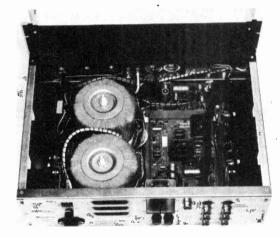
Another interesting point is that the material used for the display, a low voltage ester material would not usually be employed in watches because of its negative temp. Coeff. — higher the temp. the lower the drive required — which can lead to 'ghosting' and confusion of the display. The I'-L however can compensate for this.

Naturally, since this is the first of its kind, the price of all this invention will be high initially — but the watch will function as a chronograph too and the Jones's will NEVER be able to keep up with this.

In the shops soon we hear.



watt batteries



Here is an amp to really annoy the neighbours with. If they complain about the hi-fi again, pack up the battery cassette recorder, speakers and this PAC 250 MB, drive around the back of the house and when they're least expecting it give 'em 250W a channel straight in the back door. An outflanking move to warm Napoleon's heart.

The PAC 250 you see will run quite happily from 24V DC or 250V AC. Very handy for PA as well as neighbour baiting.

neighbour baiting.
Details from: Millbank
Electronics Ltd, Uckfield,
Sussex TB22 1PS.

Sound of safety?

A car alarm which operates on the ultrasonic area protection principle usually employed in houses is now being imported from the land of pasta and pinched bottoms.

Called the 'Break' it uses four sensors to cover the interior of any vehicle, and has adjustable sensitivity so that spurious triggering can be avoided. Once activated you have 40 secs to clear out before it goes off — so don't get stuck in the seat belt — and coming back in 10 secs to cutich it off

Once the alarm is in mid sing-song, the removal of the felon will lead to a shutdown 15 secs later. If he persists so will the alarm. Price around £50 — not including ear plugs — from:

Sofare Ltd, Stoke Heath, Market Drayton, Shropshire.

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WATFORD ELECT	DUMICC	TRANSIS	TORS	р	р	p	P
WAIFURD ELECT	UUUIUO	AC107* 2: AC117* 3!	3 BC168C 12	BF173* 25 BF177* 24	MPSA70 34	TIS43 36 TIS44 45	2N2218A* 31
33 CARDIFF ROAD. WATFORD, H	IERTS, ENGLAND	AC125* 15 AC126* 15	9 BC17D 17 9 BC171 11	BF178* 25 BF179* 30	MPSU05 48	TIS45 45 TIS46 45 TIS47 50	2N2219A* 22 2N2220A* 26 2N2221A* 23
MAIL ORDER CALLERS W Tel. Watford 40588/9		AC127# 15 AC128# 15	B BC177* 15	BF180* 20 BF181* 30 BF182* 30	MPSU52 65	TIS48 50 TIS49 50	2N2221A# 23 2N2222A# 20 2N2303# 45
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INSTITUTIONS' OFFICIAL ORDERS ACCEPTED. TR WELCOME. P&P ADD 30p' TO ALL ORDERS UNDER		AC187* 20 AC188* 20	0 BC182L 10 BC183L 10	BF196 10 BF197 10	OC28* 99	ZTX107 11 ZTX108 11	
POSTAGE AT COST. AIR/SURFACE.		ACY17 35 ACY18 40	BC186 21	BF198 18 BF199 18	OC35* 80	ZTX212 28	2N2905A* 20
Export orders no VAT. Applicable to U.K. Custome prices are exclusive of VAT. Please add 8% to 121/2 %%.	ers only. Unless stated otherwise, all devices marked *. To the rest add	ACY19 44 ACY20 44 ACY21 35	BC212 9	BF200* 32 BF224A 18 BF244 24	OC41* 48	ZTX301 16	2N2906# 18 2N2907# 20 2N2907A# 22
We stock thousands more items. It pays to visit us. We are situal Nearest Underground/BR Station: Watford High Street. Open 8	ited behind Watford Football Ground.	ACY22 40	BC213 9	BF244B 30 BF256* 50	OC43* 55		2N2926G 10 2N3011# 24
Parking space available.	Michigay to Saturday. Ample Free Car	ACY39 78 ACY41 39	B BC214 9	BF257* 26 BF258* 30	OC45* 20 OC46* 28	ZTX311 17 ZTX314 24	2N3054* 49
POLYESTER CAPACITORS: Axial lead type. (Values are in μ.F) 400V: 0-001, 0-0015, 0-0022, 0-0033 7p; 0-0047, 0-0068, 0-0	01, 0-015, 0-018 9p; 0-022, 0-033 0-33, 0-47 39p; 0-68 45p.		BC307B 14	BF259* 37 BF336 30	OC71* 25	ZTX326 40	
10p; 0.047, 0.068 14p; 0-1, 15p; 0-15, 0-22, 22p; 0 160V: 0.039, 0-15, 0-22 11p; 0-33, 0-47 19p; 0-68, 1-0 22p DUBILIER: 1000V: 0-01, 0-015 20p; 0-022 22p; 0-047 26j	p; 1-5 29p; 2-2 32p; 4-7 36p	AD161* 42 AD162* 42 AF106* 70	BC327 15	BF394 22 BF594 40 BF595 38	OC74* 45	ZTX500 13	2N3563 20 2N3614* 169
POLYESTER RADIAL LEAD (Values in µF). 250V:	FEED THROUGH	AF114* 25 AF115* 25	BC338 12	BFR39 25 8FR40 25	OC76* 36	ZTX502 19	2N3615* 135 2N3663* 24
0.01, 0.015, 0.022, 0.027 5p; 0.033, 0.047, 0.068, 0.1 7p; 0.15 1 13p; 0.47 15p; 0.68 18p; 1.0 24p; 1.5 27p; 2.2 31p.	11p; 0-22, 0-33 CAPACITORS 1000pF/350V 8p	AF116* 25 AF117* 25	BC461* 30	BFR41 28 BFR79 28	OC79# 76 OC81D# 28	ZTX504 25 ZTX531 25	2N3702 10 2N3703 11
ELECTROLYTIC CAPACITORS: Axial lead type (Values are in µ F)	22 50 44-162 400 65 5-11	AF118* 55 AF121* 48	BC547 11 BC548 11	BFR80 28 BFR81 28	OC83# 48		
63V: 0.47, 1.0, 1.5, 2.2, 2.5, 3.3, 4-7, 6.8, 8, 10, 15, 22, 8p; 47, 27p; 50, 100, 220, 25p; 470, 50p; 1000, 2200, 68p; 400; 22, 33, 35V: 10, 33, 7p; 330, 470, 32p; 1000, 49p; 25V: 10, 22, 47, 6p; 8	32, 30, 11p; 33, 100, 27p; 50V; 100, 7p; 100, 11p; 3300, 62p; 4700, 64p; 80, 100, 160, 8p; 220, 250, 13p; 470	AF124* 55 AF125* 35 AF126 55	BC557 13	BFR98 105 BFX29* 26 BFX81* 130	OC122* 48	40251# 97 40311# 50 40313# 125	2N3707 10
640, 25p ; 1000, 27p ; 1500, 30p ; 2000, 34p ; 3300, 58p ; 4700 64 8p ; 470, 16p ; 1000, 1500, 20p ; 2200, 34p ; 10V : 4 , 100, 6p ; 640	4p; 16V: 10, 40, 47, 68, 7p; 100, 125. D. 10p; 1000, 14p.	AF127* 35 AF139* 35	BC559 20	BFX84* 24 BFX85* 24	OC139* 85 OC140* 85	40315 55 40316* 85	2N3709 10 2N3710 16
TAG-END TYPE: 70V: 2000, 98p; 4700, 121p; 50V: 10,000, 255 65p; 25V: 4700, 48p; 2000, 37p; 40V: 15,000 450p. 325V: 200	5p: 3000. 75p: 40V: 4000 70p: 2500	AF178* 70 AF180* 70	BCY34* 75 BCY39* 180	BFX86* 28 BFX87* 23	OC141 * 85 OC170 * 40	40319# 71	2N3711 10 2N3772* 170
TANTALUM BEAD CAPACITORS POTENTIOMETERS (AB 359: 0.1 µ F, 0.22, 0.33, 0.47, 0.68, Carbon Track, WW Log & 5/4		AF186* 50 AF239* 42	BCY40* 78 BCY42 48	8FX88* 24 8FY18* 50	OC200* 48	40323# 60	2N3773* 288 2N3819 22 2N3820 32
1-0, 2-2μ F, 3-3, 4-7, 6-8, 25V : 1-5, 10. 500Ω 1KΩ & 2KΩ (lin. onl 20V : 1-5, 16V : 10μ F 13p each, 22 25p .	nly) Single gang LEDs plus Clip 27p TIL209 Red 13p	AFZ11 128 ASY26* 40 ASY27* 45	BCY58 22	BFY50* 20 BFY51* 20 BFY52* 20	OC203* 85	40326* 52	2N3823 * 65
47μ F, 100 40p. 5KΩ-2MΩ single gang 1 0V: 22μ F, 33, 47. 6V: 47, 68, 100. 5KΩ-2MΩ single gang D/	27p TIL211 Grn 18p P switch 60p TIL212 Yellow 22p	ASY2/* 45 ASY50* 95 ASY76* 95	BCY70* 15	BFY53* 28 BFY55* 45	SJE5039* 95 TIP29 43	40347* 80 40348* 101	2N3866* 90 2N3903 18
3V: 68, 100 μ F. 20 p each. 5ΚΩ-2ΜΩ dual gang stere	eo 70p 2 Red 15p 2 Amber Green	ASZ21 60 BC107* 9	BCY72* 16 BCY78* 20	BFY64* 40 BFY71* 20	TIP29A 44 TIP298 56	40360* 43 40361* 45	2N3904 18 2N3905 18
100V: 0-001, 0-002, 0-005, 0-01µF 5p 0-25W log and linear value	les 6Dmm LS400 255p	BC107B* 10 BC108* 9	BCZ11 145 BD112 95	BSX20* 18 BSX26* 75	TIP30 47	40406* 65	2N3906 17 2N4037* 52
0.1μF, 0-15, 0-2 9p. 50V: 0.47μF 11p 10KΩ-500KΩ dual gang	80p ORP61 84p	BC108B* 12 BC108C* 12 BC109*	BD115* 62 BD121 95	BSX29 ± 45 BSX78 ± 55 BSV954 25	TIP30B 64		2N4058* 17
Range: 0-5pF to 10,000pF 3p	TIL307 675p 2N5777 45p	BC1098 9 BC1098 12 BC109C 12	BD124* 115	BSY95A* 25 BU105* 140 BU205 190	TIP31* 40	40412# 63	2N4061 17 2N4062 13 2N4064* 120
0.047μ F 4p ; 0.1μ F 6p . 0.1W 50Ω – 5MΩ Miniat	sture Vertical & 7 Segment Displays 5-LT-01 460p	BC113 17 BC114 19	BD132* 38	BU205 190 BU208 228 E421 96	TIP31B* 40 TIP31C* 66	40594* 80 40495* 90	2N4069 45 2N4236 145
SILVER MICA (Values in pF) 3-3, 4-7, 0-25W 100Q-3-3MQ ho 6-8, 10, 12, 18, 22, 33, 47, 50, 68, 75, 0-25W 2000-4-7MQ ve	oriz. larger 10p TIL312.3 CA 105p	BC115 19 BC116 19	BD135* 36 BD136* 37	E5567 65 MD8001* 158	TIP32A* 49	40636* 125	2N4289 20
82, 85, 100, 120, 150, 220 9p each 250, 300, 330, 360, 390, PESISTORS Frie mai	TH 322 5 CC 115p	BC117 15 BC118 19	BD137* 36 BD138* 36	ME1120 25 ME4102 10	TIP328* 70 TIP32C* 70	2N697# 21	2N4859 65 2N4922* 55
1000, 1800, 2000, 2200 16p each Miniature High Stability, L	DL707 3 CA 99p	8C119# 26 BC134 19	8D139* 45 BD140* 50	ME6002 14 MJ400* 90	TIP33A* 80	2N699* 39	
POLYSTYRENE CAPACITORS: 10pF to 1nF 8p; 1.5nF to 47nF 10p	1.5p 1p FND357 120p MAN3640 175p	BC135 20 BC136 18 BC137 20	BD144* 198	MJ491* 190 MJ2955* 99 MJE340* 45	TIP33C* 105	2N707* 50	2N5172 24
V 2 2Ω 4.7M E12	2p 1.5p XAN351 3 Grn 5p 4p 180p	BC140* 28 BC142* 25	BD181* 85 BD205* 110	MJE370★ 55 MJE371★ 60	TIP34A* 85 TIP34B* 110	2N914* 32 2N916* 27	2N5180# 60 2N5191# 65
MINIATURE TYPE TRIMMERS 1%0 5W 51Ω-1M E24	10p 7p Liquid Crystal Display	BC143* 25 BC147 7	BD378* 65 BD434 42	MJE520* 45 MJE521* 65	TIP34C* 110 TIP35* 219	2N920# 51	2N5457 32
2.5-6pF; 3-10pF, 10-40pF 5-25pF; 5-45pF; 60pF; 88pF; 30p type not mixed values.	OPTO	BC147B 10 BC148 7	8D517* 65 8D695A* 75	MJE2955* 115 MJE3055* 80	TIP35B* 240	2N1131* 22	2N5459 32
COMPRESSION TRIMMERS 3-40pf 10-80pf; 25-190pf 25p 1040, 1055, 1056, 1058	1034. 1039. TIL 111/2 105p B. 1066. 1067. TIL 114 110p	BC148B 10 BC148C 10 BC149 8	BDY11 220	MPF102 30 MPF103 36 MPF104 36	TIP36★ 260	2N1303* 50 2N1304* 50	2N5777* 45
100-500pF: 1250pF 45p 1098. 1100	20p each. TiL117 164p	BC149C 10 BC153 14	BDY60* 110	MPF105 36 MPF106 50	TIP368* 300 TIP36C* 325	2N1305* 28 2N1306* 35	2N6109 45 2SD234* 50
	BRIDGE Order	BC154 14 BC157 10	BF115* 22 BF154* 25	MPF107 50 MPS3904 40	TIP41A* 63 TIP41B* 73	2N1307* 50 2N1308* 46	3N128* 85
500pf 165p 00 208/176 285p AA119 15 (pla	estic case) £2	BC158 11 BC159 11	BF158* 29 BF160 30	MPSA05 24 MPSA06 24	TIP42B★ 82	2N1670# 150	Matched
4511/DAF 115p* motion drive 325p CRO33 157 1A/ Dial Drive 4183 C804-5pF 10 15 BY100 24 1A	/50V 20 please	BC160* 27 BC167A 11	BF167 25	MPSA12 42 MPSA55 24	TIP3055# 51	2N2160* 105	10p extra
6 1/36 1 650p* 25 50pF 175p* BY126 14 1A. Drum 54mm 30p* 100, 150pF 215p* BY127 14 1A.	7400V 29 LINEAR ICS LM	304 240 308T 110	NE571 450 RAM2102-2*170		189 210 7418	2 160 4043	94 4422 545
					190 33 7418	4 135 4044	95 4433 1225
0-1-365pF	/100V 44 709C 14 pin 49 LM	318H* 205 319S* 195	RC4136D 120 ROM2513# 700	7404 14 74 7405 18 74	191 75 7418 192 38 7418	4 135 4044 5 135 4045 1 8 275 4046 1	45 4435 82 5 28 4440 127 5
0.1.365pF 245p L 3x310pF 495p 0A9 75 2A, 0A20 2365pF 275p 00.3x25pF 430p 0A47 12 2A, 0A20 24 24 24 24 24 24 24 24 24 24 24 24 24	709C 14 pin 49 LM 709C T05 35 LW 720V 46 709C T05 35 LW 7400V 53 7414 8 pin 45 LW	319S* 195 324A 79 339* 80	RC4136D 120 ROM2513* 700 SAS560 240 SAS570 240	7404 14 74 7405 18 74 7406 38 74 7407 38 74	191 75 7418 192 38 7418 193 32 7419 194 78 7419	4 135 4044 5 135 4045 1 8 275 4046 1 4047 4047 2 105 4048	45 4435 825 28 4440 1275 87 4450 295 58 4451 295
0-1-365pF 245p 00-3x310pF 495p 0A9 75 0A2 75 00-3x25pF 430p 0A47 12 2A. DENCO COILS	709C 14 pin 49 LM 709C 105 315 LM 7400V 53 723 ± 14 pin 45 LM 7600V 65 741 ± 8 pin 22 LM 7100V 72 741 ± 14 pin 32 LM	319S* 195 324A 79 339* 80 348* 95 379* 375	RC4136D 120 ROM2513* 700 SAS560 240	7404 14 74 7405 18 74 7406 38 74 7407 38 74 7408 17 74 7409 17 74	75 7418 492 38 7418 493 32 7419 494 78 7419 495 65 7419 496 57 7419	4 135 4044 5 135 4045 1 8 275 4046 1 10 115 4048 3 105 4049 4 105 4050	4435 825 4440 1275 87 4450 295 8 4451 295 8 4452 4490V 525 8 4490V 6 449
0-1-365pF 245p U-3x310pF 495p 0A9 75 0A47 12 2A, 00-2 365pF 275p 0O-3x25pF 430p 0A47 12 2A, 00-2 365pF 245p 0A70 12 2A, 00-2 365pF 0A9 0A81 15 2A, 00-2 365pF 0A9 0A9 0A81 15 2A, 00-2 365pF 0A9	700C 14 pin 49 LM 709C 105 35 LM 709C 105 35 LM 7400V 53 723*14 pin 45 LM 7401*14 pin 45 LM 741*14 pin 32 LM 700V 72 741*14 pin 70 LM 7200V 75 748C*8 pin 36 LM 7400V 79 1538 pin 150 LM	319S* 195 324A 79 339* 80 348* 95 379* 375 380 95 381N 145 381AN 248	RC4136D 120 ROM2513 + 700 SAS560 240 SAS570 240 SG3402 + 295 SL437A 560 SN72710 + 43 SN72733 + 99 SN75450 + 84	7404 14 74 7405 18 74 7406 38 74 7407 38 77 7408 17 74 7409 17 74 7411 20 74 7412 17 74	75 7418 492 38 7418 493 32 7419 494 78 7419 495 65 7419 496 57 7419	4 135 4044 15 135 4045 1 18 275 4046 1 10 115 4047 2 105 4049 4050 5 95 4052 7 85 4053	4435 825 825 826 827
0-1-365pf 245p 00-3x25pf 495p 0A9 75 2A, 00-2365pf 245p 00-3x25pf 430p 0A47 12 2A, 0A79 12	1000 100	319S* 195 324A 79 339* 80 348* 95 379* 375 380 95 381N 145 381AN 248 382 125	RC4136D 120 ROM2513* 700 SAS560 240 SAS570 240 SG3402* 295 SL437A 560 SN727710* 43 SN72733* 99 SN75450* 84 SN754548* 46 SN76003N 175	7404 14 74 7405 18 77 7406 38 77 7407 38 77 7408 17 74 7410 15 77 7411 20 74 7412 17 74 7413 30 74 7414 51 74	191 75 7418 192 38 7418 193 32 7419 194 78 7419 195 65 7419 196 57 7419 197 189 7419 1104 62 7419 1105 62 7419 1107 29	135 4044 135 4045 1 18 275 4046 1 10 115 4047 10 115 4048 3 105 4049 4 105 4050 5 95 4051 6 99 4052 7 85 4053 8 150 4055 1	445
0-1-365pf 245p 00-3x25pf 495p 0A9 75 2A, 00-2x26pf 430p 0A47 12 2A, 0A47 12 2A	30	319S* 195 324A 79 339* 80 348* 95 381N 375 381N 145 381AN 248 382 125 1458* 50 3900* 70	RC41360 220 ROM2513* 700 SAS560 240 SAS570 240 SG3402* 295 SN72710* 43 SN72733* 99 SN75450* 84 SN75450* 84 SN76003N 175 SN76013 140 SN76018* 148	7404 14 7, 7405 18 7, 7405 38 7, 7407 38 7, 7408 17 7, 7408 17 7, 7410 15 7, 7411 20 7, 7412 17 7413 30 7, 7416 30 7, 7417 30 7, 7417 30 7, 7417 30 7, 7417	191 75 7418 192 38 7418 193 32 7419 194 78 7419 195 65 7419 196 57 7419 197 189 7419 1100 119 7419 1105 62 7419 1107 29 11109 54	14 135 4044 105 105 105 105 105 105 105 105 105 105	455 435 825 487 4450 295 87 4450 295 88 4451 295 48 4452 48 4490F 895 72 4490F 255 72 4500 17 72 4500 69 10 4503 69 25 450 51 70 4507 55 80 4508 298
0-1-365pf 245p 00-3265pf 495p 00-39 75 2A-00-2365pf 495p 00-39 75 2A-00-39 75	1,000 34 709C 14 pin 4 1,000 36 709C 105 36 1,000 373 44 pin 4 1,000 373 44 pin 4 1,000 374 44 pin 4 1,000 374 44 pin 4 1,000 374 45 45 1,000 374 45 45 1,000 374 45 45 1,000 374 45 45 1,000 374 45 45 1,000 375 45	3195* 195 324A 79 339* 80 345* 95 379* 375 380 95 381N 145 381AN 248 382 125 1458* 50 3900* 70 3901* 125 3900* 70	RC4136D 120 ROM2513* 700 SAS560 240 SAS570 240 SG3402* 295 SL437A 560 SN72710* 43 SN72733* 99 SN754509* 46 SN76003N 175 SN76013 140	7404 14 7, 7405 18 7, 7405 38 7, 7407 38 7, 7408 17 7, 7408 17 7, 7410 15 7, 7411 20 7, 7414 51 7, 7416 30 7, 7416 30 7, 7417 30 7, 7420 16 7, 7421 29 7, 7421 29 7, 7421	191 75 7418 192 38 7418 193 32 7419 194 78 7419 195 65 7419 196 57 7419 197 189 7419 1100 62 7419 1100 54 1110 54 1111 68 4000	141 135 4044 4045 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	455 435 825 487 4450 295 87 4450 295 88 4451 295 48 4452 48 4490F 895 72 4490F 895 72 4503 69 10 4503 69 20 4503 69 20 4503 69 21 4503 69
0-1-365pf 245p 00-2365pf 245p 00-2365pf 2430p	3709C14 pin	3195 ± 195 324A 79 339 ± 80 348 ± 95 379 ± 375 381 N 145 381 N 248 382 125 1458 ± 50 3900 ± 60 3909 N 70 3911 ± 125	RC4136D 120 SAS560 240 SAS570 240 SG3402* 295 SL437A 560 SN727733* 99 SN75450* 84 SN76003N 175 SN76018* 148 SN76033N 175 SN760131* 110 SN76131* 110 SN76131* 115	7404 14 7, 7407 18 7, 7407 18 7, 7408 17 7, 7408 17 7, 7409 17 7, 7410 15 7, 7411 20 7, 7414 51 7, 7416 30 7, 7416 30 7, 7417 30 7, 7417 30 7, 7421 29 7, 7422 24 7, 7423 27 7, 7423 27 7, 7424 27 7, 7423 27 7, 7423 27 7, 7423 27 7, 7424 27 7,	191 75 7418 192 38 7418 193 32 7419 194 78 7419 195 65 7419 196 57 7419 197 189 7419 1104 62 7419 1105 62 7419 1107 29 1108 63 1109 54 1110 54 1111 68 1111 68 1116 198 4001 1118 83 4002 115 4005 4007	14 135 4044 4045 1 135 4045 1 18 4045 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	455 435 825 487 4450 295 87 4450 295 88 4451 295 48 4452 48 4490F 595 72 4490V 525 72 4502 120 10 4503 69 28 4506 51 770 4507 55 80 4508 298 810 4508 298 810 4511 150 810 4513 206 810 4513 206
0-1-365pf 245p 00-2365pf 245p 00-3x25pf 495p 00-29 25p 275p 00-3x25pf 430p 00-347 12 2A-00-47 12 2A-00	3	3195* 195 324A 79 339* 80 345* 95 379* 375 380 95 381N 145 381AN 248 382 125 1458* 50 3909N* 70 39019* 75 3911* 125 52AA* 750 553AA* 755 5663 275	RC4136D 120 SAS560 240 SAS570 240 SAS570 240 SG3402* 295 SN727733* 99 SN75450* 84 SN756450* 140 SN7603N 175 SN76018* 148 SN7603N 175 SN76013N 175 SN76131* 110 SN76131* 115 SN76131* 115 SN76277 115 SN76277 115	7404 14 7-7405 18 7-7406 38 7-7407 38 7-7407 38 7-7409 17 7-7410 15 7-7411 20 7-7413 30 7-7413 30 7-7414 20 16 7-7420 16 7-7420 16 7-7420 17-7	191 75 7418 192 38 7418 193 32 7418 194 78 7419 195 65 7419 196 57 7419 197 189 7419 1104 62 7419 1105 62 7419 1105 64 1110 54 1110 54 1111 68 1111 68 1111 68 1111 68 1111 68 1111 68 1112 406 112 46 4008 112 46 4008 112 46 4008 112 46 4008 112 46 4008 112 46 4008	141 135 4044 141 135 4045 1 188 275 4046 1 1 105 4047 1 105 4050 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 105 4051 1 17 4053 1 18 4056 1 105 4067 3 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4067 3 1 18 4068 1 105 4070 4070 4070 4070 4070 4070 4070 40	455 4355 825 87 4450 295 88 4451 295 48 4490F 695 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 770 4507 55 880 4518 99 110 4511 150 150 4513 206 161 4513 206 161 4513 206 161 4513 206 161 4515 299 162 4515 299
0-1-365pf 245p 00-2365pf 495p 00-9 75 2A-00-2365pf 495p 00-325pf 430p 00	3	319S* 95 329A 79 339* 80 339* 80 339* 80 35 319* 375 380 95 381 N 248 382 125 1458* 50 3909* 70 3909* 70 3909* 70 553AA* 750 553AA* 750 553AA* 755 663 275 7724* 175 11304P 260 11310P 149	RC4136D 120 SAS560 240 SAS570 240 SAS570 240 SG3402* 295 SN727733* 99 SN75450* 84 SN75603N 175 SN76018* 148 SN76033N 175 SN76131* 110 SN76131* 110 SN76131* 110 SN76131* 115 SN76131* 125 SN76131* 12	7404 14 7.7405 18 7.7406 38 7.7407 38 7.7407 38 7.7409 17 7.7410 15 7.7411 20 7.7413 30 7.7413 30 7.7420 16 7.7420 16 7.7420 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7430 17 7.7440	191 75	141 135 4044 141 135 4045 1 188 275 4046 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	455 4355 825 87 4450 295 887 4450 295 88 4451 295 48 4490F 695 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 770 4507 55 880 4508 298 10 4511 150 10 4503 298 10 4511 150 10 4503 298 10 4511 298 10 4511 298 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 206 10 4513 382 10 4517 382 11 4518 102
0-1-365pf 245p 00-2365pf 245p 00-2365pf 2430p	1,7000 34 709C 14 pin 4 1 1 1 1 1 1 1 1 1	319S* 95 329A 79 339* 80 339* 80 339* 95 339* 375 380 95 381 N 145 381 25 381 2	RC4136D 120 SAS560 240 SAS570 240 SAS570 240 SG3402* 295 SK1437A 560 SN727710* 39 SN75450* 34 SN756450* 34 SN7603N 175 SN76018* 148 SN76031 140 SN76031 140 SN76031 150 SN76131* 110 SN76131* 110 SN76131* 110 SN76131* 125 SN761131* 125 SN76113* 125 SN761131* 125 SN76113* 125	7404 14 7.7405 18 7.7406 38 7.7407 38 7.7407 38 7.7409 17 7.7410 15 7.7411 20 7.7413 30 7.7414 20 7.7416 30 7.7420 16 7.7420 16 7.7420 17 7.7430 17 7.7433 1	191 75	14 135 4044 4045 1 1 1 1 1 1 1 1 1	455 4355 825 87 4450 295 887 4450 295 88 4451 295 48 4490 525 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 70 4507 55 880 4518 150 4518 4512 98 880 4513 206 4513 206 20 4515 299 31 4517 382 21 4516 120 21 4517 382 21 4519 55
0.1.365pf 2.45p 00.2365pf 495p 00.29 365pf 2.75p 00.3265pf 430p 00.3265pf 430p 00.3265pf 430p 00.3265pf 430p 00.3265pf 430p 00.347 12 2.32	3	319S* 95 3294A 79 339* 80 339* 80 339* 80 339* 80 319* 375 380 95 381 N 248 382 125 1458* 50 3909* 70 3909* 70 553AA* 750 553AA* 750 553AA* 750 553AA* 750 11304P 260 11310P 149 11312P0 195 11488 85 11489* 90 130* 452 11489* 90 130* 452 131049* 250	RC4136D 120 SASS60 240 SASS60 240 SASS70 240 SG3402* 295 SN727710* 43 SN72733* 93 SN754540* 46 SN754540* 46 SN75603N 175 SN76023 140 SN76023 140 SN76023 140 SN76023 140 SN76018* 110 SN76023 140 SN76018* 120 SN76018* 150 SN76018* 150 SN	7404 14 7.7405 18 7.7406 18 7.7407 38 7.7408 17 7.7410 15 7.7410 15 7.7411 20 7.7413 30 7.7413 30 7.7420 16 7.7420 17 7.7430 17 7.7430 1	191 75	14 135 4044 4045 1 135 4045 1 135 4045 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	455 4355 825 87 4450 295 88 4451 295 48 4490 525 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 1770 4507 55 180 4508 298 110 4511 150 15
0-1-365pf 245p 00-2 365pf 275p 00-3 x25pf 430p	30 30 30 30 30 30 30 30	319S* 95 3294 79 3394 80 3294 95 3398 80 3199 35 379* 375 381 N 145 381 N 248 382 125 1458* 50 3300* 70 552AA* 750 552AA* 750 552AA* 750 13104 P 260 13104 P 260 13107 149 13137P0 195 1488 85 1488 85	RC4136D 120 SAS560 240 SAS560 240 SAS570 240 SG3402* 295 SK1437A 560 SN727733* 99 SN75450* 84 SN756450* 140 SN7603N 175 SN76018* 148 SN76031 140 SN76031 175 SN76131* 110 SN76131* 110 SN76131* 110 SN76031 15 SN76131* 125 SN76131* 125 SN7613* 125 SN7	7404 14 7.7405 18 7.7406 38 7.7407 38 7.7407 38 7.7408 17 7.7410 15 7.7411 20 7.7412 17 7.7413 30 7.7413 30 7.7420 16 7.7420 17 7.7430 17 7.7430 1	191 75	14 135 4044 4045 1 135 4045 1 135 4045 1 135 4046 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	455 4355 825 87 4450 295 88 4451 295 48 4490 525 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 1770 4507 55 180 4508 298 110 4511 150 15
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0-1-365pf 2-45p 00-2 365pf 2-75p 00-3 x25pf 4-95p 02-365pf 2-75p 00-3 x25pf 4-30p 00-47 1:2 2A-20 0A-7 1:2 2A-20 0A-20 0A-7 1:2 2A-20 0A-20 0A-2	1709C14 pin	319S* 195 324A 79 339* 80 329A 95 3399* 80 5379* 375 381N 145 381AN 245 382 125 381AN 245 380 95 381N 70 3909* 70 3911* 125 552AA* 755 552AA* 755 552AA* 755 552AA* 755 1309P* 70 3911* 125 1310P 149 1312P0 195 13130P 149 13130* 45 11488 85 11489* 90 11495 11495 11495 11496 11496 11496 11496 11496 11496 11496 11496 1150 11496 11496 11496 1150 11496 1150 13340P 150 133340P 150	RC4136D 120 SAS560 240 SAS560 240 SAS560 240 SAS570 240 SG3402* 295 SAS570 240 SG3402* 295 SN72733* 99 SN727454* 43 SN72733* 99 SN75456* 44 SN78031 140 SN78031 140 SN78031 140 SN78031 125 SN78031 12	7404 14 7.7405 18 7.7406 38 7.7407 38 7.7407 38 7.7407 38 7.7409 17 7.7410 15 7.7410 20 7.7411 20 7.7411 20 7.7414 21 7.7416 30 7.7417 30 7.7417 30 7.7420 16 7.7421 29 7.7421 29 7.7421 29 7.7422 24 7.7425 27 7.7426 36 7.7427 30 7.7428 35 7.7428 35 7.7432 25 7.7433 40 7.7432 27 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 112 7.7444 114 7.444 114 7.44	191 75	14 135 4044 14 135 4045 1 18 275 4046 1 18 275 4046 1 19 10 115 4047 2 105 4049 1 10 10 10 10 10 10 10 10 10 10 10 10 1	455 4435 825 87 4450 295 88 4451 295 48 4490 695 72 4490V 525 72 4501 17 72 4502 120 10 4503 69 28 4506 51 70 4507 150 80 4508 298 10 4511 150 80 4512 98 815 4510 99 110 4511 150 815 4510 99 110 4511 150 816 4512 98 817 4513 206 818 4512 98 818 4513 206 818 4513 2
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WATFORD ELECTRONICS



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(Optional extras. Probes £1.50*; Carrying Case £1.50*)

10	phonal ex	(Demonstrat				#)				
JACK PLUGS		SOCKETS		SWITCH	1ES★ 2A, 250V.	SLIDE 1 A DPI	250V: DT 14p			
chrome b	ody met		in line couplers	SPST	28p 34p		DT c/over 15p			
2.5mm 12p	8p 8 ₁	break	11p 12p	DPDT 4 pole or	38p	4 pole PUSH	2-way 24p BUTTON			
MONO 23p	15p 13	20p	18p 22p	SUB-MI	NTOGGLE	Spring SPST o	loaded			
STEREO 31p	PLUGS	SOCKETS	In Line	SP chang SPST on	/off 54p	SPDT	c/over 65p			
2 PIN Loudspeaker	11p	7p	18p	OPDT 6 tags 70p Non Locking						
3, 4, 5 Audio	13p	8p	20p	DPDT centre off 79p Push to Make 15p DPDT Biased 115p Push Break 25p						
CO-AXIAL (TV)	14p	own mu	wn multiway Switch. g Assembly. Accom-							
PHONO assorted colours	9p 12p	5p single 8p double	15p	modate	up to 6 Wafe	rs	69p 34p			
Metal screened BANANA 4mm	11p	10p 3-way		Break 8	Before Make W	afers. 1	pole / 12 way. /ay. 6p / 2 way			
2mm 1mm	10p 7p	10p 7p	=	Spacer	and Screen		47p 5p			
WANDER 3 mm	8р	8p		ROTAL 1 pole	RY. (Adjustab / 2 to 12 was	y. 2p/2	to 6 way, 3			
DC Type AC 2-pin American	15p 15p	20p 15p		pole/2	to 4 way, 4 po	ole / 2 to 3	3 way 41p			
VOLTAGE*					ALU	M.	PANEL			
REGULATORS TO3 Can Type p		DRMERS* (M a IOm A; 9-0-9 V 7		12V 100m	A BOX	ES*	METERS*			
1A +ve 5V 1.2V,	8VA: 6V-	.5A 6V5A; 9	V-4A 9V-4		A 2V2V1	.ID р 45	FSD			
MVR5 or 12 180	12VA: 4.5	15V25A 15V- V-1.3A 4.5V-1.	3A: 6V-1 2	195 A 6V-1 2/	21/4×51/6	x11/2" 6B	60x46x 35mm			
1Ave 5V, 12V 220	12V-5A	12V5A, 15V 20p p&p) V-1.5A 6V-1 5 I2V-1A 15V-:	44 15V.4	A 20V 3	Λ .	" 68	0-50µ A 0-100µ A			
Plastic (TO92) +ve 0.1A 5V. 6V 8V, 12V, 15V 30			8A 15V-8			Vs" 78	0-500µA 0-1mA			
+ve 1A (TO220)	20V- 6A (- 50VA: 6V	-4A 6V-4A; 9V-	2.5A 9V-2.	290 5A; 12V-2	A 5x4x2"	82 88	0-5mA 0-10mA			
5V, 12V, 15V, 18V, 24V 85	1 2A; 25\	15V-1.5A 15V V-1A 25V-1A;	-1.5A; 20V 30V8A 30	W8A (50	7×5×2½		0-50mA 0-100mA			
-ve 0.5A 5V. 6V.	p&p) 100VA:	12V-4A 12V- 20V-2 5A;	4A; 15V-3	350 A 15V-34	10 I 10 7 0	172	0-500mA 0-1A			
8V, 12V, 15V 95	40V-1.25	A 20V-2 5A; : A 40V-1.25A;	50V-1 5A 50V-1A 50	DV-1A (60	P 12-8-3	165	0-2A 0-25V			
—ve 1A 5V. 12V 125	p&p)	charge to be -	ddad share	650			0-50V AC 0-300V AC			
—ve 0 1A (TO92) 5V. 12V. 15V 60	postal cha	charge to be a rge.)	udeu above	our norm:	SPEA	475n each				
LM309K 135 LM320-12 165	with a sec		BΩ 0.3W	65	→ / op eacn					
LM320-15 165 LM323K 625	K1 Bla	to fit 1/4 shaft ck Pointer type		9р	2.5 3" 40 Ω 2.5	58	41/4×31/4×11/2			
LM304H 240 LM317H 100	K2 Shr	ite Pointer type in Silvered Alum		11p 12p	64 Ω 2.5 .8 Ω 5W	" 65	0-50µA 0-100µA			
LM317K 350 LM325N 240	K4 Bla	in Black Ribbed ck Serrated Met	tal top with		7" x 4" 8Ω 3W	190	0-500µ A 595p each			
LM326N 240 LM723 45	K4a As	ator 35mm diar K4 but 25mm d	liam.	22p 20p	6" x 4"	160	, coop each			
EARPHONES Magnetic	calibrated	ck Fluted, metal d 0.9, 37mm di	am	28p 28p	HEAT SINKS*					
2.5mm 18p	K7 81a	K5 but with point of K5 but wi	pered, meta	28p 1 top & 26p	T092 8p		I PROJECTS Inplex Sound			
3.5mm 18p Crystal 33p	K7a As	above but point	er on skirt	26p 10p	T05 9p T018 8p	Gen	nerator Audio			
ULTRASONIC	K12 Alu	ck or Silvered fo iminised plastic . 22mm diam	with line	16p	†0220 22 p	Part	s now lable. Send			
TRANS- DUCERS	K19 So	, 22mm diam lid Aluminium A indicator, skirtei	mplifier Kno		T03 22p T066 22p		for List.			
£3.95★ per pair					4					
74LS★ 86 90 91	43 15 60 15 104 16	8 96 26	59 160 61 450 66 52	247		Chip				
92	89 16 89 16	1 98 2	73 244 75 250	249	190 Conve	DULE f	V into a VDU by			
20 48 95 27 28 95	116 16 116 16	3 118 2	79 66 83 192	253	142 using TV-C	the new RT con	Thompson-CSF			
30 22 107	44 16 55 16	5 75 29	90 128 93 128	258	146 SF FS	ctors tex	16 line by 64 t refreshment.			
33 39 112	55 17 50 17	0 288 2	95 185 98 168	386	mana;	gement o	ement, Cursor on screen, Line			
38 39 114	50 17 70 17	4 106 3:	24 240 25 290	390 393 395	230 erasin	g, Comp uting syst	atible with any			
42 98 123	70 18 180 18	1 398 3	26 294 27 286	396 398	215 SF.F9	6364E	£11.75*			
48 120 125	60 19 60 19	0 140 3	47 148 48 186	399 445	230 AY-5-	1013UA				
51 24 132	95 19 55 19	2 130 3	52 228 53 228	447	144 SFS8	1 ROM 0102 RA				
55 30 138	85 19 85 19	4 166 3	65 65	490 668	180 74LS SN75		£1.20*			
73 46 145	108 19 170 19	6 100 3	67 65	669	182 SN 75	452	70p± 70p±			
75 48 148	173 22 96 24	1 96 3	73 180 75 160		UHF	Modulato				
78 40 153	76 24	232 3	77 212 78 18 4		(Sena	1 30p s	ule £136.50* tamps for full			
83 115 155 85 118 156	96 24	2 3 232 3	79 215		techni	cal data)				

=news digest

all change-

This is the month when the BBC plays hide and seek with the four stations. They are gonna move 'em - you've gotta find 'em again. Fun eh? Radio One goes to 275m and 285m; Radio 2 goes to 433m and 330m; Radio 3 goes to 247m; and Radio 4 vanishes onto long wave at 1500m. VHF is unchaged thank God.

The Beebs purpose in shuffling dials is to reduce interference from overseas stations. New transmitters are being fitted in some areas, so how it behaves now is no indication of how well you'll get the station once they change it around. Radio 2 will now be better in the

day, but worse at night, with Radio 3 generally better.
The movement is to fit in with new European agreements which will allow more stations with better coverage to use the MW and LW bands, so we shouldn't

Oh yes there is one more thing. Up to the switch -November 23 will dawn with the new frequencies operating - unscheduled breaks in transmission will occur in MW and LW programmes lasting between a blink and several minutes. Don't smash your set it's the BBC's fault. They're working on the transmitters and aerials now to ready them for the big switch over, and well you never know who might drop a spanner or

Details will be plastered all over radio, TV and Radio Times between now and then so don't worry about not hearing what's going on. It's most unlikely.

short stuff

 GI has released an appliance timer — the AY-3-1251 MPU-based it is, and can be used in such things as cookers to replace nasty mechanical things like clocks. Two versions are available and facilities include keyboard entry, direct display drive, four outputs et al.

 A new digital logic family called FAST (Fairchild Advanced Schottky TTL) is to be released soon. Power consumption is much lower than normal types — about 25% in fact. Typical delays are about 3 nS - hence the name 66 circuits will be released by the year's end. Price?

Competitive apparently, whatever that means.

Prom programming overnight is offered by Memec Ltd of Thame Park Industrial Estate, Thame, Oxon. A 24hr turnaround is quoted and all types of PROM can be

RCA have a new chip out which a smoke detector unto itself. It requires only an ionisation chamber and horn alarm to begin detecting and alarming. The number is T-A 10451 and it will operate on either battery or line.

 Britain has produced a new design of terminal to operate with the European OTS test sattelite. The idea is a joint venture between Marconi, the Post Office and the Department of Industry

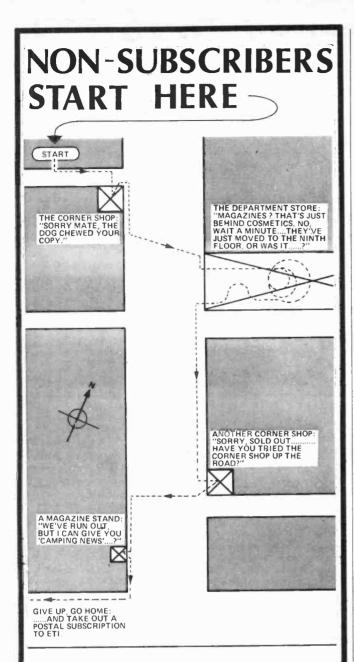
• Compe 78 will be held at Olympic this year to allow for more exhibitors. The exhibition deals with small systems, minis and micros, software and hardware and Uncle Tom Cobley and all.

 Supervisor is a remote controlled helicopter for use on the modern battlefield. It has been developed by Marconi and Westland. The machine stands about as high as a man and contains cameras and other surveillance equipment. It has just passed its first flight tests successfully and could be of great use to NATO when in

 Two books from GI to full up the bookshelves usefully are the 600p Catalogue and the 300p Applications Handbook. Both will be of great use indeed to both engineers and serious home dabblers. They cost £3.00 and £1.80 respectively from any GI distributors.

Toshiba and Rank have completed an agreement to produce TV sets and audio equipment in Plymouth and Cornwall.

 Texas Instruments new 64K RAM is at last released. Automation in production means that by 1980 each unit will require only 5 man MINUTES to produce from start to finish, and that a mere 1000 staff will be able to service entire world demand!



It can be a nuisance can't it, going from newsagent to newsagent? "Sorry squire, don't have it - next one should be out soon."

Although ETI is monthly, it's very rare to find it available after the first week. If it is available, the newsagent's going to be sure to cut his order for the next issue -- but we're glad to say it doesn't happen very often.

Do yourself, your newsagent and us a favour. Place a regular order for ETI; your newsagent will almost certainly be delighted. If not, you can take out a postal subscription so there's nothing for you to remember — we'll do it for you.

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Taylor ½ 5 x 3 500 µ A calibrated in Degrees Tilt £3.50
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'Please include 35p P&P on orders for goods where P&P is not included. All prices include V.A.T.

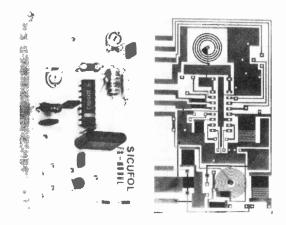
EQUIPMENT SPARES

3 Lacy Close, Wimborne, Dorset BH21 1PY

Please note that our stores at Poole Road Works are open to personal shoppers on Friday afternoons and all day Saturday.

news digest

foiled again



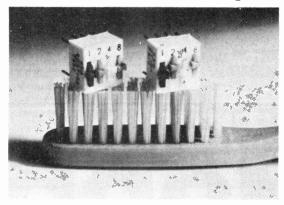
These structures made of chrome-nickel and copper represent an integrated passive circuit with capacitors, coils and resistors, the carrier being a plastic foil. The rectangular, spiral and meandrous shapes largely determine the capacitances, inductances and resistances. Using the name "Sicufol" (Siemens copper foil) Siemens is now offering modules for television sets as the first wares in this new technology.

Resistances up to 300R can be fabricated directly,

Resistances up to 300R can be fabricated directly, capacitance to 150pf/cm² and inductances up to $10\mu H.$ By meandering the track back and forth, an increase of up to 3800 per given area is possible.

The carrier foil is a kind of Teflon so you shouldn't be stuck for ideas. . . .

eat your heart out colgate



One might question the wisdom of a picture like this, bristling as it is with cunning. At least it's an excuse to brush up on DIL switches. These are made by ERG Components and can switch at up to 10 VA. Fitting a normal DIL format they are numbered in a standard BCD format, and can be very useful in any digital circuitry. Home constructors never seem to make much use of these components for reasons best known to themselves.. ERG Components, Luton Road, Dunstable, Bedfordshire.

ooops

Please note that the prices shown on the Gould Advance Ad on Page 14 of the October issue were incorrect. The correct prices are shown on page 14 of this issue. We apologise to Gould Advance and our readers for any inconvenience caused.

TTLs	by TEX	(AS	1	74283	190o	74L\$240	175ô	4007	18p	4543	180p.	LM3900	70n	TRANCICI	TORE	-	· [Comment of		- 7				-
			- 1		400p	74LS240		4008	80p	4553	450p	LM3911	130p	TRANSIST			Zp TIP41C	78p	'2N4125/6	6 22p l	DIODES	F	3A 600V	/ 72p
7400	13p	74105	65p		400p	74LS242		4009	40p	4560	250p	LM4136	120p	AC127/8	20p		3p TIP42A	70p	*2N4289	20p	BY127	12p		
7401	14p		34p		150p	74LS243	175n	4010	50p	4583	90p	'MC1310P	150p	AD149	70p		Dp TIP42C	8Zp	*2N4401/3	3 27p	OA47	9p	'4A 400V	/ 100p _y
7402	14p		55p		150p	74LS244		4011	17p	4584	90p	MC1458	55p	AD161/2		BLY83 70	TIP2955	78p		90p	'OA81	15p	6A 50V	/ 90p
7403	14p		55p		200p	74LS245		4012	18p	40014	90p	MC1495	400p	BC107/8	11p		5p TIP3055	70p	2N4871		OA85		6A 100V	
7404	17p		70p		200p	74LS251	200p	4013	50p	400B5	200p	'MC1496	100p	BC109	11p	BSX19/20 2		34p	2N5087	27p	OA90	9p	6A 400V	/ 120p
7405	18p	74116 20	00p		150p	74LS257	120p	4014	84p	40097	90p	'MC3340	120p	'BC147/8		BU105 19		30p	2N 5089	27p	OA91	9p	10A 400	0 ∨200 m
7406	32p		30p		150p	74LS259		4015	84p	14411	£10	'MC3360	120p	'8C149		*BU10B 25		12p	*2N5172	27p	'OA95		25A 400	
7407	32p	74119 2	10p	74367	150p			4016	45p	14412V	£10	'MFC4000E	12Up	'BC157/8		'BU205 22		13p	2N5179	27p	OA200	0		- 100р
7408	19p	74120 1	10p	74307	doci	74LS298		4017	80p	14433	£10	MK50398	12Up	*BC159	11p	*BU208 24	7TX500	15p	2N5191		OA202	400	TRIACS	- 1
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7410	15p		48p		200p	74LS374	160 ₀	4019	45p	LINEAR I.	C.	NE531	100p	'BC172	12p	MJ481 179	7ZTX504	30p	2N5245		1N916		3A 400V	60p j
7411	24p		55p	74393	200p	MEMORI	IES	4020	100p	'AY1-0212		'NE540	200p	BC177/8	17p	MJ491 20	2N457A	250p	2N5296		1N4148	466	3A 500V	65p
7412	20p		55p	74490	225p	2102	100p	4021	110p	'AY1-1313		NE543K	225p	BC179	18p	MJ2501 22!	5p 2N696	35p	2N5401		1N4001/2		6A 400V	70p
7413	30p			74LS SE	RIES	2102-1	125p	4022	100p	'AY1-5050		NE555	25p	'BC182/3	10p	MJ2955 10	2N697	25p	2N5457/8		1N4003/4	كالمعا	6A 500V	88p
7414	60p		75p	74LS00	18p	2102-2	110p	4023	22p	AY5-1315		NE556	70p	*BC184	110	MJ3001 22!	50 2N697	45p	2N5459	40p	1N4005	E	8A 400V	75p
7416	27p		75a	74LS02	18p	2107	500p	4023	50p	'AY5-1317	636p	NE561B	425p	BC187		*MJE340 6		20p	2N5460		1N4006/7		8A 500V	95p
7417	27p		75p	74LS04	22p	2111-1	225p	4025	20p	'AY5-1320		NE562B	425p	*BC212/3	11p	MJE2955 10	2N 708A	20p	2N5485		1N5401/3	146	12A 400V	
7420	17p		70p	74LS08	22p	2112-2	300p	4025	130p	CA3019	80p	NE565	130p	*BC214	12p	MJE3055 70		45p	2N6027		1N5404/7	100	12A 500V	/ 105pj
7421	40p		00p	74LS10	24p	2114	1200p	4026	50p	*CA3046	70p	NE566	155p	BC461	36p	MPF102 4	5p 2N930	18p	2N6247				16A 400V	
7422	22n		90p	74LS13	45p	6810	400p	4027	84p	'CA3048	225p	NE567	175p	BC477/8	30p	MPF103/4 4				420-1	ZENERS		16A 500V	/ 130p
7423	340		90p	74LS14	72p			4028	100p	CA3080E	72p	'NE571	425p	*BC516/7		MPF 105 / 6 44		25p	2N6290	1	2 7V-33V	h		
7425	30p			74LS20	22p	INTERFA	ACE.	4029		CA3089E	225p	RC4151	400p	'BC547B	16p		2N1711	25p	2N6292	00-1	400mW		THYRIST	
7426	40p	74150 10	000	74LS22	28p	ICs .		4030	55p 200p	CA3089E	4450	*SN76003N	175p	*BC549C			2N2102	60p		120p	1W	15p	1A 50V	
7427	34p	74151A	70n	74LS27	38p	MC1488	90o	4031	200p	CA3090A CA3130E		*SN76013N		*BC557B		MPSA56 3:		120p			SPECIAL		1A 400V	
7428	36o		70p	74LS30	220	MC1489	90p			CA3140E		*SN76013N		'BC559C		*MPSU06 6		20p			OFFERS		1A 600V	70p
7430	17p			74LS32	28p	75107	160p	4034	200p				120p	BCY70		MPSU56 7		20p			100+ 741		3A 400V	
7432	30n			74LS47	90p	811595	120p	4035 4040	110p	CA3160E FX209	90p 750p	'SN76023N	140p	BCY71/2	22p	OC28 13		16p		250-			8A 600V	
7433	40p			74LS55	30p	81L596	140p	4040	80p	ICL7106	750p 800p	*SN76023N		BD131/2		OC35 13		30p	40360	40p	100+ 555	€20	12A 400	
7437	35o			74LS73	50p	81L597	120p	4041		ICL8038			120p			*R2008B 20		50p	40361/2	450		-	16A 100	
7438	35p			74LS74	40p	81L598	140p	4042	80p	LM301An	340p	*SN76033N		BF 200		'R2010B 20				120p	100+ RCA		16A 400	V 180p
7440	17p			74LS75	50p	8T28	250p	4043	90p 90p	LM301An		*SP8515	750p	*BF244B	35n	TIP29A 4		24p	40408	700	2N3055		16A 600°	
7441	70p			74LS83	110p	9602	190p				120p	TBA64181	1	*BF256B	70p	TIP29C 5		30p	40409		BRIDGE		BT106	110p
7442A	60p			74LS85	100p	3002	Laoh	4046	130p	LM318	200p		225p	BF257/8		TIP30A 4		9p	40410		RECTIFIE		C106D	45p
	1126	74163 10		74LS86	40p	ROM/UA	LOY	4047	100p	LM324	70p	TBA800	90p	BF259		TIP30C 66		20p			1A 50V		MCR101	36p
	1120			74LS90	60p			4048	55p	LM339	75p	*TBA810	100p	*BFR39		TIP31A 5		65p	40594		1A 100V		2N3525	120p
	100a			74LS93	90p		225p	4049	32p	LM348	95p	*TBA820	90p	BFR40	30p	TIP31C 6:		48p					2N5060	34p
7446A	93p			74LS107		AY3-1015	5 55Up	4050	48p	'LM377	175p	*TCA 940	175p	BFR41	30p	TIP32A 6		140p	40603				2N5064	40p
7447A	70p			74LS112		AY5-1013	3 40Up	4051	80p	,FW380	75p	TDA1022	600p	BFR79	30p	TIP32C 8		240p	40673				DIEACE	CCND
7448	80p			74LS123		AY5-2376		4052	65p	'LM381AN		TL074	130p	BFR80	30p	TIP33A 9		30p	40841		"2A 100V		SAE FOR	SEND
7450	17p			74LS124		RO3-2513	3 6000	4053	80p	'LM389N	140p	TL084	130p	*BFR81	30p	TIP33C 114			40871/2	aob	*3A 200V			, LOTE
7451	17p			74LS125		SN74526	2 214	4055	125p	LM 709	36p	TL170	45p	BFX29	30p	TIP34A 11								
7453	17p			74LS132				4056	135p	LM710	50p	XR2206	400p	BFX30	34p		2N3704/		LOW PR	OFILE	DIL SOCK	ETS BY	/ TEXAS	
7454	170			74LS132		EPROMS		4059	600p	LM725	350p	XR2207	400p	BFX84/5	30p		2N3706/		8 pin	11p**	18 pin	25p	24 pin	33p
7460	17p			74LS138		1702A	600p	4060	115p	LM 733 LM 741	100p	XR2211	500p	BFX86/7	30p	TIP35C 296		9 12n	14 pin	12p		28p	28 pm	42p
7470	35p			74LS139	60p	2708	900p	4063	120p		20p	*XR2216	675p	BFX88	30p	TIP36A 270		300p		13p		30p	40 pin	51p
7472	30p			74LS139		2716	€25	4066	55p	LM747	70p	XR2240	400p	BFW10	90p	TIP36C 344		25p			OCKETS			
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7474	30p	74181 20	00p	74LS153		CPUs		4069	20p	VOLTAGE	REGUL	ATORS					2N3823	70p	8 pin				28 pin	120p
7475	36p			74LS154		6502	1200p	4070	30p	Fixed Plast				COMLEX	K	SOUND		90p	14 pin	40p^	24 pin			100p
7476	35p	74184A 15		74LS157		6800	€9	4071	22p	1A +	ve	ve	-			ATOR	'2N3903/4		DISPLA				00/507	120p
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7482	84p			74LS161		OTHERS		4075	22p		815 90		100p	TVCR	TCO	NTROLLER	2N4058/9	9 12n	DL747		225p	TIL32		130p
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On October 13th a brand new magazine is launched in the electronics field. It is written and produced by ETI staff and aimed at the newcomer to electronics — not necessarily young people.

We did think of doing an ad which would tell you about the contents in minute detail but instead we have decided to appeal to your curiosity. We don't ask you to buy it; it may be of no interest to you but we hope that some ETI readers at least will pick up a copy and thumb through it. Please put it back neatly if you don't want to buy: the next person may be more interested.

No. 1 will carry a cover date of November and will be available at newsagents on October 13th. 40p.

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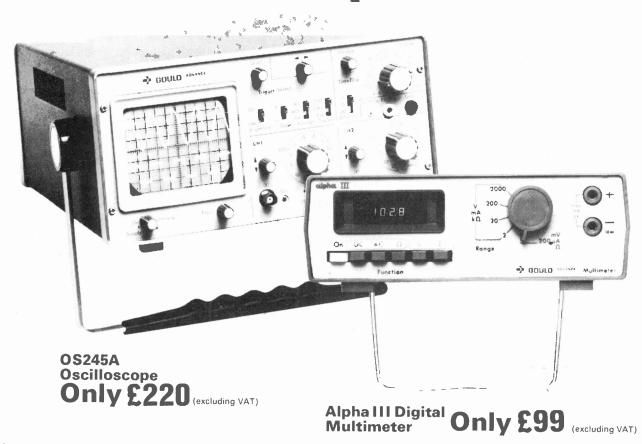
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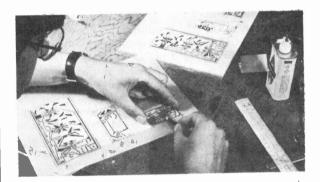
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Tallow Manual	017	Complex Sound Gen Tele Bell Extender Power Bulge	Oct 78	
	018	RF Power Meter Proximity Switch Audio Oscillator (2)	Oct 78 Oct 78 Nov 78	

ONE BOARD HOME COMPUTER

ETI, Transam and Mike Hughes, who designed the system, present the Triton — a one board computer that includes all the features expected in a machine providing the basis of a really powerful home system.

ADD A STANDARD domestic TV set and a cassette recorder to the TRITON and you have a complete home computing system that is equal to, indeed in some areas superior to, many of the commercial ready built systems now on the market.

The TRITON has been designed on a single board, which means that construction should not pose any problems providing an adequate standard of soldering is maintained throughout. The case, designed specifically for the TRITON, means that the finished unit can safely and attractively be housed. In use, with the TV set on top of the case, the TRITON will be easy and convenient to operate.

The TRITON is based on the 8080

MPU, a device which has proven itself over a number of years. This MPU has a vast amount of software available for it and the TRITON's 1K monitor system allows for easy entry and subsequent modification of such material.

The 2K TINY BASIC that is also resident in the TRITON, allows this popular, easy to learn, language to be used in conjunction with the TRITON's versatile graphic character set and unique VDU function to develop everything from games to education programs quickly and easily.

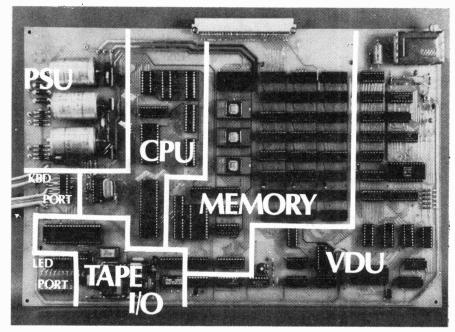
The TRITON has space for 3K of user RAM on board but the machine has been designed in order to make expansion a simple matter. All the

signals necessary to add further memory, I/O devices etc. are brought out to an edge connector at the back of the board.

It is essential to use a top quality double sided plated through board for the project. Unlike many projects the PCB is likely to be the most expensive single item you have to invest in but it is this component which brings the whole project into scope for the average constructor with no significant theoretical knowledge.

The board has been designed to keep all the most intricate wiring on the top side - in particular the connections that run between IC pins. The latter are the most vulnerable to a heavy hand on the soldering iron but this is not saying that you can afford any carelessness underneath! Use the smallest soldering iron you can lay your hands on and the bit must, certainly, be no greater than 3/32in diameter. As stated, all soldering operations should be carried out on the underside of the board; the through hole plating will route all necessary connections to the topside.

Wherever possible it is worth trying to re-inforce the through hole plating by getting molten solder to creep through the hole by capilliary action, therefore hold the soldering iron in place long enough for the heat to flow through the hole and take the solder with it. A couple of seconds longer than your usual soldering time should suffice. You will notice that on the underside of the board there are hundreds of IC pin lands that do not appear to be connected to anything. These lands must be soldered in all positions because nearly all of them go somewhere on the top side!



The single board that carries all of the Triton's circuitry with the areas concerned with various parts of the system indicated.

See John Coll's comments on the Triton in Computing Today



Transam Components Ltd of 12 Chapel St, will be sole suppliers of the Triton and will also supply individual parts for the computer.



Construction Commences

Take your time with the soldering — even at a slow pace you can complete this project in a couple of days — because it is very easy to miss a connection or produce a dry joint. We recommend that you insert one component at a time and solder it in completely before moving on to the next; a visual check of each joint is essential and if you have any doubt don't be afraid to use a magnifying glass. A few seconds wasted doing this can save hours — if not days — trying to find a single missed connection!

All the holes on the board have been pre-drilled to the correct diameters but in the event of you having a device which will not quite go through the hole do not UNDER ANY CIRCUMSTANCES attempt to drill out to size - you will ruin the through hole plating! The ONLY holes you may drill out are the fixing holes for the board and the mounting holes for the extender socket. If you have a stubbern component try scraping down the diameter of its lead with a sharp knife or use a needle file to reduce its dimensions slightly. Probably the only offender you will find in this respect is the modulator which has rather large fixing lugs that sport a taper. These

might vary a little from device to device.

We recommend the use of sockets for all the integrated circuits as it is virtually impossible to remove ICs from a double sided THP board.

Start construction by soldering in all the DIL sockets while the board is flat — it makes life much easier — and then insert all resistors and diodes. Next insert the nine board pins which connect to the transformer and IC1 (the off board voltage regulator). Proceed to solder in the in line strip sockets and the extender socket. When the latter is firmly soldered you should carefully drill out the board mounting holes with a drill using the connector's holes as a guide and then bolt it firmly into place.

Switched On System

Insert the three transistors for the tape I/O. Procede then to the capacitors and LEDs. Leave the three large smoothing capacitors till last and be very careful that you insert the LEDs the right way round. You will have to look very careful at the solid tantalum capacitors to find their polarity. You should then insert, and solder in the three preset potentiometers.

Before progressing further check

the polarity of all the diodes and electrolytic capacitors you have inserted.

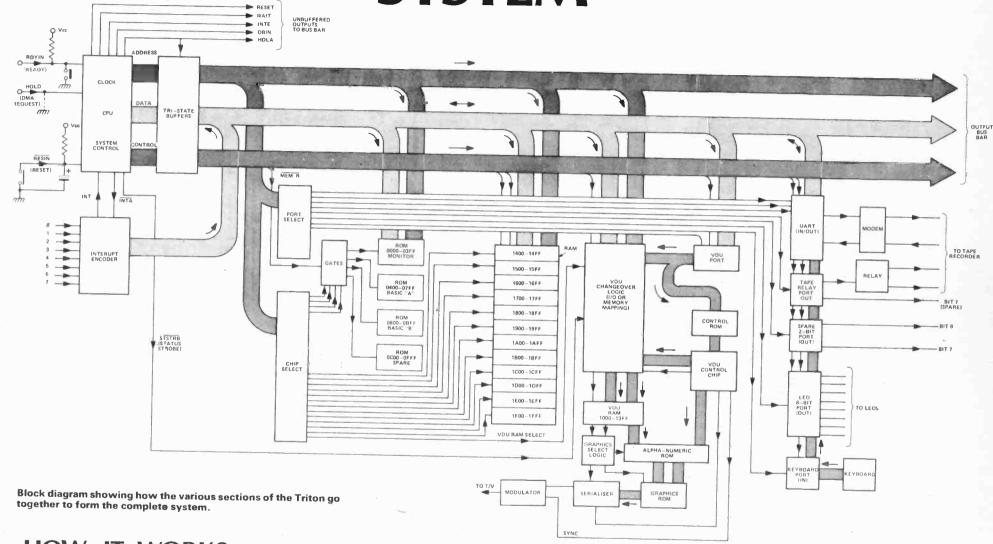
You can now insert, and solder in the three crystals making sure you have them in the correct positions. The crystals have their frequencies stamped on them (usually in kilohertz).

Continue with construction by putting in the modulator and the two on board regulators. Make sure you have the regulators in the right position. Ensure that you insert them the right way round. The metal fin should be on the face of them furthest away from the main smoothing capacitors.

Temporarily mount IC1 on its heatsink and run flying leads to the three pins allocated to it.

The great moment is close at hand but before inserting any integrated circuits give the power supply a dry run. Connect up the remaining six board pins to their corresponding terminals of the transformer and apply power. Use a voltmeter to see that you have the correct voltage rails present. You should get +5 V and +12 V at the output pins of ICs 1 and 2 respectively and -12 V at the output of IC3. You should read -5 V

SYSTEM PROJECT: Computer



-HOW IT WORKS

The heart of the system is the microprocessor (MPU) itself - the faithful old 8080A. This MPU has a very simple to understand instruction set which is remarkably versatile for those who like to dabble in work at machine code level and because of its years of experience there is a great variety of software freely available to use with it. In addition it is one of the cheapest MPUs on the

The MPU will sequence through a list of

These eight lines are decoded to activate any one of 256 possible external devices through what are called PORTS.

Before moving on from the heart of the system it is worth mentioning some of the single lines depicted on the illustration. When the computer is initially switched on it is necessary to give it the right instruction to start with so that it can sequence on from there to complete the program in a sane manner. For this reason it is usual to have the operation on the VDU screen and to do a re-initialisation without clearing all the memory (which would otherwise happen if one pressed the reset button). There are five remaining lines one of which is brought out to a spare push button on the front panel and the rest are piped down the multiway socket along with the busbars. The interrupt request lines have to be encoded and formatted into an eight bit data byte. When this is done the interrupt encoder tells the CPU with the INT

decoding the least significant eight bits of the address bus) through the Port Select logic and issues a I/OR control signal will data from the keyboard be placed on the data busbar. Working in the opposite direction, the Output Port driving a bank of eight on board LEDs is a set of eight latches which catch and hold whatever data is on the busbar when they receive a coincident pair of signals from the port selector and the I/OW line of the control bus. These onboard LEDs instructions held in memory as 8 bit bytes and on receipt of each instruction will carry out an operation which ranges from getting another byte of data from somewhere else in memory to carrying out simple logical or arithmetical operations on that data. It is not within the scope of this article to cover the inner workings of the MPU itself or, for that matter, to explain every operation that the 8080 can offer.

As it operates sequentially the MPU needs clock. In this case the master frequency is 7.20MHz which is divided down to clock the MPU every 1.25uS. This time is the duration of a microcycle and it takes from 4 to 11 microcycles for the MPU to complete an instruction.

The MPU itself has quite a large number of lines leading to it. The 8 data lines are in the form of a bi-directional busbar (i.e. can carry data to or from the MPU). To cut down on the number of wires coming from the MPU the data bushar serves a secondary purpose. It carries what is called "STATUS" information at a certain point in time within an instruction cycle. This status information is in the form of an 8 bit byte and is decoded by the System Controller. When decoded the Status byte feeds one of 5 lines with a locigal "o" which tells the rest of the system what sort of instruction the MPU is executing during that cycle. These lines are grouped together to form the CONTROL BUSBAR and are designated INTA (meaning that the computer has just been interrupted by an external "Interrupt Request"), MEMR (reading data from a memory location), MEMW (writing or storing - data into an internal memory location), I70R (inputting data from an external source - such as a keyboard or a tape system) and I/OW (outputting-data to an external destination such as a VDU or a tape system).

The 16 lines which carry a 2 byte WORD which is used to ADDRESS a specific byte of memory form the uni-directional ADDRESS BUSBAR. Using 16 binary lines one can therefore address up to 65,536 (decimal) memory locations. We have limited the capacity of the TRITON to 8K of memory but the address busbar (in common with the data and control busses) are buffered and can be fed to the outside world through a multiway connector thus allowing easy expansion to maximum capacity with add on boards.

The address bus also serves a duplicity of roles depending on whether the instruction cycle is a memory addressing or an I/O addressing cycle. As already stated all sixteen lines are used to address memory locations but during an I/O read or write cycle the CPU is limited to providing address data on the eight least significant address lines.

first instruction at address location zero. We can reset the MPU by depressing a push button or at switch on by the POWER ON RESET.

Those that want to can use the line marked HOLD for applications involving DMA (Direct Memory Access). Basically this means that by making this line go to logic "l" one can isolate the internal CPU from all three busbars (using the tristate facility of the buffers) and allow an external device to do what it will with the internal memory. We have strapped this line to "O" with a removable link so the facility is there for those who want it. RDYIN is used if any memory of peripheral is incapable of responding as fast as the computer desires. The external device can make this line go to "O" for any period of time (usually set by a monostable) and when this happens the MPU goes into a WAIT state and it does just that. It simply stops operating as long as this line is low and when the RDYIN signal is removed it carries on as if nothing had happened. The only thing it does do during this time is issue a signal to the outside world called WAIT. You can see the WAIT line designated as one of the unbuffered outputs. In addition by connecting RDYIN via a push button switch to ground one can halt the computer momentarily in the middle of any operation. Facility for bringing this out to a push button is not made on the board but it is a simple matter to pick up the right point on the top side and take it via a single wire to the front panel see the circuit diagram of this section.

The RESET output goes high momentarily when the rest button is pressed and can be used to carry a synchronous reset on external equipment; the HDLA output tells the outside world that the computer has gone into a HOLD (or DMA) state — if anyone takes the HOLD Line high; the INTE Line tells the outside world that the computer is permitting itself to be interrupted (the mnemonic stands for Interrupt Enabled) and the DBIN line indicates which way the computer expects data to be flowing on the bi-directional data bus. It goes high when the CPU is expecting data to flow INTO it.

We are using the STSTRB (STATUS STROBE) signal — to synchronise the memory mapping of the VDU — more is said about this in the relevant section.

As already implied the 8080 will allow itself to be interrupted in mid program provided that the program sets the Interrupt Enable flag. There is facility for eight possible interrupts but only seven can really be used on this machine (Interrupt 0 is redundant as it duplicates RESET). An interrupt is entered into the machine on a single interrupt request line. Of the seven usable lines we are using two within the machine to do a clearing

signal that an interrupt has been received. When the CPU is ready to be interrupted it issues an Interrupt Acknowledge signal INTA which is used to place the encoded byte on to the data bus. This byte enters the MPU and directs the computer to operate the desired subroutine. At the end of the routine the computer reverts to the main program continuing at the point where it was interrupted.

The memory of TRITON is split into three types on the main board. There are locations for up to 4K of Read Only Memory (ROM) which is split between four 2708 Erasable ROMs. These occupy address locations OOOOH to OFFFH. The standard TRITON uses the first 1K to hold Monitor and Utility routines necessary to initialise the machine and re-vector interrupts. The next 2K holds a BASIC INTERPRETER and the fourth 1K block is left spare for future expansion.

There is 1K of Random Access Memory dedicated to the VDU. This starts immediately above the ROM area starting at 1000H. Normally this RAM is addressed in synchronism with the VDU line scan by the VDU control circuitry but the CPU can take over addressing under program control (in effect interrupting the VDU). The VDU RAM can only be written into by the computer.

The rest of memory is made up of RAM which is both read and write. This area is used to hold the stacks and tables of the MONITOR and BASIC INTERPRETER (512 bytes) and the main work area starts at 1600H for a further 2½K ending at 1FFFH. This represents the full capacity of the on board memory. There is no reason, however, why further read write memory should not be added externally starting from location 2000H.

The ROM and VDU RAM areas are blocked into units of 1K — to fall into line with the types of integrated circuits used. However, the stack and work area RAMs are laid out in blocks of 256 bytes.

The high order lines of the address busbar are used to decode which block is being addressed — this is done by the Chip Select decoder. Note that the ROM chip selects are gated with the MEMR signal from the Control Bus whereas this control signal and MEMW go straight to the RAM chips. This is because the 2111 Random Access Memory ICs used have internal chip select gating and output enables.

With the exception of the VDU which is "hybrid" the rest of the system is made up from a variety of I/O stages. The most important of the latter is the Keyboard Input. The keyboard data and strobe lines are fed on to the data busbar via tri-state buffers which form the keyboard input port. Only when the computer's software addresses this port (by

help to make the TRITON system more versatile and can be used for test purposes or in specialised development applications. The LEDs themselves could be discarded and the eight lines brought to the outside world as a spare general purpose output port.

By making use of a couple of spare latches on the board it was possible to provide two spare output lines on one port and a spare line on the port which also feeds the tape

recorder power control relay. The UART (Universal Asynchronous Receiver/Transmitter) is the device which converts the eight bit wide parallel data on the busbar to a specially formatted serial stream to feed the tape recorder modulator. It also carries out the complementary function of converting a received serial stream into parallel data bytes. The device operates as if it were two input ports and one output port. One of each sort of port would be obvious for a device which receives and transmits but the requirement for a second innput port may not be so obvious. Because the device operates asynchronously from the main computer (it has its own clock operating at 300 baud) it is necessary to make the computer wait from time to time to allow the slower operating UART to complete a transmission cycle. This is indicated by the UART activating a flag which is regularly

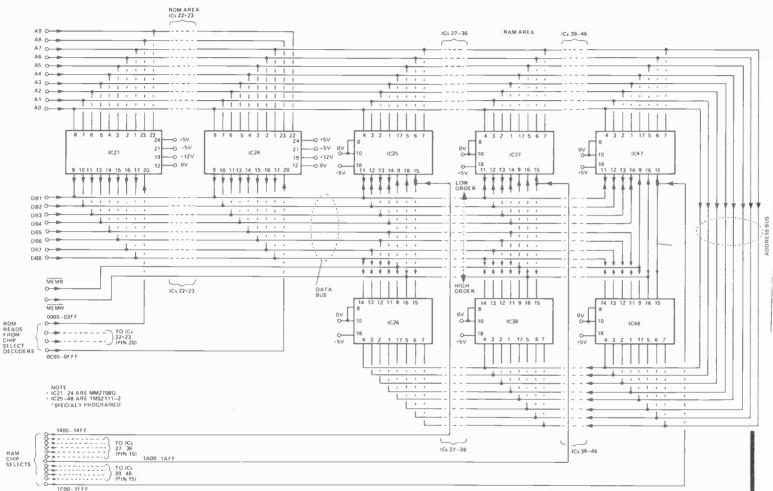
monitored by the second input port.

The VDU portion of the computer is based on the Thomson-CFS Control chip and operates in a unique manner for this integrated circuit. Not only can one output to the VDU through an output port (in similar manner to using a teletype) but one can use the computer to write data directly into the VDU's memory at extremely high speeds.

A further extension is the way the control chip has been used to handle Graphics. Instead of the usual six bit wide RAM seven bits are used in this VDU application. This way enables the use of the complete set of ASCII codes. 64 extra character codes are therefore available by using those normally associated with lower case "alpha" characters and all the control codes. Within the overal context of the computer some of the control codes serve dual purposes and the VDU control ROM inhibits printing a graphic when a control code is issued for genuine control purposes!

The graphic select logic looks at the two most significant bits of the ASCII code, determines whether or not the symbol is graphic or alpha-numeric, then proceeds to select the standard alpha-numeric ROM or the specially programmed graphics ROM. There is quite a lot of extra logic associated with this operation as well as the Memory Map/IO changeover but we shall reserve comment on this to the section describing the circuit in detail.

= RAM&ROM — PROJECT: Computer



Circuit diagram of the ROM and RAM circuitry. Note that in the basic machine IC 24 is omitted as are ICs 33-48.

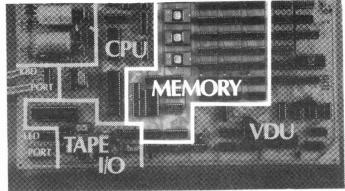
HOW IT WORKS

The circuit diagram of this section has been abbreviated as most of the memory circuitry is a repeat of the same theme. You can clearly see the difference between the ROMs and the Read/Write RAMS. There are four of the former - all 2708s but in the standard machine only three are used immediately. The 2708 is an ultra violet erasable ROM which contains 1,024 (decimal) bytes of memory each being 8 bits wide. To access a specific byte within it you need a 10 bit address and A0 through A9 are used for this purpose. The eight output pins are tri-state which are enabled by a "0" on pin 20 (the chip select input). The respective outputs from each of the ROMs can therefore be commoned together on the data bus. The "Programming Enable" pin (18) is only used when the devices are being programmed and therefore is left disconnected within the system. We use the block select signal gated with MEMR to provide the Chip Select strobe for the ROMs (this is described elsewhere).

The Monitor program is located within IC21 which starts at address location 0000H so that the computer will always go through a firmware initialisation routine when switched on. The Power On Reset ensures that the first instruction the CPU reads will be the one located at 000H. BASIC is located within ICs 22 and 23.

The RAM area of memory comprises TMS 2111-2 chips. These each contain 256 locations that are four bits wide. As we need to store eight bit bytes of data two chips are required for each 256 byte block of memory. The odd number designations IC25 to IC47 correspond to the low order nibble of the byte while the respective even numbers (IC26 to IC48) correspond to the high order. Only eight address lines (A0 through A7) are required to uniquely select a byte within this organisation of a chip pair but we need to specify which pair by means of the Chip Select lines (these have been decoded elsewhere in the system).

The 2111s have internal chip select and Read/Write gating so we are able to drive the MEMR and MEMW inputs direct from the control busbar.



at the junction between R1 and the zener diode. If all is well here; systematically check that you have the correct voltages at the sockets of **every** integrated circuit. Use the schematic diagrams to help you identify the pin numbers.

Finally check that you have inserted the single wire link to the right of the extender socket.

Insert all the integrated circuits making absolutely sure that you have them orientated correctly and have them in the correct locations. Use the dot on the UART to locate pin 1 (the notch can be misleading). Note that the orientation of ICs varies a lot on the board and you must check each one individually. Insert the 2708 EROM chip that is marked MONITOR V4.1 into the socket for IC21; the one marked BASIC L4.1 "A" into the socket for IC22 and BASIC L4.1 "B" into IC23. Insert eight TMS 2111-2 devices in IC locations 25 to 32 inclusive. The only gaps you should have on the board are the IC24 and ICs 33 to 48.

Do not bother with a keyboard at the moment but simply make up a coaxial lead to go from the modulator to the aerial socket of a standard 625 line television set. Switch the TV on and allow it to warm up checking that a raster is just visible and tune it to approximately channel 36.

Set the three on board potentiometers to their mid way

The table shows the decimal and hex codes associated with the Triton graphics and, where applicable, the key on the keyboard. The symbols may be used within a BASIC print statement or with the OUTCH monitor routine.

positions and apply power to the TRITON. You should see some change on the television screen even though you may not be spot on tune. Try adjusting the tuning over the whole range until a strong signal is locked in. You should see the welcome message:

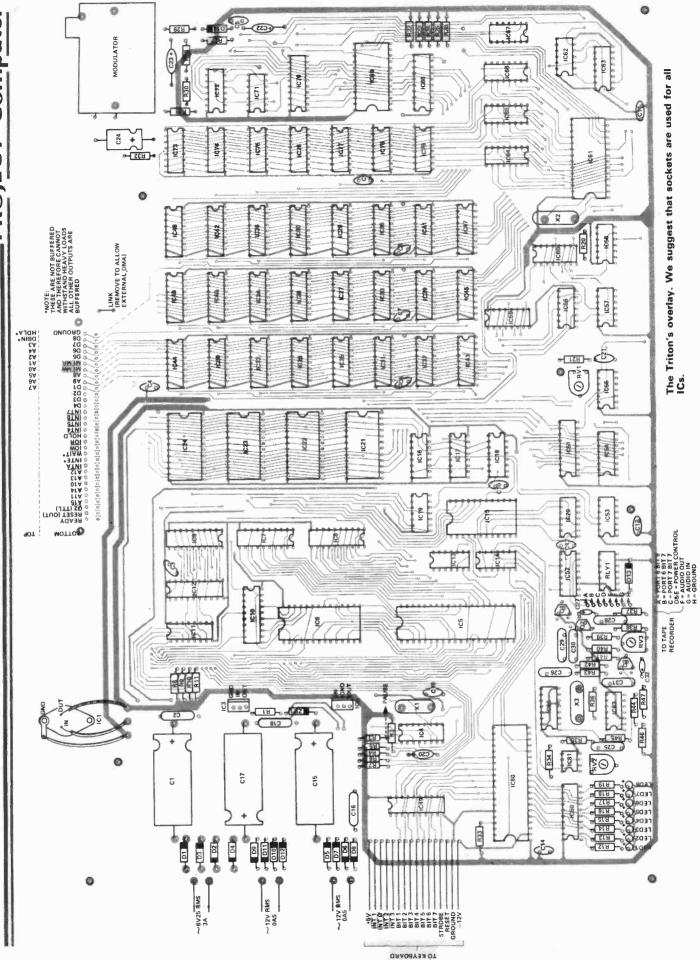
TRITON READY FUNCTION: PGIOLWT

It may respond with INVALID as the keyboard is not fitted — do not worry this is still an indication that everything is working.

Hopefully this will be the case and you can rest assured that your computer is working! Switch the computer off; wait a few seconds and switch it on again. For a fraction of a second you will see a load of rubbish on the screen which will rapidly clear and the previous message will be repeated.

Switch off and make up an umbilical cord of wires to go from the keyboard socket on the board to the keyboard and associated push switches. Use colour coded wire and ensure that you make no mistake when connecting the relevant leads to the keyboard Cinch connector. It is double sided and you must make sure to hold it with the correct

GRAP		DEC.				N. Servery				ON
	1 2 3	0 1 2	00 01 02	CONT A	01	12	23	34	GRAPHI	ICS
	4 5 6 7 8 9	3 4 5 6 7 8	03 04 05 06 07 08	CONT E CONT F CONT G	02	13	24	35	45	35
	10 11 12 13 14	9 10 11 12 13	09 0A 0B 0C 0D		E	States and			THE PROPERTY OF THE	व्यक्त
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	22	21 22	15 16	CONT V	04	15				
even une. His	24 25 26 27 28	23 24 25 26 27	17 18 19 1A 1B	CONT W CONT X CONT Y	D. D	16	27		48	
113	29 30 31 32 33 34	28 29 30 31 96	1C 1D 1E 1F 60 61	a	06	17	28			e e
D o n	35 36 37 38 39 40	98 99 100 101 102 103	62 63 64 65 66 67	b c d e f	O7			Ü	SO SO	60
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bish clear	47 48 49 50 51	110 111 112	6E 6F 70 71 72 73	n o p q r	09			`42	52	62
n the the and	53 54 55 56 57	116 117 118 119 120	74 75 76 77 78 79	t u v w x			32		53	63
e ads r. It is e	59 60 61 62 63	122 123 124 125 126	7A 7B 7C 7D 7E	z RUBOUT	10	21	33	44		



PARTS LIST-

RESISTORS (all 1/4)	W 10% unless stated)	IC17, 18 IC20	74LS139 74LS02
11201010110 (011)41	rv 1070 dilless statedy	IC 21, 22, 23, 24	MM2708Q
R1 R2, 34, 44	82R ½W 10% 10k	IC25-48 IC50, 51, 68 IC52	2111-2 74LS374 74LS75
R3-19, 22-26, 30, 33, 35, 37, 38, 40		IC53 IC54	74LS74 74S287
46, 47 R20, 36	1k0 4M7	IC55 IC56, 57, 71	74LS132
R21, 31	220R	IC59, 60, 64,	74LS08 74LS157
R27 R28, 29	470R 330R	65, 66 IC61	SFC96364
R32	15R	IC62	74LS86
R39 R41, 45	4k7 220k	IC63 IC69	74LS163 RO-3-2513
R42, 43	100k	IC70 IC72	74S472 74LS165
POTENTIOMETERS	6	IC73-79	2102-2
RV1	100R sub. min. horiz.	IC80 IC81	AY-5-1013/TMS6011NC 555
RV2, 3	preset 10k sub. min. horiz. pre-	IC82 IC83	MC14412VL LM339N
1	set	D1-4	1N5400
CAPACITORS		D5-12 D13, 14	1N4001 1N4148
C1, 15, 17	4 700u 25 V electrolytic	ZD1 Q1, 2, 3	5V1 400mW BC148
C2, 16, 18	470n polyester	LEDs 1-8	LD35Y (0.1 spacing)
C3-14 C19	47n ceramic 47p ceramic	TRANSFORMER	
C20, 23, 27, 32 C21	47u 6V3 tantalum 82p ceramic	, Т1	12V+12V at 0.5A, 8V25
C22 C24	10u 6V3 tantalum		at 3A
C25, 29, 30	100u 25 V electrolytic	SWITCH	
C26, 28 C31	100n polyester 220n polyester	PB1	DPDT Mains
SEMICONDUCTOR	RS	PB2-5 CRYSTALS	SPST
IC1	LM323K	X1	7,2000 MHz
IC2	LM340T-12	X2, 3	1.0000 MHz
IC3 IC4 IC5	LM320T-12 8224N	MISCELLANEOUS	
IC6 IC7, 8, 10, 49	8080A 8228N 74LS244		Reed Relay type, 15005, s holder, modulator (Astec
IC9	74LS245	type 1111E36), Fu	III ASCII Keyboard, 64 way
IC11 IC12	74LS148 74LS240	16 way inline PC	et (optional) Type CS/CP64, CB plug and socket Type
IC13, 58 IC14, 19, 67	74LS00 74LS32	A23-16, 8 way in Type A23-8, edge	nline PČB plug and socket connector to suit keyboard,
IC15 IC16	74LS154 74LS138		ets, IC holders and heatsink

orientation or you may have disastrous consequences with the power lines. Different types of keyboards have different connections. We refer you to the connection details supplied with your keyboard. The only comment we should make is that the specified keyboard, and some others, give you an option for bit 6 of the data. One option gives you upper case characters only while the other gives both upper and lower case. This application needs the latter. The strobel something back on the screen press is the static strobe which goes to "1" as long as a key is depressed.

Procedure

The specified keyboard does not have any built in direct function keys and these have to be provided by separate push buttons. These have to be mounted on the front panel and are used to provide RESET, INT1 (Clear Screen), INT2 (Reset without clearing memory), INT3 (Spare) and TAPE MANUAL OVERIDE — ganged with PAUSE (see descriptions elsewhere). The first four push switches all have a common ground and are "push to make" with a spring return. Use the Common lead and the respective signal leads to go to each of these switches. The fifth switch must be double pole "push to make - push to break". One pair of contacts should take the special "PAUSE" line to ground when it is on. This line does not exist in the umbilical cord coming from the board socket but must be soldered to the end of R3 going to pin 3 of IC4. The other pair of contacts is connected across the tape power control pins of the respective DIN socket.

You can make up all the above on flying leads to test the unit fully before putting it into its cabinet.

Power up again and get the initialisation message. Try pressing any key on the keyboard EXCEPT PGIOLW or T and the computer should respond by saying INVALID. Press CONTROL C and the screen should clear and re-initialise. Press RESET. When the button is released the same should happen. Try INT2 and the machine should, again, reinitialise. When you try INT1 the screen should clear without the message appearing. To get any keyboard key except those in the "key character" message (P.G.I.O.L.W.T). You should, once more, get INVALID. Depress CONTROL Conce more and your computer is re-initialised and ready for test.

Program

We must assume at this stage that you do not know anything about programming so simply follow the instructions and check that you get what is described.

Depress P on the keyboard. You will get:

PROG START =

(The computer is asking you to tell it the address of part of memory you wish to inspect) Type in 0000 followed by carriage return.

PROG START = 00000000 31 (31 is the data in location 0000)

The display will now show:

Depress carriage return repeatedly and you will get the following as you step through the Monitor program instructions

P PROG START = 0000 0000 31 0001 80 0002 14 0003 FB etc

Reinitialise with CONTROL C and then type L. The computer will again ask you for a start address but this time will list out the contents of 15 adjacent locations starting from that address. We can use this to test that our memory is there and working in the RAM area.

Answer the computer with the address 1600 and a carriage return (if you make a mistake before you press CR you can backspace with CONTROL H and change an entry but you must then type through the rest of the line on the screen). The computer will list the contents against the memory addresses and then stop and ask for "MORE?". If all is well you should see 00 in all locations. To continue type Y and keep doing this checking all the locations up to the highest order RAM on the board. Above that address the computer will read FF which indicates that there is no memory there. If you see any data above address 15FF that is anything other than 00 or FF you can be sure you have a bad connection to the RAM IC which contains the data in question. This test only holds true immediately after first initialisation and cannot be used if you have attempted to write programs.

To get out of LIST type any character other than Y and the computer will reinitialise. Carry out this or any of the other reset procedures already described and procede to check the G function. This is to facilitate running a machine code program. The computer will acknowledge

G RUN PROG START=

(this means it is ready to run but wants you to tell it from where in memory it should get its first instruction). Give it this information by typing 02B9 followed by CR. You will actually be running a re-initialisation program in the Monitor which should just acknowledge with



The Triton's board mounted in its case. Note that the extender socket is available on the right hand side of the case and that the output of the modulator is brought out to a UHF socket on the back panel. The back panel also carries the DIN sockets and the mains fuse.

FUNCTION? PGIOLWT

You are now back where you started so you can try typing W which turns the computer into nothing more than a video display typewriter. You can type away to your heart's content testing out all the alpha numeric and graphics characters using the keys in unshifted, shifted, and control mode. Do this while inspecting the coding tables shown in the section describing the VDU and get used to the cursor move commands. Type a full line of characters and adjust RV1 for best line length. To get out of this mode of operation use CONTROL C or any of the other methods of resetting.

The next test sees BASIC L4.1 in action; depress T. The computer acknowledges with

T BASIC L4.1 OK >

Type in NEW followed by CR to make sure the memory is cleared and the computer re-acknowledges with the BASIC header. Very carefully

type in the following message line by line with a CR at the end of each line. Remember you can correct by backspacing with CONTROL H before you hit CR.

>10 FOR A = 1 TO 10 >20 PRINT "HELLO" >30 NEXT A >RUN

You should not re-type-the 'greater than' prompt signs — the computer is prompting YOU with these. When you press CR after typing RUN we hope you will be surprised — you have just written your first program!

You can now be pretty well assured that your computer is working correctly and it only remains to test and adjust the Tape I/O circuits. This must be done in stages.

First check the Tape Output software. Connect an audio monitor (simple amplifier or crystal earpiece) between the 'Tape Out' socket on the board and ground. You should hear a continuous tone. Call up BASIC by typing T and enter the above program again. Once you have done this get back to the Monitor without erasing your BASIC program

(use CONTROL C). Now press 0 to call up the Tape Output routine.

The computer will ask you for a TAPE HEADER which can be anything you like written in alpha-numerics. Preferably do not use a title longer than 20 characters as you might run out of input buffer space! We suggest you type in TEST ROUTINE. Follow this with CR while listening to the tone on the ear piece. Nothing will happen on the VDU but after a pause of between 5 and 6 seconds (longer if you are using a master clock crystal lower than the 7.20MHz as specified) you will hear about 1 second of regular high speed pulses followed by a few seconds of what can best be described as 'burble'' (this is your program going out). The burble will stop and you will hear just the continuous tone you heard at the beginning. After a further 5 or 6 seconds the VDU will confirm that the file has finished by displaying END followed by the re-initialisation heading.

On A Plate

Repeat this excercise but this time connect a continuity meter across the tape power control sockets on the board. (The manual overide switch must be open circuit). While you type in the tape header code the meter should show that the relay is open circuit but as soon as you depress the CR to start the operation the relay closes and stays closed until the VDU types END. It is obvious that the 5-6 second delay at each end of the routine is to allow a portion of blank tape to go by to reduce the chance of you overlapping files or missing the start of the active tape at the beginning of a new cassette.

You must now set the Baud rate for your system. The simplest way is to use a frequency meter connected to pin 3 of IC81. Adjust RV2 until the meter reads exactly 4800 Hz. A better way, and probably more viable for most constructors, is to use a standard test tape. It is better because different tape recorders might operate at different speeds which would influence the play back baud rate of your system. This does not matter if you are only recording a playing back your own programs but if you wish to use those from other sources your overall system MUST operate at 300 baud. Using a standard test tape calibrates your overall system to 300 baud as viewed from the outside world.

Monitor Manipulation

To carry out this test properly you must have a master clock crystal having a frequency greater than 4.5MHz otherwise the VDU may not print out as fast as the data is coming in from the tape. You must also enter and run a special machine code program to facilitate the test. We will not explain how the program operates in this article except say that it accepts any data on the tape and displays it, verbatim, on the VDU. If garbarge is received and decoded garbarge will be printed. The test tape contains the alphabet followed by CR and Line Feed repeated many times over a period of a few minutes. All you have to do when the program is running is set RV3 to its midway position and adjust RV2 until you get the alphabet reliably repeated on the screen. If, at the best setting of RV2 you still get the occasional bit of rubbish try altering RV3 for best sensitivity. You should, of course, be using the phono output from your tape recorder but if you do not have this use the extension speaker socket with the volume set about 20% up from minimum.

TRITON Trials

Carry out the following instructions TO THE LETTER!

Initialise the computer with RESET; type in P and enter the start address for the program as 1600. For zero always use 0 and not o. Press CR and location 1600 will be shown to contain 00. Now use the memory change facility to start writing your program. Simply type in the following list of hexadecimal instructions - each pair of digits should be followed by CR. You will end up with a column showing address locations to the right of which is a column showing what was in that location (should have been 00 in all cases) and to the right of that the new data you have just typed in. When you have typed in the complete list of instructions use CONTROL C to re-initialise then type L and list from location 1600 (as previously described). Check that the codes in each location correspond exactly with those in the published program. Use CONTROL C to re-initialise and then type G. Enter 1600 without pressing CR at this stage. Make sure your tape recorder

is properly connected to the board and switch on the recorder in PLAY mode. Press CR and procede to adjust RV2 as previously described. You should see:

ABCDEFGHIJKLMNOPQRSTUVW-XYZ ABCDEFGHIJKLMNOPQRSTUVW-XYZ ABCDEFGHIJKLMNOP etc

until the recording ends or you switch off the tape recorder. While this is happening your computer is locked within a program loop and you will *not* be able to get out of this with CONTROL C. You will have to use INT2 to re-initialise.

Here is the program you must type

Address location Data you must enter

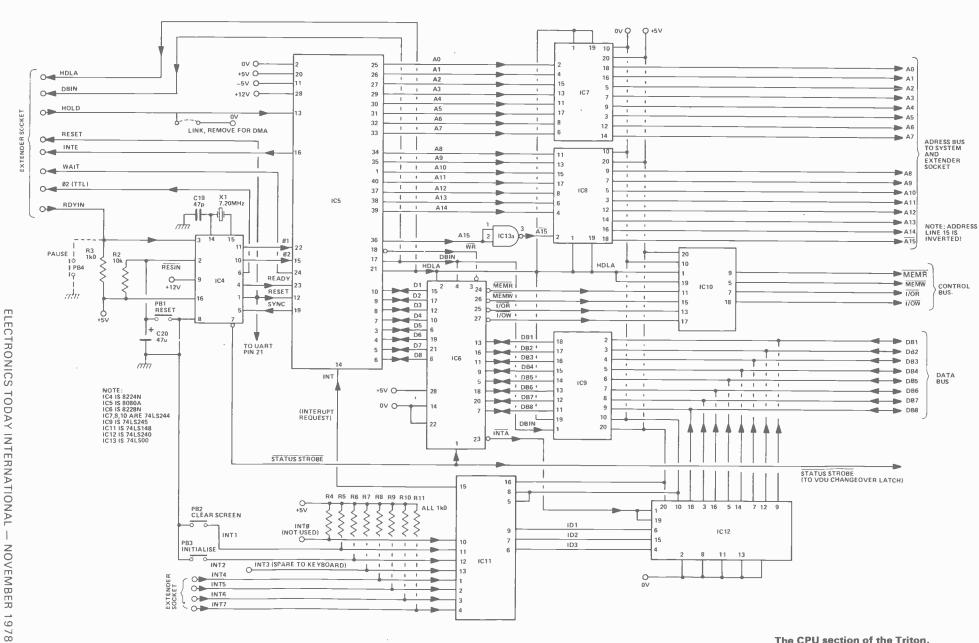
1600	CD
1601	27
1602	03
1603	CD
1604	1 D
1605	03
1606	CD
1607	13
1608	00-
1609	C3
160A	03
160B	16

Your computer is now completely set up and ready for use. You have already been shown how to enter and run simple programs in BASIC and Machine Code. Why not now read the further articles in the Supplement which will show you how to make more full use of the TRITON. You have made an extremely powerful computer whose applications are only limited by your own imagination and the development of more sophisticated software — coupled with extender boards to give you extra I/O functions (Floppy Disks, Line Printers, extra Tape Recorders, more Memory etc). Keep reading ETI for further exciting applications and developments.

The following pages contain the circuit diagrams and descriptions for the complete Triton design. 'How It Works' sections refer to the diagram they accompany.

Computing Today carries an article on using the Triton's BASIC and a review of the machine by John Coll.

A fuller description of the Triton's monitor will follow in next month's Computing Today.



OW IT WORKS

IC4 is the master clock oscillator which contains divider circuits to provide the two phase clock (\$\psi\$ 1 and \$\psi\$2) for the 8080. You can use different frequency crystals for X1 but the ideal value is 7.20MHz and this value should not be exceeded. Lower frequency devices are fine but the system will operate proportionally slower. If you put in a higher frequency crystal not only will you run into memory access time problems but the system will be operating at a rate faster than the VDU can handle. The Monitor program has provided the maximum permissable print out rate for a clock frequency of 7.20MHz.

A TTL compatible output of \$2 is available but not used on the main board; this is fed to the extender socket. The chip also contains gating circuits to synchronise the externally generated RDYIN command before feeding this to the CPU. An internal Schmitt Trigger on the reset input line (RESIN) allows a very simple charge up circuit comprising R2 and C20 to provide power on reset. Manual reset is carried out by momentarily taking RESIN to 0 volts via a push button. The clock receives a feedback signal (SYNC) from the CPU which is gated with \$1 to give a STATUS STROBE pulse at the precise moment the data busbars are carrying the status byte. The pulse (STSTB) is fed to the System Control chip (IC6) to latch the status byte and is also used by the VDU to enable Memory Mapping changeover.

C19 discourages the crystals from harmonic operation. This shifts the operating frequency by about 10Hz but this is of no real significance.

A description of the inner workings of the CPU (IC5) is beyond the scope of this article. It's general operations will however become apparent as this How It Works is read.

Note that certain outputs (namely HDLA, DBIN, INTE and WAIT) are taken to the extender socket directly from the CPU. These are unbuffered and account should be taken of this if you expand the system. Each line will adequately drive a single TTL load and maybe a handful if you use low power devices.

The HOLD line going to pin 13 of the 8080 is not used within the main board and is used to carry a DMA request which, via the HDLA signal puts all the busbar buffers into a high impedance state. This could facilitate a take over of the complete memory of this system by a peripheral device or, possibly, another computer. Normally this line should be at logic level "O" so we have hard wired it thus with a board link. This link MUST be removed, or a switch substituted, if use is made of this line!

RDYIN is normally held at level "1". If taken to "0" it causes the CPU to stop operating. Nothing happens as long as the signal is low and the contents of all internal registers within the MPU are maintained. When the signal returns to "1" the MPU carries on operating as if nothing had happened. By taking pin 3 of IC4 via a push switch to ground we have a ready made. "PAUSE" control which will enable the TRITON to stop in mid program; say, the middle of long high speed VDU output to inspect the screen.

The chances are very high that you will not need RDYIN for external systems so the feature could be built in permanently. Note should be made that it is bad practice to have a push switch hard wired to ground on this line if at any time in the future you derive the RDYIN signal from a gate. Press the button and bang goes the output stage of one

innocent gate. IC6 is an 8228N 8080 System Controller which gates out the five main control busbar signals from the status byte at the time of STSTB and holds these on latches. The chip also comprises a set of bi-directional buffers for the data busbar; the direction of these buffers is controlled by DBIN and their outputs are disabled on the receipt of a DMA request by the HDLA signal. We were not happy that this buffer alone would be capable of supporting a fully extended system hence a further buffering stage in the shape of IC9 (74LS245). Like the System Controller the latter chip is supervised by the HDLA and DBIN signals. Integrity of any DMA request is maintained on the data bus.

ICs 7 and 8 are uni-directional tri-state buffers which should allow the address busbar to feed a fully extended system. Note that we have inverted A15 prior to putting it on the bus. By doing this we have been able to economise on chip select decoding circuitry elsewhere in the main board system. This

should present no problems to anyone working with extender boards provided that this fact is remembered.

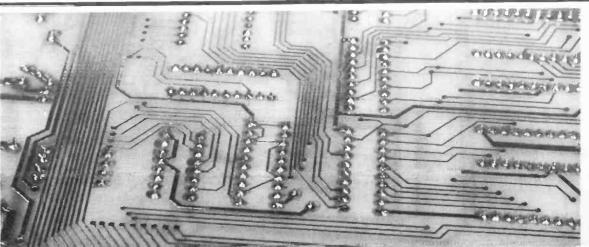
Remember, you must disable the push switch in this mode, that is why we have shown it dotted in and why Transam have not built this facility into their PCB in an obvious way. Why not use common sense and make use of this extremely valuable facility - all that is needed is the cost of 20cms of wire! You do not even need another push switch because you can use a spare pair of contacts on the Tape Control Manual Overide. It does not normally matter if you press this button provided the cassette recorder is switched off with its own control.

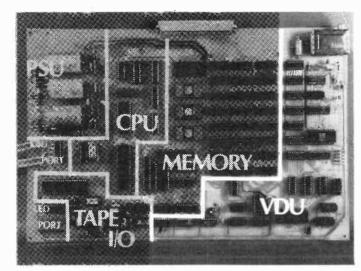
IC11 is the Interupt Encoder which has eight lines going in to it. These are normally held high by pull up resistors R4 to R11. The encoded three-bit nibble is output at pins 6, 7 and 9. If all the inputs are high all the outputs and status information. The MPU then are high and a "0" is placed on the Enable operates on the interupt routine and returns Output line at pin 15 (the latter is used to generate the INT signal — Interupt Request - to the CPU). If any single input is pulled to 0 volts, via the push switches or external logic, an equivalent code to describe that line number is output as the Interupt Data Nibble and pin 15 goes high telling the MPU that an interupt has been requested. The MPU will carry on operating until it reaches a perissable point in it's cycle to service the interupt. When this point is reached the MPU outputs an Interupt Acknowledge signal (INTA) through the status byte which is decoded and latched by the System Controller. This signal is used to activate the Output Enable of IC12 (an eight wide tri-state inverting buffer) which formats the ID nibble to make an eight bit Interupt Data byte which is then accepted by the CPU as a RESTART instruction. The program counter jumps to one of eight fixed locations in memory - the location is defined by the ID byte — while the STACK preserves all current register data

to its main program when it comes to an RET instruction.

Interupt 0 should not be used even though it is available on the PCB. It simply duplicates the manual reset operation but would create problems if used with the TRITON's Monitor program. INT1 is dedicated by the Monitor to provide a Clear Screen and Reset Cursor facility which can be carried out at any time. INT2 is also a dedicated function. The Monitor includes memory test facilites as part of the power up routine and use of the reset button will clear all memory. To by-pass this problem we are using INT2 as a nondestructive reset which, as far as any programs that are running are concerned, is just like reset and the system will re-initialise but the memory will not be cleared. ALWAYS use INT2 for reset unless one of your programs has corrupted the Monitors stack!! Only then should you press manual reset or carry out a Power On Reset by swit switching the machine off and on.

Photo of the underside of a section of the Triton's PCB. Note that although it appears that there are no connections to some IC pins — ALL pins must be soldered as these pins are used on the topside of the board.





HOW IT WORKS-

IC61, the Thomson CFS VDU control integrated circuit, has a built in clock which generates standard TV synchronisation pulses (line and field sync) on pin 26. Random interlace is used and a simplified field sync train is generated as opposed to the full. CCIR specification.

The chip, synchronously with this train of pulses, generates addresses for the VDU RAM so that the correct code of the character is selected as the TV raster spot is traversing the respective part of the television screen. An external "Picture Point Oscillator" (IC55c and d) in conjunction with a divider chain (IC63) sets the horizontal width of a character and steps the address of the control chip, output from pin 12(IC63) to pin 9 (IC61). The inverted output of IC63 pin 15 is used to latch the data being addressed by the controller into IC68 (a seven wide latch), latch the picture point pattern generated by the character generator ROM into the serialiser (IC72) and reset the picture point divider chain (IC63) at the end of each character width.

The picture point width (hence the character width and number of characters per line) is set by the frequency of the oscillator control RV1.

We are using a 7 bit wide RAM to hold the FULL ASCII code — we need this to provide capacity for graphics. The outputs of the latches feed both the standard alpha-numeric character generator (IC69) and a specially programmed ROM (IC70) which contains picture point data for the 64 graphic symbols. We use the EXCLUSIVE OR function (IC62d) on bits 6 and 7 of the ASCII code to select either the graphics or alpha-numeric ROM. The select signals go through further gating (ICs67a and d) to ensure that the integrity of the cursor generating pulse (pin 15 of IC61) is not corrupted.

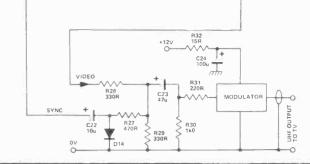
Three further address lines from the VDU controller (pins 11, 12 and 13) address the picture point data ROWS in both ICs 69 and 70. Due to a limitation caused by the internal operation of IC61 chip the row address code 000 is output for the top row and the bottom four rows of the character cell. Normally rows 0, 8, 9, 10 and 11 are used to provide inter line gaps for alpha-numeric displays while rows 1 to 7 carry alpha-numeric picture point data. We have had to take this into account when designing the font of graphics symbols - some of which cannot fill the complete character cell rectangle on the screen. Look at the table of graphics characters and you can see how we have adjusted the graphics to suit this restriction.

Further complications caused by this limitation are that a graphic must not appear on the topmost line of the television screen if that graphic contains picture points in its top row. IC61 requires there to be zeros present here in order to derive field blanking. This problem could be overcome with extra gating but this would have been at the expense of simplicity.

A similar problem (involving line blanking) is resolved by gating the video output with the INI function (pin 26 of IC61) in IC71b. Without this any graphics symbol having a picture point in its most left hand column would have caused a "wrap around" while a line that interferes with the DC level of the line sync pulse. The only problem that remains in this respect is that you will now get a single "extra" picture point showing to the right of the 64th character down a line if you use a graphic in the most left hand position of a line. This does not happen with all graphics — only those that have picture points in their most left hand column.

The five outputs from the alpha-numeric ROM are wire ORED with five of the eight

VDU section of the Triton.



outputs from the graphics ROM and held high via pull up resistors R22 - 26. They are then fed to the correct positions in the serialiser shift register IC72. Note that the remaining three outputs from the graphics ROM have to be ANDED with a signal defining whether or not the character is a graphic (done by ICs71a, c and d). This is to ensure that if alpha-numerics are printed there is a correct inter-character gap.

So far we have avoided talking about how the VDU RAM is addressed by the control

chip. Let's deal with that now.

We are allowing the CPU to memory map the VDU RAM. To do this we have had to allow the MPU to take over addressing control of the VDU RAM. This is done by taking all the address lines from IC61 and their equivalents from the system's busbar to a set of data selectors (ICs64, 65 and 66). If the MPU addresses the VDU memory location (any address between 1000H and 13FFH) the block select line (MAP VDU) is activated. This of course, could happen if ever the address busbar went into a high impedance state (during HOLD etc) so to prevent any spurious pulses affecting the operation we gate the VDU block select line with STSTB which only occurs when valid address information is on the busbar. We do the gating in a D type latch so that during the complete cycle of a VDU memory map the data selectors are set to allow the computer address bus to be transmitted to the inputs of the VDU RAM. At the end of that cycle and at all other times the data selectors hand over address control to IC61

A similar transfer of responsibility takes place between the normal input data to the VDU (which gets to it via an output port) and the main system data bus. In this case the data is selected by ICs59 and 60. These also receive their changeover instruction from the changeover latch IC53. Note that we also have to do a changeover between the internally generated memory write command (pin 17 of IC61) and the MPU's MEMW strobe. This is done within IC60.

It only remains to describe the gates on the VDUs internal data lines and IC54. The former are used to force the ASCII code for "Space" on to the data lines when pin 13 (IC61) is at "O" in coincidence with a writing

pulse to the VDU memory. This is to allow for the very useful internal function provided by the IC61 to clear the screen and reset the cursor in one operation.

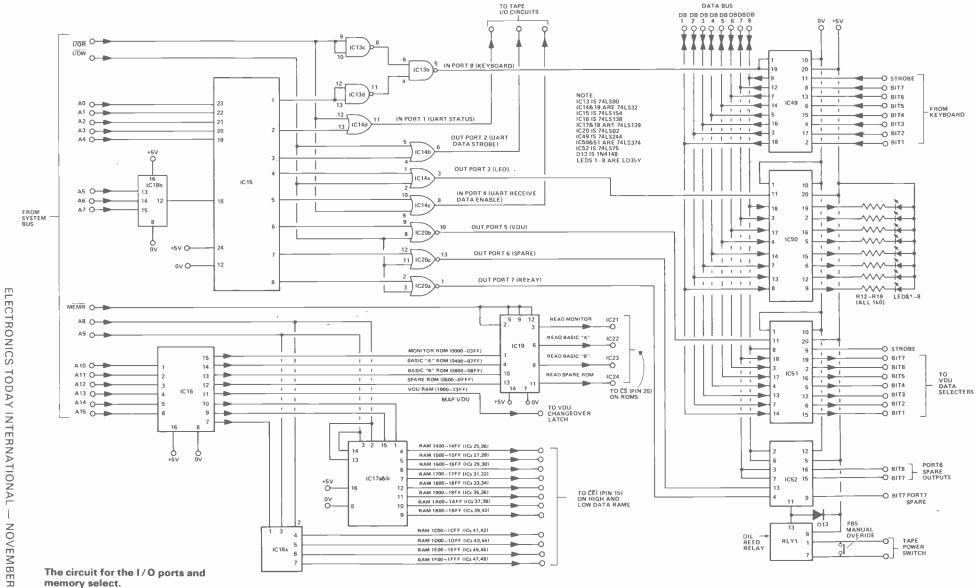
The VDU controller carries out a number of non writing functions as well as entering and addressing data within its memory. By using some of the ASCII codes as control it is possible to do such things as move the cursor in steps to any position on the screen, reset the cursor, carry out a line feed or do a carriage return clearing only the unused part of the line. There are also a couple of control codes that we wish the VDU to ignore - OOH and O4H - respectively these are NUL (or no operation) and EOT (end of text) flags. Recognition of all these special codes is carried out by the VDU CONTROL ROM (IC54). This has had to be specially programmed for the TRITON.

To get best use from the TRITON and its VDU you need to know hexadecimal and decimal values of all the ASCII codes that are used to generate alpha-numerics, graphics, and control characters. You also need to know which of the keyboard keys correspond to each graphic character. To help you we show all the graphics with their respective codes and key names in Fig. 00. Alphanumeric codes are shown in Fig. 00 and the

control codes in Fig. 00.

Normally you may output a character to the VDU for printing in I/0 mode every 8.3mS. The standard TRITON monitor errs on the safe side and has a built in delay which outputs a character roughly every 9mS. If ever you write your own software you must take this speed limitation into account. Furthermore there are two I/O operations which take a considerably longer time: these are "Clear Screen and Home Cursor" and "Home Cursor". These instructions must be followed by a delay of at least 132mS. Again the TRITON's monitor makes allowance for this but you can get direct access to these functions if you use either the "PRINT CON-TROL" or "VDU" commands which exist in BASIC LA.1. If you use these in BASIC you MUST follow them with a delay loop having a time constant greater than 132mS. (In practice we found that a 200 step "FOR -NEXT" instruction was quite safe.)

= KBD PORT —— PROJECT: Computer



9 ∞

HOW IT WORKS-

During an INPUT or OUTPUT instruction cycle the MPU will generate the address of the I/O port required on the least 8 significant bits of the address busbar. This has to be decoded to provide a single line signal which will activate the port. It is not sufficient to provide this address on its own because there is no way that the port can tell whether the select signal has come from a genuine port select instruction or whether it is the low order byte of a memory read/write cycle. Furthermore there are times within the machine cycle when the address busbar can be in a transient, or high impedance state which could cause indeterminate address information to be decoded by the port select circuits.

To prevent these problems and also to differentiate between input and output ports the decoded port line is gated with either the I/OR or I/OW control line. One or other of these lines goes to "O" after the ports select address has been placed on the busbar and terminates BEFORE the address data changes. This pulse is of the correct duration to strobe the I/O data on the data bus into or out of the port in question. Take, for example, the control of the Keyboard INPUT port. The port itself is simply an eight wide set of non-inverting tri-state. buffers permanently connected to the data bus.

Pins 1 and 19 enable the output of the port when they go to level "O". Normally these pins are at "1" and held there by the output of IC13b and keyboard data cannot affect the data bus. IC18b and 15, between them allow 16 lines to be uniquely decoded from address bits 0 to 7. We only use 8 ports on the main board so part of this facility is redundant hence not all the outputs from IC15 are used. IC18b is a 2 to 4 line decoder operating as a 3 input NAND gate. The reason for this is that the device was one left over in a half used package and its use avoided having to put in an extra IC just for the sake of one 3-input gate. When address OOH is present on the bus pin 1 of IC15 goes low which points to Port 0 (the Keyboard). This signal is ORED with I/OR by IC13c, d and b so when there is coincidence IC49 receives "0" on pins 1 and 19. Whatever data is coming from the keyboard is transmitted on to the data bus and then accepted by the CPU as genuine input data. The reason for using three NAND gates to provide the OR function is again to use spare capacity in partly used ICs.

While on the subject of the keyboard port some might question the use of only ONE port for the keyboard instead of having a second one to check the status. We get around this apparent deficiency in the INCH (Keyboard Input) sub routine of the monitor. Interconnections with the keyboard put the 7 bits of ASCII on bits 1 to 7 and instead of parity we are using bit 8 to carry the keyboard strobe. Output port 3 works in similar fashion. IC15 decodes its address on pin 4 and IC14a ORs it with, in this case, I/OW. The resultant pulse is used as a clock to the D type latches within IC50. The data is entered into the latches on the rising (trailing) edge of the pulse. Using the trailing edge does not matter here. There is just sufficient current sinking capacity in a 74LS374 (IC50) to drive a small LED direct through a 1k0 limiting resistor. The byte of data is therefore transferred from the busbar to the latches and displayed in binary fashion on the LEDs. Note that the LEDs are illuminated when a "0" is output.

The VDU, when operating in I/O mode, is situated at PORT 5. This works in much the same way as the LED port but we are using a NOR gate to give a positive going port enable pulse. Bits 1 to 7 carry ASCII data and bit 8 the VDU strobe which is formatted to have the correct timing characteristics by the OUTCH (VDU Output) sub-routine of the

Monitor program.

A further output port was required to switch the relay of the tape recorder power control (to effect automatic starting and stopping of the tape). Theoretically a single bit port was all that was required but as things turned out in the design this would have required a new integrated circuit (there were no spare latches left over anywhere else!). Because of this it was felt sensible to use a 74LS75 (IC52) which contains four latches connected as two pairs. This way we were able to provide a tape control signal to the relay at pin 11 (the Q output of one latch) by using data bit _ and this left a spare line on that port (bit 7) which can be used by the experimenter as an output line. The port to call for this line is number 7. At the same time the other pair of latches in IC52 are used as OUTPUT PORT 6 which comprises bits 7 and

8. These are also spare.

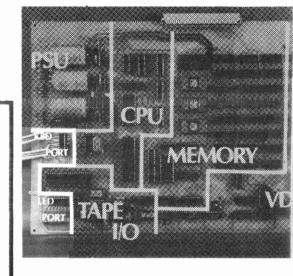
As we've moved on to the subject of tape control take note that there is a push button switch connected across the relay contacts. This is to allow manual override so that the cassette recorder can be rewound etc. under manual control without having to unplug the remote control lead. See the relevant section for more details about the serialiser I/O ports and MODEM for the tape recorder.

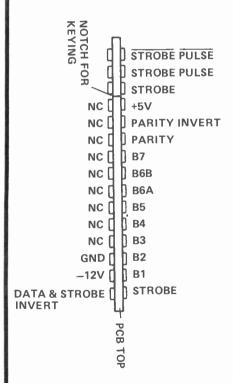
The memory of TRITON comprises four 1K blocks of ROM, one 1K block of VDU RAM and twelve 256byte blocks of Read/Write RAM. The high order addresses are used to decode individual lines which enable each block while low order addresses point to a specific location within the previously decoded block.

IC16 is a 3 to 8 line decoder but we are able to use it to decode, uniquely, eight individual blocks of 1K from the six most significant address lines. This is made possible by using A15 in inverted form and the internal gated Select inputs of the 74LS138. The four lowest order selected lines correspond to memory blocks which start at 0000H, 0400H, 0800H and 0C00H respectively and these hold the MONITOR, BASIC "A", and BASIC "B" read only memories. The block starting at 0C00H is a spare block reserved for ROM expansion. The line decoded at pin 11 of IC16 addresses the block of VDU RAM and the remaining three lines are fed to three 2 to 4 line decoders ICs 17 and 18a along with address bits A8 and

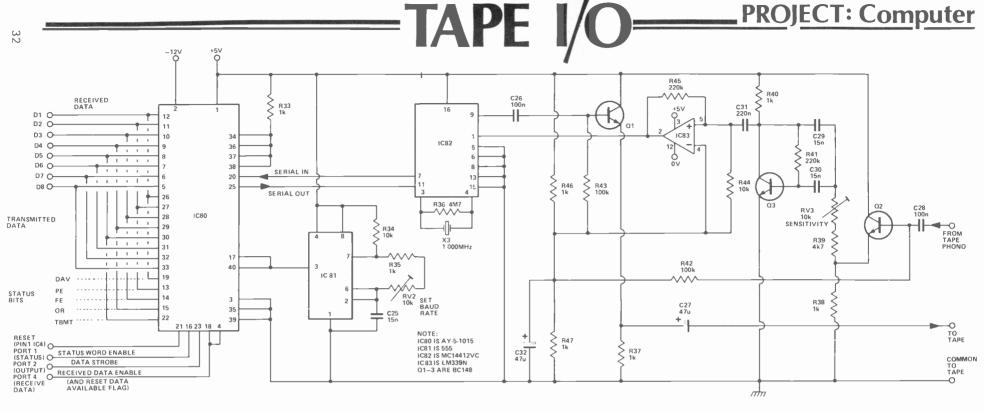
The latter three decoders break down the remaining 1K blocks into 12 blocks — each containing 256 bytes. Each of these 12 lines goes to a specific pair of random access memory integrated circuits that form the main work area of the computer.

Except for the ROMs, gating with MEMR and MEMW is carried out within the memories themselves. The 2708 read only memories only boast a chip select input and it is necessary to gate the MEMR control signal with each of the chip select lines prior to making connection with the appropriate pin. This gating is carried out by the quad 2 input OR gates contained within IC19.





The connection details for the keyboard recommended for the Triton.



The tape I/O section of the Triton system.

HOW IT WORKS

The AY-5-1013 Universal Asynchronous Receiver transmitter features tri-state outputs for received data and all status bits. Note that respective bits of the data in and data out terminals of the chip are commoned together before joining the TRITON's data bus. The Status bits of the UART are similarly commoned with the DAV (Data Available) bit tied to bit 1 on the bus; PE (Parity Error) to bit 2; FE (Framing Error) to bit 3; OR (Over Run Error) to bit 4 and TBMT (Transmitter Buffer empty) to bit 5. Note however that TRITON's standard Monitor only samples DAV and TBMT.

The DAV and TBMT flags are used to tell the system when the UART has received and has ready a complete byte of new data or when the UART has finished a current serialising cycle and is ready to accept a new byte for transmission. In actual fact the UART will accept a second byte while it is still transmitting the first due to the double buffering nature of its transmitter buffer.

should be set on a frequency meter.

In order to transmit data the TRITON Monitor first checks to see whether the UART transmitter buffer is empty by activating the STATUS WORD ENABLE which is, in effect, PORT 1. This places the status word on the data bus and the MPU checks to see whether bit 5 (TBMT) flag is at "1". If so it indicates that the UART is ready and the Monitor then outputs its data on to the busbar while activating the DATA STROBE (PORT2), DATA STROBE starts the transmission serialising cycle and the serial data is output to the MODEM (IC82) at pin 25. If the TBMT flag was at "0" the Monitor goes into a loop and waits until the UART is ready.

In order to receive data the MPU asks for status information, again through input port 1 but this time checks bit 1 (the Data Available flag). This goes high as soon as a complete serial byte has been received and formatted into parallel form in the UART's

byte of data is received. Clearly the software cycle, which carries out this operation, MUST have a shorter loop period than the period between one received byte and the next otherwise overrun errors will occur.

The Motorola single chip MODEM seemed highly attractive from the word go as it is extremely economical on external components and needs no adjustment.

The MC14412VL is such a versatile chip that it was again difficult to decide which mode it should be used in. Eventually, in order to have a frequency pair that would give best reliability with most tape recorders and to allow the MODEM to receive at up to 600 baud (not that this is used at present) we opted to go for the USA standard "originate" mode in which the transmitted frequency pair is:

MARK ("1") = 1,270 Hz SPACE ("0") = 1,070 Hz

Clearly we need to be able to demodulate the same pair of frequencies so have to The MODEM interfaces directly with the UART and only needs a crystal and resistor to lock it to the correct frequency pairs. It is most important that a crystal of exactly 1.0000MHz is used here otherwise you will not be able to use pre-recorded tapes! The transmitted carrier of the MODEM is an eight level digitally synthesised sine wave of about 300mVrms which is buffered by TR1 before being fed via C27 to the tape recorder phono input.

To carry out a demodulation satisfactorily the MODEM IC requires a very precise unity mark/space waveform at pin 1. The tolerance on the mark/space ratio has to be better than ±4%. If the carrier being played back from the recorder carries any harmonic distortion this will result in an asymmetric sinusoid which will be difficult to convert to a square wave of the above specification. To further purify the sine wave it is amplified and filtered by Q3. To some extent the input sensitivity can be adjusted by RV3 but under

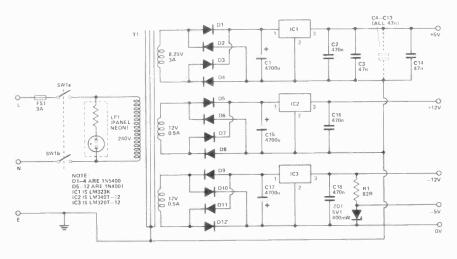
The format of serialised data in TRITON is a START bit, 8 data bits, a parity bit and 2 STOP bits. These are transmitted at a rate of 300 baud set by the clock comprising IC81 (an NE 555). Baud rate is adjustable by about ±50 percent by means of RV2 and, of course, it is important that this is accurately set if tapes from other sources (recorded in TRITON's format) are to be played back. To obtain a rate of 300 baud the oscillator must run at precisely 4800 Hz and ideally this

output latches. The MPU will loop until this condition is met. When the flag goes to "1" the MPU uses port 4 to send a "Received Data Enable" strobe to the UART. This enables the outputs of the receiver buffer latches and places the data byte on the system busbar. To prevent the system reacting a second time to the same DAV flag the pulse from port 4 is also used to reset DAV which then stays low until a completely new

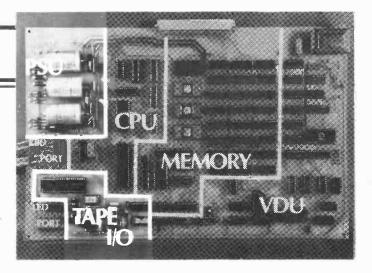
operate in Simplex mode hence pins 2, 10 and 14 of IC82 are allowed to be "1". Internal pull up resistors within the chip do away with the need for external pull ups hanging on these pins! Pin 2 actually is the "Self Test" control input which makes the MODEM's receiver demodulate the same frequency pair that is being transmitted. Keeping this active prevents any ambiguity as to whether one is "originating" or "answering".

normal circumstances (within the range of input voltages mentioned above) this should always be set in its mid-point position. The high purity sinewave at the collector of Q3 is fed to IC83 which is a zero crossing comparator which will sense the zero crossing of a sine wave to within about 3mV. With a good input signal this results in a square wave that more than adequately meets the input specification of the MODEM.

PSU



The power supply section of the Triton is based on three terminal regulators.



A close-up photograph of the PSU. Note the orientation of the IC regulators.

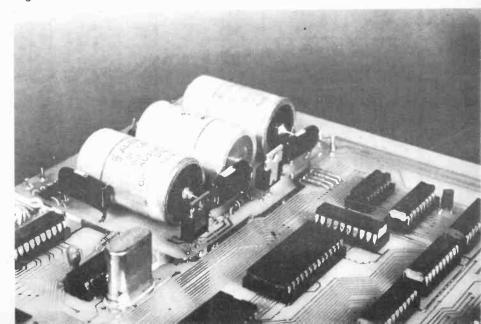
-HOW IT WORKS-

THE POWER supply has been kept as simple as possible, utilising three IC regulators to provide the main rail supplies which are +5 V at 3 A (the TRITON does not draw all this but do not rely on there being any to spare if you are thinking of hanging any other bits and pieces on this line!) +12 V at 0.5 A and -12 V at 0.5 A. A few milliamps are needed by the ROMs and the 8080 at -5 V and this is catered for by a simple zener shunt off the -12 V rail.

The \pm 12 V rails are straightforward. Dissipation by the regulators is low and no heat

sinks are necessary. The 470n capacitors on the outputs of the regulators are to prevent any parasitic oscillations. Note that the $+5~\rm V$ rail has a dozen 47n capacitors (C3 to C14) shunted across it. These are anti-spiking devices and have been placed in strategic places on the board.

To avoid excessive dissipation in the main +5V regulator (ICI) we decided on a specially wound mains transformer, hence the rather obscure specification for an 8.25 V winding.





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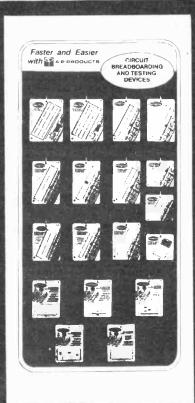


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Venus, the shrouded planet of Edgar Rice Burroughs and Ray Bradbury, has fascinated men and telescopes for many decades. It was the favourite choice to house monsters and the scientists' choice for life of a more mundane type. Conditions are not that favourable, however, but are still interesting enough to warrant the launch of the Multiprobe which should tidy up some of the mysteries remaining.



Fig. 1. The Pioneer Venus Multiprobe spacecraft; a thermal test model is shown.

THE TWO Pioneer spacecraft should reach Venus around the end of this year, being sceduled to rach orbit on the 4th December. One of these probes, known as the 'Orbiter', will circle the plant for at least one Venusian year. It will collect data on the upper atmosphere of the planet (including field strengths and the types of particle present) and will also record events occuring on a global scale on or around the planet over a fairly long period of time.

The other spacecraft will consist of a transporting vehicle, known as a 'Bus', which will convey one large probe and three small probes to Venus. All five parts of this spacecraft will enter the Venusian atmosphere at widely separated points and will transmist data back to earth. The four probes will fall to the surface of the planet and should provide much information about the lower atmosphere at four widely separated points.

Although Venus is our closest planetary neighbour, it is always covered in very thick cloud; our knowledge of this planet is therefore very limited, especially as regards its lower atmosphere. The early probes have shown that Venus has a high surface temperature and an atmospheric pressure nearly one hundred times that of the earth, but a great deal of work remains to be carried out. It is expected that the two Pioneer spacecraft will increase our knowledge of this planet by a factor of about ten. They will also greatly increase our knowledge of the solar system and are expected to provide much information which will add to our theories about the origin of the earth.

Pioneering Spirit

The Pioneer missions were conceived as long ago as 1970 as a result of recommendations made by the Space Science Board of the US National Academy of

Sciences who decided that there is a need for relatively low cost orbiter and probe landing systems for Venus investigations. Overall responsibility and control of the mission has been given to the National Aeronatuic and Space Administration (NASA) Research Centre at Moffett Field, California.

The Hughes Aircraft Company gained a contract to manufacture both space vehicles for the Pioneer mission in February 1974 after a series of competitions which started in 1972. The scientific instrument payloads were selected in June 1974, thirty instruments being included on the list. The spacecraft will be launched on top of Atals SLV-3D Centaur D-1AR rockets from Cape Canaveral, Florida. The vehicle tracking, command signal transmission and data reception will be carried out by the established US Deep Space Network stations in California, Spain and Australia.

The Multiprobe Mission

The Bus, the large probe and each of the small probes include payloads of scientific instruments. The Bus will be destroyed by burn-up in the Venusian atmosphere after its two instruments have transmitted data back to earth. It is, perhaps, somewhat surprising that work on the atmosphere and weather on Venus is expected to teach us more about the weather on earth.

The multiprobe vehicle is a circular, spin-stabilised craft with an array of solar cells around its exterior. The large probe will examine the atmosphere surrounding the planet, measuring the clouds, the atmospheric composition, etc. The three identical small probes will separate and enter the atmosphere some 7,000 miles apart two of them on the dark (night) side. They will collect information on the general circulation of the lower atmosphere.

Structure

The structure of the multiprobe unit is shown in the exploded view of Fig. 2. The cylidrical solar panel is 2.54 m (100 inches) in diameter and 1.22 m in length. The equipment shelf if 2.47 m in diameter, the electronic units and the scientific instruments being mounted on this shelf.

The large probe is at the centre of the spacecraft on an inverted conical structure, whilst the three small probes are symetrically placed around the main probe. Each probe is fixed by spring loaded clamps which can be released (pyrotechnically) about 20 days before the craft arrives at Venus so that the five sections move independently.

The probe weight, including the interfacing connection with the launching vehicle, is designed to be 920 kg. Great care has been taken in the thermal design of the craft to ensure that the temperature is kept between suitable limits; heaters and thermal blankets are included and appropriate materials with suitable thermal properties are used.

The control system employs a sun sensor and a solid state sensor which can detect the radiation from 24 stars. The vehicle contains two tanks which will be filled with 32 kg of liquid hydrazine propellant. When this liquid is allowed to pass into a chamber containing a suitable catalyst, it decomposes into nitrogen and provides a thrust of about 0.5 kg as a jet for controlling the spacecraft's trajectory, attitude and spin rate.

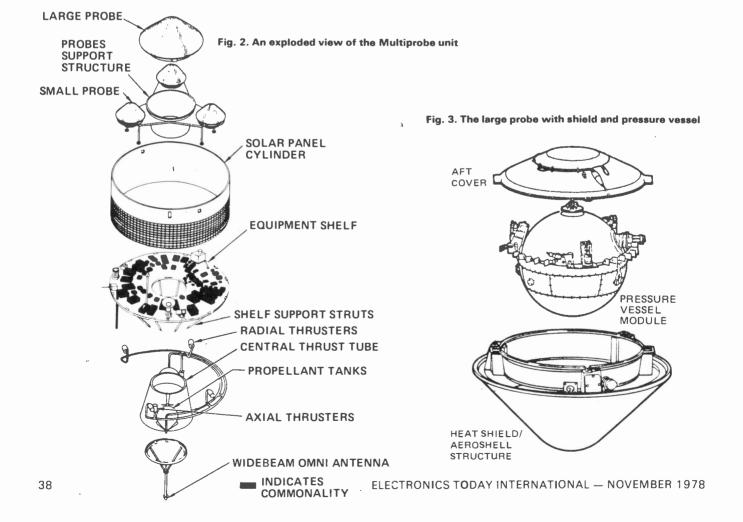
The power for the spacecraft is obtained from the cylindrical array of solar cells which has an area of just over 6 square metres. This provides 228 W when the

spacecraft is near the earth, but extra power can be obtained for a limited time from two 7.5A-hr nickel-cadmium batteries. The solar cells and batteries provide a 28 Vsupply; overload protection and undervoltage detection circuits are included in the power supply system.

Command signals are transmitted from the Deep Space Network ground stations to the Bus at 4 bits/second using pulse code modulation or frequency shift keying. The electronic on-board equipment can store command instructions for execution at some later time. Six command output modules on the equipment shelf can distribute 384 pulse commands and 12 quantitative (or analogue) commands to scientific instruments and to the spacefraft units. Commands from the earth stations modulated onto a 2115 MHz carrier wave are received by the spacecraft transponders.

Data for transmission to the ground is convolutionally encoded, assembled into 8 bit words in a 64-word frame and modulated into a data stream. Eight data input modules on the equipment shelf can receive the signals and establish up to 253 data channels with the telemetry processor for transmission to earth.

The data is transmitted on a 2300 MHz beam at a power of 10 or 20 W using one of three antennas and a data rate of between 8 and 2048 bits/second. The antennas comprise two omnidirectional types (forward and aft) to provide spherical coverage at both the transmit and receive frequencies together with a medium gain horn antenna at the aft end of the craft.



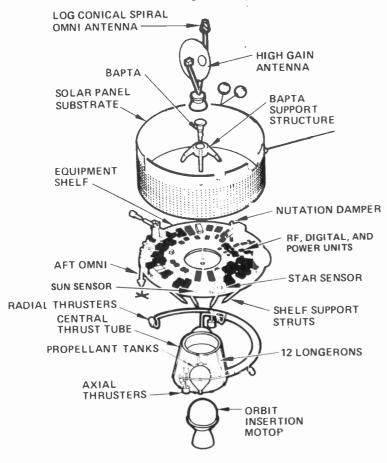
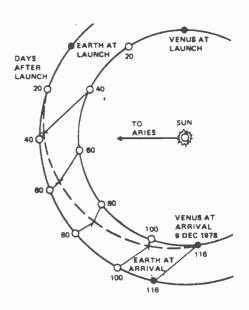


Fig. 4. An exploded view of the Orbiter spacecraft





To Boldly Go . . .

The launching vehicle will place the multiprobe space-craft into an earth parking orbit about 167 km above the earth where it will remain for 18 to 23 minutes before adopting the interplanetary trajectory shown. The spacecraft will initially be spinning at 5 RPM, but it is expected that contact with the ground station at Canberra will occur within four hours from launch and the rate of revolution will then be increased to 15 RPM by a command from the ground.

During the passage of the spacecraft towards Venus, the forward antenna will be employed to communicate with the 26 metre diameter dish aerials of the Deep Space Nwtwork. A velocity correction of up to 12 m/s can be made five days after launch and further corrections at 20 days after launch, etc. Command signals for these corrections will be transmitted from one of the huge 64 metre diameter earth station aerials.

The large probe will be separated from the Bus about 24 days befora arrival at Venus. The spacecraft axis will then be precessed so that the medium gain horn can be used for earth communication. A velocity correction of 5.1 m/s will be made to achieve the required small probe trajectory and the three small probes will be released about 20 days before reaching Venus. The spin rate will have been previously increased to 48.5 RPM so as to provide a suitable tangential velocity at separation for the small probes to acquire the desired trajectory.

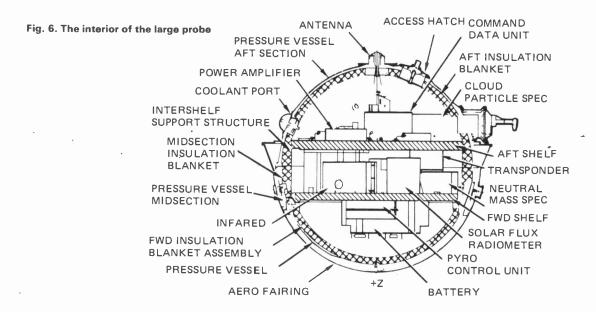
The velocity of the Bus will be corrected 18 days before its arrival at Venus to achieve the desired arrival point and to delay its arrival by 90 minutes so that all of the probes will have impacted on the surface of the planet by the time the Bus arrives in the upper atmosphere. Burn-up will occur at some 120 km above the planet.

All five vehicles will enter the atmosphere in a two hour period and all will be transmitting simultaneously, so the time of entry will be arranged to be one at which two of the Deep Space Network stations can simultaneously receive signals to avoid possible loss of data.

Large Probe Mission

The large probe is to be aimed at a point on the daylight side of Venus, decelerations of up to 400 g being possible at times during entry. The large probe parachute opens at a height of 67 km and for the next 18 minutes the probe descends under the stabilising influence of the parachute to a height of 46 km at which point the parachute is jettisoned. The probe then falls to the surface of the planet over a period of some 38 minutes.

The probe is not required to survive impact with the surface of the planet, but will withstand the pressure and temperature at the surface. This requirement together with the requirement that the probe can withstand the fierce acceleration presents many design problems unique to this mission.



The large probe and its deceleration module have a total weight of some 316.6 kg. The deceleration module provides thermal protection during atmospheric entry; it consists of a pointed nose cone of 45 angle with a diameter of 1.42 m. The base of the probe is thermally protected by a coated fibreglass aft cover.

The dacron main parachute has a diameter of nearly 5 m and is deployed by a much smaller pilot chute 0.76 m in diameter ejected by a mortar. The pull of the parachute extracts the pressure vessel module from the deceleration module.

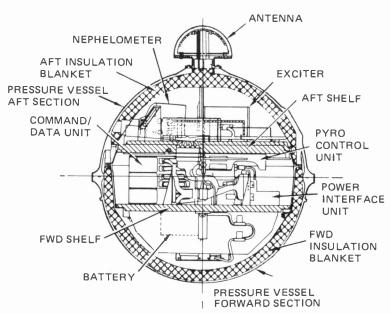
Pressure Vessel

This vessel contains nitrogen at a pressure of between about 0.5 and 2 earth atmospheres, but can withstand an external pressure of about 100 atmospheres. The 73 cm diameter titanium pressure vessel is constructed in three pieces and is about 6 mm in thickness. There are 15 apertures and 7.6 m of sealing are required to prevent gas leaks at the high temperature of the Venusian surface. The thermal insulation ensures that the electronics and instruments inside this vessel remain at a temperature not greater than 50 C even when the external temperature reaches 480 C.

A 19 cell 40 A-hr silver-zinc battery supplies power to the pressure vessel assembly. A total of 15 magnetic latching relays provide on/off control, whilst parallel fuses provide overload protection. Four solid state amplifiers, each rated at 10 W, feed a cross dipole antenna mounted on the rear of the pressure vessel which sends the data back to earth. A data rate of 128 or 256 bits/sec in a convolutionally encoded format is used, the system being capable of providing 72 data channels and 2 minor frame formats in an 8-bit word, 64 word frame. A 3072 bit memory provides storage facilities during the entry communications blackout; this blackout will have a duration of about 10 seconds.

The entire sequence of 128 commands is predetermined and programmed prior to the multiprobe launch. A timer with a 24.27 day capacity and a stability of \pm 32 seconds turns on the system prior to entry.

Fig. 7. The interior of a small probe



The seven scientific instruments in the large probe weigh a total of 35 kg and require 106 W for their operation. Three of these instruments require inlets for sampling the atmosphere and four require windows for viewing the atmosphere. All of the windows except one are made of sapphire, the exception being the window for the infra-red instruments which is a 13 carat diamond nearly 2 cm in diameter; diamond is the only material able to transmit infra-red in the 10 micron region and to withstand the temperature and pressure at the Venusian surface.

The Small Probes

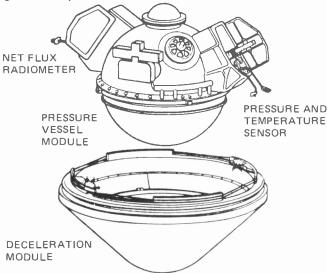
The three identical small probes are designed to measure the characteristics of the Venusian atmosphere simultaneously at three widely different locations. They are designed to withstand the high temperature and pressure at the surface of the planet, but need not necessarily withstand the impact with the surface. During entry into the atmosphere at a speed of about 11.6 km/s, a deceleration as great as 5652 may be encountered. The time of descent to the aurface will be about 59 minutes.

Each small probe contains a pressure vessel and a deceleration module. The total weight is some 97 kg. Unlike the large probe, there is no parachute with each small probe and the deceleration module is not detached during descent. The cone of the deceleration module has a diameter of some 76 cm.

The small probe pressure vessels which contain the electronics and the instruments are designed to operate with an internal atmosphere of xenon at between 0.25 and 2 earth atmospheres pressure. These vessels consist of a two piece titanium shell of about 46 cm diameter.

The small probes are each powered by a battery containing 20 silver-zinc cells with an 11 A-hr rating. Each probe employs a single, solid state power amplifier rated at 10 W RF output; this amplifier feeds a crossed dipole antenna mounted on the rear of the pressure shell. A stable oscillator maintains the S-band downlink frequency to 1 part in 10°. The data rate used from the small probe to earth is 16 or 64 bits/second, whilst a

Fig. 8. A small probe



3072 bit memory is used for storage during entry blackout and when the bit rate is being changed. A 24.27 day timer turns on the system prior to entry into the Venusian atmosphere.

The 64 bit/second data rate is used initially, but at an altitude of some 30 km above the surface the data rate is reduced to 16 bit/second to allow for the attenuation of the radio frequency signal as it passes through the denser parts of the Venusian atmosphere.

The Orbiter craft. Note the long magnetic probe to measure the ▶ magnetic field well away from any interfering field from the craft.

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The Orbiter Mission

The main aim of the Orbiter mission is to put 12 scientific instruments in orbit around Venus and to receive information from these instruments. It can be seen that the Orbiter spacecraft has much in common with the multiprobe vehicle, including a rather similar structure. Some of the most noticeable differences are the replacement of the probe structure by a high gain aerial system which can provide communication with the earth at distances of up to 250,000,000 km. A 4.5 m long magnetometer boom is also used in the Orbiter craft.

The size of the Orbiter spacecraft is similar to that of the multiprobe craft. The diameter of the cylinder of solar cells is the same 2.54 m, but the surface area of the cells is greater, being almost 7.2 m². The Orbiter is lighter than the multiprobe unit, being just under 600 kg and only 372 kg in orbit.

The slightly large solar cell area of the Orbiter provides a little more power than in the case of the Multiprobe Bus, this power being about 325 W in Venus orbit. Two 7.5 A-hr nickel cadmium batteris are also incorporated in the Orbiter spacecraft.

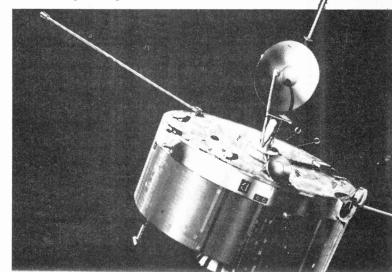
A bearing and power transfer assembly (BAPTA) serves an electrical and mechanical interface between the spinning part of the spacecraft and the despun aerial which must always point towards the earth. As in the case of the multiprobe Bus, 32 kg of liquid Hydrazine propellant is carried in two tanks and can drive seven jets, each with a thrust of about 0.5 kg, for the control of the trajectory, attitude and spin rate.

A solid propellant rocket motor, the Thiokol TEM-604, is to be used to place the Orbiter in Venus orbit. It has a velocity change capability of 1060.6 m/s for the maximum design weight.

Conclusion on Cost

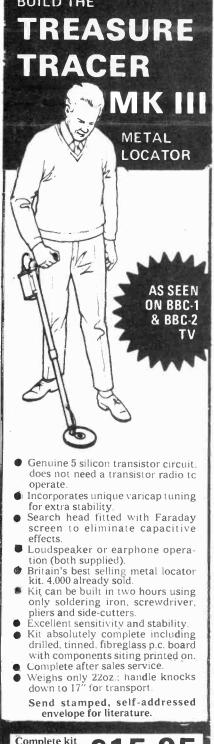
A special feature of the Pioneer missions is the relatively low cost for such an ambitious programme. In order to reduce the cost, no experimental prototype craft have been built — only the one multiprobe and the one orbiter will be made, tested and orbited. Economies have also been made by using the same type of components (such as the RF amplifiers) in the Bus, Orbiter and in the probes. Identical command and data handling circuits are used in all of the probes, whilst about 78% of the Bus and Orbiter parts are identical. The cost of developing the probes themselves has been relatively high, since they involve new techniques, whilst special facilities have had to be developed to simulate the hostile Venus atmosphere.

It seems likely that craft similar to the Pioneer type will be useful for relatively economical missions to Mars and for flying through the tails of comets.





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7440	.16	74LS05	.21	ME566	1.43	1/63	.08
7441	.90	74LS08	.21	NE567	1.62	2.2/63	.08
7442	.38	74LS09	.21	REGULA		3.3/63	.08
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7470	.36	74LS51		BC109	.12	33/63	.13
7472	.32	74LS74 74LS86	.32	BC177	.18	47/16	.09
7473	.21	74LS90	.33	BC178	.18	47/35	.12
7474	.26	74LS93	.86	BC179	.18	47/63	.15
7475	.29	74LS107	.34	8C184	.15	100/16	.11
7476	.31	74LS112	1.00	BC187	.32	100/35	.13
7483	.68	74LS123	.85	BC477	.24	100/63	.24
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7492	.42	74LS164	1.14	BFY50	.23	470/043	.14
7493	.29	74LS175	1.05	BFY51	.23	470/35	.18
7495	.51	74LS193	1.33	BFY52	.23	1000/16	.38
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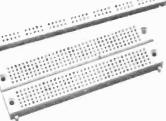
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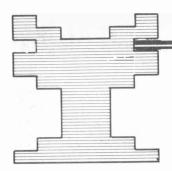
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TELEVISION

Last month we described the operation of the Tolinka chess recorder — this month we deal with construction.

LAST MONTH WE described the overall principles of the Tolinka Chess Recorder and in this final part of the project we shall describe the circuit from the hardware viewpoint and go on to give constructional details.

First inspect the board on both sides to see if any of the holes have been blocked by tinning. The easy way to clear such holes is to melt the solder and apply the sharp point of a pencil. Wipe the iron frequently on a damp sponge or cloth to avoid solder splashes.

A Small Step

The first step in construction is to make the through board links support the board 4mm approximately away from the bench surface by putting bolts in the corner holes. The side without the IC pads should be uppermost. Each of the small round pads which has a counterpart on the opposite side of the board is a pin-through whereby connection must be made through the board. A piece of wire must be inserted into each of these holes and soldered on both sides of the board.

The board should now be cleaned of flux with a cleaning agent and inspected against a strong light. Look for missed pin-throughs, solder bridges and lifted tracks checking with a continuity meter any suspected opens or shorts. Spend a lot of time at this stage because this is where faults are most likely to exist—it is possible for another observer to find obvious faults on a board which has passed a lengthy examination.

The ICs are inspected next.

Socket It To Me

Use the socket strip provided for any IC with more than 16 pins. The best way of socketing an IC is to push the pins into the socket strip and then trim off the surplus strip. Do not break off the pin carrier part of the strip until you are ready to switch on the power. This will keep the IC pins shorted together during the soldering

and assembly process. If desired socket strip or sockets may be used for the other ICs—and this is a wise precaution.

It is recommended that components be installed in the following sequence—first all discrete parts like resistors, capacitors and diodes; next integrated circuits and last the larger power supply capacitors and voltage regulators. Switches need not be installed until preliminary tests are completed and installation of the PCB behind the front panel has been carried out. The panel then forms a template which aligns the switches correctly.

Remember that the space above the board is limited and solder any bulky components beneath the board: this is certainly necessary for the power supply electrolytics. Leave the output pins of the voltage regulators unsoldered so that supplies may be checked without damage to the circuitry. Note that the power supply components are soldered directly to the tracks on the top of the board and only the wires of the electrolytics pass upwards from the underside of the board through holes. All voltage regulators are 'face down', the main 5 volt supply regulator being bolted to an area of circuit board which acts as a heat sink

Testing Time

Turn on the mains and test power supply voltages before soldering the regulator output pins down to the supply rails. Remove all ICs from their sockets and break off the pin carriers. Test voltages on supply rails again with the rails connected—do not of course fail to switch off the mains between tests. If all is well then instal the ICs and check the rails again.

Tune the TV set to receive a picture. There will be more than one picture available in the tuning range

and the best one should be found. If the picture has chessmen set up for the start of a game and move status information is correct then the printed circuit board may be installed beneath the lid of the box with the nuts, bolts and spacers provided and the keyboard switches soldered in place. If the device now functions correctly then attention may be turned to the cassette interface.

In an ideal world you could buy audio equipment which had standard sockets using standard signal levels at a standard impedence. This you could connect together with standard leads. The manufacturers of our world do not see things that way, however, and they make equipment with sockets, signal levels and impedances which are different from those of their rivals.

It will therefore be up to you, to decide upon these things as far as your own tape recorder is concerned. You might become involved in designing an attenuator to get things working properly. If you do not know how to do this and do not feel confident after reading the general remarks which follow, perhaps you really ought to be playing at something else.

The Ins and Outs

Outputs vary from millivolt level for a 5 pin DIN socket, but could be only available on a microphone input and earphone output. If inserting a plug into the socket cuts out the internal speaker the cutout switch should be disabled—try bridging it with a 33 ohm resistor. It is essential to hear the data and commentary. (Input/Output can be the same pin)

It may be that volume and tone controls have an effect on the output signal but this is not usual.

Inputs vary from millivolt level for a dynamic mike to a high level—sometimes marked AUX. The high level input should be used if

CHESS PART 2

available. The signal must be attenuated for a low level input to avoid overloading. Most recorders have Automatic Volume Control and this helps. The output from Tolinka is bursts of 3.9khz at the data rate, which is 300 baud-or 150Hz maximum. Every high bit generates 12 cycles aapproximately of the carrier. The main source of interference may be regarded as being the data rate itself and some sort of high pass filter is needed at the input and output to remove it. Attenutation may also be required to match the recorder's input characteristics. A series capacitor followed by a shunt resistor will perform both these functions and in some cases even the shunt resistor is not required—it depends upon the recorder's input impedance.

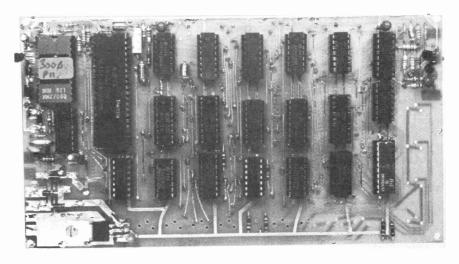
If an oscilloscope is available the recorder's output may be observed and should consist of clean bursts of 3.9 kHz separated by level blank intervals. If the signal swings up and down with the data the recorder is receiving too much signal and the shunt resistor should be reduced until this effect disappears. This process should not be carried to the point where the output level is reduced.

If in doubt use the following rules of thumb:

a) Put a 10k pot between the output and ground, taking the signal from the slider. Reduce the input level until the sound loses volume o n playback.

b) Take the output from the earphone or headphone socket. This will almost certainly cut out the recorder's internal speaker, but the switch should be easy to find and bridge with a 33 ohm resistor as described earlier. Adjust volume on playback to obtain satisfactory RECALL function. Note setting of both controls and check this setting each time.



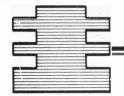


Photograph of the circuit board taken during construction. The switches are not fitted until the board is ready to be mounted in the case — Initial testing being done without them in position.

One of the exclamations often heard at a Chess Congress is 'J'Adoube' which is not a Russian four-letter-word but a polite way of informing one's opponent that a piece is not situated in the centre of the square it is supposed to be occupying;—and this fact is bugging the exclaimer who intends to adjust it but does not wish to be committed to moving it subsequently according to the rules of the game.

Tolinka has provision for moving

the pieces into the exact centre of their squares, the 'J' ADOUBE' capacitor. This component (C5) loads one of the outputs of a binary counter introducing a propagation delay which is passed down the divider chain. The value mentioned in the parts list is satisfactory for all but the most neurotic. In order that centralization may be optimized provision has also been made to fit a resistor for fine adjustment which will explain two of those redundant holes



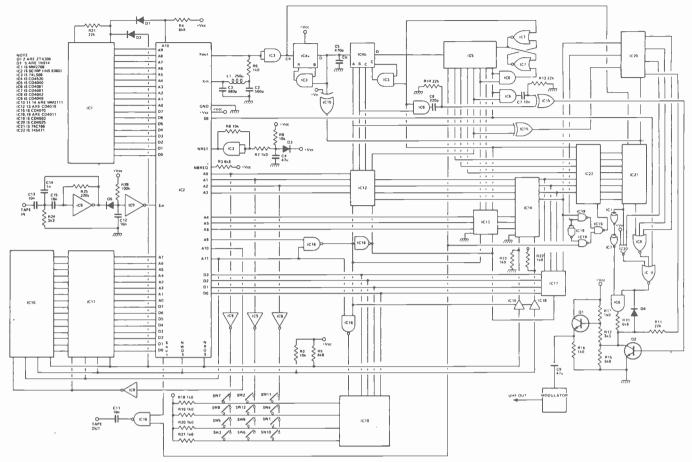


Fig. 1 Main circuit diagram of the Tolinka.

IC2 is National Semiconductor's SC/MP II. Its Program memory is stored in a 2708 type EPROM (1024 bytes). The character generator PROM (IC22) is a 74S471, 256 x 8 in structure. The RAM chips are 2111s, two (IC10 and 11) for game memory and one (IC14) for on-screen information. (There are 8 bits in game memory but only 4 in screen memory.) Top locations in game RAM are used as temporary stores for other information and this restricts the number of moves per player to 62 instead of 64 (four bytes are required to store a move).

Screen RAM is normally addressed by the VDU divider chain's outputs but the MPU must be able to address the screen as well to move the pieces around and change the status information. The address lines are multiplexed through a pair of CMOS And/Or gates (IC 12 and 13). The vertical blanking signal is wired to a sense line of the MPU so that the MPU does not access the screen during the VIDEO INTERVAL which would produce annoying flicker.

The three lowest address lines of the MPU are connected to inverter gates (ICXX) which matrix the keyswitches in a three by four arrangement. Pressing any switch connects an inverted address line signal to one of four inputs of a tri-state buffer normally held high by a resistor (R18-21) to Vcc. When the buffer is selected the inverted address line may be read as data and the switch identified with a unique code by a process already described in the Software: How it Works.

Generation of a Video Signal

All frequencies used are derived from a single MASTER CLOCK which is the MPU's own on-chip oscillator. An L/C combination sets the frequency to 1.92 MHz which defines the shortest horizontal change interval on screen at about half a microsecond. The MASTER CLOCK is divided by ten (IC4a) to give the FILE interval. Eight FILES form the visible board but the FILE interval is divided by twelve in a four-stage binary counter. The A, B & C outputs of this counter are the LETTER addresses, the D output being the LINE BLANKING interval. Thus two-thirds of linescan are the chessboard.

During LINE BLANKING a R/C monostable (C18, R14) supplies the LINE SYNC pulse. Further division of the line interval by 32 gives the RANK interval which is taken from the 5th stage of a binary ripple counter (IC5): the 2nd, 3rd, 4th & 5th outputs of this counter being the address lines to the character generator PROM. This PROM supplies the horizontal piece information as eight outputs in parallel and changes this information every other line. The 6th, 7th & 8th outputs of the ripple counter are the FIGURE addresses. The 9th output is the FIELD BLANKING pulse which is 'Anded' with the 7th stage to reset the counter after 320 counts. 256 counts, or lines, are visible as the chessboard. During FIELD BLANKING monostable (C7, R13) supplies the FIELD SYNC pulse.

LINE BLANKING is also connected to the character generator PROM to select Status/ Figures presentation instead of chess pieces. The same LINE BLANKING signal also permits the 4th output of the Board RAM, which contains the COLOUR BIT during the Chessboard interval, to address the character generator PROM instead of the 2nd output of the vertical binary ripple counter. (The COLOUR BIT is normally 'Exclusive-Or'd' with the pieces during the Chess board interval to control their colour.) This is because the larger character set of Status/ Figures symbols are required than Chesspieces — and loss of vertical resolution (cut by half) is the price which must be paid.

The eight parallel outputs of the character generator PROM are converted to a serial data stream in the Video Shift Register (IC21), driven by the MASTER CLOCK and loaded by the FILE signal.

SQUARE COLOUR is derived from RANK and FILE by Exclusive-Or function. SQUARE COLOUR, LINE BLANKING and COLOUR BIT are aligned with SERIAL VIDEO by a D type Flip Flop clocked by FILE.

LINE SYNC and FIELD SYNC are also passed through an Exclusive OR gate to form MIXED SYNC.

SERIAL VIDEO is combined with COL-OUR BIT, LINE BLANKING, FIELD BLAN-KING, etc, to form two mutually exclusive signals WHITING and BLACKING.

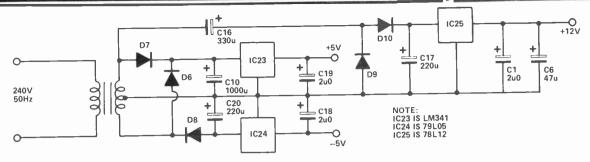


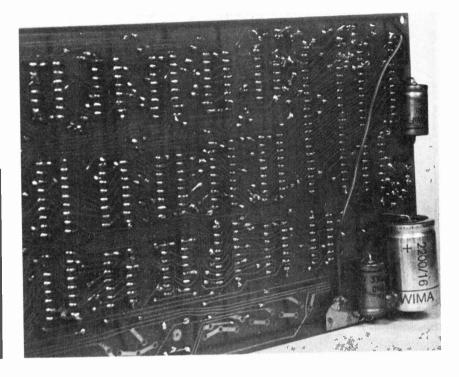
Fig. 2 The Tolinka's power supply is a straightforward design based on three monolithic regulators.

The photograph right shows how the power supply capacitors are mounted beneath the board and the wire link. Note that this photo was taken before the board was complete and not all components are in position.

BUYLINES-

A complete kit of parts for this project will be available only from Videotime Products, 56 Queens Road, Basingstoke, Hants, RG21 1REA for the all inclusive price of £109.50.

Individual parts are also to be made available but Videotime will offer help, advice and a repair service only to readers who purchase the complete kit. Note also that software, piece design PCB pattern, etc, are subject to copyright.



-HOW IT WORKS

Forming a Video Composite

The video signal is formed by combining SYNC, SQUARE COLOUR, WHITING and BLACKING at a summing point. SYNC is connected to Q2 which clamps the summing point (junction of R10, 12 and 15) to ground when SYNC is high. BLACKING is a negative going signal connected to the summing point through diode D4: when BLACKING is low the summing point is clamped a diode drop above ground. WHITING pulls the summing point up towards the positive rail through resistor R10. SQUARE COLOUR is connected to the summing point through a higher value resistor R1 and supplies two shades of grey when no other signal is present.

The signal is attenuated and passed through an emitter-follower to form a low impedance standard form video signal of approximately 1 volt peak to peak. This signal is used to drive a UHF modulator.

The reason that the SERIAL VIDEO output of IC21 is passed through a couple of spare inverter gates is to equalize propagation delays. Otherwise the black pieces have white edges.

Cassette Interface

The limited bandwidth available in audio cassette recording equipment does not permit serial data to be recorded directly. Some form of modulation is required.

In Tolinka data is recorded as bursts of a single frequency. On playback other frequencies can be filtered out and the demodulation process performed with a diode.

Three CMOS gates are used as the Modem in the final design. Any CMOS inverter will operate as a linear amplifier if a resistor is used between input and output. In this condition it may be regarded as an Op-Amp which has its non-inverting input connected to ground. A limited voltage gain of about 60 is available. The output data comes from the serial output port of the MPU and is combined with a signal of 3.9 KHz from the VDU divider in an AND gate. This supplies bursts of 3.9 KHz at data rate which can be recorded on tape.

The recovered signal is filtered by a CMOS inverter configured as a high pass filter. This rejects low frequencies at data rate and in the speech band. The output of this inverter, which consists of high amplitude bursts of 3.9 KHz, is connected to the cathode of D5. The anode of D5 is taken to the input of another inverter and a low pass filter, consisting of a resistor (R26) to the positive rail and a capacitor (C12) to ground. Gates in the same package have similar transistion points — so when there is no input the output remains at ground level. If 3.9 KHz oscillations are present at the input the output is high. The demodulated transmission is fed to the MPU's serial input.

Power-on Reset

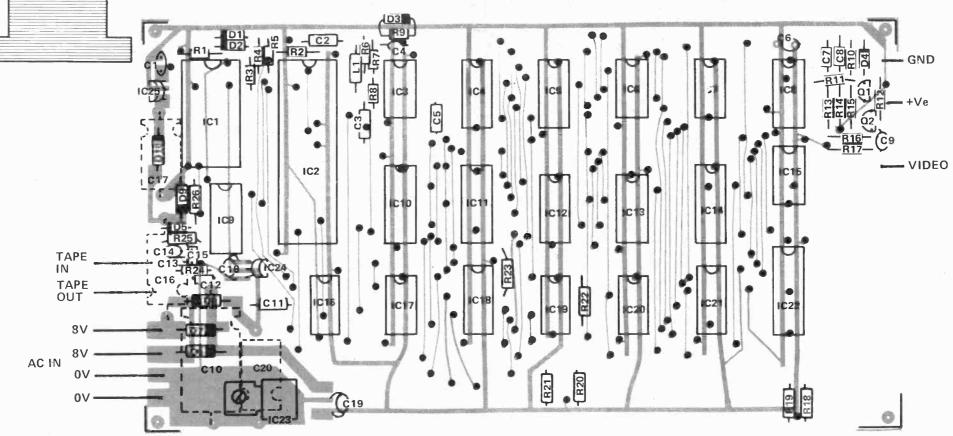
The MPU has a Reset input which clears all internal registers and restarts operations with the first instruction after it has been brought low for a specific interval of time. This function occurs when Tolinka is first switched-on and is not required again by the user.

At first sight this circuit seems to be overdesigned. In fact correct initiation of proceedings is vital and reliability suffers if any of the components are omitted. The diode connected across the charging resistor ensures that the capacitor will discharge if the power is interrupted only briefly.

ROM Select Diodes

The ROM occupies the first kilobyte of addressing space and it would seem logical to connect its Chip Select input directly to A10 because no write instructions will be made in this area. Conflict would still take place because the MPU outputs data on the bus at the start of the instruction fetch operation—this data consists of flags and upper address bits and none of it is used by Tolinka. The conflict would be harmless but for the fact that a Video Signal is being produced and processed at the same clock rate as the MPU which produces a faint pattern on screen if the Chip Select diodes are omitted.





RESISTORS		C3	680p polystyrene	IC4	CD4520	IC24	78L12
		C4, 6, 9	47u 6V3 tantalum	IC5	CD4040	IC25	79L05
R1, 11, 13, 14	22k	C5	470p polystyrene	IC6	CD4081	Q1, 2	ZTX300
R2, 8, 9, 18, 19	,	C7, 11, 12, 13,	15 10n ceramic	IC7	CD4001	D1-5	1N914
20, 21, 22, 23	1 Ok	C8	220p polystyrene	IC8	CD4042	D6-10	1 N4001
R3, 4, 5, 10, 15	6k8	C10	1000u 16V elec-	IC9	CD4049		
R6, 7, 16, 17	1k0	trolytic		IC10, 11, 14	MM2111	INDUCTOR	
R25	220k	C14	1n0 ceramic	IC12, 13	CD4019		
R26	100k	C16	330u 16V electrolytic	IC15	CD4070	L1	250u
R12, 14	3k3	C17	220u 16V electrolytic	IC16, 19	CD4011		
			•	IC17	CD4066	MISCELLANE	ous
CAPACITORS		SEMICONDUCT	ORS	IC18	CD4503		
				IC20	CD4025	PCB, transform	ner (0-8; 0-8 at 500m/
		IC1	MM2708	IC21	74C165	UHF modulato	r, switches (Schoeller-
21, 18, 19	2u06V3 tantalum	IC2	INS8060	IC22	74S471		uit, sockets, cable, nu
C2	560p polystyrene	IC3	74LS08	IC23	LM341-P5	bolts etc.	





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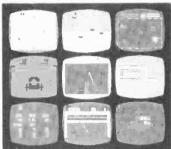
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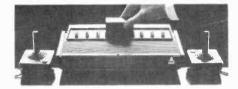
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SWITCH IN LINE SAVES NONE?

Stan Curtis of Mission Electronics, author of our series on super-fi amp design is back with us again to explain the faults inherent in many widely used comparative hi-fi tests. In particular he has a few things to say about switching methods

A SIGNIFICANT RE-APPRAISAL of amplifier design has been seen in the past few years. The revival of serious listening tests (so called "subjective" testing) has shown that laboratory measurements alone are not sufficient to indicate the performance of the amplifier when it is connected to real loudspeakers and pick-up cartridges and fed with a music signal. But it is crucially important that these listening tests be set up with great care. When different amplifiers are compared their gains should be equalised so that their outputs are within 0.1 dB of each other and preferably within 0.05 dB.

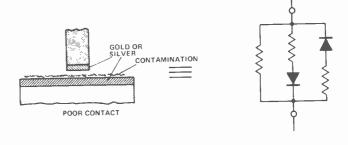
Such level changes could be incorrectly interpreted as differences in amplifier performance. The design of the passive attenuators is important to prevent any significant loading of the circuitry or any imbalancing of impedances which could upset passive filter roll-offs and so alter the frequency response of the system. Even the choice of test signal is important when setting levels. Traditionally a sine wave of 1 Hz or 400 Hz has been used. However, the author prefers to use a noise, source fed via a bandwidth limiting filter (to prevent any error by the different frequency responses of the amplifiers) as this more realistically simulates the dynamic conditions.

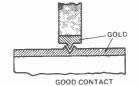
Care should also be taken in the interconnection of the different amplifiers. All connections should be as short as possible using very high quality and identical (in length and quality) cables. Wherever connections have to be made (other than at the amplifier or loudspeaker) high-quality gold-plated instrumentation connectors should be used in preference, to the rather suspect RCA Phono and DIN Connectors.

Switch Your Contacts

The next problem area is that of switching. Switching the outputs of the different amplifiers to a loudspeaker can be done using high-current, high-conductivity lever of knife switches. Relays can cause problems unless they have very strong springs; good contact design; high-current capability; and are new. The subject of switch contacts is quite complex but can be summed up as follows. A metal to metal contact is rarely a true ''short circuit.''

An almost invisible layer of oxidation or contamination forms on the contacts. This oxidation increases the contact resistance but more importantly forms a nonlinear junction that can in some ways be considered to be a voltage dependent diode-rectifier. The effect on the





Above: equivalent circuit of a mechanical switch. As you can see it is far from simple! Left: a good linear contact involves breaking the metal surface.

music signal at low levels can be imagined and — more importantly — heard! Even "pure" gold contacts and "self-cleaning" contacts suffer from this problem. A good contact can only be achieved when one contact breaks the surface of, and penetrates, the other contact metal. However, only a limited number of switching actions can occur before the contact material is sufficiently worn or damaged for inconsistent performance. Although this problem is discussed here in relation to testing it has as much significance in the design of the switches used in the amplifier.

When it comes to switching the output of the cartridges the imperfections of the switches have so much effect upon the audible quality of the signal that the listening test ceases to have any real validity.

Test point

The test itself needs further thought. The listening panel should be experienced listeners and yet not be part of a 'clique' where views are remarkable for the way they follow the 'party line.' Testing should be conducted over two or more sessions. Short sessions to perceive the performance of the amplifiers before aural fatigue sets in; and longer sessions with each individual amplifier to judge whether such fatigue is caused by the amplifier and to judge whether the apparent improvement it offered was a 'flash in the pan.'

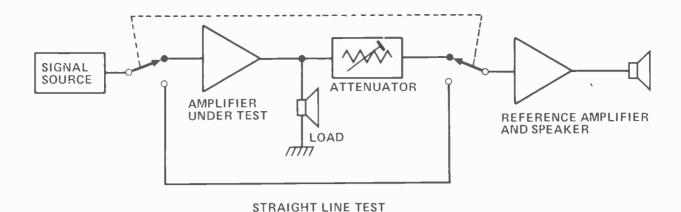


Fig. 2. The straight wire test. First popularised by Peter Walker of Acoustical Manufacturing (or Quad!) this test method has gained wider acceptance of late. It has its faults however.

During the initial sessions a number of "check" changes should be made to detect cheating (deliberate or involuntary) i.e. running amplifier No. 3 a second time as amplifier No. 7. Between each piece of music the reference numbers should be changed to minimise the effects of pre-conception. For example; if amplifier No. 3 is disliked for its reproduction of a bass drum, it may then be subconciously disliked on other pieces of music. Of course the tests should as far as possible, be conducted blind

A popular "subjective" test in use is the "Straight Wire Test." In this test the amplifier under evaluation is fitted with an attenuator at the output and substituted for a straight wire. The resulting signal is fed to a

"reference" amplifier and loudspeakers of known performance. Such a test is of help in evaluating the dependence of the amplifier on the loading made by different loudspeakers. But otherwise this test must be considered suspect. The "reference" amplifier may be far from perfect and it may well mask subtle changes. The dynamic interactions of two units in series can be quite complex and very difficult to predict in advance.

The foregoing (brief and incomplete) discussion of subjective testing serves only to indicate the difficulties that can be encountered. The reader should only consider seriously those comparative reviews where considerable effort has been expended to eliminate errors due to equipment and human beings.

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3-0-3		1A 1A		2.60		0.5	102	3.20	.70
0-6, 0-6		100		1.85		1.0	103	4.20	.85
9-0-9		330 330	235	1.95	.40	2.0	104	6.10	1.00
0.9, 0.9		500 500	207	2.35	.55	3.0	105	7.85	1.00
0-8-9, 0-8		1A 1A		3.50		4.0	106	9.80	1.10
0-15, 0-1		200 200				6.0	107	14.95	1.30
0-20, 0-2		300 300	214	2.35	.70	8.0	118	15.75	1.50
20-12-0-	12-20	700(0C)	221	3.10	.70	10.0	119	20.50	2.00
0-15-20;	0-15-20	1A 1A	206			60 VOL1	r (Pri: 220-2	240V)	- 1
		500 500					4-30-40-48		- 1
0-15-27,	0-15-27	1A 1A	204	4.75	.85	000.02		Price	- 1
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42 ANI	D/OR 24	LVOLT				0.5	124	3.40	70
	0-240 Vo					1.0	126	4.65	.85
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12V ~	24V	Ref.	£		&P	3.0	125	9.15	1.10
0.5	0.25	111	1.9		55	4.0	123	11.25	1.30
1.0	0.25	213	2.30		70	5.0	40	11.80	1.30
2	1	71	2.90		70	6.0	120	14.75	1.40
4	2	18	3.7		70		RANSFOR	and the second second	
8	4	108	6.2		.00			d 0-115-210-2	AOV.
10	5	72	6.9		00	VA	utput Tappe	Price	400
12	6	116	7.8		00	(Watts)	Ref. N		P&P
16	8	17	9.2		10	20	113	2.25	.70
20	10	115	12.7		30	75	64	3.50	70
30	15	187	16.6		30	150	4	5.35	.85
60	30	226	22.9		60		utput Tappe		.03
00	30	220					10-220-240		
30 VO	T /Pri 2	20-240VI			1	300	66	7.15	1.00
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3.0		0	5.80		00	VA	7 240 Sec: 1	Price	
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6.0	11		9.50		00	100	150	6.40	1.00
8.00		8	11.3		30	200	151		1.10
10.0		9	12.00		30	250	152	10.00 11.95	1.30
1		_			-	350	152	14.45	1.40
Į.	CAT	TALOGUE	30p			1000	156	35.00	3.00
						1000	150	35.00	5.00

IT'S FREE!

Our monthly Advance Advertising Bargains List gives details of bargains arriving or just arriving or other bargains which sell out before our advertisement can appear. — it's an interesting list and it's free — just sond S.A.E. Below are a few of the bargains still available from previous

send S. A.E. Below are a few of the bargains still available from previous lists.

FM Tuner and decoder, 2 very well made units, nice clear dial, excellent reproduction, 511.20 the pair.

12 Yolf Heavy Duty Relay, plug in type has three pairs of 10 amp changeover contacts. A transparent dust cover, price £1.08 suitable 11 pin base 65 mere reproduction at 4 mm growth of the suitable 11 pin base 65 mere reproduction at 4 mm growth of the suitable for loads approaching 1KW. Price of module and instructions £2.55.

Isahing Lights, chasing lights, random flashes, strobe effects etc. atc. can easily be achieved using our disco switches. These switches are as equipment but guaranteed periect and supplied suitable for mains working. To get some ideal of the loading number each switch is 10 amp. For the light pipe of Catherine Wheel effect order the 12 switch model with light pipe data model. Interconnecting the switches to give the fastest speed. 5 switch model £5.75. 12 switch model £5.9 switch model £5.75. 12 switch model £6.20. Reed Switches, standard 60 watt glass type. Mormal open contacts glass lengths 2", diameter %", 10 for £1, 100 for £8, 1000 for £70. Flat Reed Switches, for stacking greater quantity in confined space. Price Commander. Magnets, suitable for operating reed switches, central fixing

Flat Reed Switches, for stacking, greater quantity in confined space. Price Sop.

Formatic Magnets, suitable for operating rood switches, central fixing sole. 10 for 61.

W Shaped Flyoracant Tubes for perch light, but signs or where yes well light swelly accelered a central relating spaces ever a centined real apprat. 10'r: 10". 30 wats, made by Philips price 82:24.

Estaction Speakers, bothe 45 watsh handling power. We have 5 ar 6 different models in the chemical being the Party limes 18:39 each, again only really a bargain for callers as pestage in 61:50 per speaker.

Allo Treatments include accounter, for working Americas foot and equipment, completely anciosed in sheel metal case made for computer so obviously first class 500 watsh. With handle, efforted a booth after price only E15. These may be selied but are folly pastrasteed. Similar out 100 west 52:9.50.

ESTACTE Charger Kill. New version. Was supply two 10 amp rectifiers. 250'V brassformers and the start charge switch with instructions, price 52:75. This is probably use of the most acalled places of equipment you can beven in your agrange. Sooner or latery your smooth will leave something as and yes wrill have of flat battery, this starter will get yoe every stackly in less than 5 minutes.

Feaster Casafter by Venderoof Company, 250/240 Y mails operated, intended for surface mounting has a fixing Range at the ballow. Price 52.15.

129 Orig Proof Relay. Specially designed for going ander the bonest of e.cr. made by one

12V Drip Proof Relay. Specially designed for going ender the bossel of a car, made by one of our big massisciorers this has a ramovable semi-hard rabber cover. Contacts look settable for up to Dangs so bit could be the right one it you are thinking of making an anti-their series. Price \$1++169.

Pnaumatic Rem, for litting, thrusting, pulling etc., etc. has 2%" travel, looks large enough to open doors. kft. staircase, ventilators etc. Price £7.00.

Solder Gas BARgáin, The ETP, this is 100watt soldering gun, a very well made tool with lamp to illuminate work, has feable issalated males transformer and is built into the shockproof literosciplic case. Cames complete with apare tipe. Mains operated of course. Price \$4.501

Interested in Tape Control? American made tape punches really beautiful units full of sophisticated parts, designed we believe to automatically operate typewriters and they can of course be used to operate either pusch tape controlled machines. Reference Ne. 1s MCR Class 461. 2 reference 205 MR 50. We believe these are 8 bit tape punchas, powered from 115V SOM2 in very good condition with tape £15.00, cerriage is £3.20.

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Features include:-Full cursor control hard wired programmes automatic tape data block search, etc., etc.

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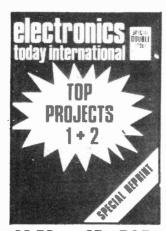
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12v-0.12v	1 amp	TM41	£3.24											
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15v	7 amp		£4.32 £4,32											
15v-0.15v	31/2 amp		£4.32											
15v-0.15v	3½ amp	T1012	£1.62											
17v	½ amp ¾ amp	TM13	£1.90											
18v	1/2 amp	TM14	£1.62											
20v 20v-0.20v	21/2 amp	TM 46	£4.32											
20v-0.20v	12½ amp	TM15	£4.86											
20v-0.20v	6 Amp	TM 15	€4.86											
13v	100 mA	TM21	£1.62											
24v	1½ amp	TM 16	£2.12											
24v	2 amp	T0017	€2.70											
24v + 2v 7 amp	2 amp	TM39	£2.97											
24v	4 amp	T10 40	£3.78 £2.43											
25v	1½ amp	TM 18 TM 39	12.98											
26v	2 amp	TM 15	£4.86											
30v	8 amp 37 amps	TM 34	£31.86											
374	21 miles													
40v tapped @ 30v. 20v & 10v	6 amp	TM 15	£4.85											
50v-2 amp with 6.3v														
shrouded		T11122	€4.86											
50v	8 amp	TM29	£11.65											
60v	2 amp	TM-46	£4.32											
60v	5 amp	TM24	£7.02											
75v-3 amp with 6.3v		70000	EB.10											
shrouded	41/	TM23 TM24	£8.10 £7.02											
75v	4½ mmp	TM24	£7.02											
70v tapped 60c & 75v	4 amp	TM25	£7.02											
100v	lamp ½ amp	TM25	£7.02											
100v-0.100v 120v	4 amp	TMSO	£10.50											
120v	8 amp	TM51	£9.50											
200v	½ amp	TM 25	£7.82											
250v-0.250v with														
6.3v 2A	50 mA	T10 36	£3.78											
250v	100 mA	THI36	£3.78											
500w	50 mA	TM 36	£3.78											
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This book is rather an unusual reprint from the pages of ETI. The series appeared a couple of years ago in the magazine, and was so highly thought of by the University of New England that they have re-published the series splendidly for use as a standard textbook, Written by Peter Sydenham, M.E., Ph.D., M.Inst.M.C., F.I.I.C.A., this publication covers practically every type of transducer and deals with equipment and techniques not covered in any other book. Enquiries from educational authorities, universities and colleges for bulk supply of this publication are welcomed. These should be addressed to H. W. Moorshead, Editor.

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Our successful beginners series came to an end some time ago now, and the whole series is available from us in reprint form. The three books between them contain all the information presented in the series (sometimes in more detail!) and together form an excellent starting point for anyone interested in learning the art of electronic

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FROM THE PUBLISHERS OF

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Each volume contains over 150 circuits, mainly drawn from the best of our Tech-Tips. The circuits are indexed for rapid selection and an additional section is included which gives transistor specs, and plenty of other useful

Sales of this publication have been phenomenal — hardly surprising when the circuits cost under 1p each! Each

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ETI MARKET PLACE

Digital Alarm | LCD



Size: 105mm wide 115mm deep x 55 mm high.

THIS IS THE THIRD digital alarm clock that we are offering (we regret the earlier versions are no longer available). We have sold thousands and thousands of these and our buying power enables us to offer a first rate branded product at a really excellent price.

The Hanimex HC-1100 is designed for mains operation only (240V/50Hz) with a 12 hour display, AM/PM and Alarm Set indicators incorporated in the large display. A switch on the top controls a Dim/Bright display function.

Setting up both the time and alarm is simplicity itself as buttons are provided for both fast and slow setting and there's no problem about knocking these accidentally as a 'locking' switch is provided under the clock. A 9-minute 'snooze' switch is located at the top.

£8-95

(Inclusive of VAT and Postage)

An example of this clock can be seen and examined in our reception at our Oxford Street offices.

To: Hanimex Alarm Offer ETI Magazine 25-27 Oxford Street London W1R 1RF

Please find	enclosed my	cheque Pl	0 for £8.95	(payable to
ETI Magazino	e) for a Hanim	ex Digital Al	arm Clock.	

Name							٠				٠				٠.				٠	٠	
Adress					٠	٠		٠	٠				٠								
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Please allow 14 days for delivery

LCD Watch



New low price!

The enormous numbers involved in ETI offers has enabled us to arrange a real bargain — a full spec LCD watch with adjustable metal bracelet for under half the point rate.

This watch gives continuous display of hours and minutes press the button once and you'll get the date (American style). After a couple of seconds the display automatically reverts to time but if you press again you'll get a continuous seconds display.

Press another button and you get a back light, enabling you to see the display in the dark. Setting, or resetting is simplicity itself and a 'hold' facility allows you to set the watch spot on. The accuracy is magnificent, as with all the current range of digital watches and battery life is well in excess of a year.

£8.95

(Inclusive of VAT and Postage)

An example of this watch can be seen and examined in our reception at our Oxford Street offices.

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Please find enclosed my cheque/PO for £8.95 (made payable to ETI Magazine) for my LCD Digital Watch.

Name	 	
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AUTOCHORD PART ONE



WHILE NOT QUITE an instrument in its own right the auto chord is certainly more versatile than the common or garden rhythm generator.

The instrument is designed to be added to the lower two octaves of an organ and will provide a variety of accompaniment controlled by the mode selected.

The specification shows that the eights rhythms provided cover most requirements and gives some idea of the extra facilities offered by the autochord.

The instrument will offer chords — major or minor third, fifth or diminished fifth and sixth of seventh. It will also provide a walking or alternate bass as well as arpeggios.

They say a picture is worth a thousand words, and at this moment we feel that at some time someone must have said much the same about sound. It's difficult to convey all the facilities offered by the auto chord on paper, so if you cannot visit Maplin's shop, where a unit will be on demonstration, you will just have to take our word that the auto chord provides everything that the solo musician could want.

The auto chord is designed to be incorporated within existing organs and is easiest to interface with a DC keyed organ although it is possible to use the auto chord with a direct keyed instrument.

Full constructional details plus a description of the auto chord in use will be presented next month.

SPECIFICATION

8 selectable rhythms

Covering waltz, rock to Latin. Latin American

rhythms can be combined. Non-Latin Ameri-

can rhythms can be combined.

5 instruments

Bass. Snare drum. Low bongo. Claves. Cymbals.

CHORD ACCOMPANIMENT (with keyboard)

Three mode selection

1. AUTO

1

3

Playing one note produces a chord structured around this note, and will play continuously.

SEMI-AUTO

rom

Individual notes or chords played are remembered and played continuously.

MANUAL

Notes or chords played only continue whilst the keys are held operated.

AUTO RESET

Variable bass. Delay-auto-stop and over-ride in all 3 modes. On/off. Walking or alternating in modes 2 and 3. A minimum of three notes. Must be played for bass accompaniment.

Auto: On/off.

Chord accompaniment: On/off.

Two octaves progressive in modes 2 and 3. Selectable maj/min 3rd/7th.

Variable tempo

Harmonic attack

Five tones added in short bursts

Arpeggio Three selectable pitches

Chord accompaniment volume

Rhythm volume

FRONT PANEL CONTROLS

SW1	Mains on/off	SW18	Auto/semi-auto/
SW2	Auto on/off	S W 1 9	manua! Auto-stop/
SW3 to 10 SW11	Rhythm select Chord on/off	3 44 1 3	continuous
SW12	Harmonic attack	SW20	Auto reset
SW13	Major/minor 3rd	SW21	Arpeggio. Off/1/2/3
SW14	7th	R13	Tempo
SW16	Bass on / off	R26	Auto-stop time delay
SW17	Bass — walking/	R131	Auto-accom. volume
	alternating	R140	Rhythm volume









Auto Organ

RESISTORS
½₩
R126 47R
R24, 25,155,157,
159, 173, 181, 199,
216, 221, 234, 231 270R R104 560R
R104 560R R80 680R
R72, 88, 101, 115,
122, 143, 156 2k2
122, 143, 156 2k2 R55, 64, 71, 100 2k7
R154 3k9
R42 56, 57, 58, 60,
162, 183, 238 4k7
R8 7 5kb
R116, 123, 125 6k8
R74, 75, 77, 117,
R116, 123, 125 6k8 R74, 75, 77, 117, 118, 124, 127, 131,
133, 135, 136, 140, 161, 182, 217, 222,
161, 182, 217, 222,
232, 235 10k R15-20 12k
R76, 93 8k2
R28-49 82 95 113.
158 172 208 250 15k
R28-49, 82, 95, 113, 158, 172, 208, 250 15k R1-11, 14, 23, 73,
90, 168 ZZK
R106, 163, 170,
177, 191 27k
R54, 229 33k
R149-153 39k
R149-153 R166, 167, 198, 203, 215, 239, 248 47k
203, 215, 239, 240 47K
R69, 70, 85, 86, 98, 99, 103 56k
R134, 230, 241 68k
R89, 102 82k
R62, 63, 66, 78, 79,
R89, 102 82k R62, 63, 66, 78, 79, 92, 120, 128, 132,
137, 160, 165, 169,
171, 174, 184, 186,
187, 200, 201, 204,
211, 212, 214, 219, 226, 227, 233, 236,
226, 227, 233, 236,
237, 240, 243, 245,
246, 249, 12, 21, 22, 27 91. 100k
R50-53, 67, 83,
o6 189 195 150k
R185, 188, 242, 244 180k R129, 138, 209,
R129, 138, 209,
210.220 220k
R176, 192, 193, 197, 205, 206, 207, 225 270k
205, 206, 207, 225 270k
R65, 114, 121, 180,
223, 224 470k
R105 820k

R105

PARTS LIST

R107, 112, 119, 175, 178, 218 R196, 213, 228 R108	1M0 2M2 4M7
¼W R141 R110 R146, 147 R144 R142, 145 R148 R194	39R 100R 180R 330R 820R 1k5 43k
POTENTIOMETERS R111 R61, 247 R164, 190, 202 R109 R68, 84, 97	1k0 47k 100k 470k 1M0
CAPACITORS C68, 72, 79, 86, 9 94, 95, 101, 103, 110 C100, 111, 85 C21, 22, 23, 107 C5, 11, 17, 25, 31 35, 70, 99, 105, 1 C10, 40, 71 C1, 12, 20, 28, 30 47, 69, 76, 104, 1 C75 C9 C13-16 C24 C6, 7, 8 C2, 66, 97, 106 C3 C19, 27, 29, 33, 38, 43 C4 C78, 80 C49, 52, 55 C44, 74, 88, 96 C73, 89 C91, 92 C39, 90 C32 C45, 87 C18, 26, 36, 41, 67, 82, 83, 84 C102, 77, 81 C34 C98	10n polyester

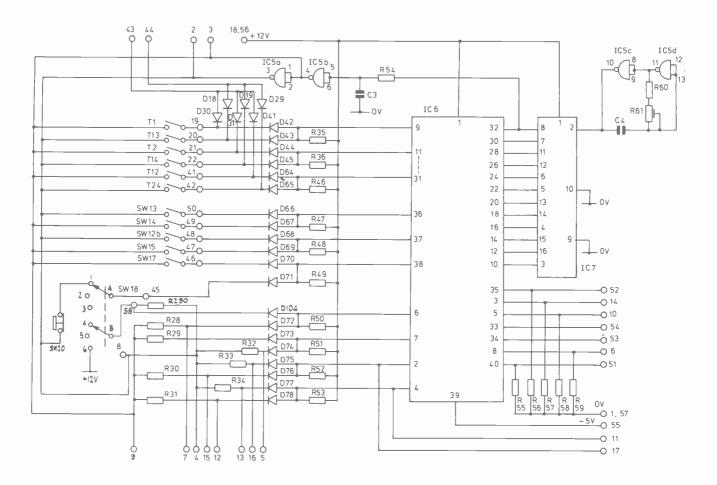
C48, 50, 51, 53, 54, 56, 59, 60, 64, 65, 63, 64, 63, 62, 62, 63	4, 10u25 V electrolytic 22u 10 V electrolytic 100u 25 V electrolytic 220u 16 V electrolytic 470u 25 V electrolytic 1000uV 16 V electrolytic
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SWITCHES	
SW1	Mains latchswitch
SW2	2 pole latchswitch
SW3 to 10	8. 2 pole latchswitch
	interdependent
SW11	2 pole latchswitch
SW12	2 pole latchswitch
SW13	2 pole latchswitch
SW14	2 pole latchswitch
SW16	2 pole latchswitch
SW17	2 pole c/over latch-
	switch
SW18	4p. 3W rotary
SW19	2 pole latchswitch
SW20	Push (break) sw
SW21	3p. 4W rotary

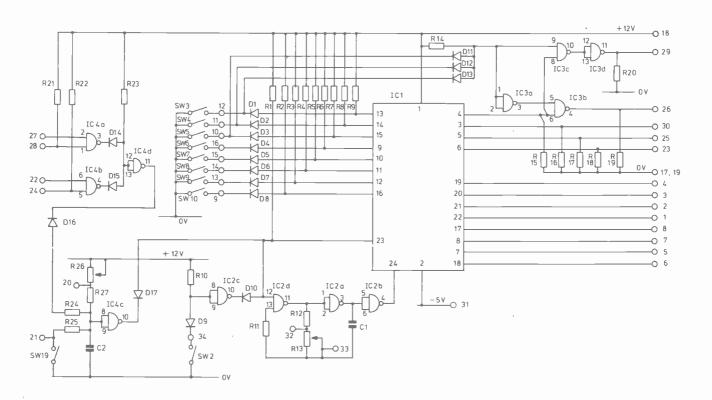
MISCELLANEOUS PCBs, 15-0-15 250mA transformer, fuse plus holder, sockets, clip on heat sinks, cable, etc.

Production problems have meant that the circuit diagrams feor this project are without the usual component annotations.

820k



Circuit diagrams of the generator and coder



-HOW IT WORKS-

PRE AMPLIFIER

The chord and rhythm outputs are amplified and filtered in ICs 9 and 11 respectively. The outputs from these devices are fed, via level control potentiometers to the input of IC10. This mixes the two signals and provides the final output of the instrument at a level suitable for feeding to a power amplifier.

POWER SUPPLY

The various ICs used in the auto chord require supplies of +12 V, +11 V, -5 V and -11 V. The +12 V line is derived from the rectified AC output of T1 by the series pass element Q5. The voltage at the emitter of Q5 is determined by D91, a zener diode. The +11 V supply is a simple shunt from the 12 V line.

The -5 V line is again a series pass circuit, this time the output voltage being set by D92.

The —11 V rail is simply stabilised by zener diode D93 as the current demanded from this rail is not enough to warrant the use of another series pass transistor.

The LED supply is taken from the negative voltage rail and is current limited by R148.

GENERATOR AND CODER

The rhythm generator section of the instrument is centered on IC1. This is the M254, a device that contains a ROM that will drive the sound generators with a selection of eight rhythms. To select a desired rhythm, the appropriate input must be taken to ground, via SW3-10, will the other inputs are held high by resistors R1-R9.

The M254 requires a clock signal to operate and this is generated by the CMOS oscillator formed by IC2. The frequency of this oscillator, and ultimately, the tempo of the rhythm, is controlled by R13.

The arpeggio, chord and bass accompani-

ment are generated by IC6, the M251.

The IC is fed with 12 input frequencies from the tone generator, IC7. This is clocked by the output of the CMOS astable based on

IC5c and d.

The M251 is used in conjunction with the M254 which is responsible for the selection of the various notes in the arpeggio/chord/bass

accompaniment.

The M251 features a number of different modes of operation, in the automatic mode, when a number of keys in the two available octaves are played, the lowest note will be

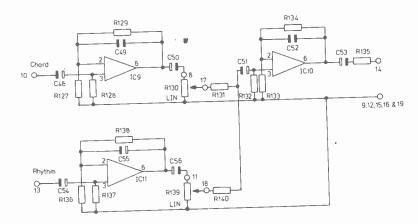
taken as a reference and memorised.

The memorized key, by means of an internal multiplexer, selects the corresponding tonic and all other notes programmed for arpeggio, chord and bass accompaniment.

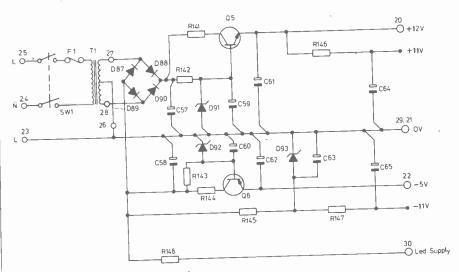
In the semi-automatic mode, the M251 will memorise the lowest four keys played together with the top note played. The circuit will then provide accompaniment until the mode is cancelled by selecting automatic mode briefly and returning to semi-automatic while no keys are played.

The semi-automatic mode can also be selected without memorization of keys.

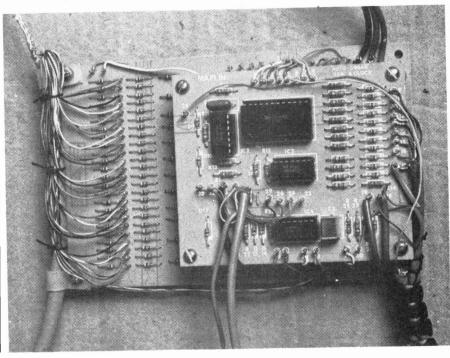
Due to the pin out restrictions of the 40 pin package a system of multiplexing has had to be adopted, this explains some of the complexity in this area of the circuit.

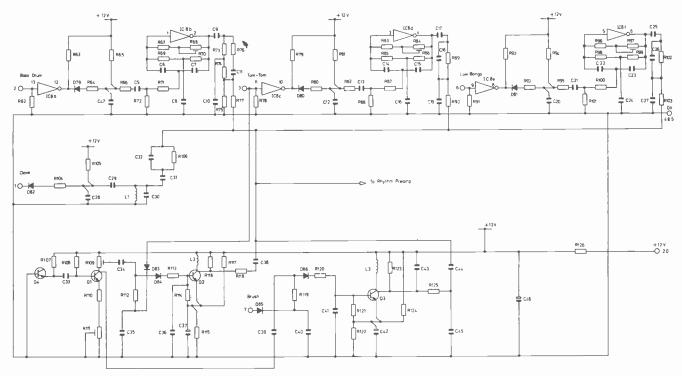


Circuit diagram of the preamplifier



Circuit diagram of the power supply





Circuit diagram of the voice generator

HOW IT WORKS

VOICE GENERATOR

THE bass drum, tom-tom and low bongo sounds are generated by the damped sinusoidal oscillators based upon the six invertors of IC8. Each of the oscillators are the same apart from the values of the timing capacitors which set the characteristic frequency of oscillation.

In each oscillator the variable resistor (R68, R84 and R97) will control the rate at which any oscillations will delay once triggered by the M254 rhythm generator.

The square wave output of the M254 is held low by a resistor, necessary because the M254's outputs are open drain, and fed via a differentiating network to the damped oscillator. A pulse from the output of the M254 will trigger the characteristic instrument sound.

In addition to the output of the damped oscillator based on IC8c and d the tom tom, to give it a more realistic sound, contains a white noise component.

The white noise is produced by the reverse biased zener effect of Q4 and after filtering and buffering, by Q1, with further filtering by Q2, is mixed with the oscillators output to provide a realistic tom-tom sound.

The brush sound consists of filtered white noise, the white noise again being generated by Q4 — the filtering this time being performed by Q3 and associated components.

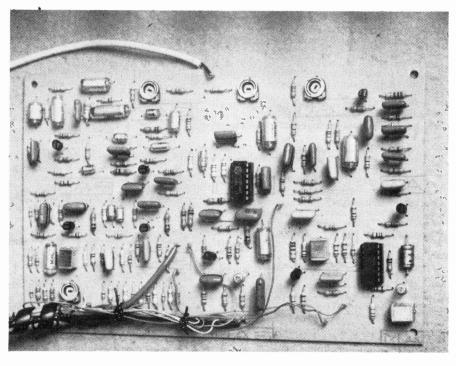
The clave output is generated by the resonant circuit centered around L1 and C30.

The outputs from the various voice generating circuits are summed and fed to the instruments pre-amplifier.

BUYLINES

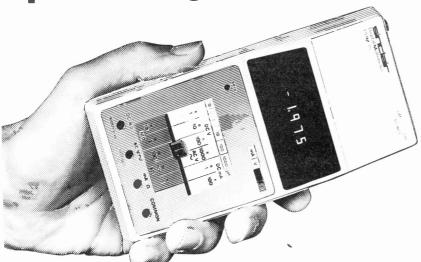
Maplin Electronics will be supplying a Contact Maplin for details of price. complete kit of parts for the auto Full constructional details for the chord, including screened boards. auto chord will follow next month.

Next month — full constructional details plus the auto stop board.



The Sinclair PDM35.

A personal <u>digital</u> multimeter for only £29.95



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A digital multimeter used to mean an expensive, bulky piece of equipment.

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The Sinclair PDM35 gives you all the benefits of an ordinary digital multimeter – quick clear readings, high accuracy and resolution, high input impedence. Yet at £29.95 +8% VAT 1, it costs less than you'd expect to pay for an analogue meter!

The Sinclair PDM35 is tailormade for anyone who needs to make rapid measurements. Development engineers, field service engineers, lab technicians, computer specialists, radio and electronic hobbyists will find it ideal.

With its rugged construction and battery operation, the PDM35 is perfectly suited for hand work in the field, while its angled display and optional AC power facility make it just as useful on the bench.

What you get with a PDM35

 $3\frac{1}{2}$ digit resolution. Sharp, bright, easily read LED display, reading to ± 1.999 . Automatic polarity selection. Resolution of 1 mV and 0.1 nA ± 0.0001 $\pm A$.

Direct reading of semiconductor forward voltages at 5 different currents. Resistance measured up to 20 Mar. 1% of reading accuracy.

Operation from replaceable battery or AC adaptor. Industry standard 10 M m input impedance.

Compare it with an analogue meter!

The PDM 35's 1% of reading compares with 3% of full scale for a comparable analogue meter. That makes it around 5 times more accurate on average.

The PDM35 will resolve 1 mV against around 10 mV for a comparable analogue meter – and resolution on current is over 1000 times greater.

The PDM35's DC input impedance of $10\,\mathrm{M}\,\mathrm{\Omega}$ is 50 times higher than a 20 k Ω /volt analogue meter on the $10\,\mathrm{V}$ range.

The PDM35 gives precise digital readings. So there's no need to interpret ambiguous scales, no parallax errors. There's no need to reverse leads for negative readings. There's no delicate meter movement to damage. And you can resolve current as low as 0.1 nA and measure transistor and diode junctions over 5 decades of current.

Technical specification

DC Volts (4 ranges)

Range: 1 mV to $1\bar{0}00$ V. Accuracy of reading 1.0% \pm 1 count. Note: 10 M Ω input impedance.

AC Volts (40 Hz-5 kHz)

Range: 1 V to 500 V.

Accuracy of reading: $1.0\% \pm 2$ counts.

DC Current (6 ranges)

Range: 1 nA to 200 mA.

Accuracy of reading: $1.0\% \pm 1$ count. Note: Max. resolution 0.1 nA.

Resistance (5 ranges)

Range: 111 to 20 Mi).

Accuracy of reading: $1.5\% \pm 1$ count. Also provides 5 junction-test ranges.

Dimensions: $6 \text{ in } x \text{ } 3 \text{ in } x \text{ } 1 \text{ } \frac{1}{2} \text{ in.}$

Weight: 61/2 oz.

Power supply: 9 V battery or

Sinclair AC adaptor.

Sockets: Standard 4 mm for

resilient plugs.

Options: AC adaptor for 240 V 50 Hz power. De-luxe padded carrying wallet. 30 kV probe.

The Sinclair credentials

Sinclair have pioneered a whole range of electronic world-firsts – from programmable pocket calculators to miniature TVs. The PDM35 embodies six years' experience in digital multimeter design, in which time Sinclair have become one of the world's largest producers.

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ĺ	$\frac{\text{(qty) De-Luxe carrying cases}}{a \text{ £3.24 (incl. VAT) each.}} \mathcal{L} \qquad \text{Address.}$	
	(qty, AC adaptor(s) for 240V' power a £3.24 (incl. VAT) each£ Post and packing (please add£0.65	
	t enclose cheque/PO made payable to Sinclair Radionics Ltd for	İ
	(indicate total amount	
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1/4 \times 2\% \times 4\% in. ¼ × 2¾ × 4 RRP €26.95



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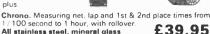
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51QR-198. 6 digit (£35.95)

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ECCTONICS today international

What to look for in the December issue: On sale Nov 3rd

ETI LIGHT SHOW

HANDS UP all those who've never been to a disco. None? Good — that means you've all seen sound-to-light units in action, although it's more than likely it was a normal three-channel affair. Usually boring, are they not?

Well ETI plans to change that next month; ours has five frequency channels, with individual level controls on each channel. Control of the lights is comprehensive to say the least. You can run the unit as a straight sound to light, or have it strobe all lights. At a

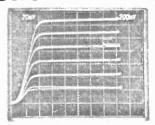
speed dependent upon music level (not volume — the unit is independent of that!) or hand over control to an internal digital circuit which produces some superb random effects. If you fancy a five colour manually controlled strobe unit it can do that as well!

Each channel handles up to 500 W of lighting, and a complete kit of parts will be available from Powertran, who designed this project especially for ETI.

Electronics in Model Railways

An essential part of the education of any young man is his electric train (checking with ETI technical staff shows all eight had one — and five still have). Most of us however remember the controls as crude; today things are changing — sophisticated electronic controls are perfectly suited to model railways and the manufacturers are about to announce some dramatic advances. We take a look at what's happening.

CURVE TRACER



Explaining the shape of Voltage-Current characteristics of diodes, transistors and other non-linear devices is usually dull as it normally involves a tedious plot of static, experimental data.

A more elegant solution is available to anyone with a DC coupled scope capable of taking an external X-input. Next month we carry a project with the additional circuitry necessary to do this yourself.

Car Anti-theft System

A simple project to build but sophisticated in its operation. It is a comprehensive system that incorporates several features of large and expensive commercial systems and using state-of-art techniques it is extremely reliable. A kit will be available of the whole project.

INDEX

A complete listing of all we've carried in ETI since our last Index (which was carried in April 1977) and went back to the first ever ETI). As our research shows that 96% of readers never throw away their copies it should be useful to most of you.

How It Works



In the November issue we begin a new type of article. The idea came to us when discussions with experts in one area of electronics admitted to almost total ignorance of other areas — especially commercial circuitry. Mass-produced electronics use techniques which are not widely understood elsewhere — we hope to put that right. In the first of this occasional series we have asked Gordon King to discect a Thorn Monochrome TV; we shall show the complete circuit and explain the function of each stage. It's not done as a beginners series but to give those outside this field the true "Inside Story."

computing today No.2

Win a TRITON Computer



Want to get your hands on a Triton Computer Kit but can't afford it (yet)? In No. 2 of our new supplement Computing Today, we have a free-entry competition for one to be won. If you've read this far you'll probably know what it's worth — but in case you don't it's about £300.

Microprocessors by Experiment

Learn about microprocessors — not from some abstract description of a make believe MPU but by hands on experience with an MPU system. The series, based on the MK14 development kit, will take you through the operation of the SC/MP MPU and show you how to use it to do everything from control your heating system to land on the moon.

I/O for 6800

The microprocessor user rapidly arrives at the need to understand and apply input/output circuitry to interface peripheral equipment to the computer system. A standard choice, when using a 6800 microprocessor, is to employ a Peripheral Interface Adapter (PIA). Many engineers now buy ready-built systems then wish to utilise the PIA as straightforward outputs and inputs. When data sheets are consulted they are found to give concise yet complete hardware and software information. The user of a ready-built system needs help in simply getting the PIA to act as outputs and inputs without becoming involved in the intricate details needed by designers of microcomputer boards. This article aims to give this help.

Features mentioned here are in an advanced state of preparation as we go to press but circumstances may affect the final contents of the next issue.

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Stat|Sci) TEXAS T125 (new LCD Sci/Stat)

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BOXES. Black A B S. Plastic with brass inserts and lid. $75\times56\times35$ mm 40p. $95\times71\times35$ mm 49p. $115\times95\times16$ mm 57p. $225\times130\times84$ mm £1.95

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MAINS TRANSFORMERS, all 240v AC primary Postage shown

MAINS TRANSFORMERS, all 240v AC primary Postage shown in brackets per transformer 6.0-6. 100mA, 9-0-9. 75mA, 12-0-12. 50mA, 75p each (15p) 0.4-6-9. 150mA, no mounting bracket, 65p. (20p). 12-0.12. 100mA, 95p. (15p). 12v. 500mA, 95p. (22p). 12v. 2. Amp. 6.2. 75. (54p). 15-0.15v. 1. Amp. 6.2. 10. (45p). 12v. 4. Amp. 6.2. 75. (54p). 15-0.15v. 1. Amp. 6.2. 10. (45p). 30-0.30v. 1. Amp. 6.2. 75. (54p). 0-12-15-20-24-30v. tapped at 2. Amp. 6.4. 50. (54p). 20-20v. 2 amp. 6.3. 50. (54p). 25v. 15. Amp. 6.1. 45. (45p). 18v. 15. Amp. rectified. 62. 00. (45p). 35v. 2. Amp. 2.5v. 2. Amp. protond. 62. 95. (54p). 20v. 2.5. Amp. 62. 20. (54p). 11. Xenon/triac pulse transformer, 30p.

SWITCHES — Min Toggle, SPST 8 x 5 x 7mm 45p DPDT 8 x 7 x 7mm 60p. DPDT Centre 0ff 12 x 11 x 9mm 75p DPDT C/O Sliders 20p. R S Single Pole C/O Push Buttons 45p Roller Micro Switches 15p Min. Micro Switches 13 x 10 x 4mm 20p Min Push to make or push to break Switches 16 x 6mm 15p

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GAIN CONTROL

PART 2

To conclude his survey of electronic gain control methods, Tim Orr presents us with more circuits which vary from a light bulb compressor to a markspace modulated universal filter unit, and a noise gate/expander.

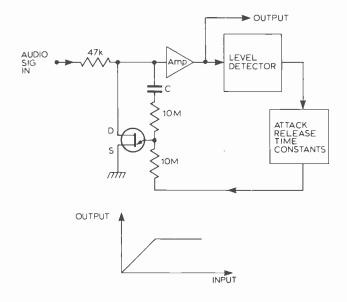
Basic Limiter Circuit

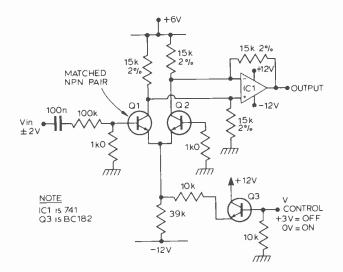
Most professional limiter circuits use a FET as the variable gain element. Relatively low distortion with a reasonable signal to noise ratic can be obtained. A basic limiter circuit is shown this being no c. fferent to previous circuits except for the variable gain element.

When a relatively small voltage (20 mV) is applied to the drain source of a FET, it acts like a fairly linear resistor. As the gate source voltage is varied, this resistor (RDS) also varies.

In fact the channel resistance RDS is inversely proportional to gate source voltage $V_{\rm GS}$. When $V_{\rm SI}$ is oV, then RDS is at its generally minimum resistance $(R_{\rm ON})$ which can be as low as 5R, but it is generally more like 100R. When $V_{\rm GS}$ exceeds the pinch off voltage (Vp or $V_{\rm GS}$ off) the channel resistance goes up to several hundred Megohms. So a junction FET can be used as a voltage controlled resistor, except that $R_{\rm ON}$ and $V_{\rm GS}$ (OFF) tend to vary widely from device to device. However with a bit of perseverance suitable devices can be selected and made to work

One circuit trick that greatly reduces distortion is shown here. Half of the audio signal at the drain of the FET is presented to the gate. This is superimposed on top of the control voltage and produces a distortion cancelling effect. Distortion levels below 0.1% can be achieved using this technique.

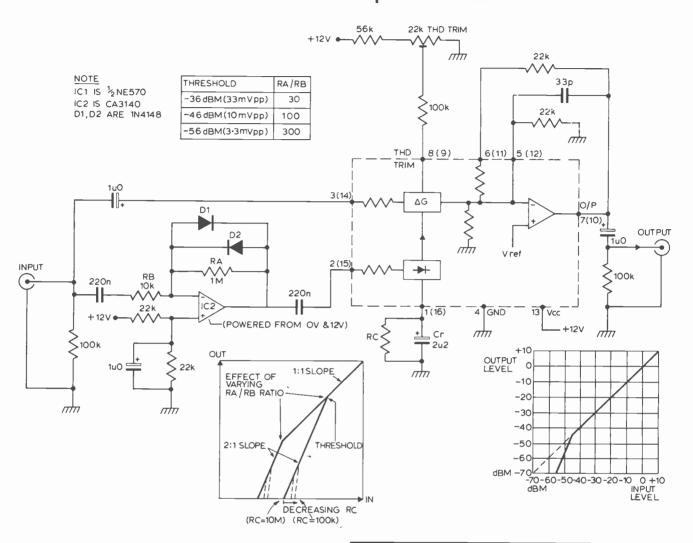




Transistor VCA

A circuit similar in operation to a CA3080 can be constructed with a matched pair of transistors and an op amp. Transistors Q1, 2 form a differential transistor pair which is used to step whatever current is available between the two collectors, just as in the CA3080. the difference between the collector currents is equal to the product of the input voltage times the current $\bar{l}_{\rm EE}$ times a constant. This difference is extracted by the differential amplifier IC1. The current $l_{\rm EE}$ is controlled by Qe. As the control voltage goes positive, Qe robs most of the current flowing down the 39k resistor, and hence $l_{\rm EE}$ and the output of IC1 decrease.

Two Channel Low Level Expander/Noise Gate



It is often required that a rather noisy signal be cleaned up a bit. This is not possible to do continuosuly, but it is possible to clean up noise in what was initially the gaps. The results of this cleaning up process can quite often be heard when telephone conversations from "foreign correspondents" are broadcast.

By turning down the signal level in the gaps, (by performing a low level expansion) the perceived sound quality improves dramatically.

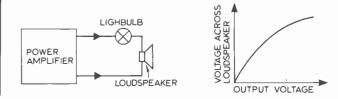
The circuit performs just such an expansion. The inputs signal passes through the variable gain cell and then appears at the op amp output. The gain of the gain cell is controlled by the signal coming from IC1. This is a high gain amplifier with diode clamping, so that the output swing is limited to about 1V0 ptp. Therefore for input signals of 10 mV pp to 10 V pp, the output of IC1 remains at about 1V0 ptp to 1V2 ptp.

So, for this range of input voltages the gain of the gain cell remains roughtly static. Now when the input level drops below 10 mV, the output of IC1 will start to fall and so will the gain of the gain cell. This produces a 2:1 downwards expansion curve, which means that the output then gets quieter at a rate faster than the input. To accentuate this effect, a bleed resistor can be placed in parallel with C.

The resistor robs some of the current that would have otherwise gone to the gain cell and causes the input output curve to roll off much more rapdily at low signal levels. Also, by varying the resistor ratio of RZ/RB, the expansion threshold level can be altered.

Incredibly Simple Compressor

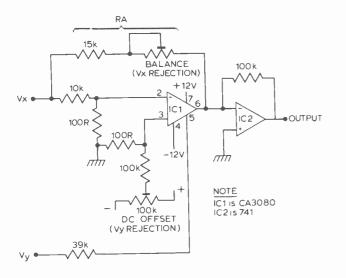
Not all gain control systems need be complicated or indeed active. One product which I saw advertised was a compressor to help prevent loudspeakeroverloads. All it was was a lightbulb in series with the loudspeaker. When the power exceeds a certain level, the lamp will turn on, glow, its resistance increases dramatically and hence a bigger percentage of the power output is dissipated in the lamp. A nice, simple solution, but I think it would require some experimentation to find the right sort of car headlamp bulb!

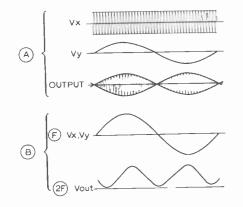


Switched Frequency Low Pass Filter

In this example the effective resistance is switched by using 4016 gates. The filter is a lowpass Butterworth and by turning gates A or B ON or OFF the cut off frequency can be altered. This allows the filter control to be physically remote or even to be computer controlled. Mark Space modulation of A and B would enable continuous control over the cut off frequency.

Four Quadrant Multiplication

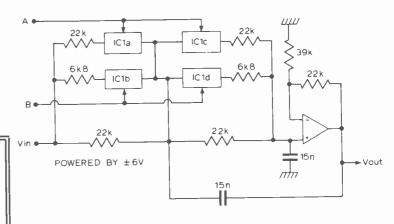




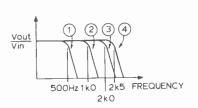
By using a few circuit tricks, the CA3080 can be made to perform 4 quadrant multiplication. In fact the CA3080 performs 2 quadrant multiplication and the trick is to move the axis on the multiplying graph. If we ignore the RA resistor chain then we have a 2 quadrant multiplier circuit similar to that shown previously. Imagine that V_x is a 1kHz sine wave. 1 Vptp and V_y is at 0V. The output of IC2 is a sine wave of fixed amplitude. Now if we connect RA, and adjust the balance control, it will be possible to cancel out the output, because the signal coming from IC1 is out of phase with that from the RA resistor chain. So with V_y set at 0 V there is no output for IC2. If V_y goes +ve, the output of IC1 will become greater than the current via the RA chain and the output if IC2 will grow.

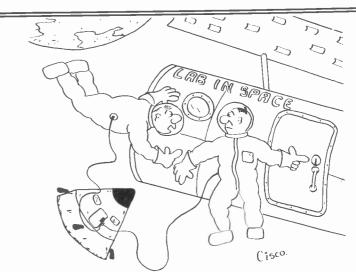
If V_y goes—ve the current through the RA chain will exceed that from IC1 and the output of IC2 will grow, the phase being opposite to that when V_y was a sinewave from an oscillator, then this circuit could be used to generate ring modulation effects.

When V_{ν} is set up 0V there may be some V_{ν} breakthrough and this can be minimised by adjusting the V_{ν} rejection preset.





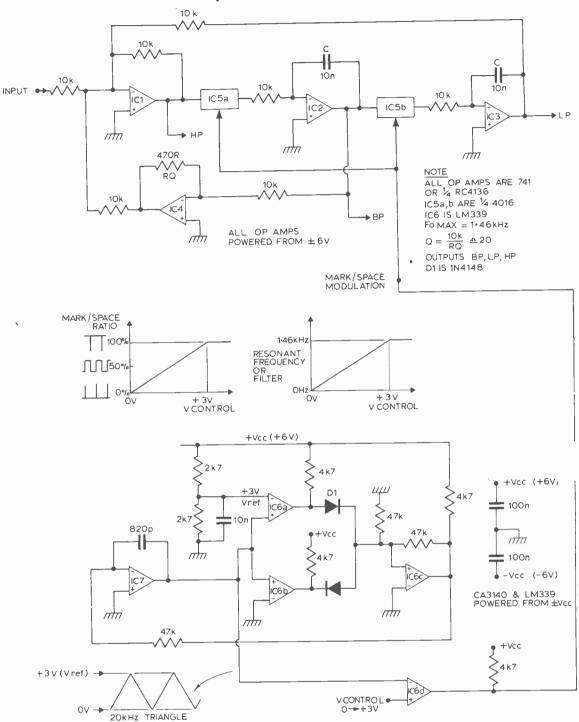




"No. I thought you were supposed to bring the key!"



Markspace Modulated Universal Filter



It is possible to change the gain of an amplifier by effectively altering the input resistor. This can be done by markspace modulating a voltage controlled switch in series with the resistor.

When the markspace ratio is low, the switch is OFF most of the time and the effective resistance is large. When the markspace ratio is high the switch is ON most of the time and the effective resistance approaches that of the series resistor.

Having generated a markspace control waveform, it is possible to gang up together literally hundreds of voltage controlled switches. This enables large numbers of variables to be simultaneously changed.

The circuit is a markspace modulated universal filter (IC-6) and the markspace generator itself (IC-11).

IC7-10 forms a triangle square wave oscillator. IC7 is an integrator whose outout ramps up and down between OV and a + 3 V reference. IC8-10 are all fast comparators. IC8 detects when the integrator outputs of IC8 & 9 are used to flip over a schmitt trigger IC10, which then drives the integrator. Thus the integrator output ramps up and down between OV and +3 V at a rate of 20 kHz.

It is important that the frequency of the markspace oscillator be relatively high. As a rule of thumb it should be 2 1/2 times the highest frequency components of the signals that you hope to process. The triangle output is fed into IC11's inverting input, the control voltage into the non inverting input. The output of IC11 is the markspace modulation which is used to drive the switches IC5,6. The filter resonant frequency is directly proportional to the mark space ratio that drives these switches

The number of IC's used is a quad package, and so is the 4016 and so can be the op amps (use RC4136). Thus the whole circuit can be realised with only 4 IC's. Also the mark space oscillator canbe used to drive other independent comparators.

240 Watts!

HY5

Preamplifier

The HY5 is a mono hybrid amplifier ideally suited for all applications. All common input functions (mag. Cartridge, tuner, etc.)) are catered for internally, the desired function is achieved either by a multi-way switch or direct connection to the appropriate pins. The internal volume and tone circuits merely require connecting to external potentiometers (not included). The HY5 is compatible with all I.L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is

I.L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is supplied with each pre-amplifier.

FEATURES: Complete pre-amplifier in single pack — Multi-function equalization — Low noise. — Low distortion. — High overload — two simply combined for stereo.

APPLICATIONS: Hi-Fi. — Mixers. — Disco. — Guitar and Organ. — Public address.

SPECIFICATIONS:
INPUTS: Magnetic Pick-up,3mV: Ceramic Pick-up, 30mV: Tuner. 100mV: Microphone. 10mV: Auxiliary 3.100mV: input impedance 47kt). at 1kHz. OUTPUTS. Tape. 100mV: Main output. 500mV. R.M.S.

ACTIVE TONE CONTROLS. Treble. ± 12dB at 10kHz; Bass. ± at 100Hz. DISTORTION. O. 1% at 1kHz. Signal/Noise. Ratio. 68dB.

OVERLOAD. 38dB on Magnetic Pick-up; SUPPLY VOLTAGE. ± 16.50V. Price. £6.27. + 78p.VAT. P&P. free.

HY5 mounting board. Bit. 48p. + 6p. VAT. P&P. free.

HY5 mounting board B1 48p + 6p VAT P&P free

HY30 The HY30 is an exciting New kit from I.E.P., it features a virtually indestructible I.C. with short circuit and thermal protection. The kit consists of I.C., heatsink, P.C. board, 4 resistors, 6 capacitors, mounting kit, together with easy to follow construction and operating instructions. This amplifier ideally suited to the beginner in audio who wishes to use the most up-to-date technology available: FEATURES: Complete kit.— Low Distortion.— Short, Open and Thermal Protection.— Easy to Build. APPLICATIONS: Updating audio equipment.— Guitar practice amplifier.— Test amplifier.— Audio oscillator. 15 Watts into 80

OUTPUT POWER 15W R.M.S. into 8Q DISTORTION 0.1% at 15W. INPUT SENSITIVITY 500mV FREQUENCY RESPONSE 10Hz-16kHz -- 3dB SUPPLY VOLTAGE #18V

Price £6.27 + 78p VAT. P&P free.



25 Watts into 8Ω

The HY50 leads I L P is total integration approach to power amplifier design. The amplifier features an integral heatsink together with the simplicity of no external components. During the past three years the amplifier has been refined to the extent that it must be one of the most reliable and robust High Fidelity modules in the World.

FEATURES: Low Distortion — Integral Heatsink — Only five connections — 7 Amp output transistors

-- No external components
APPLICATIONS: Medium Power Hi-Fi systems -- Low power disco -- Guitar amplifier
SPECIFICATIONS: INPUT SENSITIVITY 500mV
OUTPUT POWER 25W RMS in 8Ω LOAD IMPEDANCE 4-16Ω DISTORTION 0.04% at 25W at

SIGNAL/NOISE RATIO 75dB. FREQUENCY RESPONSE 10Hz-45kHz — 3dB

SLIPPLY VOLTAGE + 25V SIZE 105 50 25mm

Price £8.18 + £1.02 VAT. P&P free.

HY120

60 Watts into 80

The HY120 is the baby of LLP's new high power range, designed to meet the most exacting requirements including load line and thermal protection, this amplifier sets a new standard in modular

FEATURES: Very low distortion — Integral Heatsink — Load line protection — Thermal protection — Five connections — No external components

APPLICATIONS: Hi-F — High quality disco — Public address — Monitor amplifier — Guitar and

organ

SPECIFICATIONS:
INPUT SENSITIVITY 500mV

OUTPUT POWER 60W RMS into 8(). LOAD IMPEDANCE 4-16(). DISTORTION 0.04% at 60W at

SIGNAL/NOISE RATIO 90dB. FREQUENCY RESPONSE 10Hz-45kHz -- 3dB. SUPPLY VOLTAGE \$35V. Size: 114 x 50 x 85mm

Price £19.01 + £1.52 VAT. P&P free

HY200

120 Watts into 8Ω

The HY200, now improved to give an output of 120 Watts, has been designed to stand the most rugged conditions, such as disco or group while still retaining true Hi-Fi performance?

FEATURES: Thermal shytdown — Very low distortion — Load line protection — Integral Heasink —

No external components

APPLICATIONS: Hi-Fi -- Disco. -- Monitor -- Power Slave -- Industrial -- Public address.

SPECIFICATIONS:
IMPUT SENSITIVITY 500mV.

OUTPUT POWER 120W RMS into 8Ω. LOAD IMPEDANCE 4-16Ω. DISTORTION 0.05% at 100W at Tighat / Noise ratio 96db. Frequency response 10Hz-45kHz - 3db Supply Voltage +45V

SIZE 114 x 100 x 85mm

Price £27.99 + £2.24 VAT. P&P free.

HY400

240 Watts into 4Ω

The HY400 is LLP's "Big Daddy" of the range producing 240W into 4Ω ! It has been designed for high nower disco or public address applications. It the amplither is to be used at continuous high power levels a cooling Ian is recommended. The amplifier includes all the qualities of the rest of the family to lead the market as a true high power hi-fidelity nower module.

FEATURES: Thermal shutdown — Very low distortion — Load line protection — No external.

APPLICATIONS: Public address — Disco — Power slave — Industrial SPECIFICATIONS:

OUTPUT POWER 240W RMS into 4Q. LOAD IMPEDANCE 4-16Q. DISTORTION 0.1% at 240W at

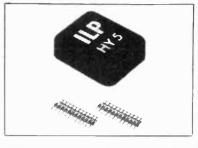
T kHz.

SIGNAL/NOISE RATIO 94dB FREQUENCY RESPONSE 10Hz-45kHz -- 3dB SUPPLY VOLTAGE

INPUT SENSITIVITY 500mV SIZE 114 x 100 x 85mm

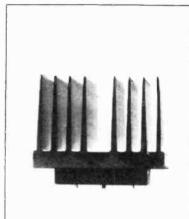
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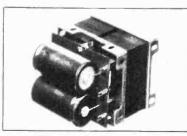
POWER SUPPLIES PSU36 suitable for two HY30's £6.44 + 81p VAT PSU50 suitable for two HY50's £8.18 + £1.02 VAT PSU70 suitable for two HY120's £14.58 + £1.17 VAT PSU90 suitable for one HY200 £15.19 + £1.21 VAT PSU180 suitable for two HY2000's or one HY400 £25.42 + £2.03 VAT











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packed in 1 hag E5
SET 5: Zeners 400mw 5
each. 20 valves 3 wolt to
33 wolt [retal 100] E5
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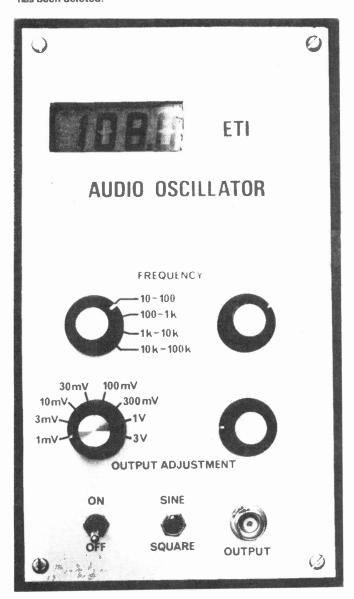
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AUDIO OSCILLATOR

WITH LCD DFM OPTION

An audio oscillator combines with a new design in frequency meters that provides accuracy and fast reading rates.

Front view of the audio oscillator. Note that this is an early prototype and the 3V range has been deleted.



THE WEIN BRIDGE oscillator published in our June issue did not provide a performance of adequate standard for many test applications-one would not have expected so from such a simplified design. Since then we have had many requests to provide a high performance oscillator.

This oscillator started life as another wein bridge, started to evolve as a voltage controlled sweep oscillator but when it became too complex reverted to a simple wein bridge.

One major problem with all home made oscillators is that of scaling the frequency dial. This is not just a problem of positioning the knob but since normally available potentiometers have a tolerance of \pm 20%, the scale length will also vary. In commercial units the use of an expensive wire wound potentiometer solves most of the problems giving reasonably accurate scaling.

We then decided to build in a frequency meter and the high power consumption and the poor resolution, especially at low frequencies, of previous designs led us to develop a completely new design.

This uses what is literally an analogue computer to convert a period measurement into frequency with some digital electronics controlling it and displaying the results. We based this on the Intersil ICL7106 IC which, due to its liquid crystal display drive circuitry, allows a low power consumption design. Due to the method of conversion from period to frequency the range is limited from about 50 to 1999 counts and therefore automatic range selection is used. As the oscillator itself has less range than this, this limitation is no problem.

To simplify wiring we initially used CMOS analogue switches to select the range changing capacitors in the oscillator but this unfortunately increased the second harmonic

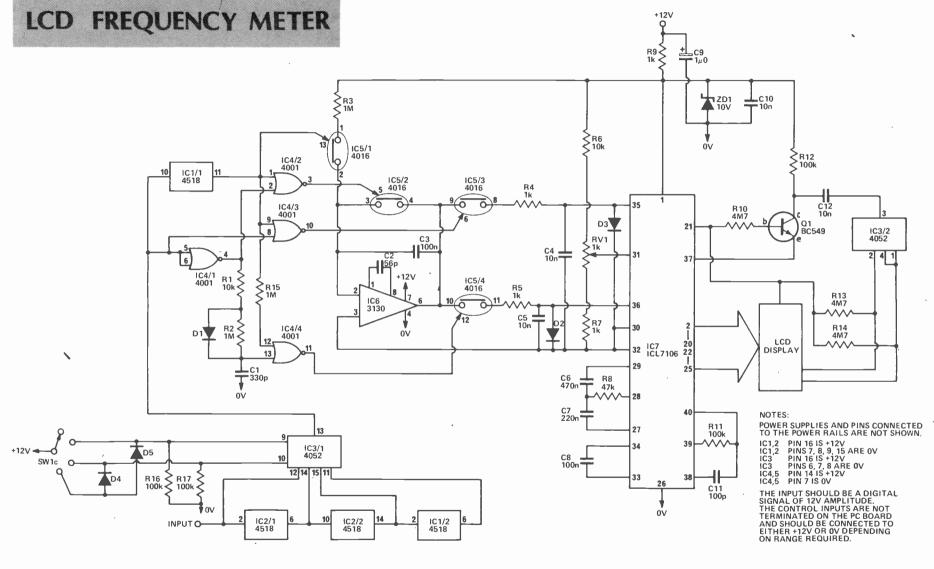
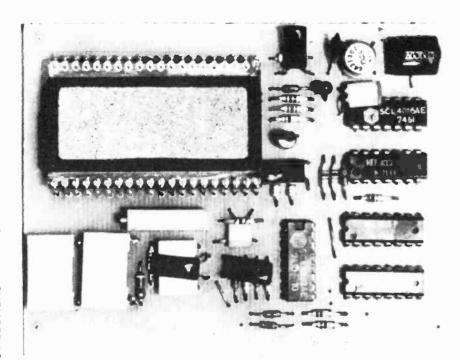


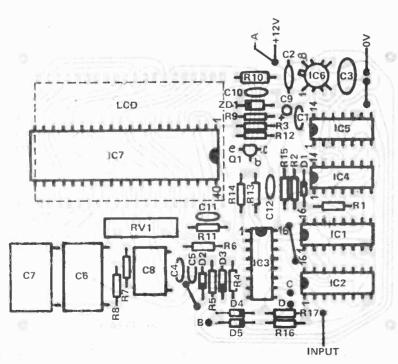
Fig. 1. The circuit diagram of the frequency meter section.

HOW IT WORKS

This section works by generating a voltage proportional to the period of one cycle and using this as the reference voltage for the Intersil voltmeter IC with a fixed voltage on the normal input. This gives the inverse function of normal operation and the display therefore is frequency.

and IC5/2 will turn on. This discharges C3 to zero volts. After a short delay to allow C3 to discharge IC5/4 is turned on transferring that voltage level onto C5. After a total of two cycles the process recommences. The voltage difference between the two capacitors is therefore the voltage *change*, (pro-





To generate the reference voltage we use an integrator (IC6) which is controlled by IC5. Operation is as follows. Initially C3 is discharged and for one cycle of the input signal IC5/1 turns on. As the IC7 provides a stable voltage between pin 1 and pin 32 of about 2.8V the output of IC6 will fall linearly with time and as IC5/1 is on for exactly one cycle the voltage change will be proportional to that period.

After IC5/1 turns off the output of IC6 will stay fixed. IC5/3 is then turned on and C4 will change to that voltage. After half a cycle IC5/3 will turn off leaving C4 at that voltage

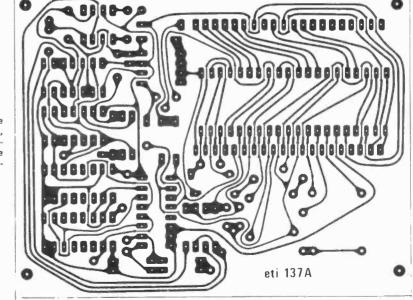
portional to frequency) thus eliminating any offset errors in IC6. The pulses which control IC5 are derived from IC1/1 and IC4.

A reference voltage less than half the input voltage will result in the ICL7106 counting past 2000 (over ranging). The two inputs must also lie within the supply rails (less 1.5V). This limits the range of the instrument from 5 Hz to 200 Hz. For the higher frequency ranges, three decade drivers are provided and the necessary output selected by IC3. The correct decimal point is also selected by the other half of this IC.

PARTS LIST

RE	SISTORS	all 1/4W 5%	C4, 5, 10, 12	2 10n polyester
	1, 6	10k	C6	470n polyester
R2	2, 3, 15	1MO	C7	220n polyester
R4	1, 5, 7, 9	1k	C9	1u0 35 V tantalum
R8	3	47k	C11	100p ceramic
R1	10, 13, 14	4M7		,
R1	11, 12, 16, 17	100k	SEMICONDL	ICTORS
			IC1, 2	4518
			IC3	4052
PC	DTENTIOMETER		IC4	4001
R∖	/1 1k ten tur	n trim	IC5	4016
			IC6	CA3130
CA	APACITORS		IC7	ICL7106
C1	1 330p cer	amic	Q1	BC549
C2	2 56p cerai	mic	D1-D5	1N914
C3	3, 8 100n pol	yester	ZD1	10 V 300mW Zener

Shown on this page are the foil pattern, overlay and photograph of the frequency meter section.



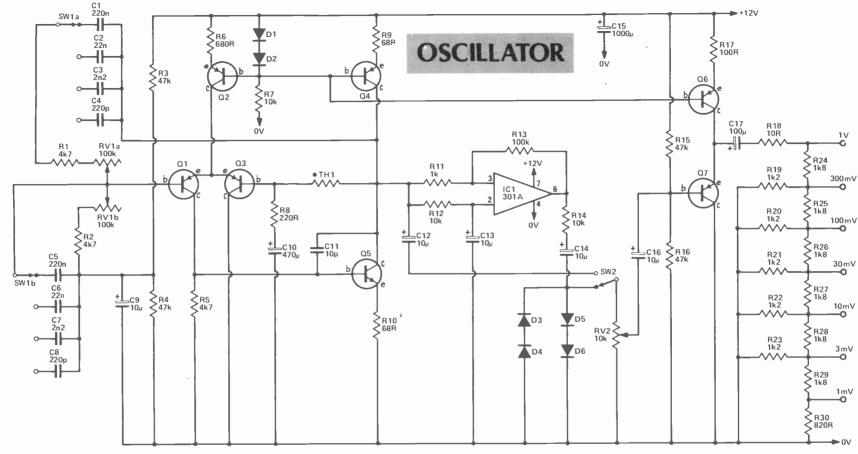


Fig. 2. The circuit diagram of the oscillator section.

PARTS LIST-

Oscillator Board CAPACITORS C1.5 220n polyester RESISTORS all 1/2W 5% C2, 6 22n polyester R1, 2, 5 4k7 2n 2 polyester C3, 7 R3, 4, 15, 16 47k C4, 8 220p ceramic C9, 12, 13, 14, 15 10u 25 V electrolytic R6 680R R7, 12, 14 10k 470µ 25 V electrolytic C10 R8 220R 10p ceramic C11 R9. 10 68R 1000u 16 V electrolytic C15 R11 1k 100u 25 V electrolytic C17 R13 100k R17 100R **SEMICONDUCTORS** R18 10R IC1 301A R19-R23 1k2 0.1 - 0.4BC559 R24-R29 1k8 R30 820R

HOW IT WORKS

The oscillator is of the conventional Wein bridge type with a differential amplifier made up by Q1-Q5. Gain stabilization is provided by the thermistor TH1. This type of circuit oscillates at the frequency where the impedance of the capacitors equals the resistors in the Wein bridge arms. With this feedback network the attenuation does not vary greatly like that of a twin tee but the phase

shift does. The result is a sine wave oscillator with low distortion.

For frequency variation a two gang potentiometer is used to give a 20/1 continuous variation with switched capacitors giving four ranges each a decade apart.

The sine wave output is converted to square wave by IC1 with the amplitude stabilized by D3-D6.

Q5 BC549 Q6, 7 BC559 D1-D6 1N914

CWITCHEC

SW2

SWITCHES SW1

Three pole four way rotary

POTENTIOMETERS

TH1

RV1 100k dual rotary RV2 10k lin rotary

THERM!STOR

type R53

MISCELLANEOUS PCB

*RV1 — the preferred curve giving best resolution is antilog. If reverse rotation is acceptable log is as good. Otherwise use a linear curve.

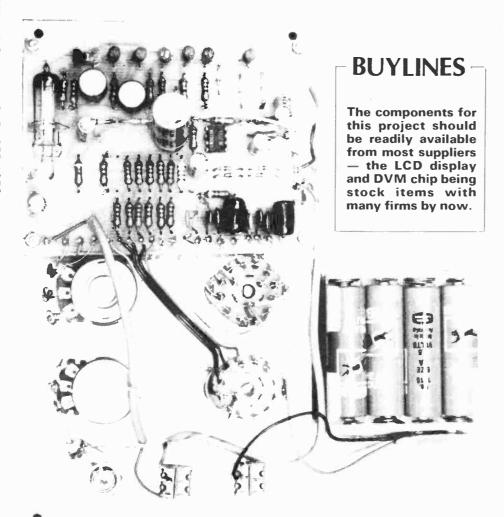
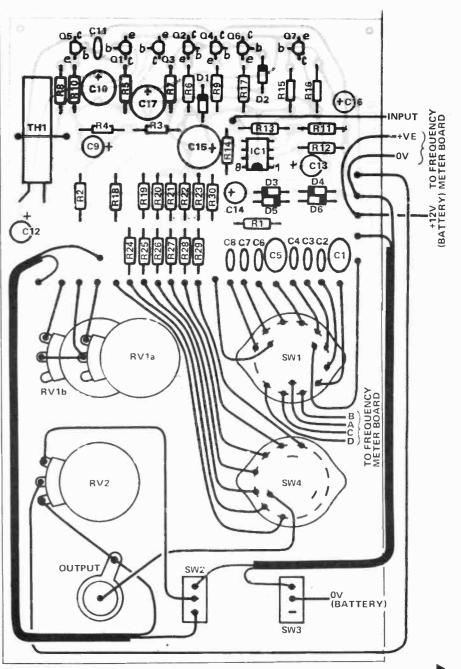


Fig. 3. Component overlay of the frequency meter board. Insert the LCD such that the +1 digit is on the left.



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INSIDE ULTRASONICS

Ultrasonic sound at very high frequencies is being used increasingly for medical diagnosis. Dr P. N. T. Wells of Bristol General Hospital reports.

THE IMPORTANCE OF ultrasonic diagnostic methods lies in the fundamental differences between them and other techniques such as radiology and radioisotope scanning. The symptoms of some diseases, and of natural conditions such as pregnancy, are best investigated by ultrasound. It maps out anatomical cross-sections, measures the performance of the heart and the flow of blood, and identifies many kinds of abnormality, including several types of cancer, all without encroaching into the body in any way.

Twenty-five years ago, doctors seeking to investigate the structures of the body had no alternative to X-rays and this often involved injections of substances to give better contrast to obtain information about soft tissues. Nowadays, ultrasonic methods have replaced radiology in helping to solve a number of clinical problems doctors depend on ultrasonic diagnosis, and patients demand this kind of investigation. The procedures are rapid and painless and nothing enters the body other than ultrasound waves. Unlike ionizing radiations, ultrasound at diagnostic exposure levels seems to be harmless.

Basic Principles

Most diagnostic applications of ultrasound depend on the reflection of ultrasonic waves at surfaces between tissue structures which differ in their so-called characteristic impedance. The characteristic impedance of a material is equal to the product of its density and the velocity of ultrasound within it. The densities of soft tissues, about 10^3 kg m⁻³ (kilograms per cubic metre), and the velocities of ultrasound within them, about 1500 m s⁻¹ (metres per second), are similar to those for water. When an ultrasonic wave strikes the boundary between tissues that differ in characteristic impedance, a proportion of the energy in the wave is reflected in much the same way that light is reflected when it meets a change in reflectivity at a surface.

The characteristic impedances of soft tissues are similar, so the echoes from their boundaries are very small. For example, only about 0.5 per cent of the

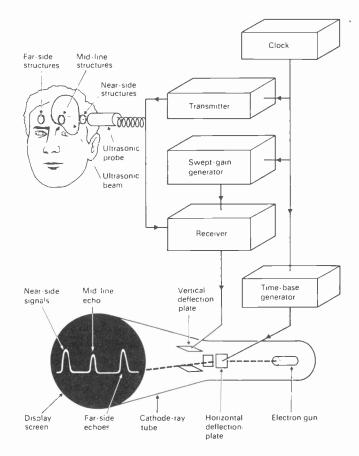


Fig. 1. Basic arrangement of the A-scope system, in use in this instance to show the mid-line structures of the brain in their relative position halfway between the sides of the skull, as indicated by symmetry of the deflections of the cathode-ray tube trace. Asymmetrical spacing of the deflections may mean that disease has brought about a physical change such as a tumour on one side of the brain. The swept-gain generator gradually increases the receiver amplification over each sweep of the time base to compensate for the attentuation of the deeper echoes by intervening tissues.

energy striking the boundary between kidney and fat is reflected. However, such echoes are large enough to be detected by a sensitive receiver, but almost all the energy crosses the boundary and is available for reflection by deeper structures.

Much larger reflections occur at boundaries between soft tissues and either bone or gas, because of large differences in characteristic impedance. These large reflections restrict the use of ultrasound in medical diagnosis. Moreover, it is necessary to exclude air from between the probe and the patient. This may be done either by examining through a water bath or through a film of oil smeared on the patient's skin.

Resolution

Ultrasonic echo-ranging techniques depend on the measurement of the time interval between the transmission of a brief pulse of energy and the reception of its echo, just as in radar. In any imaging system, whether using light, ultrasound or any other kind of radiation, the resolution is limited by the wavelength of the radiation. It is for this reason that ultrasound, as opposed to sound. is used in medical diagnosis. We need to visualise structures of only a few millimetres in size, so that wavelength has to be around a millimetre or less. In soft tissues, it is about 1.5 mm at a frequency of 1 MHz and proportionately less at higher frequencies. The highest audible frequency, about 20 kHz, has a wavelength of 75 mm. In principle, the performance might appear likely to improve as the frequency is increased, but ultrasound is attenuated as it travels through tissues and the rate of attenuation also increases with the frequency, so we have to compromise between better resolution and reduced penetration.

Pulse-Echo Techniques

In an ultrasonic instrument for diagnosis, a probe containing a piezoelectric transducer converts electrical signal into ultrasound waves for transmission into the patient. It does the opposite for the echoes.

The simplest type of ultrasonic pulse-echo diagnostic system is called the A-scope. (See Fig. 1). The clock triggers the transmitter, which feeds a brief pulse with a large amplitude to the transducer. Echoes return to the probe from those reflecting surfaces inside the patient that lie along the ultrasonic beam. Electrical signals from the echoes are amplified by the receiver and applied to the vertical deflection plates of the cathode-ray tube; the time-base generator, which is triggered into operation by the clock at the instant the ultrasonic pulse is transmitted by the probe, is connected to the horizontal deflection plates to drive the spot on the display at a constant speed from left to right. In this way the beam sweeping across the display is deflected vertically at intervals along the horizontal axis, corresponding in distance from the start of the sweep, to echo-producing surfaces at various distances along the ultrasonic beam. A special circuit in the receiver increases the amplification of the deeper echoes to compensate for their attentuation by intervening tissues. The clock operates at a repetitin rate fast enough to give a flicker-free trace on the display.

The A-scope has clinical applications in neurology, ophthalmology and internal medicine. It allows the

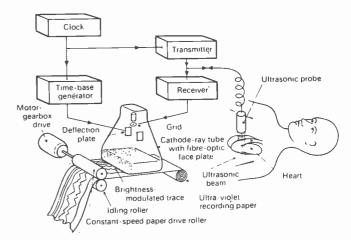


Fig. 2. Time-position recording system based on the B-scope display, shown in use for echocardiography. The fibre-optic face plate of the cathode-ray tube collects enough light to produce a self-developing trace on ultra-violet recording paper.

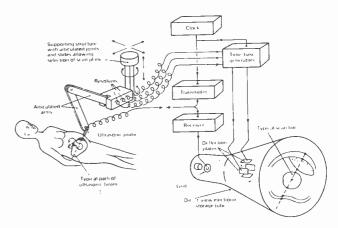


Fig. 3. Two-dimensional scanner and B-scope display system studying a foetus. The time-base generators are driven by electrical outputs from a series of resolvers that measure the position of the ultrasonic beam as it moves across the patient. Horizontal and vertical time-bases combine to deflect the spot in such a way that its movement across the display corresponds to the movement of the beam. Echoes received as the probe moves over the patient produce a cross-sectional image in a plane corresponding to that of the scan. In this example, the image is built-up on the screen of an electronic storage tube for direct viewing.

depths of echo-producing surfaces to be measured, and the characteristics of echoes from within structures to be studied.

Echoes from moving structures, such as the valves of the heart, oscillate in position along the horizontal axis, or time base, of the display. In cardiology particularly, patterns of movement can give diagnostic information. They can be studied by making recordings with the aid of a B-scope display (see Fig. 2).

In the B-scope, the time-base sweep is normally visible, but it is brightened by returning echoes to

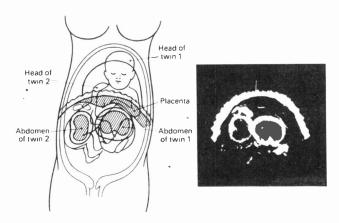


Fig. 4. A two-dimensional scan reveals twins at about 25 weeks of pregnancy. The placenta on the anterior wall of the uterus is clearly defined while the abdomens of the twins, identified in the explanatory diagram, appear in section.

produce spots of light on the display in places where, on an A-scope, there would be deflections of the beam. The positions of the spots of light correspond to echoproducing structures in the patient, and the pattern of their movement can be permanently recorded.

Cross-Sectional Images

The B-scope forms the basis of another display method, the two-dimensional ultrasonic scanner (see Fig. 3). The ultrasonic probe, instead of being held in the hand, is mounted on a scanner. It can be moved to any position in a two-dimensional plane. In this way it is possible to arrange for the beam to pass through structures lying in a chosen plane within the patient, while the position of

the probe and the direction of the beam are measured continuously by 'resolvers' mounted in the scanner. The electrical signals from the resolvers control two timebase generators, driving the vertical and horizontal beam deflection plates of a cathode-ray tube. The direction and position of the ultrasonic beam across the patient controls the position of the cathode-ray beam showing up on the display, related to the positions of the echo-producing surface.

A cross-sectional image of the surfaces can be built up photographically by a camera with an open shutter that records the bright spots on the display while the patient is being scanned. The echo information can also be stored electronically.

Two-dimensional scanners in which the probe is moved in contact with the patient produce individual images in scanning times of about 10 seconds, images can be produced at a much faster rate by moving the probe mechanically. Images in rapid succession allow physiological movements to be studied; their main importance is in cardiological diagnosis. But although these rapid mechanical scanners produce so-called real-time images, they lack flexibility. This difficulty can be overcome by using ultrasonic probes containing many separate transducer elements, operated separately or in groups, which can produce ultrasonic scans made up of parallel lines or or lines arranged in a fan shape, at frame rates of tens per second.

As well as making it possible to study rapidly moving structures, real-time scanners can also be used to explore large volumes of anatomy in a short time. A doctor using one can examine a patient in about a quarter of the time it takes with a 'conventional' twodimensional scanner.

Doppler Effect

The frequency of an ultrasonic wave reflected from a stationary structure is equal to that of the incident wave. If the beam is reflected by a surface which is moving

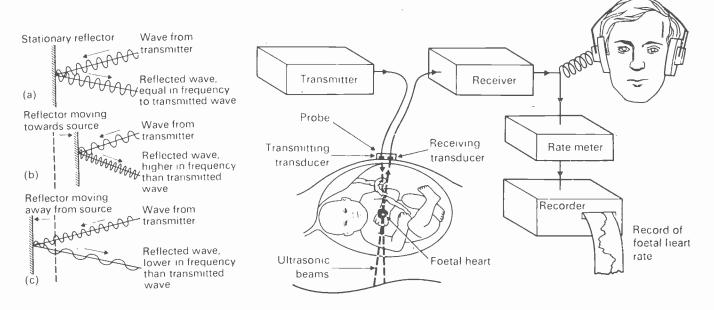


Fig. 5. The Doppler effect occurs when a wave is reflected from a moving surface, giving an upward or downward 'shift' in frequency as in (b) and (c).

Fig. 6. One use of the Doppler 'shift' is to monitor the foetal heart. The echoes usually fall in the range of audible frequencies.

FEATURE : Ultrasonics

towards the ultrasonic source, the reflected wave is compressed into a shorter space. This means that the wavelength is reduced. It shows as an upward 'shift' in its frequency. Reflection by a surface moving away from the source gives a downward shift. This phenomenon, the well-known Doppler effect, conveniently gives shift frequencies that fall in the audible range when ultrasound is reflected by moving structures in the body such as heart valves or flowing blood. A simple instrument based on this makes it possible to detect the movement of the foetal heart. Similar instruments to measure blood flow allow peripheral arterial disease to be assessed

Because Doppler shifted signals are received only from structures that move, two-dimensional maps of them can be built up by using a Doppler probe to scan the patient. In this way the distribution vessels close to the surface can be studied. Such information may obviate the need for X-ray angiography, which is a dangerous and expensive procedure

It can also be combined with other information about structure position obtained by the pulse-echo method, making it possible to map out blood vessels within the body and measure the rate of blood flow at the same time

The clinical value of ultrasonic techniques has already been proved, but their spread into general, everyday service will depend on the development of instruments that are simple to use. These, paradoxically, may be more complicated than the ones we already have. It will also mean training doctors and technicians to obtain and interpret results. But it is clear that ultrasonic diagnosis is, in many instances, the best and most economical way of getting the information essential to proper care of the patient. ETI

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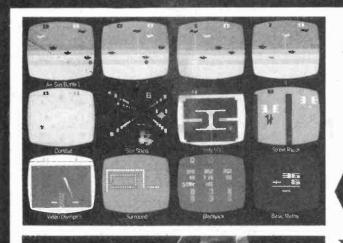
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BSR AUTOCHANGE RECORD PLAYER DECKS with cue device. 33-45-78 rpm. for 7. 10., 12 records. Fitted with SC 12M Stereo Ceramic cartridge and styll. Brand new, £14.00 + 12½% VAT

GARRARD AUTOCHANGE RECORD PLAYER DECKS. Model 6.300, with cue device. 33-45-78 rpm. for 7. 10. 12 records. Fitted with KS418 Stereo Ceramic cartridge and styll. Brand new. £16.00 + 12½% VAT

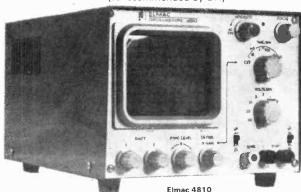
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- SPECIFICATIONS-

SPECIFICATIONS

VERTICAL DATA

VERTICAL AXIS (Y). Deflection Sensitivity — 100m V/division. Bandwidth (between 3 dB points) — DC — SMHz. Input Attenuator — (kalibrated) — 9 step 0.1 o. 2.0.5.1.2.5.10.20.50/div. Input Impedance — 1 Meg/40 pf in shunt. Input Voltage — Max — 600V P. P. HORIZONTAL AXIS (X). Deflection Sensitivity — 0.400mV/division. Bandwidth (between 3 dB points — 1Hz-350KHz. Gain Control — Continuous when time bases in EXT position. Input Impedance — 1 Meg. Input Voltage — Max — 600V P. P.

TIME BASE. Sweep Range (calibrated) — 100msec/div to 1u sec/dlv in 5 steps. FINE Control — Variable between steps — includes time-base calibration position. Blanking — Internal — on all ranges.

SYNCHRONISATION. Selection — Internal. external. Synchronisation Level — Continues from positive to negative.

ative. VER SUPPLY, Input voltage — 115/220V AC±10% at 50/60Hz. Power Oissipation — 18W F DATA — 4in. — flat face, single beam. — Maximum high voltage — 1.5kV. — Fitted with 8x10 CRT DATA — 4in. — flat face, single beam. — Maximum high voltage — 1.5kV. — Fitted with 8x10 division blue filter graticule.
PHYSICAL DATA Dimensions — 15cm (h) x 20.5cm (w) x 28cm (d). Weight — 4.3Kg (approx.). Stand—2 position flat and inclined. Case. — Steel, epoxy enamelled. Front Panel. — Aluminium, enamelled epoxy.

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audiophile

A woeful tale of the pre-amp to make you red in the face this month. Crimsons CPR1 considered by Ron Harris who recovered enough to visit Sony's launching of sixty new models!

MEANWHILE back at the Crimson pre-amp, I shall begin this month by finishing what I began last, and furnishing details of the CPR1 module. To begin with, quoting specs would be largely superfluous in this context, but as I know there are some of you out there high on numbers, try these for size:

SENSITIVITY:

3.4 mV RMS (1kHz) — mag

70 mV RMS 1kHz all others

for 770 mV RMS output

SIGNAL/NOISE: -70dB unweighted 10kHz bandwidth

mag

-86dB unweighted 10kHz bandwidth

others

CROSSTALK: THD:

-80dB 20 Hz-20 kHz

.008% any level below clipping

There are pages of figures in the leaflets Crimson issue for free, so if you've at all interested get after one of those. The nice thing about these specs is their completeness — nothing hidden away here in shrouds of triviality. All the parameters are given as test results under very precise conditions. I could find no reason to argue with any of them and as I'm usually mean and nasty about such things Crimson should take that as praise indeed.

Building Up To It

Power requirements are simply 15-0-15 at under 100mA, and mine measured in the region of 40mA per channel while in full flow. Crimson naturally produce a PSU for this, and it is termed not unreasonably REG1.

The pre-amp arrives as an assembled PCB with set of application notes, and as such cannot be considered a kit by any but the most stretched imagination. Not for the beginner this, as a fair bit of experience comes in most handy — although the notes are very good (but poorly produced) and if you're feeling brave by all means get stuck in — I shan't say 'I told you' — not too loud anyway.

After a few minutes fussing around with pen and paper I decided to house pre-amp and PSU in separate boxes — with appropriate nod in direction of Meridian — for reason of neatness and hum foiling. Let me say now that these circuits are good enough to merit such attention

As there are no tone controls, metalwork is simplified I'm glad to say, and for a basic system should be very easy indeed.

Crimson make out a very good case in their design notes for doing things their way, but nonetheless there are a few things I would like to disagree with.

Firstly they feed straight into the volume control with auxiliary inputs via the selector switch. This presents the equipment driving into the amp with a varying load, and I would personally prefer to see a high impedence buffer in there, with a lower sensitivity; than the 70 mV now prevailing, and a higher input impedance. A small point perhaps, but under music conditions a constant load is to be preferred I feel.

I'm offering up these ideas for perusal, not criticising Crimson in particular, its just that the Crimson approach encourages you to drag out your personal theories and give 'em a good airing. I'd be very interested to hear from any of you out there with your ideas on how audio design should be done — we'll print the best we get.

Back To Wires

Anyway to return to the point the CPR1 auditioned very well indeed. Mind you our first sample gave me a hard time for a while. It kept doing things it couldn't do and doing them when I least expected it. After a few bottles of Vallium and several hair pulling sessions with Crimson we discovered I'd been given a non-production board. A quick GPO job and we're back in business. Sanity is saved.

I still don't know what the odd sample was up to — and don't intend to to find out any further that way lies madness. I suspect Crimson save that board to assassinate reviewers in the most fiendish way possible. Who'd believe it was murder?

The production model has never given the slightest problem and has behaved impeccably throughout. I compliment Crimson on the attentive way they panicked along with me over the rogue PCB, several poor unsuspecting boards now on soak test because of my nervous breakdown.

Inputting Pickups

To use this input, you add a passive network to the input to optimise loading for the particular device in use. Crimson themselves recommend adding several networks and switches to increase flexibility. I don't. Switches at this signal level are a menace — if you don't believe me, see Stan Curtis's article elsewhere in this

issue. Leave out the switches and hardware for your choice of pickup — how often do you change anyway?

With the switches added a thickening of detail occurs, and transients don't transient nearly as well.

Other inputs are straightforward, although perhaps a little low on input impedance. Noise and hum were commendably low on all inputs, and the separate boxes earn their worth on first power-up. The ten second switch-on blank period to eliminate 'clunks' is a great idea, although on both my samples the delay was so long I almost had time to go make a cup of tea before power came through.

It can be most detrimental to confidence to be left standing there, soldering iron still smoking, poised over the completed unit hand on power switch counting off seconds wondering why the b. . . .y hell it hasn't come on yet. Smiles fade rapidly like that.

Listen In

On magnetic input the Crimson CPR1 produced a very nice sound indeed, of very high quality with good detail and fair extension into the bass registers. On a quick A-B with a very highly priced integrated amp the CPR1 surprised me by showing itself clearly superior! OK wiseguy — wheel out the heavies.

Now my personal idol amongst pre-amps is the Lecson AC1 which I feel has never been approached for quality of reproduction, at any price. As such it makes an excellent reference against which to judge lesser machines. However not everyone agrees, and a champion of the Naim offered up his favourite to give the Crimson a run.

You can see from the opposition how seriously the CPR1 managed to get itself taken. Against the Lecson it was frankly outclassed. The AC1 had better depth, and better bass control. Treble came out smoother from the Lecson showing up the Crimson as slightly hard in this register. Mind you the Lecson costs nearly ten times as much and the Crimson gave a very good account of itself

Comparing it with the Naim unit nearly lost me a friend. I preferred the CPR1! There was not much in it mind you, and Crimson can be justly proud to have-produced a home build design capable of this level of performance.

grumbles

A few niggles. The balance control is very limited in operation. More so than is even trendy, never mind useful, and a little extra swing would do no harm. I'm not at all happy about those auxiliary inputs really, but they seemed to cause no problems so I'll shut up about them.

In order to obtain the level of performance the design can offer very careful construction is required. All cables screened. All as short as possible. Good soldering. Good earthing. Isolated PSU and sound routing of cables carrying HT — away from anywhere at signal level. Leave the on-off switch on the PSU box so that mains need not even enter the case.

Also the subjective quality, although of a very high quality, is a little hard, and judged against the best designs around slightly lacking in detail. Still none of this detracts from the fact that here we have a DIY amplifier that can compete with the very best commercial units, and make mincemeat of many far higher priced designs. Highly recommended.

Outlook: Warm and Sony

Sony have gone berserk. Only gone and scrapped practically their entire hi-fi range they have and launched no less than 60 new models if you please. Its enough to give leaflet collectors a heart attack. There is some very clever gadgetry in amongst the flock, and scattered here before you are some of the gems.

The TA-E88 looks very, very interesting indeed, representing as it does the state-of-the-art for Japanese pre-amp design. I'm at present still on my knees to Sony (and my trousers are wearing out fast) to get a closer look so hopefully more details on that one later (Please Mr Sony? . . . Sir?).

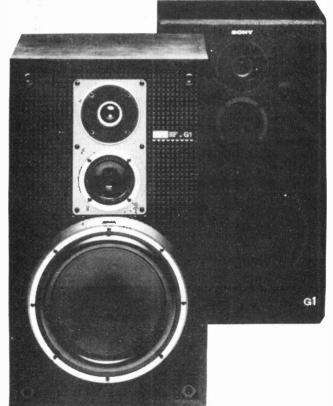
The G1 and G7 speakers came as a surprise too, they're better than any oriental offering previously to assail my ear drums, and are capable of giving any competitor a good run for its cones.

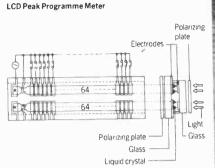
They have divided up the dealers too, creating a new super-fi franchise. This basically means that only the best dealers can sell the best of the range, although the division looks to be a bit unsure in places.

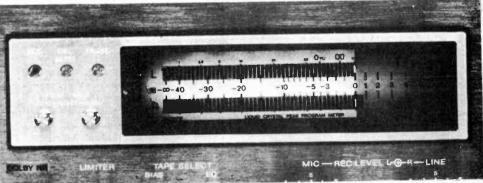


And here we have the TC K8B the new £469 cassette deck released as part of the super-fi Sony range. It incorporates that magnificent LCD display (details on the right) and on the short listen so far gave an excellent audio account of itself.

Below: the G1 speakers. Very good indeed for the price (circa £190 the pair) and deserving of none of the usual anti-Japanese speaker bias. Give them a listen if you get the chance.







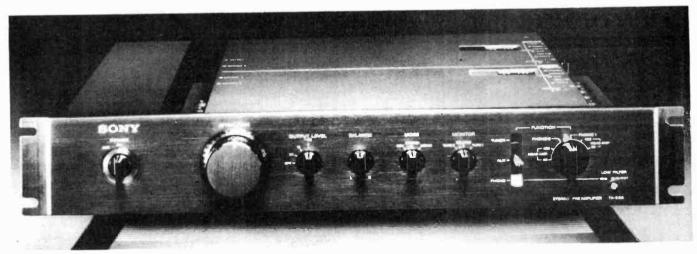


Above: the LCD level meter as used in the TCK8B in close-up. This uses 64 segments to indicate signal level, and has red settable stops to hold peak values. The colours are nice too!

This is a nice touch. A portable Elcaset machine. Gives really nice reproduction and is quite easy to cart around. All the controls are mounted so as to be accessible when in mid-carry. The format would seem to be ideal for this usage. All the quality of a reel-to-reel and no fiddling about while rapidly unspooling tape in a gale! Priced sensibly at £459 and called the EL D8 for the wandering rich amongst you.

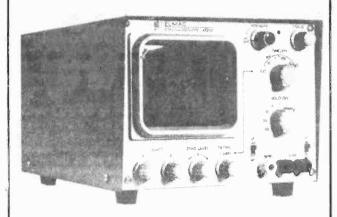
Not that I'm obsessed with cassette decks or anything, but here's another one. The TC K6B this time. It's main little gimmick is the MPU program selector. That little LED display in the centre can be stepped to read the number of the track you wish to hear. The machine will promptly go and find it and play it for you. Again LCD level meters, although not as good as the TC K8Bs obviously less segments. Below: The incredible TAE8B. The unit has two COMPLETELY separate channels inside its box. Selectable phono load on one input, and one straight in for people who don't like switches in line (Like me) at this low level. Moving coil pre-pre amp is standard of course. Price £699 (What did you expect?)





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14.31818 MH 18.000 MHz 18.432 MHz 20.000 MHz 22.1184 MHz 27.648 MHz 38.6666 MHz

100.000 MHz

SPECIFICATIONS

ELECTRICAL DATA VERTICAL AXIS (Y) **Deflection Sensitivity** Bandwidth (between 3 dB points)

Input Attenuator - (calibrated)

Input Impedance Input Voltage -- Max HORIZONTAL AXIS (X) **Deflection Sensitivity** Bandwidth (between 3dB points) Gain Control Input Impedance Input Voltage - Max TIME BASE Sweep Range (calibrated) FINE Control

Blanking SYNCHRONIZATION Selection Synchronization Level POWER SUPPLY 115/220V AC ± 10% at 50/60Hz **Power Dissipation**

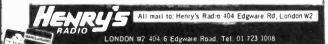
CRT DATA

PHYSICAL DATA Dimensions Weight Case Front Panel

- 100m V/division
- -DC 5MHz
- 9 step 0.1, 0.2, 0.5, 1, 2, 5,
- 10.20 50V/div
- 1 Meg/40 pf in shunt
 600V P.P
- 0-400mV/division
- 1Hz-350KHz
- Continuous, when time base in EXT position
- 1 Meg 600V P.P - 100msec/div to 1µ sec/
- div in 5 steps
- Variable between steps includes timebase calibration position
- Internal on all ranges
- Internal, external
- Continues from positive to negative Input Voltage
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- Fitted with 8x10 division blue filter graticule
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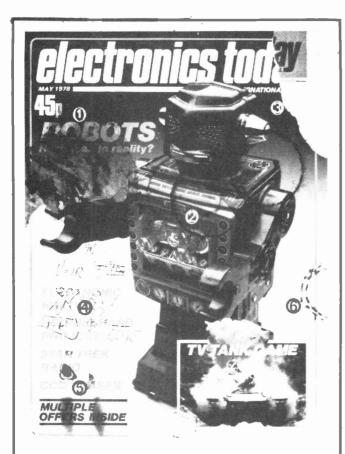
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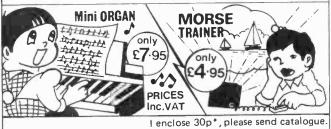
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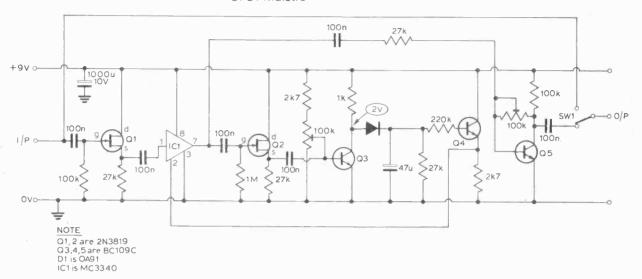
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S. D. Maistre



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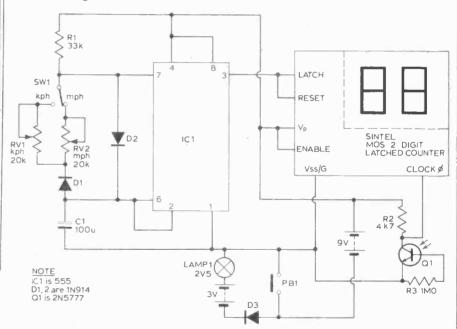
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This unit provides push-bike speed measurement between zero and 100 km hr or 100 mph! The circuit is based on the Sintel MOS counter block, which counts the pulses from the photo transistor Q1

These pulses are provided by fixing 18 aluminium 'barriers' to the wheels Q1 was an unmarked type in the prototype, in a TO 18 package. This mounts in an old felt-tip pen

Digital Bike Speed

B. Lemming



case opposite the lamp so that the barriers interrupt the beam in operation. The counter operates whilst PB1 is pressed, but latches after a time determined by RV1 or RV2. IC1 and associated components. IC1 forms a square-wave oscillator with

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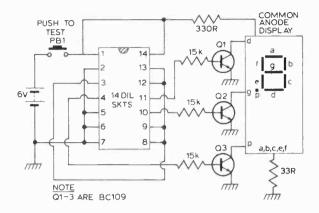




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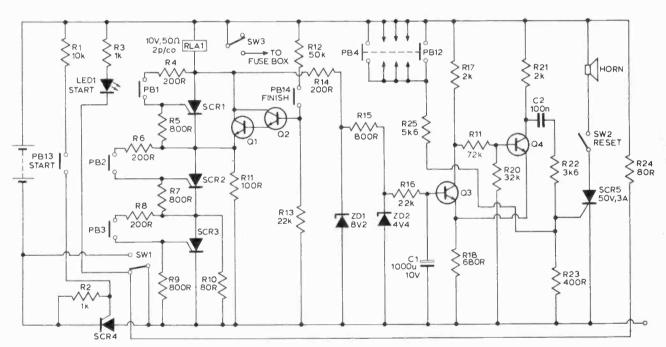
C. Ching

This circuit can be used to distinguish four types of dual input gates — AND, OR, NAND, NOR — it is also a quick method of checking IC function. If an AND gate is inserted into the socket, an A appears on the LED. An O denotes an OR gate. The decimal point is used to denote inverted function, i.e. .A is an NAND gate.



Electronic Ignition Switch

K. A. Last



NOTE
01 is BFY50
02.4 are BC108
03 are BC108C
SCR1-4 50V,1A TYPES
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LED1 is Til 209
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6.5A	v	νì	th	1	ri	g	g	er							80p
8A						-	Ϊ.								74p
12A															84p
16A														1	05p
20A															65p
25A														-1	90p
SCR	(C	1	0	6	C	1)	5	Α	/	4	0	0	٧		50p
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Standar	d 2	2	4	Ō'	V	r	n	a	n	s	F	ri	mary
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9-0-9V													90p
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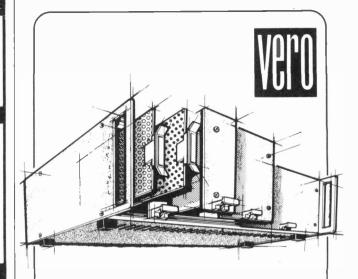
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AC126 18p		15p							74195	85p	4507	59p	8Y127 16p	2.7V to 33V	10p
AC127 17p	BC547	11p	0028	108p	2N4123	23p	7492	45p	74196	92p	4510	110p	DA47 10p		
AC128 16p	8C548	10p	0035	108p		ı	7493	34p	74197	92p	4516	110p	0A81 15p		
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AC151 42p	8CY30	67p	0084	46p	TTL	- 1	7496	62p		.	4520	110p	1N914 4p	3.75" x 5"	67p
	8CY34	74p	T1P29	40p	7400	12p	7497	240p	CMOS		4528			· ·	
												107p	1N916 5p		
AC153 58p	8CY59	24p	TIP30	40p	7401	12p	74100	95p	4000	15p	4578	27p	1N4001 4p	CERAMIC CAP 50V	
AC176 18p	8CY70	14p	TIP31	50p	7402	12p	74104	50p	4001	15p	4583	83p	1N4002 4p		0
AC187 23p	8CY71	14p	TIP32	55p	7403	12p	74105	40p	4002	16p	4585	105p		22pF to 47000pF	2p
											4000	Juah	1N4003 6p		
AC188 23p	80115	52p	TIP33	75p	7404	13p	74107	28p	4006	110p			1N4004 7p		
AD149 65p	B0121	79p	TIP34	98p	7405	13p	74109	45p	4007	16p	VDLTAGE		1N4005 10p	POLYESTER CAP 250V	
AD161 38p	80123	79p	TIP35A	253p	7406	29p	74110	46p	4008	94p	REGULATO	De .		.01, .015, .022, .033, .047, .068, .1 uF	F 5p
					7407								IN4006 10p	.152233	7p
A0162 38p	80124	97p	TIP36A	389p		29p	74116	160p	4009	46p	7805	82p	1N4007 11p		
AF114 30p	B0131	35p	T1P41A	59p	7408	14p	74118	82p	4010	50p	7812	82p	1N5400 16p	.47, .68 uF	13p
AF118 30p	80132	35p	TIP42A	59p	7409	140	74120	125p	4011	150	7815	82p		1 uF	17p
					7410								1N5401 16p	2.2 uF	28p
AF125 27p	80135	38p	TIP2955	126p		12p	74121	26p	4012	16p	7818	82p	1N5402 18p		
AF126 27p	80136	36p	TIP3055	64p	7411	19p	74122	55p	4013	35p	7824	82p	1N5403 20p	6.8 uF	48p
AF127 27p	B0137	38p	ZTX108	14p	7412	21p	74123	40p	4014	108p	7905	98p			
					7413	25p	74125								
AF139 36p	80138	38p	ZTX109	14p				45p	4015	80p	7912	98p		ELECTROLYTIC CAP 25V	
AF186 54p	80139	35p	ZTX300	16p	7414	54p	74126	46p	4016	45p	7915	98p		1uF to 47 uF	7p
AF239 40p	80140	35p	ZTX500	16p	7416	27p	74132	70p	4017	65p	7918	98p		68uF, 100uF	8p
	8F115	25p	2N706	13p	7417	27p	74136	79p	4018	90p	7924	98p			
ASY53 81p											1924	aoh	BRIDGE	150uF	9p
ASY54 81p	BF167	29p	2N1131	23p	7420	12p	74141 .	56p	4019	65p		- 1		220uF	10p
ASY55 69 p	8F173	27p	2N1132	23p	7421	28p	74142	202p	4020	95p	THYRISTOR	RS	RECTIFIERS	330uF	12p
	8F178	34p	2N1302	38p	7422	18p	74145	65p	4021	98p	1A/50V	28p	1A/50V 22p		
													1A/100V 24p	470uF	15p
8C108 8p	8F179	37p	2N1304	54p	7427	25p	74147	135p	4022	85p	1A/100V	30p			
BC109 8p	8F180	37p	2N1305	25p	7428	34p	74148	120p	4023	15p	1A/200V	38p			
BC113 17p	BF181	37p	2N1306	39p	7430	12p	74150	70p			1A/400V	40p	1A/400V 30p	RESISTORS 0.25W	
					7432	24p	74151	50p	00000				2A/50V 34p	4.7 ohms to 1 Mohm	1 m
8C117 20p	8F182	37p	2N13D8	40p					CMOS		3A/100V	36p	2A/100V 36p	4.7 0111113 TO 1 MOTHE	1р
8C119 29p	8F183	37p	2N1613	22p	7433	32p	74153	60p	4024	68p	3A/200V	38p			
BC140 34p	8F184	28p	2N1711	21p	7437	24p	74154	106p	4025	15p	3A/400V	51p	2A/200V 38p		
			2N1893		7438	24p	74155	63p	4027	35p	0107 4004	9.10	2A/400V 40p	POTENTIOMETERS (carbon)	
BC142 27p	BF185	30p		44p										1 Kohm to 2 Mehms log/linear	26p
BC143 27p	8F194	13p	2N2217	27p	7440	13p	74156	63p	4028	70p	LINEARS				
BC147 7p	8F196	13p	2N2219	21 p	7441	52p	74157	63p	4029	90p	710CN	41p		5 Kohm to 1 Mehm log switched	60p
	BF197		2N2369	16p	7442	45p	74160	g08	4030	50p	741 DIL8	22"			
8C149 8p		16p										22p			
BC157 9p	8F198	16p	2N2483	26p	7443	90p	74161	80p	4035	130p	747C 01L14	70p		PRESETS 0.1W horizontal	
8C158 9p	8F200	36p	2N2484	22p	7444	90p	74162	80p	4041	87p	748C 01L8	35p	OPTO/DISPLAYS	100 ohm to 1 Mohm	6р
	BF224		2N2905	22p	7445	70p	74163	g08	4042	75p		80p	2N5777 50p	TOO ORM TO I MONIN	op
BC159 9p		16p					74164	70p			CA3011		OCP71 70p		
BC168 8p	8F257	37p	2N2906	22p	7446	70p			4043	85p	CA3018	80p			
BC170 9p	8F258	40p	2N2907	22	7447	55p	74 165	90p	4044	96p	CA3028A	85p		TRANSFORMERS 240V primary	
8C171 9p	8F259	44p	2N2926G		7448	58b	74166	100p	4047	95p	CA3035	140p	0L704 90p	6-0-6V 100mA	95p
				470	7450	14p	4107	270p	4048				0L707 90p	0-6VX2 1AX2	360p
BC172 9p	BFR39	30p	2N2926R	100						63p	CA3036	120p	LEO 3/5mm		
8C173 9p	8FR40	30p	2N3053	100	7451	14p	74173	120	4049	40p	CA3046	75p		9-0-9V 100mA	250p
8C182 10p	8FR79	30p	2N3054	50p	7452	13p	74174	90p	4050	45p	CA3054	110p	Red 10p	9-0-9V 1A	290p
						19p	74175	70p		40p			Yellow 17p	9-0-9V 2A	400p
8C183 10p	BFR80	30p	2N3055	50p	7453				4066		CA3080	70p			
8C184 10p	BFX29	25p	2N3702	8p	7454	14p	74176	90p	4068	16p	CA3140E	74p		0-12V 2A	370p
8C186 23p	8FX30	38p	2N3703	8p	7460	14p	74177	90p	4069	16p	LM301AN	30p	Ctip 3p	0-15VX2 200mAX2	240p
		29p	2N3704		7472	28p	74178	120p					•		
BC187 26p	8FX85			8p					4070	16p	LM308N	54p		•	
8C207 13p	8FX86	31p	2N3706	9p	7472	24p	74180	85p	4071	16p	LM380N	76p			
8C212 1Op	8FX87	20p	2N3707	9p	7473	25p	74181	195p	4072	20p	LM381N	105p	Add 25p f	or p&p. All items new and full sp.	ec
			2N3710	8p	7474	25p	74182	75p		20p	NESSS				
BC213 10p	8FY50	15p							4073			25p			
8C214 10p	8FY51	15p	2N3711	8p ·	7475	48p	74184	243p	4077	65p	NE556	65p	DELT	A TECH O C	
8C237 14p	8FY53	28p	2N3772	177p	7476	28p	74190	72p	4078	16p	T8A641	240p		A TECH & C	ıu.
	8SX19	25p	2N3773	230p	7480	46p	74191	100p		19p	T8A800	70p		A I PAII O	
BC238 14p									4081						
8C301 30p	8SX20	21p	2N3866	61p	7485	90p	74192	100p	4082	23p	TBAB10	113p	62 NAYLOR	ROAD, LONDON, N20	OHN
					7486	26p	74193	110		24p			OF IEW I FOLL	,,, ,, ,,	
BC303 30p	80205	140p	2N3904	8p	/400	200	14190	110p	4086	240 (

DIODES/A 1N914 100v 1N4005 600v 1N4007 1000v 1N4148 75v 1N4733 5.1v 1N753A 6.2v 1N758A 10v 1N759A 12v 1N5243 13v 1N5244B 14v 1N5245B 15v	100 1 100 1 W 500 mV	RS mA .05 A .08 A .15 mA .05 Zener .25 W Zener .2525252525	8-pin 14-pin 16-pin * 18-pin 22-pin 24-pin 28-pin 40-pin Molex p	pcb pcb pcb pcb pcb pcb pcb pcb	S/BRIDGES .20 ww .20 ww .20 ww .25 ww .35 ww .35 ww .45 ww .50 ww To-3 Sockets 100-prv	.35 .40 .40 .75 .95 .95 1.25	TRA 2N2222 2N2907 2N3906 2N3904 2N3054 2N3055 T1P125 LED Green, D.L.747 MAN72 MAN3610 MAN82A MAN74A FND359	NPN (2NZ PNP PNP (Plas NPN (Plas NPN 15, PNP 15, PNP 15, 7 seg 5/8" 7 seg com- 7 seg com- 7 seg com- 7 seg com-	RS, LEDS, etc. 2222 Plastic .10) .15 .15 stic - Unmarked) .10 stic - Unmarked) .10 .35 A 60v .50 rlington35 Yellow .15 High com-anode (Red) .25 -anode (Red) .25 -anode (Yellow) .25 -anode (Red) .25 -acthode (Red) .25 -cathode (Red) .25 -cathode (Red) .25 -cathode (Red) .25
C MOS 4000	7400 7401 7402 7403 7404 7405 7406 7407 7408 7409 7410 7411 7412 7413 7414 7416 7427 7430 7432 7437 7438 7440 7441 7442 7443 7444 7445 7446 7447 7448 7450 7451	.10 .15 .15 .15 .10 .25 .25 .55 .15 .15 .25 .25 .25 .25 .25 .25 .20 .20 .20 .20 .20 .20 .20 .20 .20 .20	7473 7474 7475 7476 7480 7481 7483 7485 7486 7489 7490 7491 7492 7493 7494 7495 7496 74100 74107 74121 74122 74123 74125 74126 74132 74151 74150 74151 74150 74151 74156 74161 74163 74166 74175	.25 .30 .35 .40 .55 .75 .25 1.05 .45 .75 .60 .80 1.15 .35 .35 .35 .45 .35 .35 .35 .35 .35 .35 .35 .35 .35 .3	— T T T 74176 74180 74181 74182 74190 74191 74192 74195 74196 74197 74198 74221 74367 75108A 75491 75492 74H00 74H01 74H05 74H08 74H10 74H11 74H15 74H20 74H21 74H20 74H30 74H30 74H50 74H51 74H55 74H55 74H55 74H55	L — .85 .55 2.25 .75 1.25 .95 .95 .95 .95 1.40 .75 .35 .20 .20 .25 .25 .40 .20 .25 .25 .25 .25 .25 .25 .20	74H72 74H101 74H103 74H106 74L00 74L02 74L03 74L04 74L10 74L20 74L30 74L51 74L55 74L72 74L73 74L74 74L75 74L93 74L123 74S00 74S02 74S03 74S04 74S05 74S08 74S11 74S20 74S40 74S50 74S51 74S64 74S74 74S112 74S114	.35 .75 .55 .95 .20 .25 .30 .20 .35 .45 .45 .65 .45 .45 .55 .85 .35 .35 .35 .35 .35 .35 .25 .35 .35 .35 .35 .35 .35 .35 .35 .25 .35 .35 .35 .35 .35 .35 .35 .35 .35 .3	74\$133 .40 74\$140 .55 74\$151 .30 74\$153 .35 74\$157 .75 74\$158 .30 74\$194 1.05 74\$194 1.05 74\$194 1.05 74\$194 1.05 74\$100 .20 74\$101 .20 74\$102 .20 74\$103 .25 74\$104 .20 74\$105 .25 74\$109 .25 74\$11 .25 74\$11 .25 74\$11 .25 74\$11 .25 74\$11 .25 74\$12 .25 74\$11 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 74\$12 .25 <tr< td=""></tr<>
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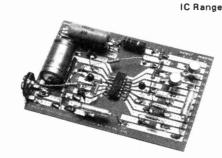
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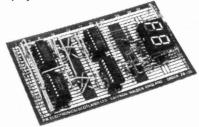
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Yellow	20p	15p	15p	disc
Green	20p	16p	16p	0 0
Orange	24p /he	29p/heL	17p	28 %
he= high efficiency	L= lens	end		10
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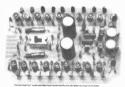
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7400	.12	7497 74100	2.38	74196 74197	.90	74L8153 74L8154	1.20	4024	.66	4518 1.8: 4518 .9i	9000	nW ZENER	, BD 133	.50	TIP418	.70	204058 .12	ELECTROLYTICS
7401	.12	74104	.40	74197	.90° 1.48	74LS155	.86	4026	1.28	4519 .50	2.7V-3	NODES 3V .09 each	BD 135 BO 139	.44	TIP41C	.80 .72	204062 .12 204124 .16	uf 16v 25v 40v 63vn 1 .045 .05 .055 .006
7402 7403	.12	.74105	.40	74199	1.48	74LS156	.86	4027	.50	4520 1.05 4521 2.00	.88 for	10: 3.50 for	/B0140	.48	TIP42B	.78	294124 .16 294126 .16	2.2 .045 .05 .055 .06
7404	.13	74107	.28 .45	74221 74273	1.50 ° 2,15	74LS157 74LS158	.47	4028	.67	4522 1.35		.50 far 100	BD 189 BF241	.60 .25	TIP42C TIP3055	.86	2015133 .16	3.3 .045 .05 .055 .06 4.7 .045 .05 .055 .06
7405	.13	74110	.46	74279	1.25	74L\$160	1.22	4030	.48	4527 1.66), ferming and		BF258	.24	T1\$43	.50°	2015136 .16 2015142 .18	6.8 .05 .055 .06 .07
7406 7407	.28	74111	.70	74283	1.70	74LS161 74LS162	.69;	4031 4033	2.34	4528 .92 4529 1.18		S.C.R's	BFX34	.85	ZTX107	.10	2N5458 .28	10 .05 .06 .07 .08
7408	.14	74118	1.60 .82	74284 74293	6.85 1.35	74LS163	1.22	4033	1.25 2.00.	4536 3.56	1Amp 4Amp		BFX85 BFX86	.23 .24.	ZTX304 ZTX450	.20 .20		22 .06 .07 .09 .13 33 .07 .085 .10 .15
-74308	.40	74119	1.30	74298	1.92	74LS 164	1.20	4035	1.00	4553 4.20 4555 85	4Amji	400V .56	BFX87	.22	ZTX502	.20	BRIDGE	47 .08 .10 .12 .19
7409 7410	.14;	74120	.82 .25	74390 74393	1.92,	74LS168 74LS169	2.00 2.00	4036 4037	2.40	4555 .85 4556 .85	7Amp 7Amp		BFX88 BFY50	.22	ZTX504	.25	RECTHFIERS 1A 100V .25	68 .09 .12 .16 .23 100 .10 .13 .18 .26
7411	.18	74122	.40	74530	2.12	74LS170	1.76	4038	1.00	4558 1.25	10Amp		BFY51	.20	ZTX550 18821	.20 .28	1A 200V .30	100 .10 .13 .18 .26 150 .11 .15 .20 .28
7412 7413	.21 .25	74123	.53	74LS		74L8173 74L8174	1.85	4039	2.80	4566 1.46 4583 .75	16Amp	100V .75	BFY52	.18	11(823	.36	1A BODY .35 6A 100V .72	220 .12 .16 .22 .32
7414	.54	74125 74126	.44	74LS01	.19 .19	74LS175	1.05	4041	.88 .77	4585 LINEAR 1.83		LEOs	BFY64 BR100	.20 .26	1N914 1N916	.06 .07	6A 200V .78	330 .14 .18 .26 .36 470 .16 .20 .29 .40
7416	.27	74128	.62	74LS02	.19	74LS189	2.85.	4042	.72	TAA5508 .35		.125im .2in	BRY39	45	184001	.05	25A 100V 1.50	680 .18 .24 .35 .48
7417 7420	.27 .13	74132	.65. 88.	74LS03 74LS04	.19 .20	74LS190 74LS191	.81 -	4043 4044	.82 .82	TAA5518 1.46 TBA129S .86	1100	.09 .10	BSX19	.18	184002	.055	25A 200V 1.00	1900 .29 .29 .41 .56
17421	.20	74136	.75	74LS05	.20	74LS 192	1.80	4045	1.40	TBA641A 1.80	Yellew	.20 .20 .20 .20	BSX21 BSY25	.20	1N4003 1N4004	.06 .875	DIAC	2200 .30 .46 .65 .95 4700 .47 .60 .90
17422 7423	.17 .25	74141	.54 .58	74LS08 74LS09	.19	74LS193 74LS195	1.00	4046	1.32	TBA800 .90 TBA8188 1.10	LEO cli		BY127	.12	144005	.08	B#100 .26	
7425	.20	74142	2.00	74LS09 74LS10	.19 .19	74LS195	1.20	4047 4048	36, 60.	TCA270S0 2.21	AC128	.14	BY164 BYX10	.45 .14	194006 194007	,065 90,	MIXED	POLYESTER 190V Radial
7426	.25	74143	2.00	74LS11	.19	74LS197	1.20	4049	.42	TDA2020 3.50	AB161	č an.	BYX36-150	.14	184148	.04	CAPACITOR	lead .001, .0012, .0015, .0018 .0022,
7427	.25 .34	74144 74145	2.08 .84	74LS12 74LS13	.19	74L\$221 74L\$247	1.12	4050 4051	.42 .84	.ZN414 1.21 - 380-14 .91		.04	C1060	.50	185400	.13 .16	PAK	.0027, .0033, .0039, .0047, .0056,
7430	.13	74147	1,30	74L\$14	1.10	74LS248	.97	4052	.84	381-14 1.30	BAX16	.05	ME0492 ME3001	.15 .12	1N5482 1N5404	.16	50 caps. Mxd. values ,pt volts and capacit-	.0068, .0082, .01, .04; .012, .015, .018, .022, .045; .027, .033, .039,
74830 7432	.30	74148	1.18	74LS15	.19	74LS249 74LS251	.97 1.00	4053	.84	1555-8 .3t		.09	ME8002	.10	1844	.05	ances. Asserted pe-	.04705; .056068, .06; .082,
.7433	.32	74151	.80	74LS20 74LS21	.19 .19	74LS253	1.00	4054 4055	1.10	702-14 .50	BC1078	.10	MJE340 MJE371	.55 . .66	18020	.11	'tyester, polystyrone.	.1, .12; .15, .07; .18, .22, .27, .08;
7437 - 7438	.24 .24	74153	.60	74LS22	.19	74LS257	1.05	4060	.98	710-14 .3	BC1086	.10	MJE521	.60	18921	.07	ceramic, polycarb- onale, mica, elec-	.33, .10; .39, .11; .47, .12; .68, .16,
17440	.13	74154 74155	1.05 .83	74LS26 74LS27	.24 .40	74LS258 74LS266	1.05 .39	4066	,48 3,50.	711-14 .37 1 1310-14 1.70		.10 .11	MJE2955	.92	18922	.00	trolytic, etc.	
7441	.52	74156	.63	74LS30	.19	74LS273	2.50	4068	.24	2501B-14 2.20			MJE3055 MPF102	.65 .32	18923 18951	.09 ,10	1.00 PER PAK	READ TANTALUM
7442	.55 .90 .	74157 74159	.63 1,70	74LS32 74LS37	.25 .27	74LS279 74LS283	.50 1.00	,4069 4070	.17	3045-14 .45 OP ANDS	BC142 BC143	.25	MPS5172	.14	28438	.50	ÊLECTROLYTIC	.115, .22, .33, .47, .68, 1µF, 1.5µF, 35V, .11; 2.2, 3.3, 4.7.
7444	.90	74160	.80	74L\$38	.27	74LS289	2.85	4071	.17	301A-8 .35		.25 .09	MPS8522 MPSA-06	.20 .22	21696 21697	.15 .15	PAK 116 weight, Approx.	6.8µF, 35V12; 10µF. 25V. 22µF.
7445 7446	.70 .70	74161	.80	74LS40 74LS42	.19	74LS293 74LS298	.90. 1.60,	4072	.17	709-8 .43	BC148	.09	MPSA-13	.24	201706	.20	50 canacitors.	16V, .13; 33µF, 16V, .14; 16µF, 35V, 15µF, 35V, 22µF, 25V, 33µF,
7447A	.84	74162 74163	.80 88.	74LS42	.53 .97	74LS352	.92	4073 4075	.17	709-14 .44 709 T099 .84		.09	MPSA-56 MPSA-93	.22	20929 201306	.20 .35	Mixed values of	16V, 47µF, 6.3V, 88µF. 3V, 88µF.
17448	.60	:74164	.89	74LS48	.97	74L\$353	1.05	4076	1.05	741-8 .23	BC158	.11	MPSU-01	.35	201307	.35	capacitances and voltages.	6.3V, 100µF, 3V, 150µF, 3V, .15;
7450 7451	.13	74165 74166	.89 °	74LS49 74LS51	.97 .19	.74L\$365 .74L\$366	.50 .50	4077 4078	.46	741-14 .21 741 T099 .44		.11 -	MPSU-51	.38	201613	.21	1.25 PER PAK	47μF, 18V20; 100μF, 10V40
7453	.13	74167	2.78	74LS54	.19	74LS387	.50	4081	.17.	747-14 .70	BC1721		DA47 GA79	.08 80.	2911711 2912219	.24 .20		CARBON FILM RESISTORS
7454 7460	.13 . .13	174170 74172	1.88 4.00	74LS55 74LS73	.20	74LS368 74LS386	.50 .37	4082 4085	.20	1458 .55 4130-Turni 1.00		.15	GAB1	.00	29122221	.17	1 WATT ZENER DIODES .	¼ Watt 1Ω-10MΩ-E12 Series .013 sach, .125 for 10 any ene
7470	.28	74173	1.18	74L874	.34	741.8670	2.00	4085	.72 .76	3900-14 ,50	BC182 BC1825	.11	0A90 GA91	.07 .075	2012272 2012272A	.18 .20	3.3V-100V .18	value, 1,00 for 100 any one value,
7472 7473	.22	74174	.89 .68	74LS75	.45	CHIO	\$	4089	1.55	VOLTAGE REGS	BC183		0A202	.085	2N2368	.20		
7474	.26	74175 74S175	4.70	74LS76 74LS78	.32	4000 4001	.14	4093 4094	.65 1.80	300 T099 1.00 309K T03 1.30	BC183L BC184	11	0C20 0C28	1.00	2N2369 2N2646	.22 -	TMS 4030 4086-817	DYNAMIC RÄNDOM-ACCESS
74874	.50	74176	.88	74L\$83	.78	4002	.16	4095	1.10	723 140K .46	BC184L	. :::	0C35	.95 · .95	2N2904	.20		Y 22 PIN DIL 4.00
7475 7476	.30 .26	74177 74178	.88 1.20	74L\$85 74L\$86	.90 .35	4006 4007	.92 . .16	4096 4097	1.10 3.50	7805 T0220 1.10		.10	0C36	.95	2H2904A	.22		e, 470ns max, read or write cycle rea all inputs. He pull up rusisters
7480	.45	74179	1.10	74LS93	.95 ,	4008	.82 1	4098	1.12	7812 T0220 1.10 7815 T0220 1.11		.12	0C71 0C84	.25 .85.	2N2906 2N3053	.18	needed. Low power di	salpetion, 350mW operating 0.3mw
7481 7482	.90 .80	74180	.90	74LS95	1.10	4009	.45	4099	1.90	7824 T0220 1.10	BC212L	.12	TIL31	1.70	21/3054	.42 .	standby. Single low cap	iacitance cleck.
7483	.72	74181 74182	1.92	74LS107 74LS109	.38	4010 4011	.48 .15	4404 4412	1.00	7905 T0220 1.55 7912 T0220 1.55		.11.	TIL111 TIP29A	1.00 .42	2N3055 2N3448	.42 .50		March Commission (Commission Commission Comm
7484 7485	.90	748182	2.30	74LS112	.38	4012	.16	4428	.00 -	7915 T0220 1.55	BC214	.13	TIP298	.50*	203702	.00	LDV	RA-PAK
7485	.88 .26	74H183 74184	.90 1.20	74LS113 74LS114	.36	4013 4014	.42 .80	4445	1.50	LOW PROFILE	BC214L BC237E	13	TIP29C	.60	2N3704 2N3705	.07		
74586	1.58	74185A	1.20	74L\$123	.82	4015	.77 .	4501	.17	BIL SOCKETS 8 pin .11	BC2616		TIP30A TIP30B	.50 .55	283700	.07		STONE ROAD
7489 7490	2.00	74186 74188	7.20 . 2.70 ·	74LS124 74LS125	2.45	4016 4017	.42	4502 4507	.88	14 pin .12	8C549 BCY42	.12	TIP30C	.65	29/3707	.08		YTELEAFE
.7491	.65	74190	1.05	74LS126	.44	4017	.77 .87	4508	2.25	16 pin .13	BCY70	.25 .17	TIP31A TIP31B	.50 .52	2013710 2013771	.07 2.20	SURF	EY CR3 OEB
7492 7493	.44 \	74191 74192	.99 .99	74LS132 74LS136	.89 .40 .	4019	.42	4510	1.05	20 pin .27	BCY71	.18	TIP31C	.55	2913773	3.10	All prices INCI time	VAT. Add 250 for P&P (Extra for
7494	.80	74193	1.05	r 74LS 138	.53	4020 4021	.92	4511 4512	.98 .82	22 pia .29	8CY72 80115	.15 .50	TIP32A TIP32B	.55	2H3819 2H3904	.20	overseas), DISCOUNT	S: Over £10 less 5%. Over £20 less
7495 7496	.54	74194	.90	74LS139	.53	4022	.82	4514	2.85	24 pm32 *28 pm44	80131	.40	TIP32C	.70	2113905	.12	10°s. Over £50 less 1: for complete list of cor	5% Over £100 less 20% Send SAE
1490	.60	74195	.84,	74LS151	1.05	4023	.15,	4515	2.80	40 pin .60	80132	.41	TSP41A	.67	2013906	.12	TOT CONSTITUTE THAT OF COL	-pononie.

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STEREO PRE-AMPLIFIERS

MC 1





CPR 1

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CPR 1—THE ADVANCED PRE-AMPLIFIER

The best pre-amplifier in the U.K. The superiority of the CPR 1 is probably in the disc stage. The overload margin is a superb 40dB, this together with the high slewing rate ensures clean top, even with high output carriidges tracking heavily modulated records. Common-mode distortion is eliminated by an unusual design R.1.L.A. is accurate to 1dB, signal to noise ratio is 70dB relative to 3 5mV, distortion < 0.055 which 3 30dB overload 20kHz following this stage is the flat gain/balance stage to bring tape, tuner, etc. up to power amp. signal levels Signal to noise ratio 86dB, slew-rate 30VL05, T.H.O. 20VLz. - 20kHz < 0088 xt any level F.E.T. muting No controls are fitted. There is no provision for tone controls CPR 1 size is 138 x 80 x 20mm. Supply to be ± 15 volts.

MC 1 PRE-PRE-AMPLIFIER

Suitable for nearly all moving-coil cartridges. Sensitivity 70 / 170uV switchable on the pic b. This module brings signals from the now popular low output moving-coil cartridges up to 3.5mV (typical signal required by most pre-amp disc inputs). Can be powered from a 9V battery or from our REG 1 regulator board.

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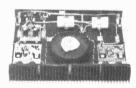


POWER AMPLIFIER MODULES CE 608 60W./8 ohms 35-0-35v CE 1004 100W./4 ohms 35-0-35v CE 1008 100W./8 ohms 45-0-45v CE 1704 170W./4 ohms 45-0-45v CE 1706 170W./8 ohms 60-0-60v

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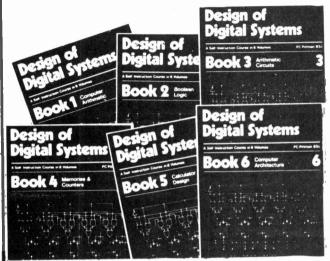
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Book 5 Structure of calculators; keyboard encoding; decoding display data; register systems; control unit; program ROM; address decoding; instruction sets; instruction decoding; control program structure.

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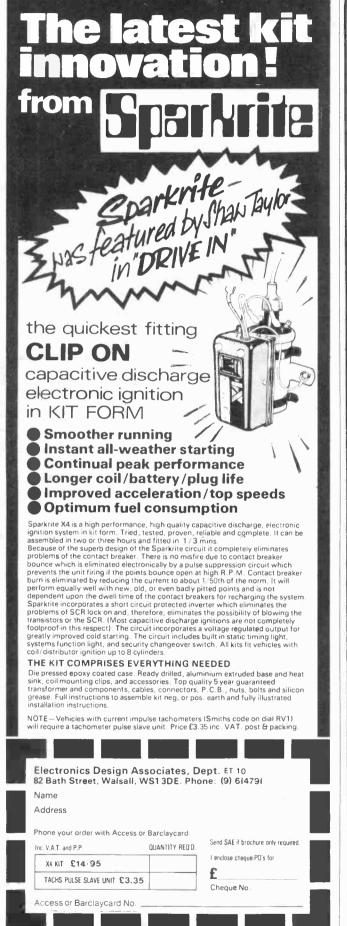
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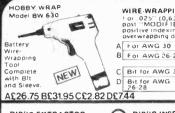
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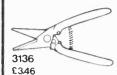
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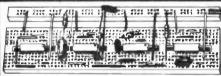


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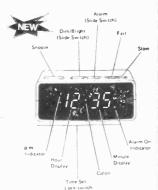


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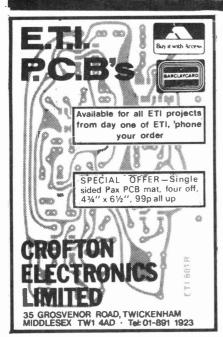
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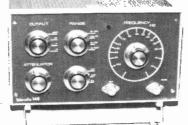


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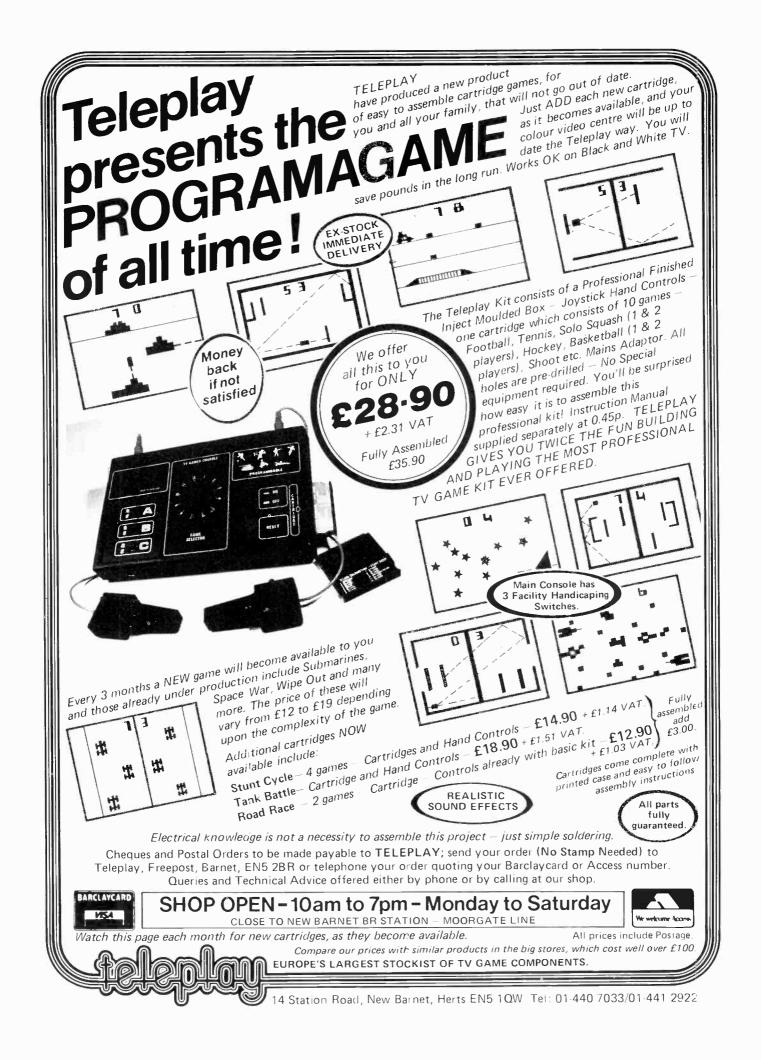
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