

# **TRANSCENDENT 2000** SINGLE BOARD SYNTHESIZER

LIVE PERFORMANCE SYNTHESIZER DESIGNED BY CONSULTANT TIM ORR (FORMERLY SYNTHESIZER DESIGNER FOR EMS LIMITED) AND FEATURED AS A

CONSTRUCTIONAL ARTICLE IN ELECTRONICS TODAY INTERNATIONAL.

The TRANSCENDENT 2000 is a 3 octave instrument transposable 2 octaves up or down giving an effective 7 octave range. There is portamento, pitch bending a VCO with shape and pitch modulation, a VCF with both low and high pass outputs and a separate dynamic sweep control, a noise generator and an ADSR envelope shaper. There is also a slow oscillator, a new pitch detector. ADSR repeat, sample and hold, and special circuitry with precision components to ensure tuning stability amongst its many features

The kit includes fully finished metalwork, solid teak cabinet, filter sweep pedal, professional quality components (all resistors either 2% metal oxide or ½% metal film!) and it really is complete — right down to the last nut and bolt and last piece of wire! There is even a 13A plug in the kit — you need buy absolutely no more parts before plugging in and making great music! Virtually all the components are on the one professional quality fibre glass PCB printed with component locations. All the controls mount directly on the main board, all connections to the board are made with connector plugs and construction is so simple it can be built easily in a few evenings by almost anyone capable of neat soldering! When finished you will possess a synthesizer comparable in performance and quality with ready built units selling for between £500 and £700!



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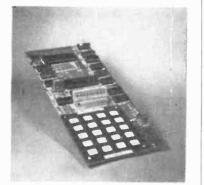


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CANADA Steve Braidwood Editor

Graham Wideman Assistant Editor

**GERMANY Udo Wittig** 

Editor





#### EDITORIAL AND ADVERTISEMENT OFFICE

25-27 Oxford Street, London W1R 1RF. Telephone 01-434 1781/2. Telex 8811896

Halvor W. Moorshead

Ron Harris B.Sc

Gary Evans

**Projects Editor** Specials Editor

Jim Perry

Phil Cohen B.Sc, William King

**Editorial Assistants** Project Design

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Paul Edwards

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Kim Hamlin, Bren Goodwin

Tim Salmon, Val Tregidgo

Mark Strathern (Manager), Tom Moloney

Administration Office Manager

**Assistant Editor** 

Project Development

Technical Illustrator

Reader Services

Advertising

PUBLISHED BY Modmags Ltd., 25-27 Oxford Street, London W1R 1RF DISTRIBUTED BY Argus Distribution Ltd. (British Isles)

Gordon & Gotch Ltd. (Overseas) PRINTED BY QB Limited, Colchester

Electronics Today International is normally published on the first Friday of the month prior to the cover date

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ELECTRONICS TODAY INTERNATIONAL - SEPTEMBER 1978



The new Science of Cambridge MK14 Microcomputer kit

The MK14 National Semiconductor Scamp based Microcomputer Kit gives you the power and performance of a professional keyboard-addressable unit-for less than half the normal price. It has a specification that makes it perfect for the engineer who needs to keep up to date with digital systems or for use in school science departments. It's ideal for hobbyists and amateur electronics enthusiasts, too.

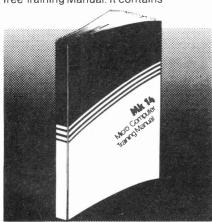
But the MK14 isn't just a training aid. It's been designed for practical performance, so you can use it as a working component of, even the heart of, larger electronic systems and equipment.

#### MK14 Specification

- \* Hexadecimal keyboard
- 8-digit, 7-segment LED display
- \* 512 x 8 Prom, containing monitor program and interface instructions
- 256 bytes of RAM
- \* 4MHz crystal
- ★ 5V stabiliser
- \* Single 6V power supply
- ★ Space available for extra 256 byte RAM and 16 port I/O
- \* Edge connector access to all data lines and I/O ports

#### Free Manual

Every MK14 Microcomputer kit includes a free Training Manual. It contains



operational instructions and examples for training applications, and numerous programs including mathroutines (square root, etc) digital alarm clock, single-step music box, mastermind and moon landing games, self-replication, general purpose sequencing, etc

Designed for fast, easy assembly Each 31-piece kit includes everything you need to make a full-scale working microprocessor, from 14 chips, a 4-part keyboard, display interface components. to PCB, switch and fixings. Further software packages, including serial interface to TTY and cassette, are available, and are regularly supplemented.

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Phil Pittman, Wireless World, Nov. 1977.

The low-cost computing power of the microprocessor is already being used to replace other forms of digital, analogue, electro-mechanical, even purely mechanical forms of control systems.

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Please send me an MK14 Standard Microcomputer Kit. I enclose cheque/ Money order/PO for £43.55 (£39.95 + 8% VAT and 40p p&p).

Allow 21 days for delivery

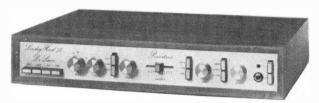
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## **AUDIO KITS OF DISTINCTION FROM**

# POWFRTRAN



#### DE LUXE EASY TO BUILD LINSLEY-HOOD 75W AMPLIFIER £99.30 + VAT

This easy to build version of our world-wide acclaimed 75W amplifier kit based upon circuit boards interconnected with gold plated contacts resulting in minimal wiring and construction delightfully straightforward. The design was published in Hi-Fi News and Record Review and features include rumble filter, variable scratch filter, versatile tone controls and tape monitoring whilst distortion is less than 0.01%.

#### WIRELESS WORLD FM TUNER £70.20 + VAT

A pre-aligned front-end module makes this Wireless World published design very simple to construct and adjust without special instruments. Features include an excellent a.m. rejection, push-button station selection as well as infinitely variable tuning and a phase locked loop stereo decoder incorporating active filters for "birdy" suppression.





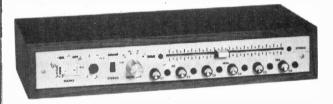
#### LINSLEY-HOOD CASSETTE DECK £79.60 + VAT

This design, published in Wireless World, although straightforward and relatively low cost rins design, published in Vireless Word, although a territorial state of performance. There are separate record and replay amplifiers and switchable equalisation together with a choice of bias levels are also provided. The nechanism is the Goldring-Lenco CRV with electronic speed control.

#### T20 + 20 AMPLIFIER £33.10 + VAT

This kit, based upon a design published in Practical Wireless, uses a single printed circuit board and offers at very low cost, ease of construction and all the normal facilities found on quality amplifiers. A 30 watt version of this kit (T30 + 30) is also available for £38.40 + VAT.





#### WWII TUNER £47.70 + VAT

This cost reduced model of our highly successful Wireless World FM Tuner kit was designed to complement the T20 + 20 and T30 + 30 amplifiers and the cabinet size, front panel format and electrical characteristics make this tuner compatible with either. Facilities included are pre-aligned front-end module, switchable afc, adjustable switchable muting. LED tuning indication and both continuous and push-button channel selection (adjustable by controls on the front panel).

#### POWERTRAN SFMT TUNER £35.90 + VAT

This is a simple low cost design which can be constructed easily without special alignment equipment but which still gives a first class output suitable for feeding any of our very popular amplifiers or any other high quality audio equipment. A phase-locked-loop is used for stereo decoding and controls include switchable afc, switchable muting and push-button channel selection, deflustable by controls on the front result. This with matches well with the T20 ± 20 selection (adjustable by controls on the front panel). This unit matches well with the T20  $\pm$  20 and T30  $\pm$  30 amplifiers.



COMPLETE KITS: Our complete kits really are complete. All of the projects shown on this page are supplied with fully finished metalwork, ready assembled high quality teak veneer cabinet, cables, nuts, bolts, etc., and full instructions - in fact everything

All of the kits shown on this page are available as separate packs (except the Powertran SFMT Tuner) for those customers who wish to spread their purchase or perhaps make their own cabinets or metalwork. Prices are given in our FREE CATALOGUE.

PRICE STABILITY: Order with confidence! irrespective of any price change We will honour all prices in this advertisement until October 31st, 1978. If ETI September 1978 issue is mentioned with your order. Errors and VAT rate changes excluded.

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and documentation.

U.K. ORDERS: Subject to 12½% surcharge for VAT (i.e., add % to the price).

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SECURICOR DELIVERY. For this optional service (U.K. mainland only) add £2.50 (VAT inclusive) per kit.

SALES COUNTER: If you prefer to collect your kit from the factory, call at Sales Counter (at rear of factory). Open 9 a.m.-4.30 p.m. Monday-Thursday.

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# news digest....

#### viewdata...prestel...

THE Post Office seems to be having a lot more success outside the U.K. with Viewdata/Prestel than it is having at home. As well as the negotiations with A.T. & T in the States the P.O. has sold the Hong Kong Telephone Company the know how to enable it to set up a system. Part of the sales pitch involved making a portable system (it weighed 56 kilos in a rather large case) and taking it to Hong Kong — a successful 'round the world' link was set up via satellite and undersea cable to the P.O. research station at Martlesham.

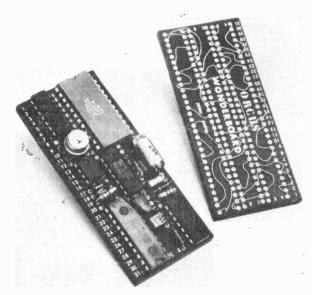
Meanwhile back at the ranch, the ambitious plans for a U.K. network have suffered some rather embarassing setbacks—the expected 1,500 sets by the end of 78 has been revised to 10,000, and at present only about 100 are installed. Of the presently installed sets the vast majority are with information providers, not customers. Also the department that specified the electronic design parameters forgot to check

with the department that certifies all equipment fit to be connected to the P.O. system. The result was that all the sets have had to be modified in case they tried to send nasty kilovolts down the line.

As well as the mechanical hitch the computer data banks are still not quite ready, all this means that instead of marketing trials the P.O. will have a basic 'test service' until the real public service starts — no definate date has been set for this yet though.

Finally, the reason for a sudden change from Viewdata to Prestel as a name has been discovered. The P.O. application to register Viewdata as a trade mark has been rejected by the trade mark office, the word Prestel has been submitted as an alternative name — but even this has not been accepted, yet. Informed opinion has it that Prestel will also be rejected, as an Italian company has used it since 1968 in the U.K. Any suggestions for a third alternative should be sent to . . .?

#### french connection

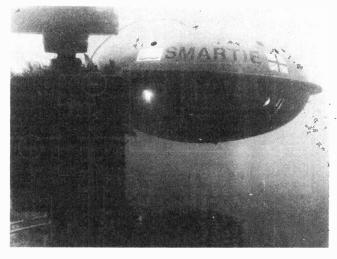


Wonderboards are a new bread boarding aid manufactured by Orcus International. Unlike normal solderless bread boards, which use metal sockets, the Wonderboards use conductive elastomeric contacts to provide the means for inter-connecting all the components. A benefit of this technique is that connections can be made to both sides of the board, giving far denser layouts than possible with conventional bread boards. Contact resistance is 10 milliohms and insu-

lation resistance 10,000 megohms between contacts.

Two sizes are available — Small Wonder (81x35x4 mm) and Big Wonder (81x140x4 mm) and naturally enough the contacts are on a 0.1 inch matrix to accommodate DIL packages. They are made in France and are available in the U.K. from Charcroft Electronics Ltd., Charcroft House, Sturmer, Haverhill, Suffolk, CB9 7XR. Price of Small Wonder is £2.30, and Big Wonder is £11.20 inclusive.

#### close encounters



Is it a bird? Is it a plane? No, it's a smartie? Everyone seems to like thinking up new acronyms, SMARTIE stands for Submarine Automatic Remote Television Inspection Equipment — probably thought up by a Mr S. Alik! Smartie is a microcomputer controlled submersible for use in the North Sea, to investigate the murky depths around oil platforms and conduct general surveys.

Equipped with multiple TV cameras, the device uses a submersible pump instead of a propellor to move around.

Benefits brought by MPUing include a simple hold command, which tells Smartie to stay where it is — with automatic compensation for water currents. Unlike conventional submersibles Smartie has a very thin (5mm diameter) umbilical cord — previous units have used bulky multicore cables

Smartie has been developed by Marine Unit Technology Ltd, with the support of the Department of Energy via the offshore Energy Technology Roard

#### pocket size



Ever needed to know how to convert furlongs per fortnight into chains per nano second? If you have then you must be a loony! However for the rest of our devoted readers, we would like to recommend the new Radio and Electronic Engineers Pocket Book. Full of useful information from CMOS data to frequency allocations, this the 15th edition has been updated by the editorial team that put the fun into electronics (you guessed, the ETI staff). We don't get commission and we still think you should buy a copy, so it must be good! Most decent (and some indecent) book shops should stock it, so keep your eyes out and have a look when you get a chance.

#### buzz buzz

A new range of solid state buzzers are available from FieldTech Limited. A minimum output of 65dB (at 3 feet) is buzzed by the 1V5 and 3VO versions while the 6, 9, 12 and 24V versions give a beefier buzz of 70dB. Each device incorpo-

rates a silicon transistor oscillator, with no mechanical bits to arc or fall apart. Further details from FieldTech Ltd, Components Division, London (Heathrow) Airport, Hounslow, Middlesex.

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		ALLERS WELCO ord 40588/9.	ME.	AC107* AC117* AC125* AC126* AC127* AC128* AC141*	23 BC168C 35 BC169C 19 BC170 19 BC171 19 BC172 18 BC177* 24 BC178* 38 BC179*	p 12 BF167 10 BF173* 17 BF177* 11 BF178* 10 BF179* 15 BF180* 14 BF181*	25 MPSA56 MPSA70 MPSU02 MPSU02 MPSU06 MPSU06 MPSU52 MPSU55 MPSU56	P 24 TIS43 34 TIS44 58 TIS46 54 TIS46 54 TIS47 65 TIS48 53 TIS49 56 TIS50	P P P P P P P P P P P P P P P P P P P
	ALL DEVICES BRAND NEW, FULL S DESPATCHED BY RETURN OF POS P.O.S OR BANKERS DRAFT WITH O INSTITUTIONS' OFFICIAL ORDERS WELCOME. P&P ADD 30p' TO ALL OI POSTAGE AT COST. AIR/SURFACE.	T. TERMS OF BUSINES RDER. GOVERNMENT ACCEPTED. TRADE AN	S: CASH/CHEQUE AND EDUCATIONA D EXPORT INQUIR	/ AC142* AC142K* AC176* Y AC187*	24 BC182	9 BF183* 9 BF184* 9 BF194 10 BF195 10 BF196	30 MPU131* 30 OC23* 10 OC25* 10 OC26* 10 OC28* 10 OC29*	39 TIS74 150 TIS90 120 TIS91 150 ZTX107 99 ZTX108 160 ZTX109	47 2N2369A* 15 18 2N2483* 28 22 2N2484* 30 11 2N2646* 48 11 2N2784 55 11 2N2904* 22
	VAT Export orders no VAT. Applicab prices are exclusive of VAT. P 12 ½ %. We stock thousands more items. It pays to vis Nesrest Underground/BR Station: Watford H	Please add 8% to devices mast.us. We are situated behind	rked *. To the rest ad Watford Football Groun	d ACY20 ACY21 ACY22 ACY28	40 8C186 40 BC187± 40 BC212 35 BC212L 40 BC213 40 BC213L	21 BF198 28 BF199 9 BF200* 10 BF224A 9 BF244 10 BF244B	18 OC35* 18 OC36* 32 OC41* 18 OC42* 24 OC43* 30 OC44*	80 ZTX212 99 ZTX300 48 ZTX301 32 ZTX302 55 ZTX303 31 ZTX304	28 2N2905A* 20 13 2N2906* 18 16 2N2907* 20 18 2N2907A* 22 21 2N2926G 10 24 2N3011* 24
H	Parking space available.	alues are in u F)		ACY41 ACY44	78 BC214 39 BC214K 39 BC214L	9 BF256* 14 BF257* 10 BF258*	50 OC45* 26 OC46* 30 OC70*	20 ZTX311 28 ZTX314 19 ZTX320	17 2N3053* 20 24 2N3054* 49 30 2N3055* 55 40 2N3108 39
ı	400V: 0-001, 0-0015, 0-0022, 0-0033 <b>7p:</b> C 10p; 0-047, 0-068 14p; 0-1, 15p; 0-1 160V: 0-039, 0-15, 0-22 11p; 0-33, 0-47 19	0-D047, 0-0068, 0-01, 0-015, 0 15 0-22, <b>22p;</b> 0-33, 0-4 <b>7 3</b> 8 <b>p;</b> 0-68, 1-0 <b>22p;</b> 1-5 <b>29</b>	p; 2-2 32p; 4-7 36	A0161*	60 BC3078 42 BC308 42 BC327 70 BC328	14 8F259* 13 8F336 15 8F394 13 8F594	37 OC71* 30 OC72* 22 OC74* 40 OC75*	25 ZTX326 30 ZTX341 45 ZTX500 45 ZTX501	40 2N3108 39 20 2N3442* 131 13 2N3563 20 14 2N3614* 169
B	DUBILIER: 1000V: 0-01, 0-015 18p; 0-022  POLYESTER RADIAL LEAD (Values in μ F). 25( 0-01, 0-015, 0-022, 0-027 5p; 0-033, 0-047, 0- 13p; 0-47 15p; 0-68 18p; 1-0 24p; 1-5 27p; 2-	0V: -068, 0-1 7p; 0-15 11p; 0-22, 0	FEED THROUGH CAPACITORS	AF114± AF115± AF116± AF117± AF118±	25 BC338 25 BC441* 25 BC461* 25 BC462* 55 BC547	12 BF595 30 BFR39 30 BFR40 30 BFR41 11 BFR79	38 OC76* 25 OC77* 25 OC79* 28 OC81D* 28 OC820*	36 Z*X502 76 ZTX503 76 ZTX504 28 ZTX531 48 ZTX550	19 2N3615* 135 15 2N3663* 24 25 2N3702 10 25 2N3703 11 25 2N3704 10
ı	ELECTROLYTIC CAPACITORS: Axial lead type 639: 0-47 1,0 1-5 2-2 2-5 3.3 4-7 6.8 8 1 7p: 50, 100, 220, 25p: 470, 50p: 1000, 2200, 35v: 10, 33, 7p: 330, 470, 32p: 1000, 49p: 25 640, 25p: 1000, 27p: 1500, 30p: 2000, 34p: 13 8p: 470, 16p: 1000, 1500, 20p: 2200, 34p: 11 TAGE-RND TYPE: 70V: 2000, 8pp: 4700, 121p 65p: 25v. 4700, 48p; 2000, 37p: 40V: 2000	10, 15, 22, 8p; 47, 32, 50, 11p, 68p; 40V; 22, 33, 7p; 100, 155V; 10, 22, 47, 6p; 80, 100, 163300, 58p; 4700 64p; 16V; 100V; 4, 100, 6p; 640, 10p; 100v; 50V; 10,000, 255p; 3000, 7	p; 3300, 82p; 4700, 84 0, 8p; 220, 250, 13p; 47 1, 40, 47, 68, 7p; 100, 12 1, 14p. 5p; 40V; 4000 70p; 250	0. AF121* P; AF125* 0. AF126	48 BC548 55 BC549C 35 BC557 55 BC558 35 BC559 35 BC730* 70 BC739*	11 BFR80 13 BFR81 13 BFR98 12 BFX29* 20 BFX81* 57 BFX84* 75 BFX85* 180 BFX86*	28 0C83* 28 0C84* 105 0C122* 26 0C123* 130 0C139* 24 0C140* 24 0C141* 28 0C170*	48 40250* 44 40251* 48 40311* 48 40313* 85 40315 85 40316* 40319*	85 2 N3705 11 97 2 N3706 10 50 2 N3707 10 125 2 N3708 10 55 2 N3709 10 85 2 N3710 16 52 2 N3711 10 71 2 N3772* 170
	TANTALUM BEAD CAPACITORS 35V: 0.1 μF, 0.22, 0.33, 0.47, 0.68, Carbot 10, 2.2 μF, 3.3, 4-7, 6.8, 25V: 1.5-10, 20V: 1.5, 1.6V: 10μ F 13p each, 22 25p, 47μ F, 100 40p, 10V: 22μ F, 33, 4-7, 6V: 47, 68, 100. 5ΚΩ	ENTIOMETERS (AB or EGEN) on Track, 14W Log & 15W Linear value 11KQ & 2KQ (lin. only) Single-ga 2-2MQ single gang 2-2MQ single gang 0 / P switch 6	OPTO ELECTRONICS * LEDs + clip Til 209 Red 13 TIL 211 Grn 24	AF186 ± AF239 ± AFZ11 ASY26 ± ASY27 ± ASY50 ± ASY76 ±	50 BCY40* 42 BCY42 128 BCY43 40 BCY58 45 BCY59* 95 BCY70* 95 BCY71*	78 BFX87* 48 BFX88* 75 BFY18* 22 BFY50* 22 BFY51* 15 BFY52* 15 BFY53*	23 OC171* 24 OC200* 50 OC201* 20 OC203* 20 OC204* 20 SJE5039* 28 TIP29 45 TIP29A	40 40320* 48 40323* 75 40324* 85 40326* 40327* 95 40347* 40348* 44 40360*	56 2N3773* 288 60 2N3819 22 85 2N3820 32 52 2N3823* 65 62 2N3824* 70 80 2N3866* 90 101 2N3903 18 43 2N3904 18
	MYLAR FILM CAPACITORS 100V: 0-001, 0-002, 0-005, 0-01μF 5ρ 0-015, 0-02, 0-04, 0-05, 0-056μF 7ρ 0.1μF, 0-15, 0-2 9ρ, 50V: 0.47μF 11ρ 10ΚC	DER POTENTIOMETERS W log and linear values 60mm -500K() single gang 7-500K() dual gang 8	2 Amber Gree Yellow 1: LH400 25: Op OCP71 11: Op ORP61 8: ORP61 8: ORP12	BC107# BC107# BC108# BC108B# BC108B#	9 8CZ11 12 80112	16 BFY55* 20 BFY64* 145 BFY71* 145 BSX20* 95 BSX26* 62 BSX29* 95 BSX78*	40 TIP298 20 TIP29C 18 TIP30 75 TIP30A 45 TIP30B 55 TIP30C	56 40361± 60 40362± 47 40406± 47 40407± 64 40408± 65 40411±	45 2N3905 18 48 2N3906 17 65 2N4037* 52 50 2N4041* 80 75 2N4058* 17 285 2N4061 17
ı	Range: 0-5pF to 10.000pF 0-015pF, 0.022pF, 0.033pF 4p PRE 0.047pF 4p: 0.1pF 6p. 0.1V SILVER MICA (Values in pF) 3-3, 4-7. 0.25	SET POTENTIOMETERS  V 50Ω—5ΜΩ Miniature Vertica zontal SW 100Ω—3-3ΜΩ horiz. larger	7 Segment Displays 5-LT-01 460 6 Til.312.3 CA 121 8p Til.313.3 CC 121	BC109Bs BC109Cs BC113 BC114 BC115	12 B0123 12 B0124* 17 B0131* 19 B0132* 19 B0133*	98 BSY95A* 115 BU105* 38 BU205 38 BU208 43 E421	25 TIP31* 140 TIP31A* 190 TIP31B* 228 TIP31C* 96 TIP32*	40 40412* 40 40467* 40 40594* 66 40495* 45 40603* 49 40636*	63 2N4062 13 95 2N4064 * 120 80 2N4069 45 90 2N4236 145 65 2N4286 20 125 2N4289 20
	82, 85, 100, 120, 150, 220 250, 300, 330, 360, 390, 600, 820 1000, 1800, 2000, 2200 20p each Minis	ISTORS — Erie make 5% Cart ature High Stability, Low noise RANGE VAL 1-99 10	OP TIL322.5 CC 14( 0L704.3 CC 99 0L707.3 CA 99 0L707.6 CA 18(	BC118 BC119*	19 BD135* 15 BD136* 19 BD137* 26 BD138* 19 BD139* 20 B0140*	36 E5567 37 MD8001* 36 ME1120 36 ME4102 45 ME6002 MJ400*	158 TIP32B* 25 TIP32C* 10 TIP33* 14 TIP33A* 90 TIP33B*	70 40673* 70 2N697* 80 2N698* 80 2N699* 100 2N706A*	68 2N4859 65 21 2N4922* 55 39 2N5135 42 39 2N5136 42 19 2N5138 20
	CERAMIC TRIMMER CAPACITORS 2-7pF; 4-15pF; 6-25pF; 8-30pF 20p 2% N 1%0	2.2Ω-4.7M E24 1.5p 1	5p XAN625 6 Grn 25i	BC140* BC142* BC143*	18 BD142* 20 BD144* 28 BD145* 25 B0145* 7 B0378*	59 MJ491 * MJ2955 * MJE340 * MJE340 * MJE370 * 110 MJE371 * 65 MJE520 *	190 TIP33C* 99 TIP34* 45 TIP34A* 55 TIP34B* 60 TIP34C* 45 TIP35*	105 2N707* 85 2N708* 85 2N914* 110 2N916* 110 2N918* 219 2N920*	50 2N5172 24 19 2N5179 60 32 2N5180 60 27 2N5191 65 30 2N5305 24 51 2N5457 32 8 2N5457 32
	2.5-6pF; 3-10pF; 10-40pF 5-25pF; 5-45pF; 60pF; 88pF; 30p COMPRESSION TRIMMERS 3-40pF; 10-80pF; 25-190pF 10-40pF	not mixed values.  RMISTORS: VA1034, 10 0, 1055, 1056, 1058, 1066, 10 8, 1100 20p ea	OPTO ISOLATORS 39. TIL 111/2 109 67. TIL114 110	bc148 BC148B BC148C	10 BD434 7 BD517* 10 BD696A* 10 BD696A* 8 BDY11 10 BDY17*		65 TIP35A 115 TIP358* 80 TIP35C* 30 TIP36A* TIP36A* TIP36B*	225   2N930* 240   2N1131* 270   2N1132* 260   2N1303* 265   2N1304* 300   2N1305*	22 2N5459 32 22 2N5485 32 50 2N5777* 56 50 2N6027 40 28 2N6109 45
1	JACKSONS VARIABLE CAPACITORS	DIODES p AA119 15 AA15 15 (plastic case)	2.2	BC157 BC158 BC159	14 BDY60* 14 BDY61* 10 BF115* 11 BF154* 11 BF158*	110 MPF105 165 MPF106 22 MPF107 25 MPS3904 29 MPSA05 30 MPSA06	36 TIP36C* 50 TIP41A* 50 TIP41B* TIP42A* TIP42B* TIP2955*	325   2N1306* 63   2N1307* 73   2N1308* 64   2N1613* 82   2N1670* 63   2N16718*	35   2S0234 ± 50 59   3N128 ± 85 46   3N140 ± 85 23   Matched
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0	1-5 Green 92p 17: 1-5 B.Y.R.W 93p 39p 17: 1-5 B.Y.R.W 93p 17: 15 B.Y.R.W 93p 17: 15 B.Y.R.W 93p 17: 15 B.Y.R.W 93p 17: 15 B.Y.R.W 93p 18: 15 B.Y.W	OA95 8 4A/600V 10 OA200 9 4A/800V 12 OA202 8 6A/100V IN914 4 6A/200V IN916 5 6A/400V	753 8 pin 150 810 159 AY-1-0212 580 AY-1-1313A 660 B AY-1-1320 635	LM3900* LM3909N* LM3911* M252AA* M253AA* MC663	70 TAA621AX 70 TAA661A 125 TAA700 750 TAA960 795 TAD100 275 TBA120S	1 228 7427 27 155 7428 35 353 7430 17 300 7432 25 150 7433 40 70 7437 30	74123 48 74125 38 74126 57 74128 74	4007 18 40 4008 87 40 4009 50 40 4010 50 40 4011 18 40 4012 18 40	69 20 4515 299 70 32 4516 120 71 21 4517 382 72 21 4518 102 73 21 4519 55
	1µ.H. 4.7. 10. 22. 33. 47. 100. 200. 470 750, 1mH. 2.5. 5. 10 35p each 43mH. 100 60p sach		AY-1-5051 145 AY-1-6721/6 195 AY-3-8500	MC724* MC1303 MC1304P MC1310P MC1312PQ	175 TBA540 88 TBA540Q 260 TBA550Q 175 TBA641 A1 195 BX or BX11	215 7438 33 7440 17 330 7441 74 7442 68	74141 56 74142 269 74143 314 74144 314	4013 42 40 4014 86 40 4015 89 40 4016 45 40 4017 89 40	76 85 4521 268 77 40 4522 199 78 21 4527 152
	VEROBOARD★ 0.1 0.15 0.16 (copper clad) (plain) 2½x3¼ 41p 33p 22p 2½x5 49p 45p 28p 3¾x3¾ 49p 45p —	IS44 20 Range 2V7 to 3A/100V* 18 39V 400mW 3p each 3A/600V* 27 Range 3V3 33V 1 3W	AY-5-1317A 630	MC14433L MC1458* MC1495* MC1496L MC1710CG	1250 TBA651 50 TBA800 535 TBA810S 92 TBA820 79 TBA920Q	180 7444 112 90 7445 94 105 7446 94 70 7447 82 260 7448 51	74147 175 74148 109 74150 99 74151 84	4018 89 40 4019 48 40 4020 99 40 4021 91 40 4022 88 40	82 21 4529 165 85 74 4530 85 86 73 89 150 93 85 74LS★
	3½ x 5 56p 60p 39p 2½ x 17 152p 121p 73p 3½ x 17 195p 163p 107p 4½ x 17 282p — 165p Pkt of 35 pins Spit face cutter 82p	30 17p esc 6A/600V 50 17p esc NOISE SCRs★ Thyristors	AY-5-3507* 415 AY-5-4007 650 CA3011* 82 CA3014* 137 CA3018* 68	MC3340P MC3360P MC3401 MEM 780 MFC4000B	150 TCA2700 120 TCA270SQ 70 T0A1022 205 TDA2020 85 TLO81CP* 97 TLO82CP*	220 7450 13 220 7451 13 575 7453 13 320 7454 13 52 7460 13	74154 96 74155 53 74156 80 74159 210 74160 82	4023 20 40 4024 66 40 4025 19 40 4026 180 40 4027 45 40	994 190 01 18 197 372 02 20 198 110 03 20 199 145 04 20 190 05 23
	Pin insertion tool 99p  VERO WIRING PEN*  Plus Spool 325p  Spare spool (wire) 80p* Combs 10p each	VARICAPS MVAM2 135 MVAM115 105 1350V 11400V 11400V 11400V 1150V 1150V 1150V 1150V 1150V 1160V 11	18 CA3020 170 12 CA3023 170 17 CA3028A* 80 12 CA3035 240 10 CA3036 110 18 CA3043 190	MFC6040 * MK50253 * MK50362 * MK50398 * NE350 NE515	650 TL084CP* 650 UAA170 635 ZN414 160 ZN424E 80 ZN425E*	130 7472 21 198 77473 3: 105 7474 2: 130 7475 3: 375 7476 3:	74163 92 74164 105 74165 105 8 74166 161 74167 20	4029 99 41 4030 58 41 4031 205 41 4032 100 41 4033 145 41	61 109 08 22 62 109 09 20 63 109 11 22 74 110 12 23 75 99 12 23
D)	FERRIC CHLORIDE* 1lb bag Anhydrous 65p + 30p p. & p.  OALO ETCH RESIST PEN* + spare tip 75p	BB102 25 3A100V 3A200V BB105B 40 3A400V 1 3A600V 1 5A400V 1	CA3046 71 CA3048 200 CA3075 175 CA3080E* 80 CA3081 190	NE543K NE544 NE555* NE5560B* NE560* NE561*	210 185 29 60 325 325 7400	7480 4 7481 8 7482 6 7483 7 7484 9	6 74173 170 9 74174, 87 2 74175 87 5 74176 75	4035 111 444 4036 325 44 4037 100 44 4038 108 44 4039 320 44	08 720 14 75 09 720 15 30 10 720 20 20 112F 1650 21 22 12V 1380 26 48
90	COPPER CLAD BOARDS * Fibre Single Double SRBP Glass sided sided sided sided 75p 30p 75p	6A400V 70 C106D 6A500V 85 TIC44		NE5628* NE565A* NE566* NE567V NE571	410 7401 120 7402 160 7403 170 7404 450 7405	13	74180 85 0 74181 165 3 74182 160 5 74184 135	4040 105 44 4041 80 44 4042 75 44 4043 94 44 4044 95 44	15F 795 27 28 48 19 280 30 22 19 280 32 27 33 1225 33 39
	DIL SOCKETS+ Low Profile (TEXAS) 8 pm 10p; 14 pm 12p; 16 pm 13p; 18 pm 20p; 20 pm 27p; 22 pm 30p; 24 pm 30p; 28 pm 42p; 40 pm 55p. 60 pm 245p.	8A400V 75 8A500V 92 10A500V 97 15A400V 185 16A400V 185 DIAC★	LOCATION 975 LOCAT	RAMZ102-2: RC4136D ROM2513* SAS560 SAS570 SG3402*	170 7406 120 7407 7408 7409 240 7410 295 7411	38 7493 3 7494 7 7495 6 7496 5 7497 18 20 7497 11	74188 275 74190 115 74192 105 74193 105 9 74194 105	4045 145 44 4046 128 44 4047 87 4048 58 44 4049 48 4050 48 44	38 39 50 295 51 295 74LS Section completed on 90F 695 next page
3	100 pins 50p; 1000 pins 350p	16A500V 210 ST2 40669 95	LM308T 110	SL4030	<b>250</b> 7412	17		4051 72 44	90V <b>525</b>

#### WATFORD ELECTRONICS



#### Introducing DM900 - The DIGITAL MULTIMETER with "Hidden Capacity" - It measures Capacitance too!

(as published in E.T.I. August 1978)
Away with analogue meters for with some of these you may often as not use a crystal ball to make circuit measurements instead gaze into our crystal — not a ball but the 3½ 0.5° LIQUID CRYSTAL DISPLAY — on our amazingly accurate DMM incorporating.

5 AC & DC Voltage ranges; 6 resistance ranges
5 AC & DC Current ranges; 4 Capacitance ranges
The prototype accuracy is better than 1%
This is a unique design using the latest MOS ICs and due to the minimal current drain, is powered by only one PP3 battery. There is also a battery check facility.
The DM900 is an attractive hand-held, light weight device, built into a high impact case with carrying handle and has been ingeniously designed to simplify assembly.
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Special introductory offer £49.95\* (p&p insured add 80p)
Calibration service charge for working Units £5.75. Readybuilt Units available by special order at £74.95\* (p&p add 80p)
(Optional extras. Probes £1.50\*; Carrying Case £1.50\*)

		(Optio	nal ex	tras, Prob (Demon:	oes £ 1	1.50★; on on at	Carryin our Sh	g Cas op)	e £1.50	<b>*</b> )	
Scre	PLUGS rome 12p 15p 23p 31p	Plastic body 8p 10p 15p 18p	open meta 8p 8p 13p	with	ed c	in line ouplers 11p 12p 18p 22p	SWITCHES+   TOGGLE 2A, 250V   SPST 28p   DPST 34p   DPDT 38p   4 pole on/off 54p   SUB-MIN TOGGLE				
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CO-AXIA	L (TV)		14p	14p		14p	ROT	ARY	Make your	Push 8	Ituaras Contrata
PHONO assorted co Metal scree	ened	-	9p 12p	5p singl 8p doub 10p 3-wa	le	15p	moda	te up t	to 6 Water	s fir	69p
BANANA	2mm 1mm		1p 10p 7p	12p 10p 7p		=	Space	ar and	Screen	o 6 *o = 1	pole / 12 way, ay. 6p / 2 way 47p
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EARPHON Magnetic 2.5mm 3.5mm Crystal CRYSTAL MICROPHI INSERT	18p 18p 33p	K6 K7 skiri K7a K8 K12 22n K19	d 0-9, 3 As K5 Black Calibe As ab Black Alum nm dian	37mm diam but with p Knurled, crated 0-9 : ove but poil or Silvered unised plas	n pointer taperer 30mm inter or d for Si- tic with	on skirt d. metal n skirt ider Pot n line ind	28p 28p top & 26p 26p 10p licator.	T09 T05 T01 T02	2 8p 9p 8 8p 20 22p	5mi	Syringe 125p ation Kit for TO66 or
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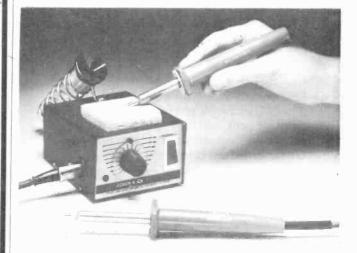
# news... digest

#### distorted truth

In the July Oscillators article we mentioned the Intersil 8038 function generator IC — in fact we said that distortion changes with frequency, and frequency is not a linear function of control voltage. Both statements are only true under certain conditions. Jayen Developments have pointed out that within the audio range both

distortion and deviation from linearity are negligible (<0.1%) the device only goes haywire above approximately 100kHz and below about 20Hz. As we said in our July 1977 Data Sheet on the 8038, it is an inherently versatile device with some drawbacks - but overall it has a lot going for it!

#### sawn off



Adcola have gone and cut 22mm off the length of their 101 temperature controlled soldering iron, leaving it with a barrel only 45mm short. The new model (101TS) is also lighter than its brother (sister?) by 16 per cent at 42gms. The idea behind the amputation is to give more precise control of the hot end - needed with modern components, which can be easily damaged by excess heat.

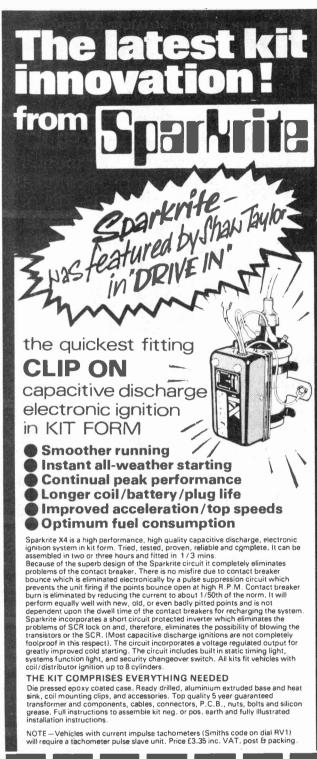
The temperature control is provided by a thermocouple feeding an op amp and special power control c, which uses the zero crossing technique to eliminate RF interference. Control is within 2% of the set temperature as shown on the control unit/stand dial. Full details and spec sheets from Adcola Products Ltd, Adcola House, Gauden Road, London SW46LH.

#### boris slain

Regular readers (aren't you all!) will have seen the item in last months News Digest about Boris the chess machine. Fidelity Electronics who make the Challenger felt that Boris's challenge should be taken up, and arranged a seven game tournament at the recent Chicago Electronics Show

Boris was set on 3 minute response time and the Challenger set at a similar level. The

result was Boris 0 Challenger 7, a veritable wipeout! The average response time of the Challenger was only 2 minutes 15 seconds. The game of the century would be to pit Boris and Challenger 10 against each other on their largest response times (99 hours and 24 hours respectively) – but a game like that could well take so long it would be the game of next cen-



#### 

# Airamco Ltd. MICRO COMPUTER PRODUCTS

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Components

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(equivalent of above) T.I.
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8 for £26.00
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8 for £26.00
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8 for £139.00

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Semiconductor prices are always changing and the trend is generally downwards. So ring for latest up-to-date details.

# ...news digest...

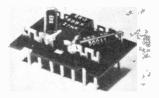
#### junk calls

From the land that brought us Muzak and MPUs comes the Junk call — the same as Junk mail but verbal! A machine is being used to dial up to 1,000 numbers a day and make a prerecorded sales pitch, unlike junk mail there is no way of knowing when the call will be junk or not. By dialing up numbers from 0001 to 9999 the machine annoys everybody who answers on a particular exchange, even if you hang up

it holds the line open until the pitch is finished — this has caused emergency calls to be delayed in some cases.

Ten states are considering legislation to curtail the activities of the machines. However they intend to exempt charities, pollsters and politicians. Some people want an electronic 'no thanks' sign to be developed, although nobody is quite sure how it would work. What next?!

#### diy dil



A new dil package is available from Erg Components, designed to house "numerous" components the pack has two rows of 7 linked terminals. The links can be easily broken with wire cutters if required. Uses suggested include hybrid circuits, passive networks and board to board coupling (using ribbon cable out the top). Two versions of the snap on cover are available one 5.7mm high, the other 8.9mm, connection links and pins are hard gold plated. Erg Industrial Corporation Ltd, Luton Road, Dunstable, Beds. LU5 4LJ.

#### bulble memories

AND IBM said 'Let there be light' and there was — but it moved! Boffins at the IBM research labs in San Jose have been investigating microscopic sources of light in a certain electroluminescent thin film, and have discovered that they move about and repulse each other. The effect starts when a high frequency voltage is applied across the films, and reaches a peak of activity at

about 50kHz.

The anology with magnetic bubbles has given the researches the idea that they should try and find a way of controlling the light bubbles. They still don't know exactly what causes the effect, one suggestion is that the materials are riddled with microscopic defects in crystalline structure. Wonder if they are feeling 'light headed' with their discovery?

#### odds & ends

- \* Polaroid are about to release an automatic focusing camera that uses an ultra-sonic transducer to measure distance.
- \* Computers stores in the US are opening up literally every day we have just heard that 700 have been identified by someone preparing an exhibition! In addition to those dedicated to Home computers, office equipment suppliers and camera shops are at the forefront when it comes to jumping on the bandwagon; even Macey's stores have now got a computer department in some of their stores.
- \* Sanyo have demonstrated a 6 mm thin solid state green and black television. The display is made out of 6,144 green LEDs in an area only 50 mm by 75 mm. They hope to have a commercial set by 1981.
- \* A radar based overspeed detector is in use in the U.S. of A, the unit measures your speed and lights up a neon sign saying YOUR SPEED IS.... REDUCE SPEED. The unit is very effective, only problem was the local hot-rodders using it to check their top speed! Problem solved by limiting display to 75 instead of 99.

# WART TO WORK FOR ETI2

#### **Advertising Sales**

We are looking for someone to assist our Advertisement Manager in selling space in ETI and associated publications soon to be announced; this is a new position.

We have a strong preference for someone with an interest in electronics and although experience in selling would be useful, we will consider those wishing to enter the field.

ETI's 100% plus increase in advertising billing in 12 months has not been brought about by hard selling but by offering objective advice and talking facts, not promises; we are looking for a person to continue these traditions. The successful applicant will be based at our Oxford Street offices but a degree of travelling will be involved; a company car will be supplied. The salary is likely to be in the range £3,500-£4,000 p.a. depending on age and experience.

#### **Art Editor**

ETI has a vacancy for an Art Editor. The job involves design and preparation of artwork of the editorial contents of the magazine. (Camera-ready pages are prepared by our printers so this will not form part of the work but rough layout instructions need to be prepared. Techanical drawings are produced by existing staff.)

Cover design forms a significant part of the work and supervising freelance photographers is also necessary. Essential qualifications are experience of artwork and working to a schedule with a team. Strong preference will be given to someone with magazine experience. The salary is dependent upon experience but will be in the range £3,750 to £4,750 p.a.

Applications, in writing, should be made before August 31st to

Halvor Moorshead, Editor, ETI Magazine, 25-27 Oxford Street, London W1R 1RF.

# new from ( ne Stop Technology Shopping

ONCE UPON A TIME, a manufacturer's stamp on an IC was an indication of almost total product reliability. But as complexities have multiplied, and prices have become competetive - the delineation between 100% functional and 95% functional ICs has got a lot less clear. But now more than ever, you cannot afford to waste time and effort on anything less than the very best - so at the OSTS, we have a strict policy to supply parts only from BS9000 approved sources. No nondescript clearance lines of dubious pedigree, only the very best. If you are a designer, or simply a keen hobbyist, you may buy from the OSTS with total confidence.

As you may already know, we make a point of backing our products with extensive lab and technical facilities; so next time you want to buy your components, ask what support your present supplier can offer - and if it comes from BS9000 sources......we look forward to supplying you!!

to supplying you !!

Please note that OSTS prices exclude VAT at 8% throughout this side of the page. Most ambit items are at 12% except those marked \* Please keep orders separately totalled, although a single combined payment, and 25p postage charge, will be sufficient.

#### CD 4000 CMOS

4000	17p	4059	563p	4522	1
4001	17p	4060	115p	4527	13
4002	17p.	4063	109p	4528	10
4006	109p	4066	53p	4529	14
4007	18p	4067	400p	4530	9
4008	80p	4068	25p	4531	14
4009	58p	4069	20p	4532	3.
4010	58p	4070	20p	4534	6
4011	17p	4071	20p	4536	3
4012	17p	4072	20p	4538	1 !
4013	55p	4073	20p	4539	1
4016	52p	4075	20p	4541	1
4017	80p	4076	90p	4543	- 1
4018	80p	4077	20p	4549	3
4019	60p	4078	20p	4553	4
4020	93p	4081	20p	4554	1
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4024	76p	4089	150p	4559	3
4025	17p	4093	50p	4560	2
4026	180p	4094	190p	4561	
4027	55p	4096	105p	4562	5
4028	72p	4097	372p	4566	1
4029	100p	4098	110p	4568	2
4030	58p	4099	122p	4569	3
4031	250p	4160	90p	4572	
4032	100p	4161	90p	4580	6
4033	145p	4162	90p	4581	3
4034	200p	4163	90p	4582	1
4035	120p	41.74	104p	4583	
4036	250p	4175	95p	4584	
4037	100p	4194	95p	4585	1
		AFOR	22-		

91p 69p 51p 55p 248p 99p 149p 98p 206p 260p 300p 125p 382p 103p

250p 83p 90p 85p 85p 80p 150p 130p 99p 60p 55p 65p 65p 65p 120p 4502 4503 4506 4507 4508 4510 4511 4512 4513 4514 4515 4516 4517 4518 4519 4520 4521

#### Micromarket

6800 se 6800P 6820P 6850P	ries £13 £6 £6.75	8216 8224 8228 8251 8255	£2.25 £4 £5.25 £8 £5.40	2114 2708 Develo	£10 £10.55 pment 00 £220
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#### Voltage Regs

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1	1 amp in IEC connector	£4.83
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7400	13	20	7455	35	24	74126	57	44	74185	134		74362	37
7401	13	20	7460	17		74128	74	. 1	74188	275	1	74365	4
7402	14	20	7463		110	74132	73	78	74190	115	92	74366	4
7403	14	20	7470	28		74133		29	74191		92	74367	4
7404	14	24	7472	28		74136		40	74192	105	180	74368	4
7405	18	26	7473	32	38	74138		60	74193	105	180	74373	7
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7412	17	24	7482	69		74147	175		74200		374	74390	14
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7417	30		7489	205		74154	96		74248		90	74399	15
74 20	16	24	7490	33	90	74155	54	110	74249		90	74445	6
7421	29	24	7491	76	110	74156	80	110	74251		90	74447	9
7422	24	24	7492	38	78	74157	67	55	74253		105	74490	14
7423	27		7493	32	99	74158		60	74257		108	74668	11
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7437	40	24	74107	32	38	74166		110	74290		90		SUP 2p
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7442	70	99	74112	88	38	74170	230	200	74299		300	ICL7106CP	
7443	115	1	74113		38	74172	625		74300		350	DVM ic 9	55p
7444	112		74114		38	74173	170		74302		350	KIT 248	3 O p
7445	94	1	74116	198		74174	87	120	74324		115	ICL7107CP	
7446	. 94		74118	83		74175	87	110	74325		140	LCD DVM	
7447	82	99	74119	119	1	74176	75	1	74326		140	KIT 206	55p
7448	56	99	74120	115		74177	78		74327		145	1	
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7453	17		74124		88	74183		210	74260		26		
7454	17	24	74125	38	44	74184	135		1		I	quantity pr	1085

# From the World's leading radio

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	CA3089E/KB HA1137W/KI CA3189E TBA120a/SN TBA120S MC1350P	IA402 famous FM IF system B4420 as 3089 + deviasion mute update with deviation mute 76660N FM if and detector higain version TBA120 age IF amp synch AM demodulator precision 4 quad multiplier popular double balanced mixer ons circuits	1.94 2.20 2.75 0.75 1.00 1.20 1.35 6.86 1.25
	KB4412 KB4413 KB4417 KB4423 Audio pream LM381 LM1303 TDA1054 KB4417	stereo high gain/low THD stereo audio optimized OA high quality with alc option see above	0.50 2.55 2.75 2.55 2.55 2.55 1.81 0.99 1.95
	Audio Power TBA810AS TDA2002 TDA2020 TCA940 ULN2283 LM380N8 LM380N14 HA1370	amps. 7W RMS overload protected 8W/2Ω in pentawatt package 15W RMS hift power do coupled 10W higher voltage 810 1W 2.5 to 12 v supply capability 1W power 2.5W power Hiff 15w in easy heatsink pack	1.09 1.95 2 99 1.80 1.00 1.00 1.00 2.99
1'	Stereo Decod MC1310/KB CA3090AQ uA758 Bu		2.20 3.25 2.20 1.55

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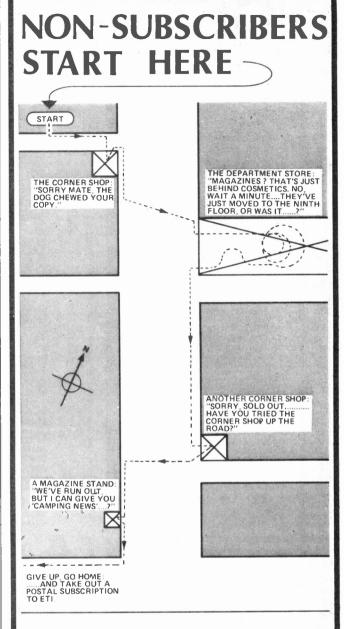
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TRADE ENQUIRIES WELCOME

CONCERT SOUND SYSTEMS come in many sizes, shapes and forms and I don't think I've ever heard two systems that sound identical in the same half. The sound engineers have different design philosophies although they share a common objective.

Expressions such as 4 way crossovers, front loaded horns, radials, dispersion angles, etc, are bandied about when the crews get together on tour but what really makes a good "state of the art" sound system? A system that, given the hundreus of variables such as hall acoustics, mood of audience, time available for set-up and tuning, road damage (that must be taken into account at every concert), will consistently deliver the best possible sound to the audience.

For some of the answers let's look at a system I designed for the Australian tours of Rod Stewart and Abba. The 'Jands No. 1 Touring System' weighs 28 tonnes and delivers a power output of 24 000 Watts RMS.

Let's follow the sound from its source looking first at microphones. The majority of these are made by Shure — type SM 58 for vocals and SM 57 for instruments. On the drums I use some other favourites such as Sennheiser MD 421 or AKG D12. The actual set-up depends on taste and the way the kit is tuned. The mics plug into 20-way multi-core cables leading to the mixer in the hall. The multi-core input box also has splitting outputs to feed any mic to the stage monitor mixer located on one side of the stage. The house mixing console is custom designed by myself and Jands consultant 'electronic genius' Phillip Storey. This is a 24 track in, 16 track out, studio style board made super-rugged for the 'road'. It has many facilities not normally needed on a PA mixer, such as the ability to do a stereo house mix, a separate stereo recording mix, a mono TV mix and an all-up 16 track output all at one time.

Why such extravagance? It is because in Australia (due to the limited audio facilities in TV OB vans) we often get asked if we can do all the above — for a live TV show with an album to be released later, so the extra features can be readily justified.

**Tuning Up** 

The stereo 'house mix' outputs of the board feed to a set of one-third-octave stereo graphic equalisers. These are set up using pink noise and real time analysis to accurately 'tune' the sys-

FLECTRONICS TODAY INTERNATIONAL -

# ROCK SOUND

The last couple of years have brough bigger and better equipment to the concert stage . . . here Howard Page of Jands Ltd describes the equipment used in presenting artists like Rod Stewart and Abba to Australian audiences exceeding 30,000 and this illustrates the techniques in use today.

This set-up shows the speakers used at the Sydney showground for the Rod Stewart concert.



tem for both the hall and, in some cases, the type of sound required. The stereo signals then feed a set of stereo DBX 160s (Compressor/Limiter) which are set as a final safeguard on the system to ensure the amplifiers are not driven into consistent square waves, one of the primary causes of speaker system failure.

Having been tuned and compressed as necessary the signals feed into a custom-built switchable 3, 4 or 5 way stereo electronic cross-over unit, the design of which is classified information. Also feeding in and out of the mixer are what we call FX devices, ie, echo unit, flanging units, extra compressors for various instruments, digital delay devices, etc, these are used as required.

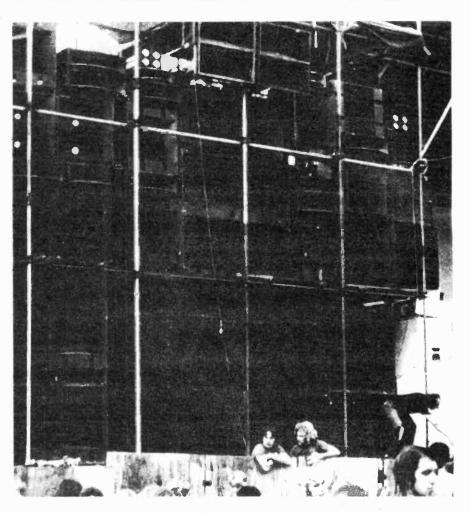
Once the sound has been divided it is sent down a separate multi-core cable called a system feeder which plugs into the amplifiers on stage behind the speaker stacks. The amplifiers we use are the finest available 'state of the art' units: Phaser Linear 700B, Crown DC 300A, SAE 17K111CM, and a new unit we're especially proud of, our own Jands J600S which is proving equal, if not superior to, anything available from overseas.

Each amplifier rack unit contains switching and matching systems to enable complete flexibility and access should a failure occur. Heavy duty speaker cables connect the amplifier outputs to the final link in the chain, the speaker units themselves. These, in the No 1 System, are for the 'Lo Boxes' custom-designed Super 'W's containing 4×15 JBL (all components in the system are JBL) speakers; for the 'Hi Bass' or 'Mid Bass' another custom-designed front loaded 2×12" speaker box tuned reflex porting (for use as the bass unit in a 3-way system); for the 'Mids' JBL 90° and 60° Radial horn units with high powered compression drivers; and for the 'Highs' 2402, JBL 075 radiator units

Well, that's it, total cost approx. £150 000 but it represents where concert sound reinforcement is at now. Certainly a far cry from a column speaker on each side of stage but its worth it when I hear members of the audience muttering as they file out "They sound just like their record."

#### **Ample Amperes**

One of the biggest problems now facing Jands when operating a PA and lighting rig, such as that used on the Rod Stewart tour is to ensure



Above is the tower of speakers used at one of the smaller gigs on the tour! Below, the scene as seen from behind the main control desk — the diminutive figures on stage are ABBA.



ELECTRONICS TODAY INTERNATIONAL — SEPTEMBER 1978

#### JANDS CONCERT SOUND SYSTEM AS USED BY ABBA/ROD STEWART TOURS **OF AUSTRALIA**

#### MONITORS

Mixer: Twenty input and six output buses. Each mic can be mixed onto one or all of the six buses, with or without tone control. This gives up to six separate monitor mixes so that each musician can have the extra foldback mix he requires. Each feed then passes through a graphic equalizer and into a Jands J600S to feed a foldback system.

#### Foldback Speaker System:

Each Side  $1 \times JBL 4550$  with two JBL 2220.

 $\times$  JBL 4560 with one JBL 2220.

× JBL 90° horns. × JBL 2390 horn lens

**Back Monitor**  $4 \times JBL 4560$  bass bins.

 $2 \times JBL 90^{\circ}$  horns.

4 × wedge monitor housing one JBL 15 Front

bass and one JBL horn and driver.

#### MAIN SYSTEM

2 × 20-way multicore cables feed the signal from forty microphones to the front of house mixer. A Jands 24 channel in and 16 channel out mixer.

The custom-designed 24 track, 16 track out mixer has the following facilities on each module

Selectable Input Attenuation

Channel Mute

3. Mic Phase Reverse

Mic/Line Switch

High Pass Filter (250 cycles 18dB/octave)

**Equalizer Bypass** 

Lo; Mid; High; 18 dB Boost/Cut at four selectable frequencies

Pan Pot

Eight Full Stereo Group Select Buttons

10. Solo Prefade Listen Button

There are eight stereo sub groups with two other sets of eight for making separate mixes of the sub group for recordings, TV, etc.

At the mixer are two 19" electronics racks. The effects rack and the main system rack housing

One third octave (27 band) stereo graphic DBX 160

2 × limiters DBX 160 2 × Jands 4-way crossover

The signal passes through each item then goes via a separate multicore to the stage to drive the amplifiers.

At each side of the stage are built the sound towers. These being 24 × 12 with three levels. Better dispersion is achieved by stacking high rather than wide. Each stack has the following

8 × Amplifier Racks each containing 3 amplifiers these being Crown DC300A Phase Linear 700B and Jands J600S.

#### The Speaker System:

12x4 130 (Jands designed W Bins with four JBL 15" speaker in each). 12xW cabinets containing two JBL 15" speakers.

24xJBL 4560 Bass cabinets with one JBL 15 speaker.

16xDouble 12 cabinets (Jands design) containing two JBL 12 speakers.

16xDouble 12 cabinets (Jands design) containing two JBL 12 speakers.

20xJBL 90 horns

16xJBL 60° horns.

8xJBL, long throw horns.

48xJBL 075 high frequency.

The total JBL count on the Rod Stewart / Abba main system Sydney Concert was 80x15 speakers. 32x12 speakers.

44xHorns and drivers.

48xHigh frequency.

Total value at your local hi-fi shop approx. £150 000.

The entire system is equalized before each concert using a pink noise generator and a Real Time Analyzer.

adequate mains supply (240 V). Simple arithmetic gives power consumption: the PA has six amplifier racks per side, and each rack has three stereo amplifiers each drawing four amperes. Total consumption is  $2 \times 6 \times 3 \times 4 = 144$  A. Stage equipment, including special effects, can easily draw 100 amperes. The lighting system comprises 100 lamps, each drawing 4 amperes. This adds another 400 amperes to the total requirement!

#### **Dim View Of Noise**

To help eliminate dimmer noise in the PA system using the three phase supplies, the lights are placed across two phases with sound and stage equipment across the third phase.

The power supply Jands now insist on is 300 amperes per phase with a solid neutral. The electrical code permits a much lighter neutral than active in most installations, the assumption being the load can be expected to be balanced across three phases and hence little neutral current flows back to the sub board. With the lights full up and no PA (as occurs at the end of each song) there is a great strain to pull the neutral towards the lighting phases and with a soggy neutral it is possible to get over 300 volts appearing on the PA phase (the neutral drifting 50 volts above earth).

#### **Earth At Stake**

Power is run from the sub-board to the dimmer racks and audio equipment via 416/0178 glass-insulated rubber sheathed mining trailing cable (cable rating 320 amperes and the copper core being 14 mm diameter). Each cable is fitted with a 350 ampere connector imported from Switzerland.

Each lighting phase runs direct into a dimmer rack housing 35 2 kW dimmer modules. The sound phase runs into a 19 electronics rack containing two 150 A breakers, one to feed PA the other the stage gear. Each breaker is connected to an earth leakage detector set to trip when more than 20 mA flows to earth. The current required to cause a fatal electric shock is 50 mA Hence if any person comes cin contact with a live wire on stage they cannot receive a fatal shock.

To avoid dimmer noise in the PA system it is often necessary to get a separate earth for the audio so Jands always carry a 6 foot solid copper earth stake and 10 kg of salt (for making a brine solution for better earth contact). ETI

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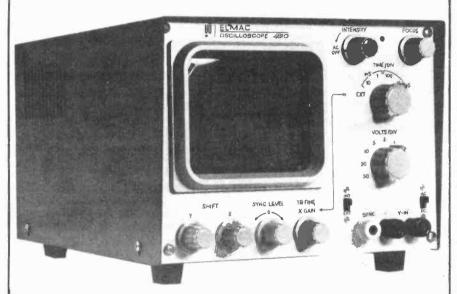
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CRT DATA

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Oscilloscope

# FM TUNER

Bill Poel of Ambit has designed for ETI the International Mk3 FM tuner. Using a modular concept the performance of the Mk3 puts it in the top flight of tuners. With the digital tuning option the design is unequaled in specification at its price.

The Mk 3 will strike most potential constructors with one main feature it has a digital frequency readout. This is a genuine count readout, and is included here as most constructors' big bugbear where radio construction is concerned, is the mechanics of the drive and its calibration. The unit is a complete RFI proof module, and although it is not cheap at around £45, it also incorporates an AM frequency option (fed from a plug at the rear of the unit in this case. Wait for the MW/LW add-on tuner) and the time. And since most listeners will want to know the time of the programmes, this is not an unnecessary extravagance. It further means that the tuner PSU is kept warm and running the whole time the unit is plugged in. Contrary to the beliefs of some, electronic devices left permanantly 'on' do not tend to explode or generally degenerate. In this case, leaving the 12V PSU running, permits the tunerhead local oscillator to be run constantly, and thus attain a steady state frequency stability that is very useful. For reasons of power economy, the mean amongst you may wish to disable everything but the clock / display module. But that's up to you, and really isn't warranted.

In case there are those amongst you not keen to lay out for the DT1200 module, an alternative circuit to drive an analogue frequency meter is offered as an alternative. And then the cheaper MA1012/1023 digital clock modules may be incorporated instead

since the chances are that most of your friends will still think you have the very latest in digital FM tuners.

#### **IF Stages**

The IF and decoder systems are chosen for very low distortion and very wide separation. There are those in the HiFi fraternity who will insist that two six pole linear phase filters will narrow the bandwidth too severely for proper FM stereo to pass through. However, it can be shown that the 200kHz of this design is quite sufficient — especially since the HA1196 PLL decoder incorporates a bandwidth/separation optimizer

circuit. Sceptical observers have been shown THD of less than 0.1%. and separations of 60dB at 1kHz in this system - which is really the ultimate justification anyway. To achieve these figures, it was necessary to build and align our own stereo encoder generator, using some of the spectrum analyzer exotica that doesn't usually find its way into consumer electronic designs. The system is optimized for about 50% modulation levels in the form shown here. This represents a more realistic approach in terms of UK broadcasting than full 75kHz, since programme dynamic range

#### **SPECIFICATION**-

A correctly aligned unit will provide the following level of performance: (Measured at  $50\%\,$  modulation)

Mono sensitivity

Stereo

Stero THD

Mono THD

19/38kHz

Stereo separation at 1kHz 10kHz Image/spurious rejections adjacent channel alternate channel Ultrasonic rejection of 50dB S/N2/3uV EMF 30dB S/N 0.9uV 50 dB S/N 9uV 30dB S/N 5uV

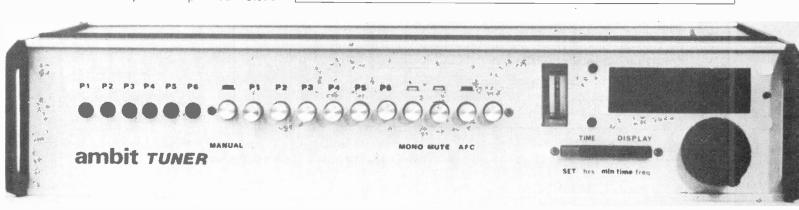
0.1% 0.1%

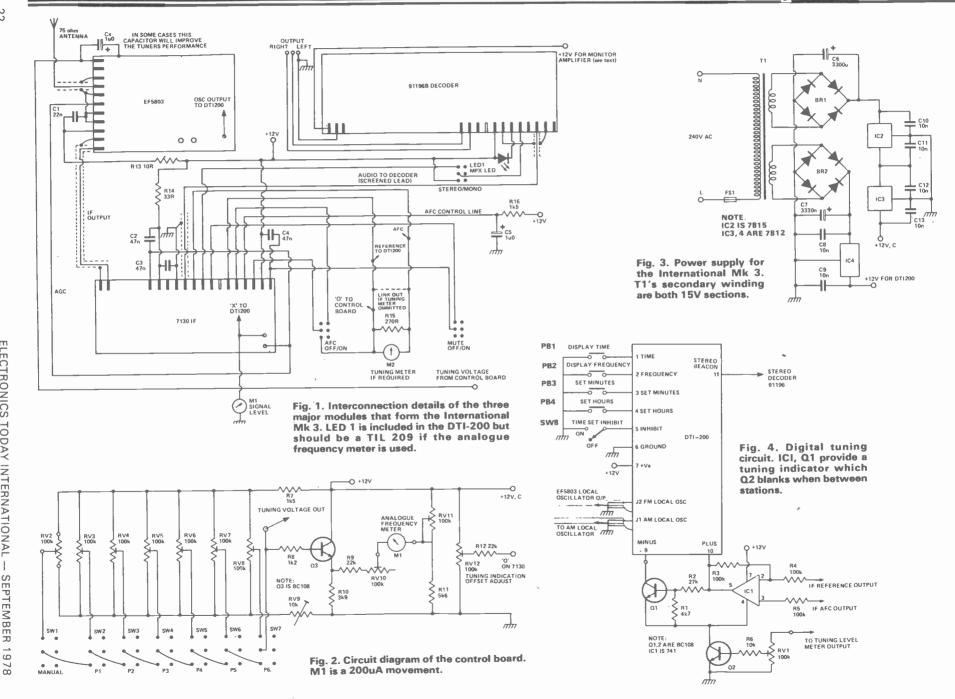
40dB 30dB

better than 90dB 30dB

65dB

60/85dB





#### **HOW IT WORKS**

To start at the beingging, all radio receivers have an antenna. This should ideally deliver about 1 mV of the desired stereo FM station in most tuner applications, although this system is designed to operate with rather less. The tunerhead system comprises two similar dual gate MOSFET stages, using low noise types of VHF devices from either the BF900 or MEM680 series. Each stage provides 22dB of gain, which can be readily controlled along the gate 2 line with AGC from the main IF amplifier system. The interstage coupling is very loose - imparting a narrow peak to the coupling passband for best rejection of the spurious signals encountered in FM band two tunerheads.

By the time the amplified RF signal reaches the mixer, it is processed through five tuned circuits at the RF frequency — and these must be made to match each other in a process known as tracking. It is not much use having 3 circuits at 89.4 and the other two at 89.1 MHz since signal would only be lost in the detuning effects, but the susceptibility to spurious signals will increase as the overall bandpass response begins to acquire some humps in odd places:

To assure good tracking of the RF — and also the oscillator, at this frequency, the EF5803 employs totally symmetrical layout of all frequency determining components so that all circuit strays will be balanced in each individually screened compartment.

At the input to the mixer stage, the signal is fed into the signal gate of the MOSFET — and the local oscillator is fed into the control gate, producing a multiplicative mixing effect for good dynamic range and isolation of the oscillator frequency from the effects of strong signals that tend to 'pull' the oscillator in some bipolar mixer designs. The products of mixing are signal frequency plus oscillator, and signal frequency minus oscillator. The latter is the desired IF signal, and this is selected out of the drain circuit at 10.7MHz in a bandpass pair. The drain also provides a wideband derived AGC signal for the second RF stage to prevent exceptionally powerful RF signals exceeding a level where the mixer tuned circuit has volts of RF signal - which may then be rectified in the varicaps and superimposed on the tuning voltage, creating some very undesirable cross modulation effects in the whole front end. This AGC circuit only operates at inputs of more than about 5mV - when the AGC that is derived after the IF selectivity has usually already taken the line of zero volts. It is therefore aimed at signals just outside the IF bandpass, but still sufficiently close to the RF bandpass to cause problems.

The IF sections comprise a MOSFET preamp, with AGC from the IF AGC line, followed by the first of two linear phase filters. Correct termination of the filter leads to a very smooth bandpass characteristic that permits low distortion stereo to pass through unhindered or deformed in any way. The full multiplex composite spectrum is an AF signal bandwidth of 55kHz — and in the FM system. which is too complex to explain here — a transmission bandwidth of around 200kHz is considered necessary. Ceramic IF filters are a lot better than they used to be - but the coil/capacitor arrangements of linear phase filters have superior stability, and much better skirt and spurious responses in strong signal environments such as the EF5803 will provide. A second MOSFET/Filter stage precedes the main IF element, a rather comprehensive device that performs IF amplification functions, including limiting, detection, signal level meter drive, AGC drive, centre tuning drive, noise muting and deviation, muting systems. The IC which performs all these functions is the CA 3189E.

The IF of an FM tuner is probably one of the key areas of the whole tuner specification. It determines just about every 'audible' parameter and so must be given close attention for its effect on sound quality. Of the key subject areas of sound quality as applied to FM tuners, the signal to noise ratio is one of the most important — and so a wide dynamic range is necessary. This is ultimately determined by the choice of IF IC, and to a lesser extent the stereo decoder — at present, the specification of the CA3189E is capable of coping with the broadcaster's specifications. Distortion of the device is largely up to the external circuitry that is used in the detector circuit. Here the transfer characteristic of the discriminator is up to the board layout, and the quadrature components. Ad double tuned circuit, with critical coupling, is used to provide the detector with a THD of less than 0.05% when everything is correctly adjusted. The detector cannot be set up using the transfer curve method very satisfactorily. an audio spectrum analyser is best, with distortion factor meter next best — although a lot slower.

The IF system also provides an accurate muting method, that cuts out interstation noises when tuning the band. It operates in two ways — firstly by noting the signal to noise ratio of the incoming signal at the detector stage, and cutting in when the S/N is sufficiently degraded. However, although this method has been considered satisfactory for a long time past — there are certain shortcomings when tuning through a strong signal, where the edge of the discriminator curve can provide two additional detection transfer slopes at either edge of the desired passband. This leads to some loud rasping as

only half the signal is being processed in this way.

So the secondary muting technique is employed, whereby the signal is muted after it passes sufficiently off-tune to begin to become distorted. This method is known as deviation muting – and the control voltage is readily obtainable from the AFC voltage – which is in fact the DC level present at the detector, though decoupled from audio. If this voltage exceeds a predetermined level — the mute operates. This feature also assists greatly in fine tuning the unit — since it is not possible to listen to a detuned and thereby distorted signal, when the muting circuit is switched on.

The muting voltage may also be taken to the stereo decoder to prevent chattering of the stereo switching circuits as the unit is tuned through the band.

There are also two signal level voltages available – one for driving the tuning meter, and one for driving the AGC. The two are related, so that the AGC threshold may be preset to operate at any signal level – thus avoiding the tendency of the AGC to operate too suddenly in conjunction with high gain, high signal level handling tunerheads such as the EF5803. In this circuit, AGC begins to operate at about 1mV of antenna signal.

In the stereo decoder, the signal first passes through to the 'birdy filter', which restricts the audio bandwidth to below 55kHz – preventing an adjacent channel signal from beating with any of the decoder pilot tone frequencies and products creating the faint warbling that can appear on stations in crowded conditions.

filter is rather crucial, and an LC arrangement, in the form of the common delay line, is used for the most readily adjustable combinations of HF signal attenuation and AF signal attenuation. Many IF systems pour forth many millivolts of 10.7MHz and 21.4 MHz in the audio line — and the 'active' filter arrangement is not as effective in attentuating these frequencies and maintaining good phase response.

The decoder IC itself is the HA1196. Most people will know about the MC1310 — the original PLL stereo decoder IC — and much of the HA1196 is similar, except that the distortion is rather better, it possesses an adjustable separation facility, and best of all, it provides low distortion AF gain specifically derived to drive the pilot tone filter. Attenuation of 19 and 38kHz components of the AF voltage is essential to prevent HF intermodulation in the amplifier — and the BLR3017N unit also provides a steep cutoff after the audio bandwidth of 15kHz. The HA1196 drives the conventional LED beacon — and as mentioned already, has a stereo muting

facility via an external control voltage - which may be supplied at high impedance.

#### The Control Sections

Apart from the signal processing, the control aspects of this tuner require explanation and comment. First and foremost the digital frequency readout unit.

The DFM unit is a ready made 'black box,' incorporating FM and AM frequency and 12 hour quartz clock functions. It is unique at present - but it should be pointed out that the DT1200 is primarily based on USA markets, and so the count resolution is alternate 100kHz channels in 88-108, and 10KHz channels in the medium and long wavebands. Purists will no doubt realise that there are stations in the UK broadcasting in between these frequencies — although the BBC sticks fairly well to the alternate 100kHz pattern. This design can be run with the tunerhead powered continuously - since the clock/ frequency counter needs continuous power - and so achieve a stability otherwise unheard of. The varicap tunerhead also permits a selection of preset stations, through switched multiturn potentiometers.

For those of you not sufficiently enthusiastic about digital tuning, an option is described for an analogue frequency meter indicator — driven from the main tuning voltage line. The accuracy is not overwhelming — but the narrow spread of the UK FM band means that most listeners quickly appreciate the relative locations of their local transmissions. The meter is driven from an emitter follower circuit to isolate the actual tuning voltage from the dangers of picking up stray hash and noise along the meter lines. If driven directly from the tuning voltage, tapping the meter can reproduce a hollow mechanical clunk in the audio.

Finally, the PSU looks straightforward enough, but it must be carefully decoupled to prevent RF noise getting any further around the tuner than essential. Most voltage regulation sources are producers of wide band RF noise — and so careful filtering and decoupling is used as close to the source as possible. The supply for the audio monitor stages of the decoder board (2xLM380) is separately derived — and need not be stablised — to prevent modulation peaks detuning the whole thing.

The supply for the DT1200 requires careful filtering at the exit of the box — since the display of this unit is strobed at about 500Hz. The main tuning voltage to the EF5803 is decoupled at the entry of the shielded can, decoupled the entry of the shielded can be prone to picking up any radiated hash that is floating around.

considerations generally limit the levels used. This approach trades off a little ultimate distortion for a few dB signal to noise ratio. Subjectively, this is more than justified.

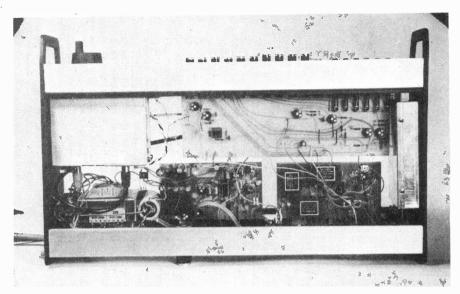
In fact, the decoder used here incorporates a 2W per channel monitor amplifier feature for persons requiring the unit to be self supporting as a very classy bedside radio — or as a means of monitoring programmes without upsetting the whole Hi-Fi operation. This is mentioned briefly here, and will not be covered in great detail in the text, but if it is to be incorporated, please follow the directions supplied with the module carefully.

Metering facilities are provided both in the DT1200 module where FM detune is indicated by illumination of the + and - on the display - and separately with moving coil meters if desired. A signal level meter is considered to be a desirable feature in a unit of this sophistication (to make certain you are getting the most local transmission from the multiple relays of the BBC), and the centre zero tuning indication is essential for the very best fidelity in narrow IF systems. The pedantic may also like to run a pair of PPM/VU audio level meters driven from the decoder output - but that is something considered unnecessary here.

#### **Construction**

The modules are fitted (Fig. 1) in the order shown, and it is desirable to follow the earth path layout shown on the wiring connection diagram (Fig. 2) if the problems of HF and VHF earth loop instability are to be avoided. Such instability is the curse of RF, and the reason why otherwise competent engineers have been known to lock themselves away in the loo when asked to ''just debug the VHF frontend" AF instability has the delightful quality that it can be heard, and so progressive fault tracing can be a relatively simple and speedy matter. With RF, the engineer's 'ear' is the spectrum analyser (just as the ear is a reasonably good audio spectrum analyser). Whilst the home constructor is usually blessed with an ear or two, the latter instrument is not as commonplace as it ought to be.

In other words, the unit may sound quite healthy on reasonable signals, but on weaker signals, the



Picture showing the internal layout of the International Mk 3 FM Tuner. The modules can clearly be seen mounted in their edge connectors with the PSU bottom left. The RF shielded DTI is top left.

whole thing oscillates around an unforgiving earth loop and the signal sensitivity appears to be unreasonably impaired. A quick check for stability is to listen to interstation noise with the mute 'off.' The noise should be smooth and white, clean and bright etc. (Sung to the tune of Eidelveis) It should not be crackling and broken up, or buzzing with a low level hiss.

When the system is really well set up, generator EMFs of 0.63 uV can provide full limiting on mono. This is very close to the theoretical limit of the system, and whilst some of it may be due to leakage effects, it still illustrates that not only is the nature of the signal VHF —but you are dealing with amplification levels vastly in excess of anything likely in an audio environment.

#### **Edge Connectors**

The modules fit into 0.2in edge connectors for ease of assembly, and it is recommended that the edge connectors should be very carefully wired with the modules in situ outside the case, the whole lot being transferred to the inside when it has been ascertained that the system is 'go.' The Swiftcase lends itself very nicely to this approach, since it comes virtually completely apart into a stack of plates and screws. In fact, it is rather better to solder the units together to avoid the dangers of interconnection degradation, but many people still feel happier if the units can be dismantled easily,

although this is really not necessary (hopefully). The PSU is simple enough, but remember that RF environments call for extra attention to potential RF noise sources such as the regulator device itself. The curse of tuners is frequently next door's fridge thermostat or the slightly noisy fluorescent tube fittings. An IEC type of mains filter is very useful here, and it also doubles for the mains input socket. One of the bolt-on extras envisaged for this unit is a noise blanker system to take out any residual click type interference that inevitably starts up during the quiet passages of Beethoven's 6th.

Interconnection of RF and IF signal paths should be made with RF coax. The antenna input should certainly use good 75/50 ohm coax - though the use of lesser types of screened cable is permissible for the IF connection - and of course the audio connections. Always use stranded cables for the rest of the wiring, since single solid cables will send you completely up the nearest wan if you ever have to manipulate the circuitry in the case. Units wired in this way will also not be eligible for the alignment service that is being offered to the constructor.

The connection of the frequency counter should also be made via coax of an RF nature, but since this is well buffered from the actual tunerhead oscillator, it may not be essential. The take off for the external connection of the AM local oscillator (when your MW/LW tuner is ready) should be made with the same coax.

#### PARTS LIST \_

#### REFERENCE SERIES MODULES

IF Strip 7130 91196(91196B) Decoder EF5803 Tuner Head Digital Tuning Indicator DTI200

RESISTORS 4k7 R2 27k R3, 4, 5 100k R6 10k R<sup>1</sup>7 1k5 **R8** 1k2 R9, 12 22k R10 3k9 R11 5k6

#### CAPACITORS

R13

R14

R15

22n polyester C2, 3, 4 47n polyester C5 1u0 electrolytic C6, 7 3 300u 35 V electrolytic C8-13 10n polyester

10R

33R

270R

#### SEMI CONDUCTORS

74 1 7815 IC3, 4 7812 01, 2, 3 BC108

#### **POTENTIOMETERS**

100k preset RV1, 10, 11, 12 RV2-8 100k diode law-type AB47

#### **SWITCHES**

Double Pole Charge Over SW1-7 SW8 Single Pole on-off PBI-4 Push to make, release to break

MISCELLANEOUS

West Hyde Swift Case, Meters (200uA), edge connectors, transformer (15-0, 0-15), screened lead etc.

#### **BUYLINES** -

Ambit International of 2 Gresham Road, Brentwood, Essex will be supplying a complete kit of parts for this project.

The cost of the tuner with DTI-200 will be £139.00. Without the Digital Tuning option the kit will be £99.00.

#### **Switch On And Test**

Never complete a project of this complexity and simply press the mains the signal when switched on switch. Always build up gradually, starting with the PSU on its own - ie disconnect the supply feeds to the rest of the works - and check the voltage. Leave the PSU running for an hour in this fashion, since experience has shown that many PSU reservoir capacitors are at their most fragile during this period. The slim chance of 500-1 is sufficient odds to let the PSU have a good soak before endangering the rest of the works. Next, hook up the power to the frequency counter, and check out the time function, following the setting details in Fig. 3. It is quartz referred, so it should be very accurate indeed. If nothing happens, check your switching wiring very carefully and try again.

Now monitor the supply current to the rest of the circuit and connect. Over 150mA means you have a problem, so then you must methodically disconnect each module's supply in turn, until offending connection is located. The usual trouble is 'frilly' wire terminations, so do not immediately despair that all is blown up and disasterously defunct. Also check that any decoupling electrolytics on the connectors and harnesses are correctly rated and polarized.

It is hoped that the process of test will quickly get you to a state where noises are apparent - and don't forget to set the audio output level pots on the decoder so that they are about halfway. And remember to leave the mute and AFC buttons in the "off" state until you have started to get recognisable sounds through the system.

The function of the tunerhead can be verified to a certain extent by switching the display to FM frequency readout. You will be able to see the frequency — to the nearest 200kHz -- as the tuning is varied. Unless you live in a really bad location, a degree of sound from a BBC transmission will be readily obtained with a simple piece of wire poked into the antenna socket. If you get the right sort of no-signal 'hiss' but no stations, and the DEM is indicating tuning is going on, check the RF and IF signal leads for shorts and problems at the connectors.

The muting used is a combination of deviation and noise muting -

which means that unless a station is reasonably accurately tuned to start with, the mute cannot open to pass Furthermore, the mute is tied in with the operation of the stereo switching of the decoder, so that stereo is automatically inhibited as the signal goes off tune, preventing the littering crashes that are sometimes found in such systems. The mute will not always completely kill all background noise, since it is set to lift on the slightest vestige of a signal. Usually 1uV or so.

#### **ACC Circuitry**

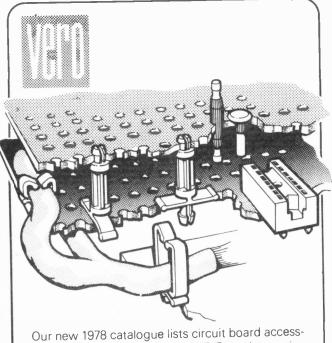
The AGC threshold point and operating level are factory set in the IF module, but those of you who know what you are doing may wish to tweak these controls to optimize for a particluar location condition. The unit cannot be seriously detuned with these controls - though most of the others should be left well alone. If you feel it is essential to have a tweak of the coils to get the thing going, then do not under any circumstances do so. The problem will only be worsened by a quick tweak of a trimmer, and must be sought elsewhere.

The AFC function of the tuner is readily confirmed. Slightly detune the transmission, and switch in the AFC. The signal will be pulled closer to the centre of the passband. Some listeners believe that operation of the AFC is detrimental to listening quality. In this tuner it is not so, since AFC controls all the tuned circuits of the VHF tunerhead, and not merely the oscillator. So the tracking of the tuned circuits is not in any way impaired when the AFC is operational.

As mentioned in the 'How it Works' section, the AFC is also programmable in its effect, so you may increase it up to the point at which it becomes overpowerful with respect to ease of tuning.

#### In Conclusion

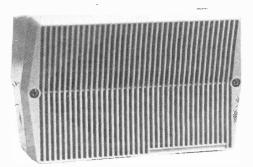
An alignment service for tuners constructed according to the contents of this article will be available for approx £10.00. But the units must be working to a degree where alignment will consist of final trimming and tweaking to optimize the final unit. It cannot encompass trouble shooting of smoking regulators and vapourized ICs at the basic charge. EII



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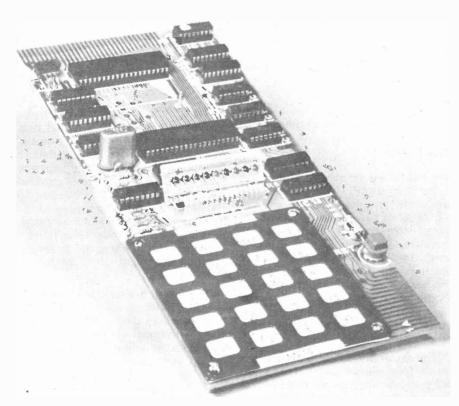
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# SCIENCE OF CAMBRIDGE'S MK 14 REVIEWED

Gary Evans has built and used Science of Cambridge's MK14, a kit that seems to offer a true low cost development system for National's SC/MP microprocessor. Here is his report.



The MK14 development kit from Science of Cambridge. The kit show has the optional add-on RAM I/O chip, top of board, and RAMs fitted. The edge connector at the top of brings out the I/O connections while the connector at bottom right (below reset switch) allows a remote keypad to be added to the system.

THE MK14 WAS LAUNCHED by Science of Cambridge earlier this year. The product, described as a microcomputer kit, sells for around £40, and features a SC/MP II microprocessor together with keyboard, display, 256 bytes of RAM (two 256×4), 512 byte monitor program (two 512×4) and various other items of hardware, that together provide the means by which machine language programs for the SC/MP may readily be written and debugged. The MK14 will also prove valuable to those who wish to learn more bout the ins and outs of using a typical 8 bit MPU, without having to spend the rather large sums of money associated with the purchase of some other development systems.

At this low price however just what does the MK14 have to offer in terms of performance and what corners, if any, have been cut to meet this low price tag.

#### **Demanding Supply**

Before going on to describe the kit in detail though, it's as well to mention the supply problems that Science of Cambridge have had in meeting the demand for MK14 kits over the past few months. Initial problems with supplies of semiconductor devices and later, more acute troubles with production of PCBs, have lead to a large backlog of orders building up.

This situation is slowly being rectified as alternative suppliers are sought where the original has failed to keep to delivery dates and the 21 day delivery time quoted by Science of Cambridge should be met on all new orders and the backlog soon cleared.

Now to the kit itself and first let's

say that we found the MK14 to be a very good product and the comments that follow should be read with this in mind. We remark upon a number of features which in our view detract from the overall performance of the kit, but with these rectified, the Science of Cambridge are looking at some of them at present, we would have no hesitation in recommending the MK14. Suffice it to say that even

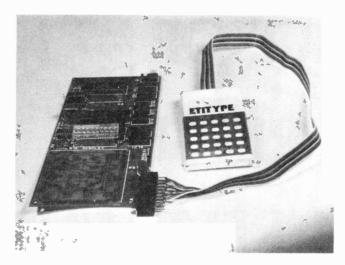
The MK 14 is a kit and is supplied as a plated through PCB together with some 14 ICs, a four part keyboard, display, reset switch, crystal and various resistors and capacitors, which must be carefully assembled according to the detailed instructions in the MK14's manual. The only equipment required is a soldering iron, solder and a pair of side cutters.

The manual assumes very little knowledge of electronics providing a guide to the identification and orientation of the various components supplied. The manual does however assume a knowledge of the resistor colour code and a section describing this might be a valuable addition to help those who have little experience of electronics.

The kit is not supplied with sockets though the manual "most strongly recommends" that sockets are used, a view we share, the extra cost of sockets proving its worth if any fault finding/system expansion proves necessary.

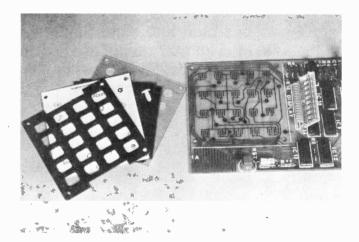
#### **Assembly Point**

Assembly of the kit is straightforward. The only area we thought likely to confuse was around ICs 12 and 13. At first sight it seems that there are 18 DIL holes on the PCB whereas the ICs to be fitted are 16 pin devices. A closer look however reveals that the pair of holes nearest the edge of the PCB are unused, a remnant of some previous layout? A very minor criticism however and if the manual's instructions are carefully followed and a reasonable standard of soldering maintained (notes on soldering technique mean that even those who have not soldered before should be able to tackle this kit) the assembly of the electronic components should pose no problems. With all the electronic work complete the keyboard and display can be fitted. The display is an eight digit calculator type and is connected to the PCB via a short length of ribbon cable. Again a couple of spare



One way around the bad keyboard of the MK14, a cheap calculator is modified to provide the system's input.

The pieces that go to make up the MK14's keyboard.



holes on the PCB but it is fairly obvious where everything goes.

#### **Key Feature**

The keyboard is one of the areas where cost cutting is apparent. It is a sandwich type construction consisting of a metal plate with some 20 holes, corresponding to the 16 hex character and the four command words (more of these later), under which a ledgend sheet is positioned. The next layer consists of a sheet of conductive rubber. The last layer of the construction is a sheet of card with a matrix of holes similar to that of the top plate punched out of it.

The assembly is held together by a set of four plastic pegs. These will prove almost impossible to fit unless they are first "squeezed" with a pair of pliers. Even when fitted they are not really up to the job and, as you can perhaps see in our photograph, on our kit we used four 4BA bolts in place of these pegs with far more satisfactory results

This arrangement is mounted above an area of the PCB that has a pattern of interlocking 'fingers' etched onto it. The idea is that the conductive rubber, usually seperated from the PCB by the layer of card, will bridge the gaps between the 'fingers' when sufficient pressure is exerted on the foam to force it down onto the board through the holes in the separator. That's the theory, in practice the operation is to, say the least, clumsy.

Science of Cambridge are aware of the difficulties of using the keyboard and are working on a number of solutions. These include providing plastic buttons to enable a more even pressure to be applied to the conductive foam or, a more expensive proposition, the provision of individual switches for each switch function.

A further solution is to connect an external keyboard to the MK14 via the keyboard edge connector. No details of the connection pattern for this are included in the manual, although

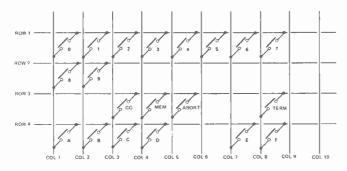


Fig. 1. The manual does not show it, but here is the connection information for that add on keyboard.

Fig. 2. Below, the memory map of the MK14 shows how the partial memory decoding of the kit results in the monitor and RAM I/O appearing all through the lower 4K of memory

F00	RAM (STANDARD)
E00	RAM I/O
D00	DISPLAY
C00	RAM I/O
B00	RAM (OPTIONAL)
A00	RAM I/O
900	DISPLAY
800	RAM I/O
000	
	MONITOR
600	
	MONITOR
400	
	MONITOR
200	MONITOR
000	MONITOR
000	

GND GND COL 7 COL 8 COL 6 COL 9 COL 5 COL 10 COL 4 COL 3 COL 2 COL 1 ROW 3 ROW 4 ROW 2 ROW 1

it is fairly easy to trace the PCB tracks and work out how the extra keyboard should be wired up.

With assembly of the MK14 complete the manual suggests that you put the kit to one side for 24 hours (to rest the eyes) before inspecting the PCB for signs of solder splashes or of IC pins that have not been soldered. When satisfied that all is well it's time to power up and begin to get to know the machine.

#### **Working Model**

As the MK14 features an onboard 5 V regulator, a power supply with a DC output in the range 7-35 V can be used, although the regulator will require a heat sink if supplies near the upper limit are used. In addition if the supply has a lot of ripple on it an additional capacitor of about 2 000u should be fitted in the space provided on the PCB.

Upon switch on, if all is well, the display should show a series of dashes in the four leftmost positions followed by two blank displays and a further two dashes in the righthand positions. The group of four characters will form the address field, while the group of two digits will become the data field.

If instead of a nice row of dashes you get some other display try pressing reset. If things are still not right, turn off the power and check PCB again. Science of Cambridge tell us that they have had very few kits returned and the faults have been due, in the main to hairline solder splashes or, in some cases to PCBs that have been incompletely etched in some areas. Another reason for return is the apparent faults thrown up by an inadequate power supply. The supply must not drop below 7 V when on load and must not present too much ripple to the MK14 (this latter problem manifesting itself

as apparent keyboard bounce).

If your MK14 will not go after all reasonable attempts to get it up and running Science of Cambridge offer a get you going service at little more than the cost of postage and replacement parts — expect to pay more if you haven't used sockets though.

#### **Routine Example**

The monitor program used by the MK14 is the same as that of the National Introkit plus Keyboard kit combination (KITBUG) and as such features four command words: GO, MEM, ABORT, TERM.

The dashes referred to above indicate that the MK14 is awaiting a GO or MEM command. The first of these to be introduced by the manual is the MEM key. This allows the user to display the contents of the MK14's memory. After pressing the MEM key a four digit hex number may be entered via the keyboard, the MK14 echoing this number in the address field as it is entered and displaying the contents of the memory location pointed to by the entry in the data field.

To examine the next memory location all that it is necessary to do is to operate the MEM key again whereupon the number in the address field will be incremented by one and the contents of the corresponding memory location displayed in the data field.

#### **End of Term**

The MEM key is also used in conjunction with the TERM key to modify the contents of the MK14's RAM. The location which is to be modified is first pointed to using the MEM instruction as above. The TERM key is now pressed and the two digit hex character we wish to enter can now be input via the keyboard, it being echoed as input in the data field's display. Further operation of the MEM key will increment the address pointer as before, the TERM key preceeding any data input. In this way a program can be built up in the system's RAM.

To execute a program entered in the above manner the GO key is used to set the address pointer to the memory location at which we wish to enter our routine.

The ABORT key will return the system to a condition in which it ex-

pects either a MEM or GO command. A reset will have much the same effect except that ABORT will not destroy the contents of the SC/MPs registers.

The manual takes the user through the operation of the command keys by describing the entry and execution of a sample program. The manual however fails to give an exact definition of their use or function and a section expanding on this aspect would be valuable.

The manual also makes no mention of how to input and output data from a user program. This together with the fact that sections on the basic principles of the MK 14 and a section on SC/MP architecture and instruction set would still leave the person with no knowledge of microprocessors a trifle lost is a little disappointing.

Science of Cambrige tell me however that the reason for this state of affairs is that a section covering programing, which will cover some of the above points was inadvertantly omitted from the manual. These details will however be included in all kits sold from now on

In addition to the addendum covering programming, a section making the use of the various programs listed in the manual a little clearer will also be included with all MK14s. There are some 22 program listings under the headings of Mathematical (multiply, divide etc.), Electronic (pulse delay, random noise etc), system (single step, relocator etc), games (moon landing, mastermind, etc), music (organ, etc) and miscelleaneous (message reaction timer, etc). Together these provide a good way of becoming familiar both with the MK14 and with the SC/MP MPU.

For those who start with a little more idea of MPU operation the

complete monitor listing included in the manual will prove a valuable aid in trying to get the most out of the system. There is also a full circuit diagram for those who wish to extend the basic system.

#### 1/0, 1/0, It's Off To Work . . . . .

That then is the basic MK14, but what of expansion? The PCB includes space for the addition of a further 256 bytes of RAM and of the National INS8154 RAM I/O device. This latter IC will greatly extend the scope of the MK14 kit providing as it does a set of 16 lines (configured as two seperate eight bit ports) each of which may be seperately defined as either input of output under program control. The IC also provides a further 128 bytes of RAM. The manual describes the use of the RAM I/O chip's various features including a section of the IC's use in handshaking mode. Connections to this IC are brought out to an edge connector at the top of the board. Again, although the manual describes the device, the explanation is brief, and for those unfamiliar withthe IC, will leave questions unanswered

As well as the extra memory and 1/O chip referred to above. Science of Cambridge plan to introduce a number of other MK14 expansion aids. First on the cards is a cassette I/O using a simple tone burst system together with a new monitor to include the software for this interface and to provide for easier data entry (getting rid of the MEM-TERM-MEM approach) and providing an offset calculation function. The space for these extra routines has been found by tidying up National's original monitor. Note that these new ROMS will be compatible with existing hardware and the cassette I/O can be used with Mk1 monitors by storing the necessary software in user RAM. Plans also, include a PROM programmer for a fusible link PROM and a low cost VDU.

#### **Last Night Of The PROMS**

The basic kit, as a cost saving measure, adopted a system of partial memory decoding and the basic board's maximum RAM complement of 640 bytes cannot be extended without alterations to the board—hardly worth bothering with a VDU. However the alterations to the PCB, involving the use of gates, at present used, enable up to 4K of memory to be addressed are not that major details of such modifications will be made available.

A second volume of programs is also in preparation. This will highlight the MK14's rôle as a control system with programs that should find a wide range of applications.

To sum up, at £40 the MK14 while not perhaps a "microcomputer" is an easy to build development kit that provides an excellent way of getting to know about MPUs. The system is let down at present by its poor keyboard and by some ommissions in the kit's manual

Science of Cambridge are however aware of these faults and are working on them. As for value for money, the MK14 is certainly the cheapest development kit that we know of, and with the cost of components bought individually coming to more than the kit price, its got to be a good buy.

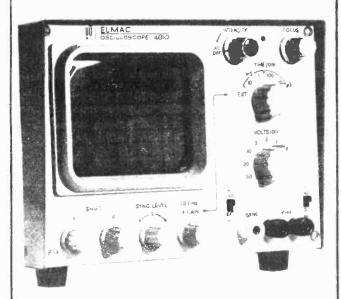
The MK14 is not a toy and with the low cost addons planned by Science of Cambridge, should prove a powerful tool to those wanting a versatile MPU development system at under £80.

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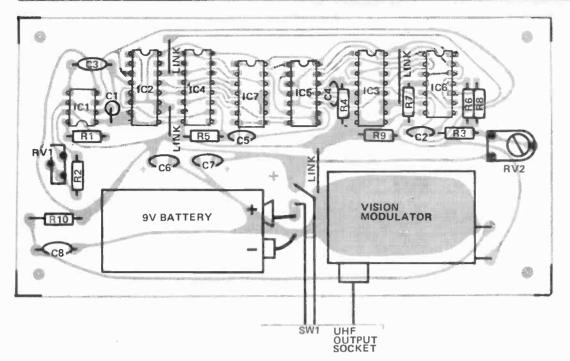


Fig. 1. The cross hatch generator's overlay is shown left.

#### PARTS LIST

RESISTORS R1 R2,7,8,	1k0 4k7	C4,5 C6,7	100p ceramic 33u 16 V tantalum
R3,4,5,6 R9 R10	10k 330R 110R	SEMICONDUCTORS IC1 IC2,3 IC4	5 555 4027B 4040B
POTENTIOMETERS RV1 RV2	5k miniature preset 25k miniature preset	IC5 IC6 IC7	4011B 4001B 4012B
CAPACITORS		MISCELLANIEOLIS	

180p ceramic

22p ceramic 10n polyester MISCELLANEOUS PCB as pattern, case to suit, output socket, single pole toggle switch, 9 V battery, Astec UMIIIE 36

#### **BUYLINES**

The only component liable to be difficult to obtain is the Astec UHF modulator. These are available from most suppliers of TV game kits, Watford Electronics and Teleplay are examples. Make sure you get a vision modulator, sound modulators look the same but will not work in this application! All the CMOS and other components is widely available. The PCB will be available from usual suppliers who advertise regularly in the magazine.

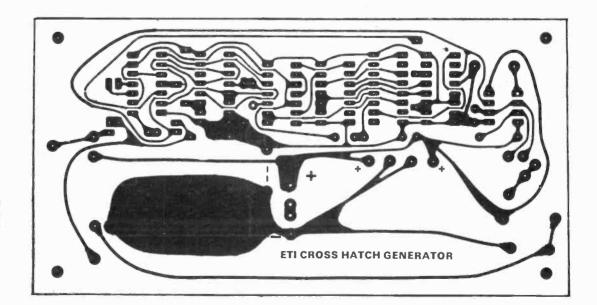
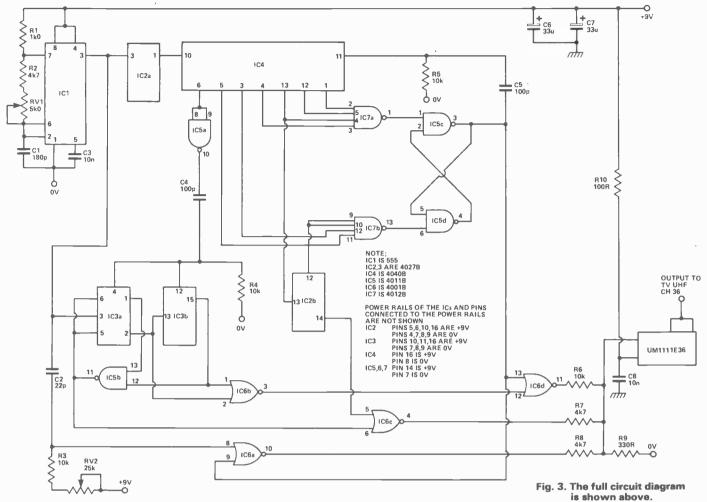


Fig. 2. The foil pattern of the cross hatch generator is shown full size on the right.

C1 C2 C3,8

PROJECT: Cross Hatch



**HOW IT WORKS** 

A TV picture is made up of a series of horizontal lines equally spaced down the screen with the information transmitted in a serial form along with the necessary synchronization pulses. There are 625 lines in each complete picture but these are transmitted as two "frames" each of 312½ lines with the second frame interlaced between the first giving a total of 625 lines. This is to reduce flicker of the picture which would otherwise occur.

To simplify our circuit and prevent a double horizontal line we have used 624 lines which eliminates the interlacing. The TV set automatically accepts this change.

To synchronize the TV set we need a  $192\mu s$  wide pulse every frame (20ms) and a  $4\mu s$  wide pulse every line ( $64\mu s$ ). All pulses, including the information, are derived from a single 249.6 kHz oscillator IC1. This is divided by 2 in IC2a and then by 2496 by IC4 giving an output of 50 Hz. This IC is a 12 stage ripple counter which, while normally dividing by 4096, can be forced to divide by 2496 by

decoding (IC7) the outputs from the 7th, 8th, 9th and 12th stages and reseting IC4 back to zero. The output of IC7 toggles the RS flip flop IC5/c, IC5/d which resets IC4 via C5. This flip flop is reset by the decoded output from the 4th and 5th stages of IC4. This occurs  $192\mu s$  later; thus the output from IC5/c is the frame sync. pulse.

To generate the line sync. pulse the output from the 3rd stage of IC4 (15,600 Hz) is used to reset both halves of the dual JK flip flop IC3. This IC is then toggled by the 249.6 kHz clock until, after three pulses, both "Q" outputs are '1' when IC5/b detects this and disables IC3/a, IC6/b decodes the second of these clock periods and this becomes the line sync. pulse. These pulses are combined in IC6/4 to give a combined sync. pulse.

The 249.6 kHz is differentiated by C2/R3 and after being quared up by IC6/a is used to generate 16 white spots on each line which results in vertical lines. These pulses are deleted during the frame sync. period to prevent interference to synchronization. Due

to variations in the CMOS a trim potentiometer is provided to give equal width to the vertical and horizontal lines.

The horozontal line is generated by IC2/b (JK flip flop) and this IC is toggled by the 8th output (487.5 Hz) of IC4 and is reset by the output of the 4th stage (64µs later). This gives a single white line every 16 lines. To prevent this line interfering with the line sync. pulse the output of IC2/b is combined with that of IC5/b which is high for a period 4µs before the line sync. pulse to 4µs after the pulse. This gives a short black region on both ends of the line (normally off the screen). The outputs of IC6/b, IC6/b and IC/c are combined by R6-R8 to give a composite video signal. Note that the video information gives positive pulses while the synchronization pulses are negative.

The video signal is fed to the UHF modulator. This is a ready built unit that is adjusted at the factory to operate on channel 36. R10 and C15 decouple the supply to the modulator.



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# DESIGNING HI(EST)~FI AMPS PART 2

Stan Curtis considers which parameters matter most in super-fi audio, and how they can best be optimised.

FOR MANY YEARS it has been the standard practise to specify and compare amplifiers through their ability to handle a continuous (steady state) sine-wave signal. Thus such a signal is used to measure power-output frequency response, harmonic distortion, crosstalk, input overload capability, intermodulation distortion, damping factor, and gain! Unfortunately many engineers and Hi Fi pundits still believe that such information is ALL that is necessary to quantify an amplifiers performance and to compare it with others. Not so.

Steady-state sine-wave testing can however, tell only part of the story and can often be misleading. Music contains complex wave forms with a spectral content of greater than eight octaves and dynamic ranges of up to 100 dB. Yet such complexity is readily understood by the human brain which, in mastering the subtleties of spoken language, has evolved the ability of extraordinary auditory sensory perception. The music signal, as with all audio signals, can be considered in terms of two variable qualities. — the frequency domain, and the time domain.

**Frequently Timely** 

The frequency domain is the area that has monopolised engineers thought for so long. Even the most complex music signal can be represented by a Fourier Analysis.

This develops mathematical equation which lists separately each frequency, making up the signal, together with its phase and amplitude. However, a Fourier Analysis is only complete in the case of simple waveforms, with more complex waveforms it becomes only a convenient approximation.

Of course, in order to make a Fourier analysis of a signal the components of that signal have to be analysed over a period of time such that complete cycles of the lowest frequency can occur.

Thus we take consideration of the Time Domain.

Where steady-state signals are concerned the Time Domain is not normally considered as the signal is of a continuous unchanging nature between any two periods. If the "time window", during which the signal is Fourier analysed is reduced progressively it becomes

apparent that an accurate spectral analysis becomes less possible. It can then be seen that the important characteristics of the signal are amplitude and rate of change. In other words it's envelope.

What is required is the amplification of an audio waveform in such a way that the ear can detect no degradation.

#### What Do We Want?

Let us consider ways in which such degradation can occur. The waveform envelope can be distrorted by amplitude changes of any component or by changes in the phase relationship of the component harmonics.

Experimental work has established that changes in the relative amplitudes of the harmonic structure of the

waveform are readily detectable.

Other work has shown that the qualitative characteristics of a complex sound depend upon the phase relationships of the component harmonics. It would seem that as a phase difference must be interpreted as a time delay between the component parts of the signal then a sufficient phase shift in a system must eventually become audible as these component parts are moved in respect to each other in time. In practise large phase shifts are very audible and indeed telephone lines are often subjected to phase and delay correction to render speech intelligible. However, establishing an acceptable degree of phase shift is extremely difficult.

Following the arrival of the ''linear phase' loudspeakers great controversy has raged over whether phase shifts effect sound quality. A study of the experimental

work performed to date shows that

i). It seems to be very difficult to repeat someone else's experiment (and get the *same* results!)

ii). It seems, on balance, that where recurrent waveforms (steady state) such as sine-waves (and instruments producing a "continuous" although decaying tone) are concerned; then quite large phase shifts, between the extremes of the frequency band, have no identifiable effect on sound quality.

However, a phase non-linerarity on the leading edge of a true transient appears to be audibly more perceptible. Particularly on speech and percussive sounds.

#### **Bandwidth and TID**

Transient signals cause many problems for amplifiers of which phase lineratity is but one. Other problems are; instability and ringing, clipping, slew-rate limiting, and transient intermodulation distortion. Transient intermodulation distortion (TID or TIM) is an effect that has been much in vogue in the past 3 or 4 years but which is often misunderstood. TID can be predicted mathematically but such a description is out of place here. TID most commonly occurs when an amplifier, with overall negative feedback over several stages, is driven by a large enough signal whose frequency (or equivalent rise time) is above the open loop bandwidth of that amplifier.

Because the feedback loop is fed from the output of the amplifier, it cannot be operating until signal current flows at the output. i.e. during the open-loop rise time of the amplifier.

The outcome is very large signals occuring in the intermediate stages of the amplifier causing those stages to distort or even to clip. With some amplifiers this clipping (which cannot occur with any steady-state signal) can cause the stage to latch-up for a time until the operating conditions restabilise.

Thus not only is the leading edge of the signal severly distorted — in some cases it is removed completely.

TID is therefore a form of overloading that is dependent upon both amplitude and time. This is audibly (but at a higher signal level) similar to cross-over distortion, as both effects cause phase and amplitude modulation of the signal due to momentary change in gain. (Remember that at the corss-over point zero, there is a no current flow in the output stage and hence no feedback current and so the amplifier is momentarily open-loop).

#### **Making Big Bands**

TID can be avoided by careful design an amplifier whose open-loop bandwidth is greater than the highest freqiency of the input signal. The maximum bandwidth can then be defined at the input by a passive RC Filter. Thus if we decide upon a maximum signal bandwidth of 20 KHz than our filter will limit the signal waveform rise-time to T=0.35

$$T = \frac{0.35}{20 \text{kHz}}$$
  
i.e. 17.5 uS

Our amplifier's open-loop bandwidth should be designed to be, say 23 kHz, giving it an open-loop rise-time of 15 uS. and freedom from TID. If however, in the interests of a good specification, and possibly better reproduction, we decide upon a closed-loop bandwidth of 100 KHz (i.e. a rise time of 3.5 uS.) then our amplifier will need an open-loop bandwidth of greater than 100 kHz to maintain freedom from TID effects. In a power amplifier such performance is not easy to obtain.

Fast power transistors are notoriously easy to blow-up and are expensive. The common form of lag compensation (used where the open-loop bandwidth is perhaps 2 kHz) has to be replaced by lead compensation.—

Another technique is an extension of the first in that the preceding stage of the power-amplifier is designed to have a lower open-loop band width than the next.

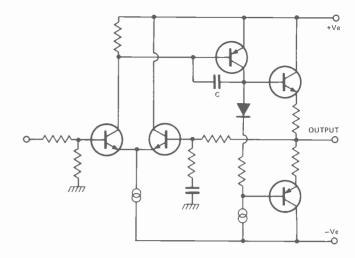


Fig 1. Circuit diagram of a typical amplifier circuit which employs lag compensation techniques — provided by C.

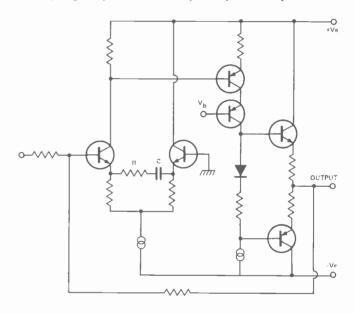


Fig 2. The other method. Lead compensation illustrated. Components R and C provide the time constant.

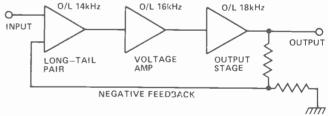


Fig 3. Third method of avoiding TID. Making each stage in the design have a wider B/W than the preceding one.

#### **Important or not?**

Many people now consider that TID is unimportant or that it doesn't exist. This is partly because it is very difficult to measure and only readily visible in the laboratory in the 'clipping' stage. To reach this stage with most amplifiers (but not TID — free designs) requires either fast rise-time or high signal levels or both.

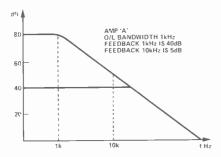


Fig 4. This amplifier design has a limited open loop bandwidth and the THD will rise with frequency.

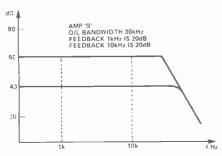
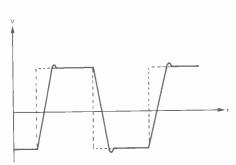


Fig 5. Contrast this with figure four above. The bandwidth here is much wider, resulting in a more linear THD response.



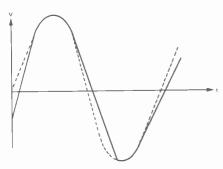


Fig 6. The effects of slew-rate limiting on a signal passing through an amplifier prone to this fault. Top: a squarewave, note the slight overshoot. Below that a sinewave. In both cases the dotted line represents the input.

Conditions that are unlikely to occur in practise.

However, a large degree of non-linearity and hence bad intermodulation will still occur with more realisable input signals. Although this cannot be measured yet (how do you measure say, 5% IM over a period of 5 milliseconds??) it can be predicted mathematically and, just as important, heard. Amplifiers free of TID have a very "open" quality with accuracy of depth.

#### **Benefits Conferred**

An amplifier designed with a wide open-loop bandwidth, for low TID often has other, more tangible, benefits. The high frequency THD is usually no higher that at the mid-point; in stark contrast to more traditional designs. This is because gain is still available at high frequencies for negative feedback.

#### Slew Who?

Such amplifiers also usually have much higher slew-rate. Slewing-rate defines the speed with which the amplifier can deliver output voltage to the load. For example, if an amplifier has a maximum output of 100 volts p/p and a rise-time of 10 uS. then the amplifier, if it were perfect, should have an output of about 80 volts after 10 u secs in response to a suitable square wave input. In other words the output voltage would have risen at the rate 8V/uS.

However, amplifiers do not generally respond to large changes as fast as their small signal characteristics predict, for circuit and transistor capacitances can be charged only as fast as their driving circuits allow. In its simplest form the slew-rate of an amplifier defines how fast the output voltage can change for large signal conditions, and it is normally quoted in Volts per microsecond. The maximum slew-rate of an amplifier is usually limited by the slowest stage in its circuit.

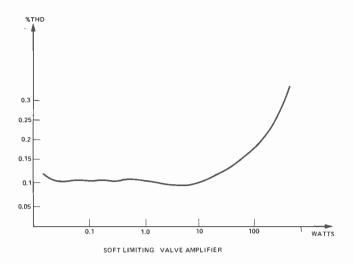
That stage will have an operating current I (as set in the design) and a capacitance C (usually a frequency compensation capacitor)

Thus it a transistor stage has a standing current of 100 u A and is compensated by a 43 pF capacitor then its Slew-Rate will be

Depending upon the design some circuits have a different Slew-Rate depending upon whether their output is negative-going or positive-going. Slew limiting also defines the full-power bandwidth; a figure more commonly quoted by manufacturers.

$$fp = \frac{SR (10^6)}{2 \pi E op}$$
 E op = peak output swing in volts 
$$fp = Full power bandwidth in Hertz.$$

Thus in a 100 Watt (into 8 Ohms) amplifier having full-power bandwidth of 20 kHz the required minimum slew-Rate would be about 5V/uS. This is, however, the absolute minimum figure and experience suggests that such an amplifier would have a hard, gritty high-frequency sound. Such an amplifier should have a Slew-Rate of greater than 20V/us to be certainof avoiding the increase in distortion caused by the gradual onset of slew-limiting.



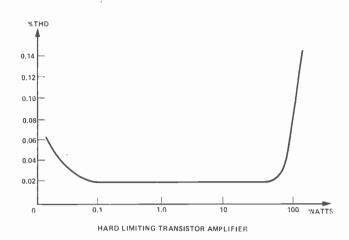


Fig 7. A comparison of the limiting characteristics — in general — of both transistor and valve amplifier types. There is a body of opinion which holds these curves to be the whole truth as to why valve amplifiers are preferred by many musicians.

Unfortunately the higher the power output of the amplifier the greater the required slew-rate as more volts are swing at the output in the same period of time and so as our 100 W amp needs a 20V/uS an otherwise identical 50 W amp needs 14V/uS and a 20W amp needs only 9V/uS.

**Clip Around The Ear** 

But these forms of distortion tend to give subtle audible effects compared to the most common amplifier problem — that of clipping. Clipping occurs when an amplifier is overloaded by high level signal peaks. Such peaks occur frequently in much music material and so the manner in which the amplifier clips determines its audibility. A soft, clipping effect where the distortion rises gradually (typical of valve amplifier circuits) is audibly preferable to the hard clipping typical of transistor circuits.

Worse still, some amplifiers tend to suffer saturation

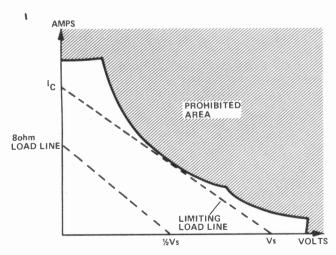


Fig. 8. Illustrating the load line conditions for output stages,

effects on clipping, and take a time to recover; thus artificially extending the length of time the signal is clipped. The use of overall negative feedback to reduce distortion unfortunately makes things worse. Overall feedback effectively linearises the clipping making it hard the distortion changes from 0.01% (say) to 10%, and quite suddenly too.

#### **Designing A Designer**

We have covered just a few of the requirements a designer must consider when working upon the design of power-amplifier. There are many more to be considered to even rough out a design specification before the circuit hardwave is considered. The following sequence is mandatory:

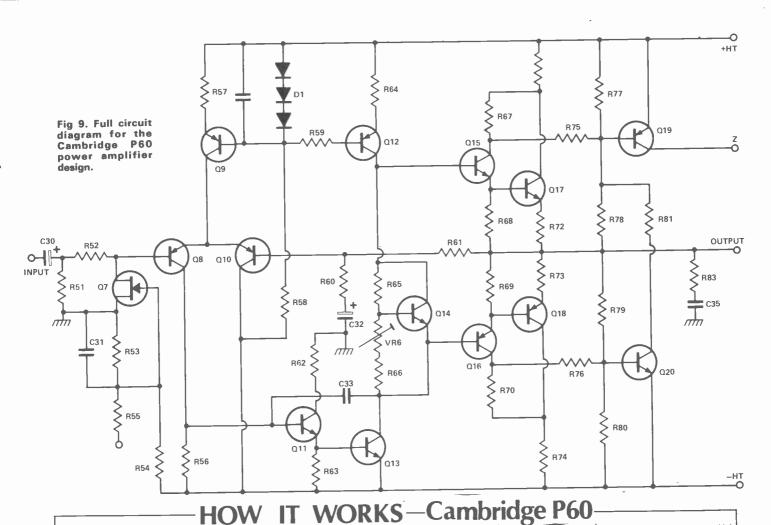
- 1. What parameters are important to prevent any audible degradation of the signal?
- 2. Detail a performance specification that meets the requirements of (1).
- 3. Decide upon the circuit technology necessary; Bipolar; MOSFET; Valve; Class A; Class B; Switching; fact; slow; etc; etc.
- 4. Perform a development programme to produce a prototype.

At this point the designer has to admit that it's a real world and that his performance specification cannot be achieved in a way that is acceptable to the accountants, salesman, customer, customer's wife or whoever else is around. Trade-offs are necessary and much to the 'art' in amplifier design is in the deciding which defects and degradations are more acceptable than others.

As an illustration of the changes in design approach over the years we will briefly illustrate three designs for which the author has been responsible:

- 1. Cambridge Audio P60 (P80) (designed 1974)
- 2. Lecson AP3 Mk II (designed 1976)
- 3. Mission Electronics Voltage Amplifier

(designed 1977)



The P60 power amplifier is of a conventional design but with care being taken to optimise each stage. Q8 and Q10 form a long-tailed pair with Q9 as their emitter current source. Q8 and Q10 must be very closely matched for minimumDCoffset and for maximum common-mode rejection to avoid H. T. ripple appearing at the output. The next stage is the Q13 voltage amplifier which is loaded by a current source (Q12) instead of the more common "bootstrapped" resistors. Note that Q13 is buffered

from the long-tail pair by an emitter follower (Q11) to prevent any loading of that stage worsening the distortion characteristics.

Capacitor C33 gives lag compensation which defines the dominant pole of the amplifiers. The open-loop bandwidth is quite high (for this type of circuit) at 12 kHz but none the less this amplifier is prone to TID effects. The protection circuit is very unusual in that the output is limited by an FET (Q7), Q19 and Q20 each form conven-

tional V-I summing circuits which monitor the loading on the output stage.

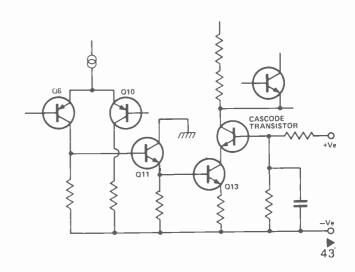
If either Q19 or Q20 turns-on, the gate of the FET Q7 (normally biased-off by R54 to the negative HT) is biased positive and it starts to turn-on. It then acts as a potential divider with R52 and thus attenuates the audio signal. This protection only turns on at the equivalent of 50 W into 2 Ohms load and when it turns on it only adds moderate distortion (0.2% typically) as distinct from clipping.

**Improvements** 

The P60 is capable of good mid-band performance (THD 0.01% at 1 kHz 30 W) but its high frequency distortion is poor because of the limited open-loop bandwidth. Generally this amplifier performs well at low and moderate levels but at high levels its sound quality becomes hard and aggressive. Some improvements to this circuit can be quite simply made as follows:

- 1. A resistor is fitted between Q10 collector and the negative rail to give better balance between Q8 and Q10:
- 2. A cascode transistor is fitted to Q13 collector to reduce ''Early effect'' distortion due to the collector-base capacitance of Q13.
- 3. An emitter resistor is fitted to Q13 to provide local negative feedback.

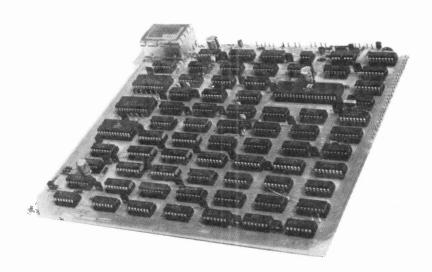
Fig 10 (Right). Showing how some of the improvements mentioned can be added to the P60 basic design.



ELECTRONICS TODAY INTERNATIONAL — SEPTEMBER 1978

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APPLICATIONS: Public address - Disco - Power slave - Industrial

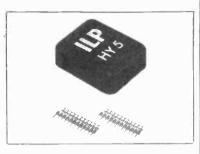
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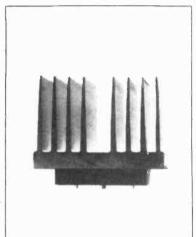
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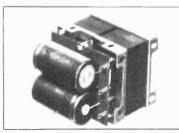
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United States

THE FIRST FREE-FLIGHT test of the Space Shuttle, watched by 60,000 people marked the commercement of the final phase of months of testing at Edwards Air Force Base in the Mojave Desert of Southern Cal fornia. At the end of January 1977 the Shuttle was moved from its assembly facility at Palmdale, along 58 km of specially widened roads to Edwards AFB, for the first Appreach and Landing Test (ALT).

The Space Shuttle is the first of a new breed of spacecraft which is designed to be reused. Previously,

spacecraft which is designed to be reused. Previously, the technology available meant that each scacecraft could be used only once, but for any ong-term program of space research this is extremely wasteful. Everything was built to the highest standards and ther used only once. The Space Shuttle changes this. The Space Shuttle Orbiter vehicle is designed to land intect in the same manner as an aircraft, and the solid rocket boosters used to provide the enormous thrust at takeoff are also reusable. In fact, a Space Shuttle can be launched as quickly as 160 hours after anding from the previous mission, although a two-week ground turnaround is the goal in actual use. goal in actual use

Up Up And . . . The Shuttle is launched vertically, attached to an external tank which contains the ascent fuel burnt by the Orbiter's main engines, and two solid rocket boosters. At lift-off all the engines fire in parallel, the SRB's each generating 11.800,000 Newtons of thrust and the three Orbiter engines each generating 2,100,000 N. The two SRB's are jett soned once they burn out and are recovered after a parachute descent. The external tank is jettisoned before the Orbiter attains orbit.

The orbital manoeuvring system is used to make any adjustments to the orbit or any manoeuvres that may be

# 

Hidden in the wake of the Apollo's the Shuttle Orbiter heralds the beginning of a new age — the age of the true spacecraft. With its initial testing completed, we take a detailed look at the first (proven) spaceship to land on Earth!

required during the mission. The jets for this system are mounted near the nose and in pods on the upper rear of the fuselage. These jets can pitch, roll or yaw the O biter. The Orbiter is designed to carry a crew of seven (early

Enterprise

missions cal for four), including scientific and technical personnel and a payload up to 18 m long and 5 m in diameter. Because of the low g torces at launch, only 3g and less than 1.5g on re-entry, space flight is no longer limited to intensively physically trained astronauts—now experienced against the second to the seco access to zero g, vacuum conditions.
Payloads up to 29,500 kg can be placed into orbit.

These can range from small satellites to fully equipped scientific aboratories, and not only can the Space Shuttle launch payloads into orbit, it can also retrieve and return them, and service or refurbish satellites in space. The versatility of the Shuttle's cargo opens up whole new areas, e. space manufacturing

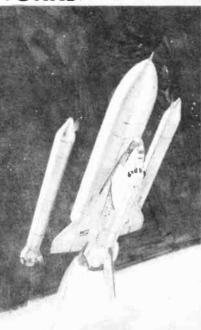
Upon completion of the various mission duties, the crew will prepare the Orbiter for re-entry — this is when the Space Shuttle really flies. The Orbiter, since it moves in the two media of air and vacuum, has two separate manoeuvring systems. One is the orbital manoeuvring system referred to above, and the other is a set of aerodynamic control surfaces that act in much the same way as convertional aircraft.

There are sever aerodynamic control surfaces on the

## HOW IT WORKS=



LAUNCH



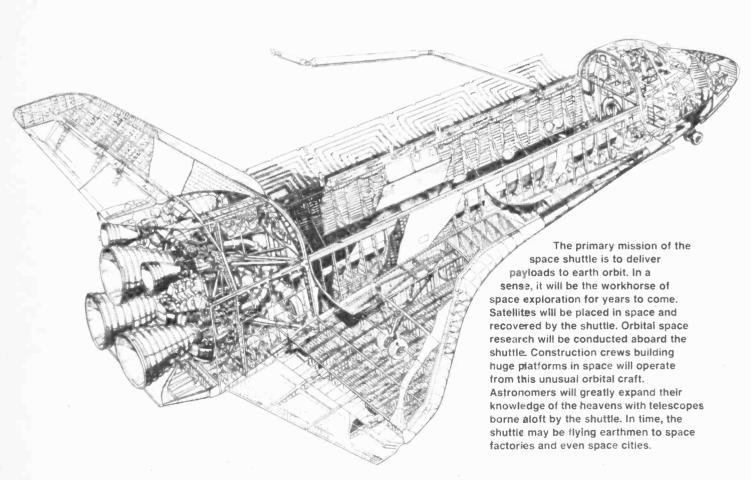
SEPARATION OF SOLID-ROCKET BOOSTERS
HEIGHT: 46 kilometers
(28 miles)
VELOCITY. 5008 km/hr
(3112 mph)

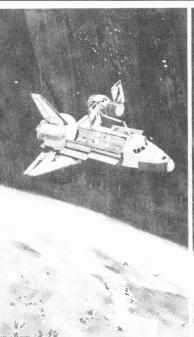


SEPARATION OF EXTERNAL TANK

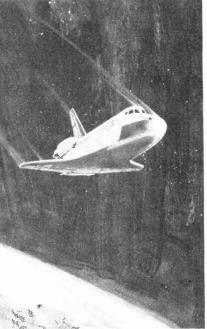


ORBIT INSERTION AND CIRCULARIZATION
HEIGHT: 185 kilometers
(115 miles, typical)
VELOCITY: 28,300 km/hr
(17,600 mph)





ORBITAL OPERATIONS
HEIGHT 161-966 (silometers (100-600 miles)
DURATION 7 30 days



ATMOSPHERIC ENTRY
HEIGHT 122 islometers
(76 miles)
VELOCITY 26 765 km/hr
(16,633 mph)



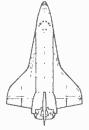
CROSSRANGE: ±2011 kilometers (±1250 miles) (from entry path)
VELOCITY 335 km/hr

(208 mph)



SERVICING FOR RELAUNCH





SHUTTLE ORBITER



DOUGLAS DC-9

Orbiter. Four of these are on the rear of the wings and are called 'elevons' — they combine the effects of elevators and ailerons. The fifth surface is at the bottom rear of the fuselage between the wings, and assists the elevons in controlling the pitch of the craft. It also protects the rocket engine nozzles from buffeting in the airstream during re-entry. The two remaining panels are on the rear of the vertical tail and can be used as a rudder or spread apart to form a 'speedbrake' by increasing the drag. This is used to limit the airspeed during landing.

At low speeds these surfaces act in a conventional manner. However, at supersonic speeds above Mach 1.5, the effect of some of the control surfaces is reversed, or not the expected one, which makes flying in a conventional manner impossible! To get round this problem, the Space Shuttle, unlike most aircraft, which use mechanical or hydraulic links between pilot and controls, uses a digital 'fly-by-wire' Flight Control System. This is based on three on-board IBM System / 4 Pi AP-101 computers which monitor their own operation to provide a measure of fail-safe redundancy.

## **SPECIFICATION**

#### LENGTH

SYSTEM: 56.1 meters (184 feet) ORBITER: 37.1 meters (122 feet)

#### HEIGHT

SYSTEM: 23.1 meters (76 feet) ORBITER: 17.4 meters (57 feet)

#### WINGSPAN

ORBITER: 23.8 meters (78 feet)

#### WEIGHT

GROSS LIFT-OFF 1,99 million kilograms (4.4 million pounds)

ORBITER LANDING: 84.8 thousand kilograms (187 thousand pounds)

#### THRUST

SOLID-ROCKET BOOSTERS (2): 11.6 million newtons (2.6 million pounds) of thrust each

ORBITER MAIN ENGINES (3): 2.1 million newtons (470 thousand pounds) of thrust each

#### CARGO BAY

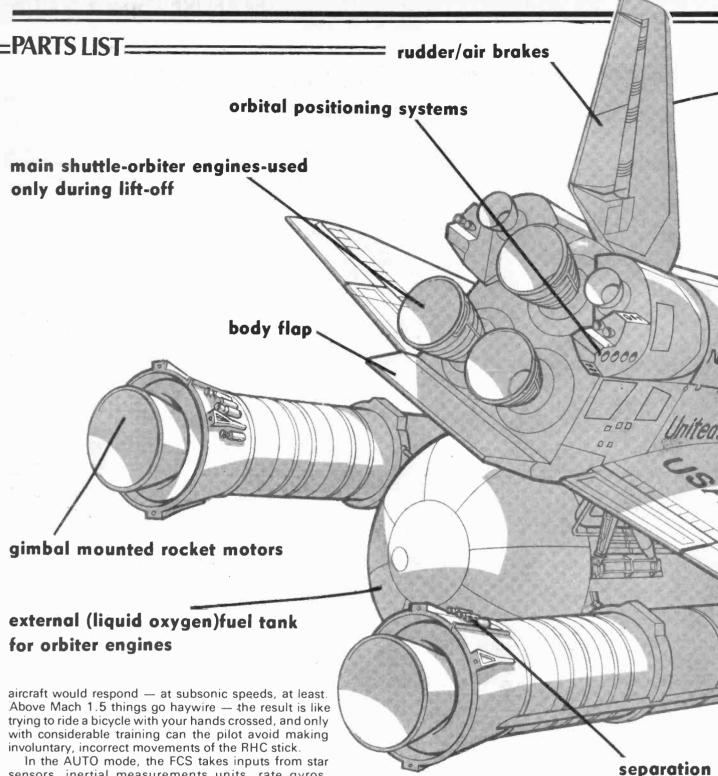
DIMENSIONS: 18.3 merers (60 feer) long, 4.6 merers (15 feer) in diameter

ACCOMMODATIONS: Unmanned spocecraft to fully equipped scientific laboratories

#### Flight Modes

The Flight Control System (FCS) can be operated in three modes: Direct (DIR), Control Stick Steering (CSS) and AUTO. The mode can be selected separately for pitch, Iroll/yaw, speedbrake and body flap controls.

In DIR mode, the pilot grips a small stick called the Rotational Hand Controller and ordinary pedals. Movements of these inputs to the FCS produce movements of the control surfaces in the same way as a conventional



In the AUTO mode, the FCS takes inputs from star sensors, inertial measurements units, rate gyros, accelerometers, and air data sensors, and compares this with the desired trajectory, automatically making corrections to keep on the path. In fact, the Orbiter can land itself from orbit completely automatically, with the only pilot intervention required being landing gear extension and operating the brakes on the runway!

In the CSS mode, the Flight Control System interprets between the pilot and the control surfaces. The pilot uses the Rotational Hand Controller and pedals, but the FCS accepts these inputs as rate commands in pitch, roll or yaw — in other words, the way the pilot wants the Orbiter to move. These commands are compared with inputs from the rate gyros and accelerometers, and

generates control signals to implement the pilot's commands. In this mode the FCS automatically takes account of the reverse effects produced by the aerodynamic surfaces at high airspeeds.

motors

#### Re-entry

The Orbiter starts re-entry at a high angle of attack, around 30 to 40 degrees, so that the bottom of the wing and fuselage are exposed to the airstream. The under surface is covered with a high-temperature structure of reinforced carbon-carbon on the leading edges and

## - reinforced carbon edge

payload doors crew module positioning motors silicia heat resistant tiles solid fuel rocket booster

special silica tiles over most of the other surfaces to maintain the airframe within acceptable temperature limits.

Unfortunately, because of the high angle of attack, moving the RHC to the left in the DIR mode causes the Orbiter to roll to the right. This is because the right elevon is deflected downward, but this causes drag, and turns the vehicle to the right. This increases the lift on the left wing, so it lifts, causing the right roll. In the Control Stick Steering mode, though, this problem is taken care of by the Flight Control System, and the pilot simply moves the stick the way he wants the vehicle to

go and it responds in the correct way.

The angle of attack must be carefully controlled to avoid overheating problems during the descent. To accomplish this, the Shuttle banks at up to 80 degrees, and so flies on a curved path. This would take the Shuttle away from its target and so, several times during the re-entry, the bank angle is reversed, and the vehicle starts turning back towards its target. This manoeuvre is complicated by the fact that, because of the high angle of attack, the rudder is virtually in a vacuum, and so these turns are executed by rolling the Shuttle.

Approach

Finally the Orbiter is down to a speed of Mach 1.5, and begins to fly like a conventional aircraft. It is now at a height of 21,000 m and about 50 km from its landing field. From now on, things are straightforward as the pilot closes in using conventional electronic navigation equipment like TACAN and Microwave Scanning Beam Landing System. As he turns to the final flitepath, the pilot will use the speedbrake on the tail to lose both speed and height. During this phase of the landing, the

> The complete system. The only nonreusable section is the fuel tank for the Orbiter en-gines. This gines. drawing shows clearly the dif-ferent types of thermal protection adopted on different parts of the Orbiter.

reinforced carbon leading edges

parachutes packed in nose

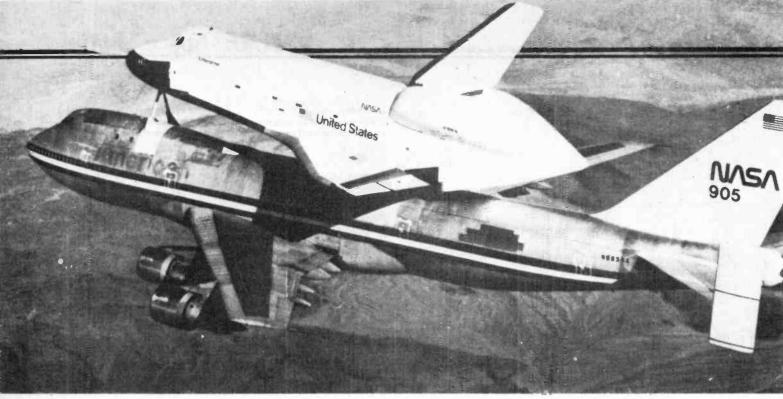
speedbrake is normally open at 45°. If the Orbiter is high, the pilot will open the speedbrake and steepen his descent; if low, he will close it and fly a shallower glidepath...

The Orbiter makes final approach at 540 km/hr and at an angle as steep as 24". At 600 m, the pilot starts to pull up, or 'flare', and at 300 m, the landing gear is dropped. The vehicle touches down at 350 km/hr; at this point it is losing 9 km/hr of speed every second and stalls at 280 km/hr, which is why the land is at such high speed.

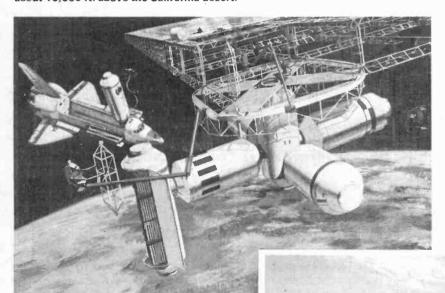
The Approach and Landing Test were designed to check out the performance of the Shuttle during this phase of the mission. They were also designed to check the performance of that now-famous 747/Space Shuttle combination which will continue to fly, delivering Orbiters to the launch site from the production line and landing sites.

First Flights

The first flight of the Space Shuttle took place on 12th August last year. At 8 AM, the 747 Shuttle Carrier



Space Shuttle Orbiter 101 rides "piggyback" atop NASA's 747 Carrier Aircraft in the first series of captive approach and landing tests concucted at NASA's Dryden Flight Research Center at Edwards Air Force Base in California. With the Orbiter unmanned and its systems inactive, the highly successful first tests verified the safe operation of the combined vehicle configuration, Photo was taken at about 16,000 ft. above the California desert.



Space Shuttle can deliver both the materials and the machinery requirect to build large space structures, such as this demonstration satellite solar power station. After being fabricated and assembled in low earth orbit, a power station would be transferred to its permanent place in geosynchronous brhit (about 22,000 miles out in space). There it would bearn a continuous stream of microwave energy to earth receivers, which would convert the emergy to electricity. When completed the station would be 1000 feet square and 25 feet thick.

The Shuttle orbiter cargo bay — which is larger (60 by 15 feet) than most freight cars — will accommodate a great variety of payload combinations. Payloads can be installed or removed while the orbiter is either horizontal or in the vertical position on the launch pad, as shown here, which greatly enhances operational flexibility. The payload "changeout" room is located in the white structure on the left.

ELECTRONICS TODAY INTERNATIONAL — SEPTEMBER 1978

Aircraft with its piggyback Orbiter took off on time — the only problem had been a fault in one of the AP101 computers, but that unit was quickly replaced.

At 8.47 the pair were at 8,539 m, and the Boeing started a 7° dive. At a speed of 280 kts, and a height of 7.346 m, the Boeing pilot informed the Shuttle crew that they were ready for separation. The crew, Haise and Fullerton, fired the separation bolts and lifted away, rolling to the left while the 747 dropped to the right. Following a pair of right and left rolls to put some distance between the two craft, Haise tried a practice flare and some banking manoeuvres. This gave the computers at Johnson Space Centre the opportunity to calculate any deviation from the predicted lift/drag ratios, information which would allow a more accurate landing. In fact, the JSC ground controllers muffed it by assuming that the Orbiter was in level flight, whereas it was actually climbing, so they concluded that the lift/drag ratio was lower than predicted.

Haise could not open the speedbrake beyond  $45^{\circ}$ ; this was a mission constraint to avoid steep glideslope angles. Performing a flare at 270m, Haise touched down 600m beyond the expected touch down point at a speed just over 360 km/hr. The overshoot was no problem, as runway 17 at Edwards AFB is 11 km long, but with the wheels on the ground, Haise opened the speedbrake to  $90^{\circ}$  and the nose wheel came down. The flight had lasted just 5 min 23 sec.

The first three flights were made with a streamlined tail fairing covering the dummy rocket engines at the tail. The fourth flight, on 12th October, was made with the fairing removed, giving a slightly reduced lift/drag ratio. Otherwise, the vechicle did not behave significantly differently.

#### **Next Comes Nothing**

With all the approach and landing tests completed, the Shuttle programme moves into its next phase which takes it into space. In the middle of 1979 the Orbiter will be lifted from Cape Kennedy for its first real flight. At present the projected date is sometime in June, but this may well change.

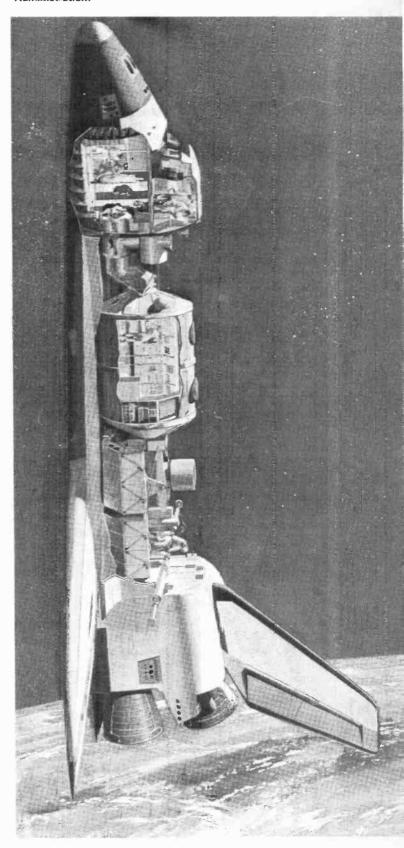
Rockwell are already selling space in the cargo bays—and doing very well too. One of the first payloads will be the Euro Space Lab, which will use the Orbiter's ability to stay put in space for up to a month or more. Cargos are being accepted from commercial firms too—so if you fancy sending a package into space this is your chance. Move quickly though because space in space(!) is harder to get than Star Wars tickets and bookings stretch out a few years into the future.

#### **Hopeful Sign**

Of course the Shuttle gives us the capability to build space stations at last, with all that implies — solar power, weather control, observatories and starships. It may be a long time before Man does reach for the stars, but at least we've taken the first step.

Our thanks to Rockwell International — Space Division — for their assistance in the preparation of this article.

A key Shuttle payload is Spacelab, center, a multipurpose laboratory that will enable scientists to conduct experiments in the gravity-free environment of space. The lab is being produced by the European Space Agency (ESA), a consortium of European nations, in cooperation with the National Aeronautics and Space Administration.





# CCCTONICS TOTAL international

ALL IN OUR OCTOBER ISSUE: ON SALE 1st SEPTEMBER

# CLICK ELIMINATOR

Gordon King explains and reviews Garrards ingenius (but simple in theory) device for removing those annoying 'clicks' caused by scratches on your favourite LPs.

# TECH TIPS SPECIAL

Next month we present a bumper 8-page special of your circuit ideas.

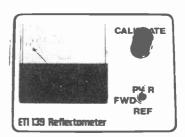
# PROXIMITY SWITCH

This switch, which is activated when an object approaches, will find a multitude of applications ranging from things like burglar alarms to light switches that will activate as someone walks through a door. The switch is a true proximity switch so you do not have to have any hands free to activate it!

# COMPLEX SOUND GENERATOR

Complete with a simple probe keyboard, this inexpensive unit is really a 'one chip' synthesizer capable of producing a variety of grunts, groans or squeaks. It can be used as a simple sound effects unit or simply as an organ capable of producing an enormous number of sounds.

# RF POWER METER



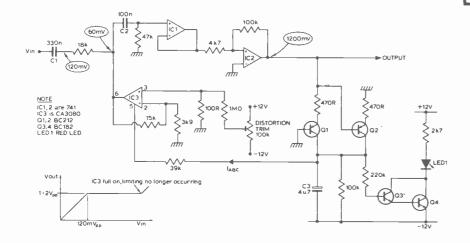
An indispensible tool for the radio amateur or communications serviceman alike. The unit is both an RF power meter and SWR meter which will operate with RF from 100kHz to 100MHz, and can be built to cope with powers from 500mW to 500W.

the

popular

Following

# Voltage Control Of Gain



articles on Op-amps and Oscillators, Tim Orr has once again put pen to paper to reveal the techniques behind the theory and practice of Voltage Control of Gain, and once more gives many circuits, each of which is a project in itself.

Features mentioned here are in an advanced state of preparation but circumstances may affect the final contents.

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F8	8 Bit CPU (3850)	16.95	FLOPPY OISC CONTE	(WD)	55.95	UHF Vision modulator Standa		2.90
280	8 Bit CPU (3880) (2MHz)	14.90	17718-01	(WD)	57.95	UHF Vision modulator 10 MH		
Z80A	8 Bit CPU (4MHz)	20.50	17715-01	, ,	07.00	VHF Vision modulator channe		3.10
CDP1802CD	8 Bit CPU	19.95				Sound modulator compatible	with above	2.90
2650	8 Bit CPU	24.95	PROM'S					
AM2901	4 Bn CPU	22.95	1702A	256 x 8 8it (1 us) TS Erasable	5.00	AY-3-8500 6 Games	4.90	PCB 1.90
6502	8 8rt CPU	11.95	2704	512 x 8 Bit (540ns) TS Erasable	13.00	AY-3-855D 6 Games	3.90	PCB 1.9C
6800	8 Bit CPU	11.95	2708	1024 x 8 Bit (450ns) TS Erasable	9.00	AY-3-8610 10 Games		PCB 1.90
6800	MPU	25.00 9.95	2716 (5 + 12)TI	1024 x 16 Bit (450ns) Erasable	25.00	AY-3-8760 Motor-bike		PCB 1.90
8008-1 8035	8 Bit CPU	22.00	2716 (5v)INTL	1024 x 16 8it (450ns) Erasable	29.00	AY-3-8710 Tank-battle	6.90	PCB 1.90
8080A	8 8it CPU w/clock 64 byte ram	7.95	2758 (5v)	1024 x 8 Bit (450fs) Erasable	22.60	AY-3-8603 Road Race	5.90	PCB 1.90
8085	8 Bit CPU	19.90	D3601	1K Bit 256 x 4 OC (70ns)	4.50			
TMS9900TL	8 8rt CPU (1.2us) 16 8rt CPU	75.00	D3604	4K Bit 512 x 8 OC (90ns)	11.00 11.95			
		78.00	5203AQ	256 x 8 Bit (1 us) TS Erasable 512 x 8 Bit (1 us) TS Erasable	11.25			
8080A SUPPORT			5204AQ	512 x 8 8it (500ns) Erasable	14.50	TELETEXT		0+VAT 121/2%
8212	8 Bit I/O Port	3.50	6834 •	512 x 8 Bit (500ns) Erasable	14.95	TIFAX XM11 Teletext board		
8214	Priority Interrupt Control	9.00	6834-1	32 x 8 Bit (50ns) OC	4.00	For ORACLE and CEEFAX Re	ady-built tested and	guaranteed 17.50
8216	Bi-Directional Bus Driver	3.75	82S23B	256 x 4 Bit (50ns) TS	4.25	Power supply for XM11		17.90
8224	Clock Generator & Driver (2MHz)	3.50	82S129B	32 x 8 Bit (50ns) OC	3.50	Key pad for XM11	and the filter for the same	
8224-4	Clock Generator & Driver (4MHz)	9.95	82238	32 x 6 bit (30hs) 00	4.25	Interface to TV varies from Mo addition of 4 resistors (e.g. No		n be as simple as
8226	Inverting Bi-Directional Bus Driver	3.95	82S123B		7.20			9+VAT 121/2%
8228 8238	System Controller & Bus Driver System Controller & Bus Driver	7.95 7.50				DECCA colour TV with MX11	rererext £38	Ø + VM I 127770
8251	System Controller & Bus Driver	9.95	DYNAMIC RAMS					
8253	Programmable Communication Int Programmable Interval Timer	18.95	416D/4116	16K x 1 8it (200ns) (16 pin)	25.00			
8255	Programmable Peripheral Int	12.00	1103	1024 x 1 Bit (300ns)	1.00			
8257	Prog Direct Mem Access Cont	17.95	2104	4096 x 1 Bit (300ns) (16 pin)	4.00	HARDWARE AND ACCES		
8259	Programmable Interrupt Cont	17.95	2107B	4096 x 1 Bit (300ns) (22 pin)	4.25	Std Ribbon Cable	10 Way	.78 per metre
8275	Programmable CRT Controller	53.00	21078-4	4096 x 1 Bit (270ns) (22 pin)	3.95	Screened Multicore	4 Way	.40 per metre
8279	Programmable Keybd / Disp Int	20.00	TMS4050	4096 x 1 Bit (300ns) (18 pin)	4.00		8 Way	.55 per metre
6800 SUPPORT	1 Togrammable Reyou 7 Disp Int	20.00	TMS4060	4096 x 1 Bit (300ns) (22 pin)	4.50		12 Way	.72 per metre
6810P	128 x 8 Static Ram (450ns)	4.95	TMS4070-2	16K x 1 Bit (200ns) (16 pin)	26.00	Edge Connectors	20 Way	1.00 per metre
	128 x 8 Static Ram (250ns)	6.00	4096	4096 x 1 Bit (300ns) (16 pin)	4.00	Edge Connectors	8 Way	1.50
68810P 6820P	Peripheral Interface Adaptor	7.50	4116/416D	16K x 1 bit (200ns) (16 pin)	21.00		12 Way	2.00
6820P	Peripheral Interface Adaptor	7.50	MM5270	4096 x 1 Bit (200ns) (22 pin)	4.50		16 Way	2.50 2.80
6828P	Priority Interrupt Controller	11.25	MCM6605	4096 x 1 Bit (200ns) (22 pin)	5.00		24 Way	3.60
6834P	512 x 8 Bit Erasable Prom 500	16.95					32 Way 40 Way	4.10
6850P	Asynchronous Comm Adaptor	9.75				PCB Edge Connectors	8 Way	2,50
6852P	Synchronous Serial Data Adaptor	11.75	STATIC RAMS	** * 1450	1.20	r CB Lage Connectors		3,10
6860P	0-600 RPS Modem	10.00	21L02	1K x 1 (450ns) 1K x 1 (350ns)	1.60		1 2 Way 1 6 Way	3.70
6862P	2400 RPS Modulator	14.50	21L02		1.75	Rectifier Bridges	4 Amp 100V	1.95
6871P	Clock	28.00	21L02	1K x 1 (250ns)	10.75	riectines oridges	6 6 Amp 200V	3.90
6875P	Clock	8.75	410D	4K x 1 (200ns)	1.00		0 0 Amp 2009	3.90
68BOP	MPU Bidir Bus Ext	2.50	1101A	256 x 1 (1 usec)	2.95			
Z80 SUPPORT DI	EVICES		2101-1	256 x 4 (450ns) 1 K x 1 (1 usec)	1.25	Variable Voltage regulator (in	nut 4.40V output	
3881	Parallel I/O Controller	9.90	2102	1K x 1 (1 0sec)	1.40	1 2V-37V. 1 5 A)	put 4 401, output	8.50
3882	Counter Timer Circuit	9.90	2102-1	1 K x 1 (450ns) 256 x 4 (450ns)	3.95	Variable Zener diode (3-30V.	775MW)	1.95
CHARACTER GE		5.50	2111-1	256 x 4 (450ns)	2.95	LM 309K regulator (others a	vailable)	1.35
2513	Upper (—12 ± 5)	6.75	2112-1 2114-3	1024 x 4 (300ns)	9.95	and a second sec		
2513	Lower ( 12 ± 5)	6.75	2114-3 2125L	1024 x 4 (300hs)	11.10	LED displays	0 3 ins.	1.80
2513	Upper (5 volt)	9.75	2125L 2147	4096 x 1 (70ns)	29.50		0 6 ins	3.00
2513	Lower (5 volt) (avail 5 / 15)	10.95	31L01	16 x 4 (110ns)	2.50	LED discrete	0 125 or 0 2 in	s 10 for 1.20
2516	Upper (—12 ± 5)	10.95	3106	256 x (Tri-State)	3.95	HEX LED display	0.27 ins. Red	9.95 ea
MCM6571	Upr , Lwr & Greek-Up Scan	10.95	3107	256 x 1 (80ns)	3.95	Crystals	3.579 MHZ	1.95
MCM6571A	Upr Lwr & Greek-Down Scan	10.95	TMS-4044(45)	4K x 1 (450ns)	9.95		4 MHZ	2.90
MCM6574	Character Gen	11.25	4200A	4K x 1 (250ns)	12.95		4.43 MHZ	1.25
MCM6575	Character Gen	11.25	TMS-4045(45)	1024 x 4 (450ns)	11.00	DIL Sockets	14 Pin	.19
USRT			5101	256 x 4 (650ns)	8.30		16 Pin	.19
S2350	500K Baud (5 volt)	10.75	74C89	16 x 4 (275ns)	3.25		18 Pin	.25
	2004 Badd (2 40H)		74005	16 × 4 (60ns)	2.25		20 Pin	.30
UART'S AY51013A	30K Baud (-12 +5)	5.25	745201	256 x 1 (50ns)	4.50		24 Pin	.40
AY5 1013A AY5 1014A	40K Baud (5 volt)	8.25	P8101	256 x 4 (450ns)	4.20		40 Pin	.60 .45
	20K Baud (5 Voit)	5.25	P8155	256 x 8/22	17.00		28 Pin	.45
TR1602B TMS6011	200K Baud (-12 + 5) 200K Baud (-12 + 5)	5.95	P8156	256 x 8/22	18.00	Vero DIP 8 x 8 Printed Bo		
IM6402	200K Baud (5 volt)	9.00	8599	16 x 4 (50ns)	1.88	Full Range of Resistors, Capi	acitors, TTL, CMOS, 1	Fransistors
IM6402	200K Baud (5 volt)	9.00	9102BPC	1K x 1 (250ns)	1.65	Vero products available		

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# WHEEL OF FORTUNE

ETI's project team is in a real spin this month with their Wheel of Fortune game.

ONE ARMED BANDITS with no arms, Pinball tables with an MPU at their centre - the world of electronics has a lot to answer for. Is nothing sacred?

The answer to that last question as far as we at ETI are concerned is not a lot. We've taken the liberty of implementing that traditional fairground attraction, the Wheel Of Fortune in our own electronic fashion. The game usually features a large wooden wheel and ratchet arrangement, the stall either accepting bets on which of the ten numbers will be under the pointer when the wheel stops, or, perhaps, suggesting that a message under the pointer will give an indication of what the future holds in store for you you will meet a tall dark stranger, you will marry young and have 2.4 mortgages, etc.

#### **Will O Fortune**

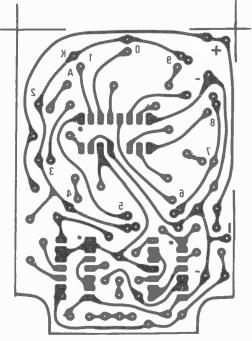
Our game accurately apes the real thing, the circle of LEDs simulating the spin of the Wheel getting under way as a pair of touch contacts are crossed with you palm (or more likely finger). The movement of the LEDs will then slow down to, it seems, an excrutiatingly slow speed until it finally stops. All this visual activity is at the same time accompanied by a clicking sound that simulates the ratchet sound of the real game.

#### **Wheel Meet Again**

It's easy to become a trifle blase about electrical games, particularly in the face of the never ending stream of things that we see in the shops at present, but even the most hardened people, and we've got some fairly hardened people here at ETI, found ▶



Fig. 1. (left) shows the overlay for the Wheel of Fortune game while Fig. 2. (right) is the full size foil pattern of the game's PCB.



## PARTS LIST-

I		-/ 414 8	J LIJ I	
l	RESISTORS (all 1/4)	N 10%)	SEMICONDUCTOR	RS
ı	R1	2M2	IC1,2	4011B
İ	R2	1M0	IC3	4017B
I	R3	100k	D1,2	1N914
ł	R4	470k	LED1-10	TIL209
J	R5	4M7		
I	R6	1 Ok	MISCELLANEOUS	
I	R7	330R		piece, drawing pins,
ı	CARACITORS		vero box, PCB as p	attern
ı	CAPACITORS	100 10 1/1		
ı	C1	100u 10 V tantalum		
1	C2	1u0 10 V tantalum		
	C3	22u 10 V tantalum		
	C4	100n polyester		
	C5	1n0 polyester		

### **BUYLINES**

None of the components used in the Wheel of Fortune game should prove hard to find as most will be stock items in many component shops. Make sure that the tantalum capacitors specified for C1, 2 and 3 are used as the circuit makes use of the low leakage characteristics of these components.

the Wheel of Fortune to be fun. If you start thinking about building it now it might just get finished for Christmas.

#### Construction

Start by mounting all the components on the PCB with the exception of the LEDs. Pay attention to the orientation of the polarity sensitive devices and, for choice, mount the ICs in holders. In order to

squeeze everything into the small box we used, the PCB tracks have been made quite fine so be careful when soldering that no excessive amounts of heat are applied to any sections of the board.

As can be seen from the internal photograph of the game, the back of the crystal earpiece is removed before mounting the device in the case. This is to ensure adequate room between the IC and earpiece.

The touch contacts formed by two drawing pins are glued to the front panel. When the case has been prepared place, but do not solder the LEDs, into the PCB and offer them up to the case. Solder one lead of each LED. At this stage make sure that all the devices are properly seated, then solder the second lead.

That just about completes the construction, just connect up to a battery and place your bets.

## **PROJECT: Wheel of Fortune**

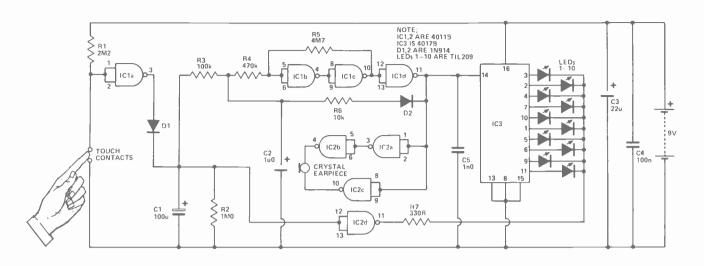


Fig.3. Full circuit diagram of the Wheel of Fortune game.

## HOW IT WORKS

THE Wheel of Fortune circuit can be broken down into a number of distinct sections; the display circuitry, an audio stage, a VCO, and a touch sensitive/monostable configuration.

In the "off" state R1 holds the input of IC1a high and hence the output of this gate, wired as an inverter, is low and C1 is discharged. Bridging the touch contacts causes the gate's output to go high and C1 to be charged up via D1. When the finger is removed from the touch contacts and the output of IC1a returns low, C1 is prevented from discharging into this gate as D1 is now reverse biased, instead C1 discharges slowly via R2. The VCO is formed by the components

The VCO is formed by the components associated with IC1b, c and d. The circuit in fact generates a series of constant duration negative going pulses separated by "spaces" whose duration can be varied by the control voltage

When the control voltage (the voltage on

C1) is below a threshold level that is equal to half supply voltage the circuit will not oscillate. If we now assume that the voltage on C1 rises to supply, as would be the case when the touch contacts are bridged, C2 will start to charge up. The voltage avross C2 is applied, via R4, to the schmitt trigger formed by IC1a and b. As the voltage applied to the schmitt crosses its upper switching threshold the output of IC1d, which inverts and buffers the schmitt's output, will go low. This will cause C2 to be rapidly discharged via the relatively low impedance path offered by R6 and D2. As the voltage on C2 crosses the lower threshold of the schmitt the output of IC1d returns high and C2 once more begins to charge. The time taken for the voltage on C2 to reach the schmitt's trigger point is dependent on the voltage across C1. Thus when the voltage on C1 is large, C2 quickly reaches the trigger point and the VCO pro-

duces a high frequency, this requency reducing as the voltage of C1 falls.

The output from the VCO is fed both to IC3

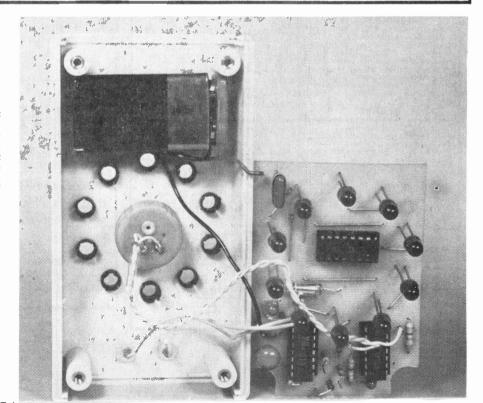
The output from the VCO is fed both to IC3 to drive the ring of LEDs and to IC2a, b and c to produce the audio output.

The crystal earpiece that provides the "clicking" is driven from a bridge circuit. This effectively doubles the voltage applied to the transducer and hence, from  $P = V^2/R$ , doubles the audio output.

The LEDs driven by IC3 have their cathodes connected via R7, to the output of IC2d. The output of this gate will normally be high, going low when the voltage on C1 is above half supply. As IC3 outputs are active high the display is thus enabled for a period of time that is slightly longer than the duration of the VCO's oscillation.

C3 and C4 are included to decouple the supply while C5 is needed to prevent any RF interference affecting the circuit's operation.

Photograph of the game's inards. Note that the back of the crystal earpiece has been removed to ensure sufficient clearance between it the IC directly below when the box is assembled. The drawing pins that form the game's touch contacts are glued to the front panel with an epoxy adeshive, the tips of the pins can be seen at the bottom of the picture.



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- 2: Coffee stains (instant).
- 3: A useful-sized bit of stiff paper to stop the window from rattling.
- 4: Rough calculations for your new combined egg timer/laser cannon project.
- 5: ETI makes a fair soldering iron stand.
- 6: The dog insisted on carrying your copy to you along with your slippers.

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#### **157160 STANDARD TIMER AND CONTROLLER (STAC)**

NATIONAL

The standard timer and controller chip is a preprogrammed member of National's Controller Oriented Processor (COP) family. The device is designed for use in repetitive timing application where 1 to 4 outputs are to operate at 4 user-programmed times. Minimal external hardware is needed for complete system implementation due to direct display drive capability and a key-switch interrogation feature. Strap selection for 50/60 Hz input and 7-day/8-day mode has been included for added versatility.

#### Initialization

Power for the device is a single power supply of 7V9 to 9V5. Proper initialization will occur internally if the supply rise time is between 11 µs and 1 ms. If the supply rise time to final value exceeds 1 ms, an external RC network with a time constant in excess of the supply turn-on time should be placed on the Power On Reset (POR) pin. This delays initialization until control the power supply voltage is within specifications. Initialised conditions are. (a) time (realtime clock) at 00:00, (b) all set point times to 00 00 and all outputs off, (c) all days valid, (d) present day counter to day 1, and, (e) real-time clock mode.

Setting the time is performed in the normal real-time clock mode by depressing the SET HOURS (10) or SET MINUTES (9) keys. Each depression will cause an increment of the hours from 0-23 or minutes from 0-59, respectively, holding the appropriate key depressed will cause the numbers to roll (slew) at a 4/second rate. Normal operation is to slew the value close to the desired setting and then "bump" it to the final value

#### OPTION SELECTION

Strap switches can be used to implement key functions. Figure 1 illustrates "strapping" of keyswitch functions 1-5

#### **Programming**

For proper operation, the system must have 1 or more of its set point times loaded. To load (or program) set points, the DATA ENTRY key (5) must be depressed momentarily to take the system from the normal real-time clock mode to the data entry mode. Upon activation, 1 of the set point times will be displayed and its output status will be shown on the decimal points of the display. After power-up, this will be 00.00 and the decimal points will be off. To examine or go to another set point, the ADVANCE SET POINT key (6) is depressed in the data entry mode for each new time. The 4 values are held in a revovling stack (similar to a calculating stack) and each advance causes it to roll 1 position. Four advances returns to the original position.

To activate a set point, the hours and minutes will be loaded with the same SET HOURS (10) and SET MINUTES (9) keys used in setting the real-time clock. In addition the SET STATUS (8) key is activated and is used to load the output(s) to be activated at the programmed time. Depression of the SET STATUS key causes the 1st decimal point to turn on (which will correspond to output 1 turning on at run time). If this output is the only one to be used at this programmed time, one can go to the next set point by using the ADVANCE SET POINT key. If, however, the

#### **Features**

- 24-hour real-time clock with 4-digit display
- 60 Hz (50 Hz option) timing derived from the power line
- 4 Control outputs at each set point time
- 4 set point times may be programmed with repeat every 24 hours
- Valid day programming to "skip" certain davs
- Manual mode to verify programming
- Transducer input to force to a preset condi-
- Time of day reset to ease time setting or to allow use as a sequence timer
  High speed ''demonstration'' mode for
- verification of capability t
- Single 9V power supply

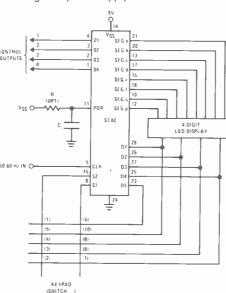


FIGURE 1. Typical STAC Connection

desired output is to be either output 2, 3 or 4, the set status key should be pressed again to advance to number 2, 3 or 4. Each advance turns off the previous decimal point.

If a combination of outputs is designed (such as numbers 2 and 4), the HOLD STATUS key (2) is used to hold the number 2 decimal point on before the SET STATUS key advances through 3 to number 4. With the use of the HOLD STATUS key and the SET STATUS key, any combination of the 4 outputs can be programmed at each set point. If an error in programming occurs, using the SET STATUS key from position 4 will clear all data (including that set by the HOLD STATUS) and the proper information may be re-entered by following the proper sequence.

If conditions permit, the programming can be verified on the actual outputs by using the MANUAL key (1). This key, when depressed in the data entry mode, transfers the decimal point set-status data to the output latches; thus, the motor, solenoid, valve, or whatever is being controlled will be activated. When all 4 times and their respective output conditions have been programmed, the system is returned to the real-time clock mode by another depression of the DATA ENTRY key. If the valid day information is not used, the system is ready to operate

#### Dual-In-Line Package

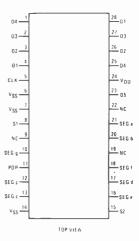


FIGURE 2: Pinouts

Depression of the DAY MODE key (7) enables setting and display of the current and valid day information. The current day is displayed in the left-ost digit of the display and the validity of the day in the right-most digit with a "1" for a valid day, and "0" for an invalid "off" day. As the clock steps through the week, the programmed conditions occur on all valid days and do not occur on invalid days. The SET DAY key (10), when depressed in the day mode, advances to the next day upon each depression. The SET STATUS key (8), in the day mode, is used to change the validity information. Another depression of the DAY MODE key will return the system to the realtime clock mode.

Closure of the HOLD STATUS/DEMO key (2) will provide a means to rapidly cycle through the programmed sequence or set up an 'in store' display. With this key closed in the real-time clock mode, time is advanced at the rate of 1 hour per second; thus, a 24-hour day requires 24 seconds to verify and a 7-day week requires less than 3 minutes.

Closing key 6 during the real-time clock mode (either normal or demo operation) will reset the clock time to zero without changing the set point timing but will reset the valid day information.

#### **External Inputs**

The MANUAL / REMOTE TRANSDUCER key (1), when depressed in the real-time clock mode, will override any time-related programming and immediately force output 1 on and 2 through 4 off. This condition will remain until the next valid set point occurs.

#### **Using It**

A table of key functions and an example program are given on the next page, the permutations are endless!

Further details available from National Semiconductor Ltd., 19 Goldington Road, Bedford, MK4 03LF.

#### MM57160 STANDARD TIMER AND CONTROLLER (STAC)

NATIONAL

KEY	KEY		FUNCTION	de la company
NO.	SWITCH NAME	REAL-TIME CLOCK MODE	DATA ENTRY MODE	DAY MODE
1	MANUAL/ REMOTE TRANSDUCER	Remote transducer input; forces output 1 ON, outputs 2–4 OFF until next valid set point after switch is off	Manual verification mode; allows data to be transferred to outputs 1–4	(None)
2	HOLD STATUS/ DEMO	Allows rapid demonstration of sequence by advancing clock at rate of 1 hr/sec	Holds output N ON while programming advances to output N+1, N = 1-4	(None)
3	8 DAY	Specifies 8-day cycle in lieu of 7-day	Specifies 8-day cycle in lieu of 7-day	Specifies 8-day cycle in lieu of 7-day
4,	50 Hz	Specifies 50 Hz line frequency input	Specifies 50 Hz line frequency input	Specifies 50 Hz line frequency input
5	DATA ENTRY	Places unit in the data entry mode	Returns unit to the real-time clock mode	(None)
6	ADVANCE SET- POINT/ RESET TIME	Rasets time of day to 00:00 without changing set points but resets all days to valid	Advances display to the next set point so that it may be verified or altered	(None)
7	DAY MODE	Places unit in the day mode	(None)	Returns unit to the real- time clock
8	SET STATUS	(None)	Controls programming of outputs; resets output N to "0" (unless preceded by HOLD key) and advances to output N+1	Alternate action key; changes day from valid ("1") to invalid ("0") and vice-versa
9	SET MINUTES	Advances minutes display of real-time clock	Advances minutes display of selected set point	(None)
10	SET HOURS/ SET DAY	Advances hours display of real-time clock	Advances hours display of selected set point	Advances display to next day—must be set to current day before returning to real-time clock mode

#### **Programming Example**

- 1. Output 1 should turn on at 2:00 a.m., and turn off at 4:00 a.m. each valid day.
- 2. Output 2 should turn off at 2.05 a.m. and turn back on at 4:00 a.m. each valid day
- 3. Output 3 should turn on at 2:00 a.m. and turn off at 2:05 a.m. each valid day.
- 4. Output 4 should turn off at 3:01 a.m. and turn on at 4:00 a.m. each valid day.
- Monday through Friday are valid days Saturday and Sunday are invalid.
- 6. It is now Monday, the time is 1:00 a.m.

Given these conditions, it is now advisable to construct an "output truth table":

TIME/OUTPUT	01	02	03	04 .
2:00 AM	ON	ON	ON	ON
2.05 AM	ON	OFF	OFF	ON
3:01 AM	ON	OFF	OFF	OFF
4:00 AM	OFF	ON	OFF	ON
3:01 AM	ON	OFF	OFF	OF

The following key sequence may be used to load the preceding program into the  ${\sf STAC}$  memory.

KEY DEPRESSED	DISPLAY	NOTES
	0000	Initial display
Data Entry	0000	
Set Hours	0100	
Set Hours	0200	
Set Status	0 200	Set point 1 at 2.00
		a m., output 1 ON

Key Depressed	Display	Notes
Hold Status	0 200	Hold output 1 ON
Set Status	0 2 00	Output 2 ON
Hold Status	0 2 00	Hold output 2 ON
Set Status	0200	Output 2 ON out
		put 3 ON
Hold Status	0200	Hold output 3 ON
Set Status	0.2.0.0.	Output 4 ON
Advance Set Point	0000	
Set Hours	0100	
Set Hours	0200	
Set Minutes	0201	
Set Minutes	0202	
Set Minutes	0203	
Set Minutes	0204	
Set Minutes	0205	
Set Status	0 205	Set point 2 at 2:05
		a.m.; output 1 ON
Hold Status	0.205	Hold output 1 ON
Set Status	0.2 05	Output 2 ON
Set Status	0.20.5	Output 2 OFF, out
		put 3 ON
Set Status	0.205.	Output 3 OFF, out-
		put 4 ON
Advance Set Point	0000	
Set Hours	0100	
Set Hours	0200	
Set Hours	0300	
Set Minutes	0301	
Set Status	0.301	Set point 3 at 3.01
		am, output 1 ON
Advance Set Point	0000	
Set Hours	0100	
Set Hours	0200	
Set Hours	0300	
Set Hours	0400	

Key Depressed	Display	Notes
Set Status	0.400	Set point 4 at 4.00
		a.m., output 1 ON
Set Status	04 00	Output 1 OFF, out
		put 2 ON
Hold Status	04 00	Hold output 2 ON
Set Status	04 0 0	Output 2 ON, out
Set Status	04 00	put 3 OFF
Set Status	04 00	Output 3 OFF out
Data Entry	0000	put 4 ON Present time
Data Entry Day Mode	1 1	Day 1 valid
Set Day	2 1	Day 1 valid
Set Day	3 1	Day 2, valid
Set Day	4 1	Day 3, valid
Set Day	5 1	Day 4, valid Day 5, valid
Set Day	6 1	Day 5, valid
Set Day Set Status	6 0	Day 6, valid
Set Day	7 1	Day 6, invalid
Set Status	7 0	Day 7 valid Day 7, invalid
Set Day	1 1	Return to current
Jet Day	' '	day
Demo	(Running)	Run thru at least one
1	(Transing)	24 hour cycle inter-
		mittently (use Hour
		& Minute keys to
		"nudge" display to
		set points) to verify
		output settings. After
		passing set point just
		prior to present time,
		release Demo key
Set Hours	0100	Present time

Programming of the STAC is now complete. The program will continue in 24-hour, 7-day cycle until manually altered.

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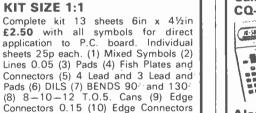
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# STAC TIMER

The odds were STACed against ETI's projects team this month, but once again they've come through with the goods

THE NAME OF this project is derived from that given by the manufacturer to the IC around which it is built. STAC stands for Standard Timer And Controller and the device is part of National's COPS (Calculator Orientated Processor System) group, a series of, what are in effect, dedicated microprocessors.

The STAC provides a 24-hour clock with four digit display, much as any clock IC, but has four control outputs which may be programmed to turn on, turn off, or to retain their current status at any one of four preset times during the day. STAC also has the facility to "skip" certain selected days within its seven or eight day (selectable) cycle.

The IC is thus a perfect basis for many control applications from central heating installations to fish tanks and hi-fi systems. We will not give details of the interfaces between STAC and the outside world, as with so many potential uses, the circuitry will have to be selected with the particular environment in which you wish to use your STAC in mind.

#### A STAC In Time

Setting up the STAC is quite straightforward and is rather like using one of the programmable calculators with which many of us are familiar.

At switch on the STAC is initialised to a state where the clock is at 00 00, all set points are zero and outputs off, all days are valid with the present day set to one. The display will show the clock output.

Setting up the clock follows the usual procedure adopted with any digital clock. Pressing the SET HOURS or SET MINUTES will advance the appropriate digits at a rate of four per second.

The next task is to enter the four set points, the times at which the outputs will change and the exact manner in which they will change. To program the STAC it must be taken out of the clock mode and put into the data entry mode by pressing the DATA ENTRY key.

At this stage one of the set point times will be displayed. These values are held in a revolving stack and to examine the next the ADVANCE SET POINT key is pressed, after four "advances" the original value is displayed.

Any one of the set point time is set up with the SET HOURS and SET MINUTES keys as with the clock. The conditions that the outputs adopt at the set point are set up with the SET STATUS and HOLD STATUS keys.

Indication of the condition of the four outputs is provided by the decimal points of the display, if the decimal point is on the corresponding output is on the left-hand point represents output one. At power up all decimal points, thus outputs, are off.

Operation of the SET STATUS key will cause the first decimal point to turn on (output one on at run time). Each subsequent operation will cause



the current output to turn off and the next to turn on.

If a combination of outputs is required the HOLD STATUS key may be used to hold the current decimal point on when moving to the next. To do this the key must be operated before SET STATUS is used to advance to the next.

#### **Status Symbol**

Operating the manual key while in the data entry mode will cause the decimal point status information to be transferred to the outputs for

verification.

When all programing is complete, STAC may be returned to the clock mode by a second operation of the DATA ENTRY key.

While in the data entry mode, the valid days may be set up. The DAY MODE key will cause the current day to be displayed (as a number from one to seven) in the left-most digit of the display. The current status "1" for valid, "O" for invalid, will be

displayed in the right-hand digit.

SET DAY will advance to the next day while SET STATUS will change the validity.

DAY MODE will return the system to the clock display.

If the HOLD STATUS is operated in the clock mode, time is advanced at a rate of one hour per second, this enables program information to be checked.

The ADVANCE SET POINT KEY if

used in the clock mode will reset the clock but leave set point times unaltered although the day information will be reset.

#### **Needle In A STAC**

When programming the STAC it is best to draw up a table of set point times and the state of outputs at each of these as an aid to entering the data in a logical fashion.

An example of programming STAC is shown in the ETI data sheet elsewhere in this issue.

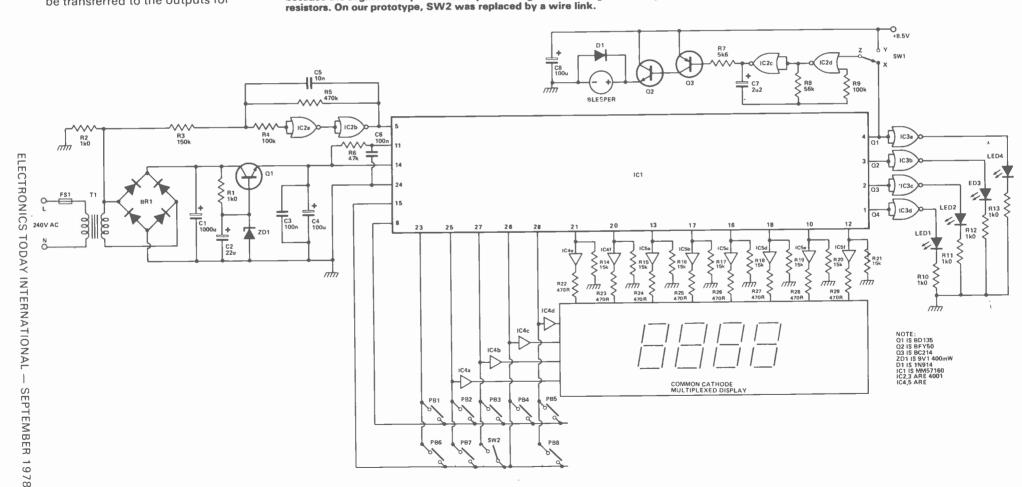
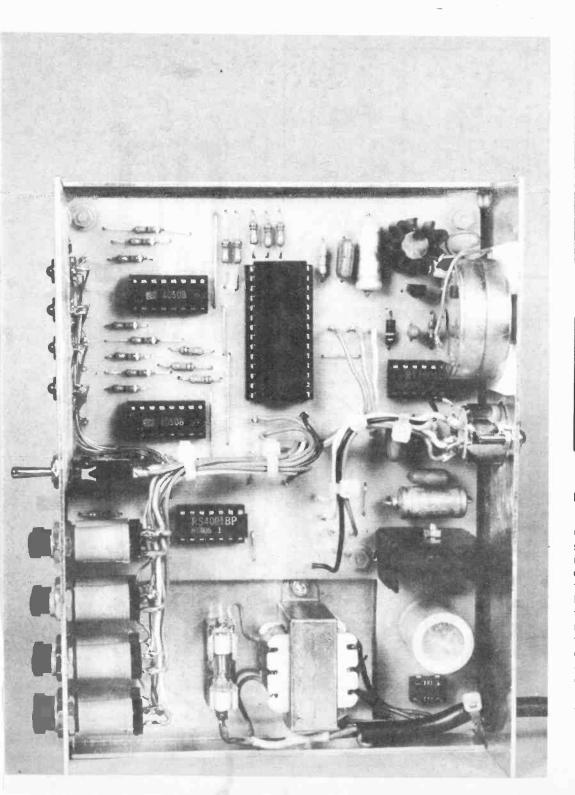


Fig. 1. Full circuit diagram of the STAC timer. Resistors R 14-21 are necessary

because the segment outputs will not provide logic level swings without pull down



## HOW IT WORKS

The power supply for the STAC timer is that R14-21 are required to pull down the regulated by Al after having been smoothed and rectified by C1 and BR1 respectively.

C6 and R6 ensure that the rise time of the voltage on pin 11 is such that proper initia-

tion of the timer takes place.

The unsmoothed output of the transformer is taken to the shaping circuit provided by IC2a and IC2b together with associated components. This acts as both a schmitt, to clean up the wave form, and a monostablee, to ensure that any transients on the mains are not counted by the timer's input circuits.

The operation of the STAC IC is described in the main text, the programming switches referred to being PB1-8 and SW2. The display is driven via the buffers in ICs 4 and 5. Note

segment outputs of the STAC in order to provide a suitable display drive signal.

The outputs of the STAC are active low and drive LEDs 1-4 via the buffer invertors in IC3 to provide an indication that a particular output is 'on'. The invertors ensure that a LED is lit when output is active.

Output 1 can be applied, via SW1, to the astable formed by IC2 c and d. When the output goes low it enables the oscillator which drives the buzzer via Q2 and Q3. The buzzer produces an audible tone when a DC voltage is applied to it. D1 prevents anh back EMF generated by the buzzer causing damage to O2 or O3.

## **BUYLINES**

The STAC timer will be available from Electronics in Edgeware Road for National Semiconductor suppliers and the rest of the components should be generally available.

In case of difficulty a suitable display can be obtained from Audio

£1.25. They can also supply a suitable buzzer at 25p.

The case can be obtained from Marshall's and Watford, although there are a lot of similar cases around in most loca shops.

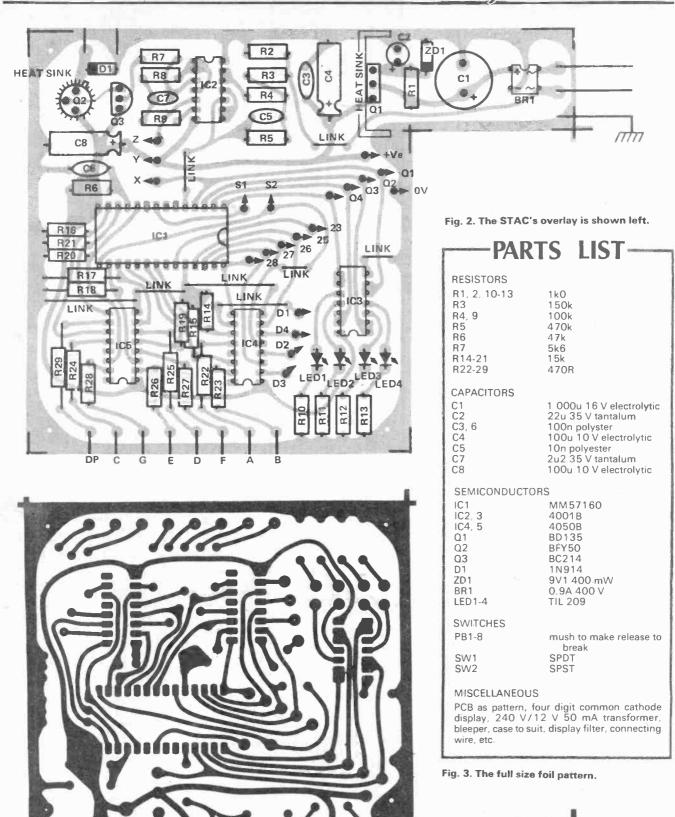
#### Construction

Construction of the STAC timer should not pose any special problems if the overlay shown is followed carefully. Bear in mind, though, that the power supply, due to the size limitation placed upon the transformer by the case used, is run near its maximum rating. This means that the buzzer, which increases the current drawn by the unit from the 45mA with the buzzer inactive but display and LEDs on to 90mA with buzzer active, should only be run for a maximum of about half an hour.

It also means that although the power supply connections are brought out they should only be used, at most, to power an interface circuit that does not draw excessive current from the main unit.

The STAC's outputs are capable of sinking 20mA and if they are to be used to control any devices that require more drive than this, these limitations should be borne in mind and suitable drive circuitry provided.

By the way, if you happen to come up with some ingenious application for your completed STAC timer. perhaps you would let us here at ETI know about them.



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TRA	PIZ	TORS		2 <b>N339</b> 3	0.17	2N4037	0.60	2N5192	0.80	2N6124	0.45	BC198A	0.16	BC1788	0.35	8C213C	0.15	BC337	0.20	8D240A	0.49	8F160	0.33	BFR79	0.30	ME4001	0.16	TIP30C	0.70
			0.35		0.17			2N5193	0.75	2N6125	0.47	BC1D8B	0.16	BC179	0.25	BC213L	0.17	BC338	0.23	BD240C	0.59	RF161	0.65	BERRE	0.30	ME4002	0.16	TIP31A	0.54
2N696	0.39	2N2218	0.33	2N3394		2N4059			0.75	40361	0.55	BCIOSC	0.17	BC179A	0.25	BC213LA	0.17	BC547	0.13	B0241A	0.49	BF 167	0.37	8FR81	0.38	ME4003	0.16	TIP31C	0.72
2N697	0.31	2N2218A		2N 3395	D.19					40361	0.55	BC109	0.17	BC1798	0.25	8C213LB	0.17	8C547A	0.13	BD241C	0.65	BF 173	0.37	BFX29	0.34	ME41D1	0.11	TIP32A	0.59
2N698	0.49	2N2219	0.38	2N3396	0.19	2N4060			0.97			BC1098	0.17	BC1790	0.26	BC213LC	0.17	BC547B	0.13	8D242A	0.55	BF177	0.27	BFX30	0.34	ME4102	0.11	TIP32C	0.82
2M699	0.58	2N2219A	0.39	2N3397	0.19				0.37	40363	1.45		0.17	BC182	0.20	BCZ13LC	0.17	BC548	0.13	BD242C	0.62	BF178	0.27	BFX84	0.30	ME4183	0.11	TIP4 I A	0.76
2N706	0.30	2N2220	0.39	2N3438	0.85	2N4062		2N5246		40408	0.82	BC109C			0.12	8C214B	0.17	BC549	0.14	BD243A	0.65	BF179	0.33	8F x 85	0.38	ME41D4	0.11	TIP41C	0.97
2N7D6A	0.30	2NZ2Z1	0.25	2N3440	0.75	2N4064			0.44	40409	0.82	BC140	0.30	8C182A	0.12	BC214C	0.17	BC5498	0.14	80243C	0.67	8F180	0.37	BFX86	0.30	ME6101	0.22	TIP42A	0.86
2m708	0.30	2N2221A	0.25	2 N 3 4 4 1	0.92	214074			0.44	40410	0.82	BC141	0.32	BC1828			0.17	8C549C	0.15	BD244A	0.70	BF 181	0.37	8FX87	0.35	ME6102	0.22	TIP42C	1.08
2N718	0.30	2N2222	0.25	2N3442	1.45	2N4121			0.44	40411	3.10	BC147	0.13	BC182L	0.15	BC214L BC214LB		BC557	0.14	B0244C	0.87	BF 182	0.37	8FX88	0.30	MJ2955	1.35	TIP2955	0.70
2N718A	0.54	2H2222A	0.25	2N3638	0.17	2N4122			0.44	40594	0.87	BC1478	0.13	BC182LA	0.15		0.18	BC558	0.13	BD245A	0.69	BF 183	0.44	BFX89	1.37	MJE340	0.62	TIP3055	0.59
2N720A	0.85	2N2369	0.27	2N3638A		2N4123		2N5296	0.44	40595	0.98	BC148	0.13	BC182LB	0.15	BC214LC	0.18	BC559	0.15	B0245C	0.65	8F184	0.41	BFY50	0.27	MJE370	0.62	TIS 34	1.05
211722	0.45	2N2369A	0.27	2N3702	0.14	2N4124			0.44	40673	0.80	BC148B	0.13	8C183	0.12	BC237B	D.15	BCY7D	0.13	BD245L	0.72	BF 185	0.37	8FY51	0.27	MJE371	0.86	TIS42	0.50
2N727	0.50	2N2646	0.80		0,14	2N4125		2N5447		40669	1.30	BC148C	0.13	BC183A	0.12	BC238A	0.13		0.26	BD246C	0.93	BF194	0.16	BFY52	0.27	MJE 528	0.50	TIS43	0.47
2N914	0.38	2N2647	1.55	2N37D4		2N4126			0.16	AC126	0.48	BC149	0.15	BC1838	0.13	BC2388	0.13	BCY71	0.28	BB433	0.44	BF 195	0.16	BFY9D	1.35	MJE521	0.70	TIS90	0.22
28916	0.33	2N 2903	1.60	2N3705	0.14				0.20	AC127	0.48	BC1490	0.15	BC1B3C	0.13	BC238C	0.13	BCY72	0.10	8D434	0.46	BF196	0.16	BRIDI	0.55	MJE2955	1.65	TIS91	0.27
2N917	0.38	21/2904	0.31	2N3706	0.14				0.38	AC128	0.48	BC157A	0.15	BC1831	0.15	BC2398	0.16	BØ115	0.55	B0434	0.46	BF197	0.18	88739	0.55	MPE3055	1.05	TIS92	0.33
2N918	0.45	2N2904A	0.31	2N37D7	0.14			2N5458		AC151	0.43	BC158A	0.15	BC183LA	0.15	BC239C	D.17	BD 131 BD 132	0.35	BD436	0.46	BF198	0.19	BSX19	0.35	MPF 102	0.33	TIS93	0.36
2N929	0.37	2N2905	0.31	2N3798	0.12	2N4288		2N5459		AC152	0.54	BC1588	0.15	BC183LB	0.15	BC257A	0.18		0.40	BD437	0.55	BF199	0.19	BSX20	0.35	MPF103	0.44	7TX300	0.17
2N929A	0.37	2N2905A	0.31	2N3709	0.12	2N4289		2N5460		AC153	0.59	BC159A	0.17	BC183LC	0.15	BC258B	0.19	BB 135	0.40	B0436	0.55	BF224J	0.22	BS X21	0.35	MPF104	0.44	ZTX301	0.17
2 N 9 3 D	0.37	2N2906	0.25	2N3771	2.16	2114347			0.37	AC153K	0.59	BC1598	0.17	BC184	0.12	BC2598	0.19	BD136		80529	0.49	BF 225J	0.27	BU104	1.80	MPSA05	0.44		0.27
2N930A	0.95	2N2906A	0.25	2N3772	2.20	2N4348			0.40	AC176K	0.70	BC160	0.38	BC1848	0.13	BC300	0.43	BD137	0.41	B0530	0.55	BF244A	0.38	BU 105	1.55	MPSA05	0.27		0.27
29(1711	0.30	2N 2907	0.25	2N3773	3.15	2N4918		2N5486		AC176	0.54	80161	0.38	8C184C	0.13	BC301	0.43	BD138	0.43	80535	0.70	BF244B	0.33	BU126	1.08	MPSA06	0.27	ZTX 304	0.27
2N1889	0.30	2N2907A	0.25	2N3819	0.36	2N4919			0.64	AC1B7	0.59	BC167	0.13	BC184L	0.15	BC302	0.37	BD139	0.43	80536	0.70	BF245A	0.44	BU204	2.20	MPSA12	0.44	ZTX330	0.22
2M1890	0.30	2N2923	0.17	2N3820	0.39	2N492D		2N5492		AC187K	0.65	BC167B	0.13	BC184LB	0.15	BC303	0.54	80140	1.90	80537	0.74	BF245B	0.44	BU205	2.40	MPSA14	0.33	ZTX500	0.16
2N 1893	0.30	2N2924	0.17	2N3821	0.96	2N4921		2N5494		AC188	0.54	BC168A	0.13	BC184LC		BC307	0.16	8D1B1	2.20		0.77	BF257	0.35	8U206	2.70	MPSA55	0.27	ZTX530	0.25
2N2102	0.50	2N2925	0.19	2N3900	0.28	2N4922		2N5496			0.65	BC1686	0.13	8C212	0.15	BC307A	0.16	BB182	2.35	B0538 B0539	0.60	BF 258	0.35	BU208	2.70	MPS A56	0.27	-	
2N2192	0.58	2NZ926	0:17	2N3901	0.30	2N4923		2N6027		AD161	1.00	BC168C	0.13	BC212A	0.15	BC307B	0.16	BD183	0.95	80548	0.60	BF 259	0.35	MEG401	0.22	R20088	2,45	FUL	1
2N2193	0.50	2N3053	0.25	2N3903	0.20	2N4924		2N6107		AD 162	1,00	BC1698	0.13	BC212B	0.15	BC308	0.16	BD187			1.32	BF 336	0.42	ME0482	0.22	B2010B	2.15	RANG	GE
2N2193A	0.52	2N3854	0.72	2N3904	0.18	ZN5086		2N6108		AF106	0.60	BC169C	0.13	BC212L	0.18	BC3088	0.16	B0235	0.46	BDX14	1.90	BF337	0.49	ME0404	0.17	TIP29A	0.49	IN OL	UR I
2N2194	0.42	2N3055	0.75	2N3905	0.18	2N5087		2N6109		AF109	0.52	BC177	0.22	BC212LA		8C309A	0.16	BD236	0.44	BOX18 BOY20	1.10	BF 338	0.52	MED412	0.22	TVP29C	0.65	NEV	H.
2N2194A	8,45	2N3390	0.50	2N3906	0.18	2N5088		2M6111		BC107	0.16	BC177A	0.22	8C212LB	0.18	BC3098	0.16	B0237	0.44	BDY55	1.90	8F839	0.30	MED414	0.22	TIP3GA	0.54	CATALO	DGUE
2N2195	0.40	2N3391	0.40	2114031	0.55	2N5089		296121			0.16		0.25	BC213	0.15	BC309C	D.16	B0238	0.44	8DY56	2.10	BF240	0.29						
2N2195A	0.40	2N3391A	0.45	2N4032	0.65	2N5190		2N6122			0.16	BC178	0.22	BC213A	0.15	BC327	0.22	BD239A	0.59	BF115	0.39	BFR41	0.30	For	discount	quantity pr	ices con	taci us nov	₩
2N2217	0.55	2N3392	0.17	2N4036	0.72	2N5191	0.75	2H6123	0.48	BC108	0.16	BC178A	0.25	BC213B	0.15	BC328	0.20	B0239C	0.39	01113	0.33	Br#41	0.00						-

LINE	AR C	IRCU	TS	-			
CA3018	0.75	LM379S	4,25	LM7815K	1.75	TBA530	2.3
CA3018A	1.10	LM380N8	0.96	LM7824K	1.75	TBA5300	2.4
CA3020	2.20	LM380N14	1.08	LM78L05CZ	0.30	TBA540	2.6
CA3020A	2.50	LM381AN	2.70	LM78L12CZ	0.30	T8A5400	2.7
CA3D28A	0.90	LM381N	1.69	LM78L15CZ	0.30	TBA550	3.6
CA3D288	1.25	LM3B2N	1.32	MM5314	4.60	TBA5500	3.8
CA3030	1.50	LM384N	1.55	MM5316	4.60	TBA56080	3.0
CA303BA	2.20	LM386N	0.88	ME555	0.33	TBA57D	2.1
CA3038	2.90	LM387N	1,10	NE556	0.85	TBA5700	2.2
CA3038A	4.10	LM388N	1.00	ME558N	1.98	TBA7000	2.3
CA3045	1.55	LM389N	1,00	WE560	4.50	TBA 720A0	2.0

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77 (M702C)
2.45 (M709)
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6.80 (M 1.00 MESSIO ... 0.81 MESSIO ... 0.81 MESSIO ... 0.70 MESSIO ... 0.70 MESSIO ... 0.70 MESSIO ... 0.49 MESSIO ... 0.47 MESSIO ... 0.40 MESSIO .. CA3048
CA3080
CA TBA7500 TBA800 TBA800 TBA820 TBA928 TCA160C TCA160C TCA730 TCA740 TCA750 TCA750 TCA760 TCA105 TCA440 TDA1022 TDA1024 TDA1034 T0A2020i UAA170 UAA180 TL080CP TL081CP TL082CP TL083CN TL084CN LF355N LF355N LF357N LF13201N LF13331M

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СМ	O.S.	4021	1.05	40508	0.85	4077	0.70	
		40228	1.90	4051B	0.85	4078	0.27	
4000	0.22	4023B	0.22	4052B	0.86	40818	0.24	
4001B	0.22	4024B	0.76	4053B	0.98	4082	0.27	
4002	0.22	4025B	0.22	4054	1.48	4085	0.89	
4006	1,25	4027B	0.55	4055	1.65	4086	0,89	
4007	0.22	40288	0.92	4056	1.65	4089B	2.10	
40088	0.99	40290	1.10	4059	6.00	40938	1.00	
4009	0.58	4030	0:84	4060B	1.15	4094	2,30	
4010	0.58	40318	2.25	4063	1.35	4095	1.30	
4611B	0.22	40358	1.30	4066B	0.75	4096	1.30	
4012	0.22	4037	1.20	4067	4.85	4897	4.65	
40138	0.52	404 I B	0.85	4068	0.27	4098	1.00	
4014	1.00	4042B	0.86	40698	0.24	45108	1.20	
4015	1.05	4043	1.05	40706	0.85	4511	1.75	
4016	0.52	4044	1.00	40718	0.24	4516	2.10	
40178	105	4045	1.76	4072	0.27	45188	1.20	
4018B	1.05	40468	1.50	40738	0.24	FULL RA	MI 22M	
4819B	0.52	4047B	0.96	40758	0.24	OUR		
4020B	1.15	4049	0.96	40768	0.99	CATAL		

#### TTL & CMOS

	I I L OI	CIA	103					
	74LS00N	0.26	74LS168N	2.43	7404N	0.17	7496N	0.70
	74LSD1N	0.26	74LS 169N	2.43	7405N	0.22	7497N	1.95
	74LS02N	0.26	74LS 174M	1.33	7406N	0.56	74100N	1.40
	74LS03N	0.26	74LS175M	1.26	7407N	0.55	74107N	0.35
	74LS04N	0.29	74LS181N	3.95	7408N	0.22	74118N	0.95
	74LS 10N	0.26	74LS189N	3.74	7409N	0.22	74119N	1.40
	74LS 12N	0.26	74L\$190H	1.00	7410W	0.20	74121N	0.28
	74LS 13N	0.58	74LS191N	1.00	7411N	0.26	74122N	0.55
	74LS14N	1.43	74LS192N	1.98	7412N	6.20	74123N	0.55
	74LS20H	0.26	74LS193N	1,98	7413N	0.36	74125N	0.45
	74LS26N	0.39	74LS196N	1.28	7414N	0.80	74141N	0.86
	74LS27N	0.50	74C00N	0.24	7416H	0.36	74148M	1.35
	74LS28N	0.42	74C02N	0.24	7417H	0.36	74145N	0.86
	74LS30N	0.26	74C04N	0.24	7420N	0.22	74150N	1.20
	74LS37N	0.32	74C08N	0.24	7423N	0.32	74151N	0.76
	74LS38N	0.32	74C1DN	0.24	7425N	0.32	74153N	0.76
	74LS40N	0.32	74C14N	1.41	7427N	0.32	74154N	1,20
	74LS42N	1.07	74C20N	0.24	7430N	0.22	74155N	0.70
	74LS4ZN	1.09	74C30N	0.24	7432N	0.30	74157N	0.78
	74LS48N	1.09	74C32N	0.24	7437N	0.35	74150AN	1.10
	74LS49N	1.09	74C42N	0.92	7438N	0.32	74161AN	1.10
		0.26	74C48N	1.38	7440N	0.20	74162AN	1.10
	74LS51N 74LS54N	0.26	74C73N	0.54	7441AN	0.84	74163AN	1.10
		0.42	74C74N	0.56	7442N	0.76	74164N	1.36
,	74LS73N 74LS76N	0.42	74C76N	0.54	7445N	1.40	74165N	1.36
	74LS78N	0.42	74C83N	1.30	7446AN	0.90	74167N	2.50
	74LS83AN	1.20	74C85N	1.30	7447AN	0.80	74174N	1.60
	74LS85N	1.10	74C86N	0.64	7448N	0.80	74175M	1.00
			74C89N	4 39	7450N	0.22	74176N	0.90
	74LS90N	1.10	74C90N	0.85	7451H	0,22	74177N	0.90
	74LS91N	1.20 0.86	74C93N	0,85	7453N	0.22	74180N	1.00
	74LS92N		74093N	1.04	7454N	0.22	74181N	2.00
	74LS93N	1.10	74C93N	1.22	7460N	0.22	74182N	0.80
	74LS95AN 74LS96N	1.35	74C158N	4.14	7470N	0.46	74184N	1.50
	74LS107N	0.42	74C151N	2.47	7472N	0.30	74185N	1.50
	74LS107N	0.42	74C154N	3.68	7473N	0.44	74188N	3.25
		0.80	74C157N	2.21	7474N	0.32	74189W	2.60
	74LS122N 74LS123N	0.83	74C160N	1.11	7475N	0.60	74190N	1.40
	74LS124N	2.70	74C161N	1.11	7476N	0.45	74191N	1.20
		0.50	74C162N	1.11	7480N	0.60	74192N	1,20
ı	74LS 125N 74LS 126N	0.50	74C163N	1.11	7481N	1.00	74193N	1.20
l		0.44	74E164N	1.04	7482N	0.90	74196N	1.20
ı	74LS136N		74C193N	1.11	7483N	1.05	74197N	1.00
l	74LS156N	1.20	74C195N	1.04	7484H	1,20	74198N	2,00
ı	74LS158N	0.65	7400H	0.17	7492N	0.45	74199N	2.00
l	74LS150H	1,43	7401N	0.17	7493N	0.45		
l	74LS161N	0.85	74B2N	0.17	749416	0.90		
ı	74LS162N	1.43	7403N	0.17	7495N	0.76		
п	7 AL 2   F.A.In	1.43	( TOUR	9,11	4 4 2 4 M	9,70		

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# audiophile....

With the vast numbers of amplifiers available today, choosing the one most suitable for your own particular requirements can be a daunting task. Ron Harris explains the steps you can take to make it as easy on the tranquilisers as possible.

AMPLIFIERS are perhaps the most extensively specified hi-fi unit, and whereas this could be a good thing if all the manufacturers agreed which specifications to quote, (and how to quote them) there seems an ever increasing divergence of opinion and technique.

This of course provides the hardened enthusiast with hours of harmless amusement meandering along the twisted webs spun across the ad pages. Great fun to figure out whether the Xplam 500 with its 2.0 MW (UHF) really is more powerful than the Tinne Special at a mere 800.17 W (RMS at 100.3 Hz). Isn't it?

When attempting to select yourself an amplifier, either as a first system or an upward move, there are a few things you can remember to make life easier for yourself.

#### Watt to do first

Before anything else you need to decide how much power you're going to need. This really depends on how big your listening room is, and how efficient the chosen loudspeakers are at turning electrical power into sound energy.

So, strange as it may seem, the first step in amplifier selection is made with a tape measure — find out the size of the room in which the amp has to work. Ignore protesting females and displaced cats during this operation.

Once you know the volume of the room, a good estimation of how many watts are wanted can be gained by allowing 25 W, good old fashioned RMS watts — but we'll return to that later, for the first 1000 cu. ft. and then 10 W per thousand cubic feet thereafter. For example if your living room is 10ft x 20ft x 8ft = 1600 cu. ft. you need 35 W a channel MINIMUM.

This assumes loudspeakers of average efficiency, always a dangerous thing to do I know, but unless you're using horn-loaded units — in which case you'll have far too much power, or transmission lines — for which add 15 W to the estimate, this will generally be O.K. Efficiency varies from manufacturer to manufacturer, with the extremes being represented by the Wharfedale E' series at the high end, down to the KEF 104 and IMFs at the other.

#### A power of good

Let's go back for a minute to the question of how that power rating should be quoted. Perhaps the most meaningful figure is the half-power bandwidth. This tells you the frequency range over which the amp will deliver at least HALF its rated power into a given load.

This is of more use than even an RMS figure, as these are usually quoted at 1 kHz only. For example consider these two units

AMP A 50 W RMS 1kHz Power Bandwidth 40Hz-10kHz into 8 ohms.

AMP B 40 W RMS, from 20Hz to 20kHz into 8 ohms.

Differently specified, and at first glance Amp A is more powerful. But this is not useful extra power at all. At 40 Hz the unit is only capable of delivering 25 W into the load, and above 10kHz the power is similarly restricted. Amp B, however, can produce 40 W at both these frequencies, and would thus handle extreme bass and treble much more confidently.

Amp B is thus more honestly specified. Look for the range of frequencies over which power is available, and remember the audio spectrum is approx 20Hz to 20kHz.

#### Ample funds?

Having worked out how much power you need, you can scan the ads within your price range to find out which units are suitable. If you're at all serious about hi-fi don't scrimp on the output to save pennies. Nothing sounds worse than a 10 W amplifier trying to pretend it's a 50 W and fooling no-one. Reserve power is a necessity, not a luxury.

Most systems incorporate at least two sound sources; tape, records, radio, etc, and so the next stage is to decide what peripherals the unit has to control. Do you need filters? Tone controls? Three tape deck inputs? Two



Taking the first step to choosing an amplifier can be fraught with unexpected perils . . . . . . .

record decks? There is a great variety of available combinations to choose from, and only you know which facilities you really need.

However, remember that the more stages you force the signal to travel through on its path to the speaker cones, the more it gets modified. For best reproduction keep things as simple as possible, filters and tone controls in particular should be avoided if possible, as a high quality source will need only minimal equalisation. If your room is particularly bad acoustically, buy a graphic equaliser and do the job properly.

By now the number of models to choose from will have fallen quite considerably, and it's possible that a shortlist can be compiled. (It's probably wise to let the feminine member of staff have a look over the prospective additions to the family — just to make sure that the Pioneer you've set your heart on doesn't clash with the frame around Aunt Nellie's picture, and gets heaved out the first week.)

Specifake shunned?

Every company produces masses of literature on their produce, all loaded with loaded figures. I haven't forgotten these specs at all, I just don't regard most of them as particularly useful in selecting hi-fi. Once you've got your shortlist make the final choice on grounds of compatibility with other equipment — and how the amplifier SOUNDS.

Try to hear your choices in a direct comparison against each other if possible, and even more importantly through the type of speakers you will be playing it with at home. All amplifiers are load sensitive to some degree, and the resulting sound can be changed dramatically by this fact.

Different speakers will present a different electrical load to an amplifier's output stages, which will mean that when you take it home the result may be totally different to that produced in the showroom. Most dealers carry a good range of speakers, and some are willing to arrange home demonstrations.

The main trap to avoid though is to start comparing numbers studiously, and conclude that an amp with 0.04% THD will sound better than another with 0.1%. It might do — but it's just as likely to sound worse. Leave the numbers alone, and give your ears a chance.

#### In summary then

- 1 Decide how much power is necessary in your room.
- 2 Set your price limit.
- 3 Decide what facilities you need.
- 4 Draw up a shortlist.
- Make the final selection by listening and comparing models through the same speakers — which should also be the type you use at home.

ETI

7400 TT 7400 7401 7402 7402 7403 7404 7405 7406 7407 7408 74508 74509 7410 7411 7411	71. 7497 1.12 74100 1.12 74104 1.12 74105 1.12 74107 1.13 74109 1.13 74110 1.14 74110 1.14 74110 1.14 74110 1.14 74110 1.14 74110 1.14 74120 1.15 74121 1.18 74122 2.11 74123	.46 742 .70 742 1.80 742 .82 74 1.30 74 .82 74	197 .90 198 1.48 199 1.48 221 1.50 273 2.15 279 1.25	74LS153 .58 74LS154 1.28 74LS155 .80 74LS155 .80 74LS156 .80 74LS156 .80 74LS160 1.22 74LS161 1.22 74LS161 1.22 74LS164 1.22 74LS164 1.22 74LS164 1.27 74LS167 1.70 74LS167 1.70 74LS167 1.70 74LS167 1.70	4025 4026 4027 4028 4029 4030 4031 4034 4035 4035 4035 4036 4037 4038	.86 .15 1.28 .50 .67 .86 .48 2.34 1.25 2.00 1.00 2.40 .99	4518 1.02 4518 99 4519 59 4529 1.05 4521 2.00 4521 2.00 4522 1.35 4527 1.80 4528 .02 4528 1.10 4536 3.59 4556 4.20 4556 8.10 4556 8.12 4556 8.12 4556 8.12 4556 1.40	400mW ZENER DIODES 2.17-33V .09 each S0 for 10: 3.50 for 50: 8.50 for 100 (lay elst).  1.Amp 200V .30 Ahmp 200V .30 Ahmp 200V .50 7/Amp 100V .50 7/Amp 100V .50 10/Amp 200V .65 10/Amp 200V .65 16/Amp 200V .75 16/Amp 200V .75	BD 133 BD 135 BD 139 BD 139 BD 139 BD 139 BF 241 BF 258 BF X 34 BF X 35 BF X 36 BF X 3	550 TIP418 44 TIP416 48 TIP424 48 TIP426 60 TIP426 225 TIP036 24 TIS43 25 ZTX 107 22 ZTX 506 220 ZTX 506 20 II821 18 18823	.22 .10 .20 .20 .20 .20	214056 12 214052 12 214124 16 214126 16 215133 16 215136 16 215142 18 215142 18 215142 28 BRIDGE RECTIFIERS 1A 100V 23 1A 200V 30 1A 600V 35 5A 100V 72	ELECTROLYTICS  of 10v 25v 40v 63v 1  1 945 95 95 965 96  22 945 95 95 965 96  3.3 945 95 965 96  6.8 95 955 96  6.8 95 965 97 89  10 95 96 97 89  12 96 10 12 16 23  100 10 13 18 25  150 11 15 22 32  220 12 16 22 38	
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## microfile....

## Gary Evans has been trying to do-it-himself this month but only landed in trouble with a COP

THE TITLE Do-it-Yourself Computer Show would have been more apt if, when applied to the event held at the West Centre Hotel between the 22 and 24 of June, it had been preceeded by a negative. Most of the exhibitors required little more of the user than to plug their machines in, insert a disk and hit return, and as such reflected the current US trend towards the slick, glossy, expensive hardware/software package.

In the States the Personal Computing Industry is seen, by the people with the cash, as one of the major growth areas of the next few years. As such it is attracting a lot of the venture capital that is looking for a profitable outlet. At the same time it is realised that the gains that these injections hope to promote are unlikely to come from the low-cost home DIY products, but from the education/small business sector.

#### HI Finance, Bye DIY

This latter market demands the ready built, cosmetic package of hardware, together with readily available software packages and support and considers the £2,000-£5,000 price bracket of such systems to be far cheaper than any viable alternative.

We shall then soon see a polarisation of the micro computer market. Thus at the DIY show we still saw the likes of Bywood, Micros, NASCO, Newbear, and Science of Cambridge with products that require that people do it themselves, but the pleasant "club like" atmosphere of last year's show was missing. The event, instead of being a meeting of keen, often naive, (in terms of computing) hobbyists, was instead of gathering of calculating, if adventurous, businessmen.

It seems that a large section of the DIY computing field has passed through the first few tentative steps of youth and has already reached a, to me, saddening maturity.

#### **Osbourne On Finance**

As well as the exhibition, the DIY show also featured a number of lectures throughout the first and second days. One of the first speakers was Adam Osbourne, a well known figure in the States, who has been involved in the development of the Personal Computing Industry right from the early days.

During his talk he put forward the following, if unusual, nevertheless sound advice. He said that when choosing between systems of similar performance, one should not look at the detailed specs, of each product, and make the choice on these grounds, but at the financial stability of the companies marketing the products. In this connection Osbourne highlighted the questionable tactics employed by some of the concerns trading in the US, one of these being the adoption of a scheme that, for want of a better phrase, can be termed forward financing.

The idea is, briefly, this. You have a product for which you feel there would be a ready market but no money to take the idea to the production stage. Approaches to banks and other financial institutions meet with rejection as the venture is seen (by them) to be too risky. Fairy godmothers not being too thick on the ground, even in the States, the solution adopted by some is to advertise the product heavily, this takes supprisingly little cash, gather in the money sent in response to the ad. and use this as your development fund. If all goes well, and it rarely does, you might be able to ship the first units before the customers start screaming for their goods (which may not even be designed) or their money (which you no longer have). Even if you manage this tight-rope act the first few batches of the product are likely to be riddled with faults because of the hurried nature of the developForward financing can, and has, worked but it is at best sharp practice.

I'm not suggesting that these tactics have been employed to any great extent in this country, but at least one company I know of in the personal computer area (marketing low-cost terminals) is in some financial trouble.

#### Sccch . . . . Do You Know Who?

The advertising department of ETI drop the odd clanger or two (spot the error on page 48 of the July issue) but then a certain Project Editor is covered with something other than glory at present. It's nice to know however that we are not the only human beings (less than perfect that is) about, some work at the offices of Commodore's PR agency.

Below we see Kit Spencer and Derek Rowe of Commodore but who's that on the right?



Said agency sent out a photo showing Kit Spencer and Derek Rowe of Commodore extolling the virtues of PET to some, unnamed, customer. It would have been better to name the person however as some might have recognised Chuck Peddle as the man who conceived and designed the PET and who probably knows more about the machine than anyone. He was pictured on a brief visit to London but nobody told the PR people that.

I get a lot of letters detailing the activities of various Computer Clubs around the country and it seems like a good idea to collect all these together and publish the list in ETI. So please if you run such a club, and would welcome new members please drop me a line. If you have already written to me please write again as my filing system is, shall we say, in a mess and your letter is as likely to be filed under "threatening memos from the editor" as anywhere else.

Just to be corny, could you please mark your letters club call.

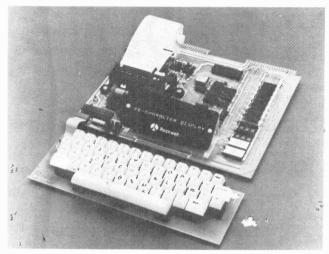
By the way if your club is in the habit of inviting guest speakers along, I'll be only to happy to come along and say a few words but will probably spend more time listening to what you, the reader, have to say and I mean that most sincerely.

#### **Blue Chip News**

The series of single chip MPUs from National that go under the generic title of Calculator Orientated Processor Systems, or COP Systems, include devices that have been programmed by the manufacturers to provide various dedicated control functions, including timers and a number cruncher for general purpose use.

National however hope that design engineers will develop their own programs for the COP series that will suit their own particular needs. Because the memory of a COP is not normally available to the outside world, and is at any rate not alterable it having been masked at the manufacturing stage, some form of development system that can imitate the performance of a COP must be provided to the software engineer.

Had National taken a leaf out of the SC/MP book and called this dedicated series of processors Simple Microprocessors an obvious name for this machine would have been SIMu-



Rockwell's AIM-65 interface module featuring the 6500 MPU, keyboard and thermal printer.

lator. With a name like COP the name is equally obvious but far less repeatable — long live National.

Before leaving this subject area when you next hear someone working with an Intel 8085 exclaim SOD it, technically speaking he's requesting that data be output via the devices Serial Output Device port. Of course if he's not speaking technically then yet another of murphy's laws has probably come to light.

By the way, while not promising to publish all suggestions, if you can come up with any likely ideas for pin description of IC's of the future, send them to me at ETI.

#### Well Rock On

Rockwell is a company that, as far as the amateur is concerned, seems to have kept a low-profile in the micro/computing fields. Their calculators are well known however and their products are well known to industry. One of their MPUs is at the heart of the Monitel telephone charge monitor mentioned in News Digest recently for example. Their latest release is also likely to bring the name of Rockwell to the attention of the aforementioned computer hobbyist.

Described as an Advanced Interface Module and designated AIM-65, the machine (pictured) features a full alphanumeric keyboard, 6500 processor with ROM monitor, dual cassette plus TTY interfaces and, the main attraction, a 20 column printer.

The 20 character wide display is formed from 16 segment characters providing the usual 64 character ASCII subset. The printer features

built in memory, decoding and drive circuitry.

Not much to say about the keyboard and the 6500, by now familiar as the device around which the PET and KIM-1 are built.

The cassette interfaces can be switched between two standards, an ASCII KIM-1 standard and a binary blocked file assembler compatible.

The Monitor/Debug commands are too numerous to detail here, suffice it to say they are far more comprehensive than the minimal functions provided by many systems.

The AIM-65 provides on board sockets for a 4K Assembler or for an 8K BASIC Interpreter thus making the system easily expandable.

For full details of the AIM-65 contact Pelco (Electronics) Ltd. at

Enterprise House 83-85 Western Road Hove Sussex BN3 1JB.

#### **Point Of Scale**

One of the many ways in which micros have improved the quality of life is in the area of Point Of Sale (POS) terminals, cash registers to you and I. A few years ago parting with your money would be accompanied by a series of whirrs, groans, clicks, with a final, puny, ting. Nowadays things are almost musical, the entry verification tones providing the melody while the chatter of the thermal printer takes care of the rhythm.

In fact I'd swear that I heard one of the things rendering the song 'money money' the other day — well at least the machine was honest.

EII



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747C DIL14 748C DIL8	50p 30p	LM3909 MC1310P	60p 150p	7403 7404	12p 10p	74104 74105	40p 40p	Transistors	_	BC479	18o	TIP34A	95 <sub>0</sub>	2N2926G	10p	
CA3011	80p	MC1312P	160p	7405	13p	74107	28p	AC125	18p	BC547	11p	TIP34B	128p	2N2926R	8p	Spec
CA3014	130p	MC1314P	300p	7406	29p	74109	45p	AC126	18p	BC548	10p	ZTX107	14p	2N3011	22p	Meg. Pote
CA3018	80p	MC1315P	520p	7407	29p	74110	46p	AC127	17p	BC549	11p	ZTX108	14p	2N3053	18p	Carb
CA3020 CA3028	160p 125p	MC1330 MC1458N	100p 35p	7408	12p	74111	70p 160p	AC128	16p	BC550	14p	ZTX109	14p	2N3054	50p	5K-2
CA3026		MC1496N	60p	7409 7410	14p	74118	82p	AC176 AC186	18p 24p	BC558	12p 13o	ZTX300 ZTX301	16p 16p	2N3D55 2N3121	50p	5K-2
CA3036	170p	NE555	18p	7410	18p	74119	130p	AD161	38p	BC559 BCY70	14p	ZTX302	23p	2N3121	25p 25p	5K-2
CA3042	170p	NE556	60p	7412	21p	74121	25p	AD162	38p	BCY71	14p	ZTX303	23p	2N3440	80p	Pres
CA3043		NE560	300p	7413	25p	74123	40p	AF124	27p	BCY72	140	ZTX304	25p	2N3441	120p	Subr
CA3046		NE561B	350p	7414	45p	74125	45p	AF125	27p	BD115	52p	ZTX310	13p	2N3442	135p	0.1V Cers
CA3052	150p	NE562B	350p	7416	27p		46p	AF126	27p	BD131	35p	ZTX311	14p	2N3702	8р	Mini
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CA3090	400p	SN76023N	140p	7425	22p	74148	120p	BC108	8p	BD 138	38p	ZTX503	20p	2N3708	8p	0.01
CA3123	150p	SN76033N	200p	7426	25p	74150	62p	EC108B	. 8p	BD139	35p	ZTX504	25p	2N3709	8p	0.22
CA3130	90p	TAA621A	215p	7427	25p	74151	48p	5C108C	-10p	BD140	-35p	ZTX530	30p	2N3710	8p	Eiec
CA3140	90p	TBA120S	65p	7428	34p	74153	60p	BC109	8p	BF244B	36p	ZTX550	24p	2N3711	8p	Axia 25V
LM300H	130p	TBA540	200p	7430	10p	74154	106p	BC109C	10p	BFX29	25p	2N696		2N3715	10p	23p
LM301AN	30p	TBA641 TBA800	240p 70p	7432	24p		48p	BC147	7р	BFX84	23p	2N697	12p	2N3819	2.2p	Tan
LM304H LM308H	150p	TBA920	320p	7433	32p	74156 74157	63p 40p	BC148 BC149	7p 8a	BFX87 BFX88	20p 20p	2N698 2N699	28p 50p	2N3823 2N3824	65p	0.1.
LM318CN	170p	TCA270SQ	200p	7437	24p 24p	74160	60p	BC149	9p	BFY50	20p	2N706	13p	2N3824 2N3866	75p 55p	4.7
LM324N	75p	TDA1002	450p	7440	13p		80p	BC158	9p	BFY51	15p	2N706A	13p	2N3903	8p	22 (
LM339	60p	TDA1022	570p	7441	52p		80p	BC159	9p	BFY52	15p	2N708	20p	2N3904	8р	Opt
LM380N	50p	TDA 2020	320p	7442	40p	74163	80p	BC167	80	8U105	170p	2N914	22p	2N3905	8p	All t
LM381N	105p	ZN414	75p	7443	90p		60p	BC168	8р	BU205	140p	2N918	30p	2N3906	8p	1: (
-				7444	90p		90p	BC169	8p	BU208	160p	2N919	50p	2N4037	30p	'
CMOS				7445	70p		100p	BC169C	9p	MJ2955	98p	2N920	54p	2N4058	12p	
4000	15p	4040	90p	7446	70p			BC170	9p	MPF 1 0 2	36p	2N929 2N930	25p	2N4059	10p	1
4001	12p	4041 4042	80p 75p	7447 7448	55p 50p			BC171 BC172	9p 7p	MPSA06 MPSA56	30p 30p	2N930 2N1131	20p 23p	2N4060 2N4061	12p 12p	DL7
4002	16p	4042	75p 85p	7450	13o			BC172	9p	TIP29	40p	2N1132	23p	2N5179	50p	DL7
4007 4008	94o	4049	28 <sub>0</sub>	7451	13p		90p	BC177	14p	TIP29A	44p	2N1302	38p	2N5457	32p	DIL
4009	46p	4050	45p	7452	13p		70p	BC178	14p	TIP29B	40p	2N1303	54p	2N545B	30p	1 8
4010	50p	4051	88p	7454	13p		90p	BC179	14p	TIP29C	60p	2N1304	54p	2N5459	32p	1
4011	12p	4052	88p	7460	13p			BC182	10p	TIP30	40p	2N1613	22p	2N5777	50p	Qua
4012	16p	4066	35p	7470	28p			BC182L	10p	TIP30A	48p	2N1671	130p	Diodes		109
4013	35p	4068	16p	7472	22p		195p	BC183	10p	TIP308	55p	2N2160 2N2243	100p 28p	0A47	10p	-
4015	80p	4069 4070	16p	7473	22p 20p			BC183L BC184	10p	TIP30C TIP31	70p 50p	2N2243 2N2297	45p	0A91	5p	
4016	45p 55p	4071	16p 16p	7475	20p			BC184L	10p	TIP31A	50p	2N2368	15p	0A200	6р	All
4017 4018	90p	4072	20p	7476	20p			BC207	10p	TIP318	60p	2N2369	16p	1N914	4p	Piea
4020	60p	4073	20p	7483	750			BC208	8p	TIP31C	65p	2N2484	22p	1N916 1N4001	5p 4p	and
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4022	85p	4078	16p	7486	26p			BC212	10p	TIP32A	60p	2N2904	22p	1N4002	6p	Sen
4023	15p		15p	7489	100p			BC212L	10p	TIP32B	75p	2N2094A	23p	1N4148	3р	
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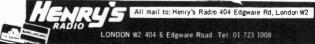
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A COUPLE OF MONTHS ago I suggested some refinements which would be rather useful in connecting a cassette recorder to a microprocessor.

The intention of the original article was to suggest a standard form for recording files on a cassette tape, regardless of the actual bit recording form (CUTS, Tarbell, etc). One requirement is some form of identification Of Start Of Record, this cannot be a 'special' character as any of the 256 possible characters coudl appear on the tape as data, similarly a sequence of characters may exist on some tape somewhere as data. What is required is a marker which is not data and could not appear by accident — a reflective strip is one possible answer, other solutions are a hole, blank tape, a tone or a highspeed signal. Most of these have limitations when applied to the various types of tape machines available including Audio Cassette, digital cassette or even paper tape. The idea of recording and playing back an indication on the tape at high speed is an interesting idea (for which I am indebted to Mr Fielden of Suffolk). On a digital cassette drive, those of the high price and low dropout, the motor speeds are usually the same in both directions which makes this idea perhaps only applicable to these machines. My extension of this idea conforms with the requirements to have a signal which cannot be recognised as data because the data is written at high speed so that it will be ignored by a normal speed read. The tape can be initialised in either of two ways -

#### **Initial Reaction**

- 1. A series of tape marks can be written on the tape during a high speed forward write operation, these are intersperced with blank tape of enough duration to allow for a tape stop, transfer to slow speed, read data record and then revert to high speed. Using this method the format of the records must be fixed length, the formatting program will calculate how much blank tape is required from the given record length.
- 2. The first record written onto the tape is an End Of File indicator written at fast forward speed and containing information about the tape, number of records, units of tape used, etc. An alternative is to record this information twice, once as a tape header record and once as a trailer. In this case the header and trailer could form an index to the contents of the tape and the theoretical space available on the tape.

Each header record would be required to be read in either direction and thus the bit pattern of the fist byte in the record should be the inverse of the last, usually this bit pattern is used to act as a START byte and thus act as a double check that a record starts here. If a UART or similar device is used within the system it will cuase 11 bits instead of 8 to be recorded on the tape, this system assumes that 8 bits are recorded but coudl be used with inginuity wit an 11 bit system. If we choose the bit pattern x'5A' or 0101 1010 it can be recognised by reading in either direction, even by a UART. We should also include a direction byte to indicate which direction the tape is reading, I would suggest '01' for forward and x'FF' for reverse (ie +1 and -1), this indicates both the direction of bit shifting required and also the byte storage into RAM.

#### **Heading For Use**

Once we have the two ends of the header record defined we can fill up the rest of the header with useful data, eg

Indent or SOR code. x'5A' Forward Read code. x'01

Record Number Single byte number of this

record in file

Start Address. Address (2 byte) at which to store record, can be over-

ridden.

Length of record 2 byte length, in a tape header

this would be max size any 6 byte name, eq. HEADER, Record Name.

INDEXb, PAYROL, etc.

X'48' (= H for Header) x'54Record type code. (T = Trailer), x'44' (D = Data)

A 2 byte address to which Transfer address. execution should be transferred at EOR, can be over-ridden

as an option.

A single byte record number Chain from code

which is compared with the 'last record' in RAM, if x'00'

ignore.

Chain to code. Single byte record number of record which should follow this

in chaining sequence, x'FF' = end of chain, x'00' ignore User header data, eg Index,

Header data. etc. Length as defined above. A checksum byte which is Checksum code.

compared to that computed during the read operation, a difference could indicate a

read error.

Duplicate for Reverse read. Length of record. End address.

IE. Start address for reverse read, can be used for checking during forward read.

Record Number Two Duplicate for reverse read.

Reverse read code. x'FF' End of Record code. x'5A'

Note that the data above is repeated for each record in both fast and slow modes, except that in a fast mode the record length would be zero to indicate a fast header and that the 'Record number' field would indicate the number of the next data record in slow mode which will be encountered in the current direction of travel

TTLs t	y TEX	(AS			190p	74LS245 <b>30</b>	4012	18p	4006 <b>95</b> p		TRANSISTORS   8FY		TIP41C 78p	'2N4125 · 6 22p	DIODES	3A 600V 72
7400	13p	74105	65p		400p 400p	74LS251 200	ip 4013		4007 18p	LM741 22p LM747 70p	AC127/8 20p BFY AD149 70p BFY		TIP42A 70p TIP42C 82p	'2N4289 20p	'BY127	12p 4A 100V 95
7401	14p	74107	34p 55o		150p	74LS257 120 74LS259 17	71 4045		4008 <b>80</b> p 4009 <b>40</b> p	LM748 35p	AD161/2 45p BLY		TIP2955 78p	2N4401/3 27p 2N4427 90p		9p 4A 400V 100 15p 6A 50V 90
7402 7403	14p 14p	74109 74110	55p	74293	150p	74LS298 24	4016	45p	4010 <b>50p</b>	LM3900 70p	BC107/8 11p BRY	/39 45p	TIP3055 70p	2N4871 60p	'OA85	15p 6A 100V 100
7404	17p	74111	70p		200p	74LS373 20	p 4017		4011 17p	LM3911 130p	BC109 11p BSX BC147/8 9p BU1	(19/20 <b>20p</b> ) 105 <b>190</b> p)	TIS43 34p	'2N5087 <b>27</b> p	'OA90	9p 6A 400V 120
7405	18p	74116	200p		200p   150p	74LS374 19	4018 4019		40014 <b>90</b> p 40085 <b>200</b> p	LM4136 120p 'MC1310P 150p	BC147/8 9p BU1		'TIS93 30p 'ZTX108 12p	'2N5089 27p '2N5172 27p	'OA91 'OA95	9p 10A 400V200 9p 25A 400V400
7406 7407	32p 32p	74118 74119	130p 210p	74366	150p	MEMORIES	4020	100p	40097 <b>90</b> p	MC1458 55p	BC157 8 10p BU2	205 <b>220</b> p	'ZTX300 13p	2N5172 27p	OA200	9p 23A 400V400
7408	19p	74120	110p	74367 74368	150p 150p	2102 100	p 4021	110p	14433 £11	MC1495 400p	*BC159 11p *BU2 *BC169C 12p *BU4		'ZTX500 15p	2N5191 83p	'OA202	9p TRIACS
7409	19p	74121	28p	74390	200p	2102-1 <b>12</b> ! 2107 <b>60</b> !		100p 22p		'MC1496 100p 'MC3340 120p	BC169C 12p BU4 BC172 12p MJ4		*ZTX502 18p *ZTX504 30p	2N5194 90p 2N5245 40p	'1N914 '1N916	**************************************
7410 7411	15p 24p	74122	48p 55p	74393	200p	2111-1 329	4024	65p	SHIFT	'MC3360 120p	BC177/B 17p MJ4	491 <b>200</b> p	2N457A250p	2N5245 40p		7p 3A 500V 65
7412	20p	74125	55p	74490	225p	2112-2 300	p 4025		REG. AM2833 400p	'MFC4000B 120p		2501 <b>225</b> p 2955 <b>100</b> p	2N696 35p	2N5401 50p	1N4001/2	5p 6A 400V 70 6A 500V 88
7413	30p	74126	60p	74LS SE	RIES	2114 <b>120</b> 6 6810 <b>40</b> 6		130p 50p	AM2833 400p	MK50398 750p NE531 130p		3001 <b>225</b> p	2N697 <b>25p</b> 2N697 <b>45p</b>	2N5457/8 40p 2N5459 40p		6p 8A 400V 75
7414 7416	80p 27p	74128	75p 75p	74LS00	22p	0010 401	4028	84p		*NE540 200p	BC187 30p MJE	E340 <b>65p</b>	2N706A 20p	2N5459 40p		57 8A 500V 95
7417	27p	74136	75p	74LS02 74LS04	22p 22p	INTERFACE	4029	100p 55p	LINEAR I.Cs 'AY1-0212 800p	NE543K 225p		E2955 100p E3055 70p	2N708A 20p	2N5485 44p	1N5401/3	14p 12A 400V 85
7420	17p	74141	70p	74LS08	22p	ICs MC1488 9	4030 4031	200p	'AY1 1313 668p	NE555 30p NE556 70p		F102 45p	2N918 45p 2N930 18p	2N6027 48p 2N6247 190p		19p 16A 400V 110
7421 7422	40p 22p	74142	200p 90p	74LS10	24p	MC1489 9	o 4033	180p	AY1-5050 211p	NE561B 425p	BC477/8 30p MPF	F103/440p	2N1131/220p	2N6247 190p 2N6254 130p		16A 500V 130
7423	34o	74147	190p	74LS13 74LS14	45p	75107 <b>16</b>			AY5-1315 600p AY5-1317 636p	NE562B 425p		F105/6 <b>40p</b>	2N1613 25p	2N6290 65p		9p THYRISTORS
7425	30p	74148	150p	74LS14	100p 22p	75182 23 81L595 14			'AY5-1320 320p	NE565 130p NE566 1550		SA06 <b>30p</b> SA12 <b>50p</b>	2N1711 25p 2N2102 60p	2N6292 <b>65</b> p 3N128 <b>120</b> p		15p 1A 50V 40
7426 7427	40p 34p	74150 74151A	100p 70p	74LS22	28p	811596 14	o 4041	80p	'CA3019 80p	NE567 1750	'BC5578 16p MPS	SA56 32p	2N2160 120p	3N128 120p 3N140 100p	SPECIAL	1A 400V 65
7428	34p 36p	74153	70p	74LS27 74LS30	38p 22p	81L597 144			*CA3046 <b>70p</b> *CA3048 <b>225p</b>	RC4151 400p		SU06 63p SU56 78p	2N2219A 20p	3N201 110p	OFFERS	3A 400V 90
7430	17p	74154	100p	74LS47	90p	81L598 14 8T28 25			CA3080E 72p	'SN76003N 175p 'SN76013N 140p	BCY70 18p MPS BCY71/2 22p OC2	28 <b>130</b> p	2N2222A 20p 2N2369A 16p	3N204 100p 40290 250p		- 00001 200
7432 7433	30p 40p	74155 74156	90p	74LS55	30p	9602 19	4046	130p	'CA3089E 225p	'SN76013NC120p	BD131/2 50p OC3	35 <b>130p</b>	2N2484 30p	40360 <b>40</b> p		£20 12A 400V 160 16A 100V 160
7433	35p	74157	70p	74LS73	50p 40p	ROM/PRON	4047	100p 55p	*CA3090AQ <b>375</b> p CA3130E <b>100</b> p	SN76023N 140p		008B <b>200p</b> 010B <b>200p</b>	2N2646 50p	40361/2 45p	100 + BCA	16A 400V 180
7438	35p	74159	190p	74LS75	50p	745188 22	5p 4048		CA3140E 70p	'SN 76023NC120p 'SN 76033N 1750	BF200 32p R20 BF244B 35p TIP2		2N2904/525p 2N2906A 24p	40364 120p	2012055	£36 16A 600V 220
7440 7441	17p 70p	74160 74161	100p 100p	74LS83	110p	74S387 40	3p 4050	49p	CA3160E 90p	'SP8515 750p	'BF256B 70p TIP:	29C 55p	2N2907A 30p	40408 <b>70</b> p 40409 <b>65</b> p		BT106 110 C106D 45
7442A	70p	74162	100p	74LS85 74LS86	100p	93436 65		80p 80p	FX209 <b>750p</b> ICL7106 <b>925p</b>	TBA641B11 225p TBA800 90o	BF257 8 32p TIP:		2N2926 9p 2N3053 20p	40410 <b>65</b> p	RECTIFIER	S 'MCR101 36
7443	112p	74163	100p	74LS90	90p	93446 <b>65</b> AY3-1015 <b>60</b>			1CL8038 340p	TBA800 90p TBA810 100p	BF259 36p TIP3		2N3053 <b>20p</b> 2N3054 <b>85p</b>	40411 <b>300</b> p 40594 <b>97</b> p		- 1140050 120
7444	112p	74164	120p 100p	74L\$93	90p	AY5-101350	p 4055	125p	LM301An 36p	TBA820 90p	'BFR40 30p TIP:	31C 62p	2N3055 48p	40594 <b>97</b> p 40595 <b>105</b> p		22p '2N5060 34 30p '2N5064 40
7446A	93p	74166	140p	74LS107 74LS112		AY5-2376 €	4056		LM311 190p LM318 200p	TCA940 175p	'BFR41 30p TIP'		2N3442 140p	40603 58p	*2A 50V	30p
7447A	70n	74167	200p	74LS123	75p	RO3-2513			LM324 70p	TDA 1022 600p XR2206 400p	BFR80 30p TIP		2N3553 240p 2N3565 30p	40673 <b>90</b> p 40841 <b>90</b> p	12A 100V 12A 400V	35p PLEASE SEN 45p SAE FOR FUI
7448 7450	80p	74170 74172	240p 720p	74LS124	180p	SN74S262 €	4063	120p	LM339 <b>75p</b>	XR2207 400p	*BFR81 30p TIP:	33C 114p	'2N3643/4 48p	40871/2 90g		60p LIST
7451	17n	74173	120p	74LS132 74LS133	2 120p	TM5601150	4066 4068		LM348 95p LM377 175p	XR2216 675p XR2240 400n	BFX29 30p TIP: BFX30 34p TIP:		2N3702/3 12p 2N3704/5 12p			TS BY TEXAS
7453	17n	74174	93p . 85o	74LS138		EPROMS	4069		'LM380 99p	XR2240 400p 2N414 90p	BFX84 5 300 TIP:	35A 225p	2N3704/5 12p *2N3706/7 14p	8 pin 11p		25p 24 pin 33
7454 7460	17p	74175 74176	90p	74LS 139	60p	1702A 60	4070	30p	'LM381AN 150p	ZN424E 135p	BFX86/7 30p TIP:	35C 290p	'2N3708/9 12p	14 pin 12p	20 pin .	28p 28 pm 42
7470	38p	74177	90p	74LS151 74LS153	100p	2708 <b>90</b> 2716 <b>300</b>			LM389N 140p LM709 38p	ZN425E 400p ZN1034E 200p	BFX88 30p TIP: BFW10 90p TIP:	36A 270p 36C 340p	'2N3773 300p '2N3819 25p	16 pin 13p		30p 40 pin 51
7472	30p	7417B 74180	160p 93o	74LS157		4702 90	OP 407		LM710 <b>50p</b>	95H90 800p	BFY50 22p TIP		"2N3819 <b>25p</b> "2N3820 <b>50p</b>	WIRE WRAP		55p 40 pm 120p
7473 7474	34p 30p	74180	200p	74LS158	120p		4075	22p	VOLTAGE REGU				2N3823 70p	8 pin 30p 14 pin 40p		30p 4∪pm 120p 30p'
7475	36p	74182	90p	74LS160 74LS161	J 13Up 1 100∞	CPU <sub>0</sub>	4076 4081	107p 22p	Fixed Plastic TO 22			4.50	2N3866 90p 2N3903/4 18p	DISPLAYS		FND500/507 120
7476	35p	74184 <i>4</i> 74185	150p	74LS162	140p	4040A <b>67</b> 6502 <b>120</b>		80p	1A +ve 5V 7805 9	ve On 7905 120n	SFF 96364 £1		2N3905/6 20p	3015F		TIL311 600 <sub>1</sub>
7480 7481	50p	74186	700p	74LS163	120p	6800	e <b>9</b>   4093			Op 7912 120p	TV-CRT CONTROL	IC.	'2N4036 <b>65p</b>	DL704/7D7		TIL312/3 110; TIL321/3 130;
7482	84o	74190	100p	74LS164 74LS165	120p		4098 4411	£10 120p	15V 7815 9	Op 7915 120p	Converts TV into To		2N405B/9 12p 2N4060 12p	FND357		TIL330 140
7483A	90p	74191	100p 100p	74LS173	3 110a	OTHERS 3245 45	4500	120p		Op 7918 120p Op 7924 120p	computing systems 2	ZB biu OIS	2N4061/2 18p	VAT RATES		8% except marked
7484 7485	100p	74192	100p	74LS174	110p	6820 <b>60</b>	450	70p	100mA TO-	92			'2N4123/4 22p		at 121/2%	
7486	34p	74194	100p	74LS175	110p	6850 70	Op 4507	55p 99p	5V 78L05 3	5p 79L05 80p 5p 79L12 80p	LEDS 0 125	Plance	dd 25n ng	p & VAT a	t appropr	iate rates.
7489	210p	74195 74196	95p 95p	74LS190	100p	8205 <b>32</b> 8212 <b>22</b>	UP 451	150p	12V 78L12 3 15V 78L15 3	5p /9L12 80p 5p 79L15 80p	TiL32   R 75p	Cause a	ou zop po	ges, etc. 0	rders acc	ented
7490A 7491	33p 80p	74197	80p	74LS191 74LS192	100p	8216 22	e_   4514		OTHER REGULA							
7492A	46p	74198	150p	74LS193	3 140p	8224 40	Op 4516		LM309K 135p	TBA625B 120p	TIL211 Gr 20p TIL212 Ye 25p	CALLER	S WELCO	ME BY APF	POINTME	NT.
7493A 7494	33p	74199 74221	150p 160p	74LS195	140p	8226 <b>57</b> 8251 <b>70</b>	3P 4520	100p	LM317T 200p LM323K 625p	TL430 65p 78HO5KC 675p	Tit 216 Red 18p	TEL	21121	054	ATL	CITD
7494 7495A	84p 70p	74251	140p	74LS196	120p	8255 <b>55</b>	4520		LM723 37p		10 2 TIL220 Red 16p			UIVI	$A \coprod $	C LTD
7496	65p	74259	250p	74LS246		4000 SERIE	4553		OPTO-ELECTR	ONICS	TIL222 Gr 18p					
7497	180p	74265 74278	90p 290p	74LS241	245p	4000 15p	4560	250p	2N5777 45p		T1L 2 28 Red 22p			ad, Londo		000000
74100 74104	130p	74279	140p	74LS242 74LS243	245p	4001 17g 4002 17g			ORP12 90p ORP61 90p		MV5491 TS 120p Chos 3p	Tel: 01	-204 433	3	Telex	922800
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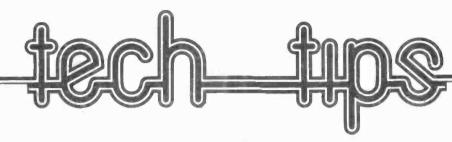
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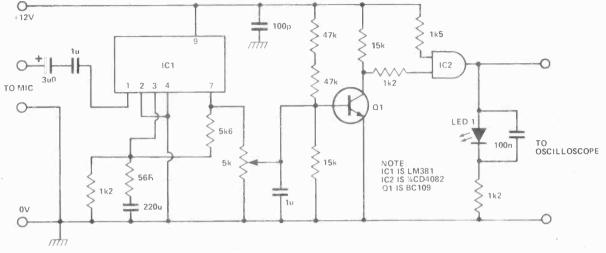
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## Morse Code On The Oscilloscope

#### S. J. Stamps

This circuit enables morse code to be displayed as dots and dashes on an oscilloscope screen. By speaking into a microphone, saying 'dit' and 'dah' as appropriate, short and long pulses appear on the screen in a format similar to that of written morse.

One half of an LM381 and a BC109 are used to amplify the signal from the microphone, which is then clipped into digital form by the AND gate. The output from the circuitry is fed to an oscilloscope set to 2V/cm and 5ms/cm, set to trigger on the

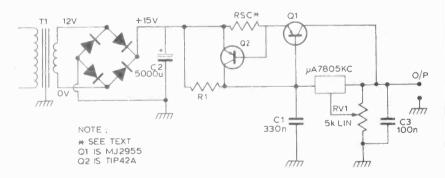
start of a 'dit'.

Input to the circuit can be from a microphone, or tape recorder. If words are recorded onto the tape with the microphone and then played back via the circuit, practice at reading morse is possible.

#### **High Current Regulator**

#### N. Grav

This circuit can supply 10A at 5V which falls to about 8A at 15V, — (make sure your transformer can take it!). The circuit is fairly straightforward. Most of the output current flows through Rsc and Q1 (less than 1A flows through the regulator), the current being regulated by the current flowing through the e-b junction of Q1. Voltage is regulated by the  $\mu$ A7805 and controlled by RV1, giving a variation from 5V to 15V.



Output current is limited by Rsc and can be calculated from

$$Rsc = \frac{0.9}{----}$$

For currents greater than 5A, Q1 should be mounted on a heatsink. Q2 and the regulator should run cold (if not there's something wrong!).

Tech-Tips is an ideas forum and is not aimed at the beginner. We regret we cannot answer

queries on these items.

ETI is prepared to consider circuits or ideas submitted by readers for this page. All items used will be paid for. Drawings should be as clear as possible and the text should preferably be typed. Circuits must not be subject to copyright. Items for consideration should be sent to ETI TECH-TIPS, Electronics Today International, 25-27 Oxford St., London W1R 1RF.

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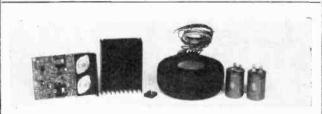
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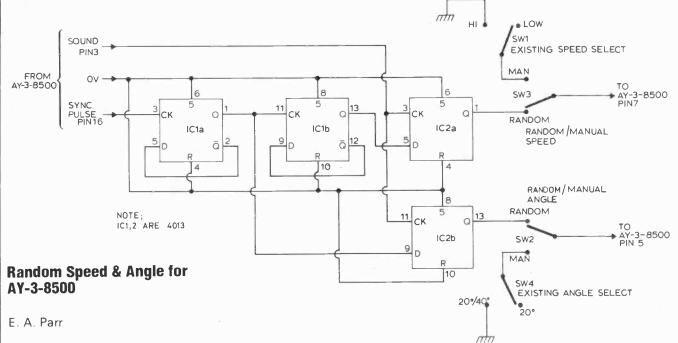
Ť JÍ.O.	: Typically < .01% any power, 1kHz, 8 ohms
T.t.O.	: Insignificant
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The games can be made more exciting and realistic if the speed and rebound angle vary randomly at each bounce or when a player hits the ball. This can be simply achieved with the addition of two 4013s (Dual D type).

SW1 and SW2 are the existing manual select switches. ICI forms a

AY-3-8500 chip via the random select switches. To ensure that these only at each bounce). This pulse train will, TV games units.

two bit counter, clocked on by the of course, overlap several sync pulses, sync pulses from pin 16 on the AY-3- but the effect of the angle and speed 8500 chip. This counter will assume a changing rapidly for 32ms is not random state from bounce to bounce. noticeable and the ball speed and The two D type flip flops in IC2 are angle stays constant after leaving the connected to pins 5 and 7 on the bat or boundary until the next interception.

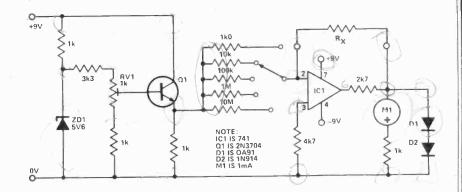
Because the two ICs are of CMOS change at a bounce, these two D types construction they will have little effect are clocked by the sound output on battery consumption, and the (which consists of a 32ms pulse train circuit can be easily incorporated into

#### **Linear Scale Ohmmeter**

M. Roberts

This circuit has several advantages over other linear scale ohmmeters.

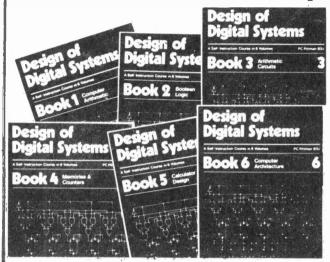
Only one preset resistor is used for all the ranges, simplifying the setting up and reducing the cost. Diode clamping is included to prevent damage to the meter if the unknown resistor is higher than the range selected. The use of a FET 741 Opamp reduces any zero error and led, a 10k precision resistor is placed justed for full scale deflection.



makes offsetting unnecessary.

in the test position, Rx, the meter is When the meter has been assemb- set to the 10k range and RV1 ad-

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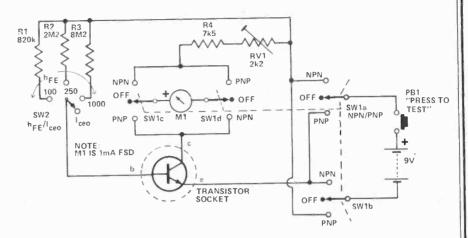


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G. Smith

This transistor tester works by injecting a known current into the base of the transistor under test, and measuring the collector current. The values of R1, R2 and R3 give a base current of 10, 4 and 1uA which gives a FSD on the meter for transistors with a gain of 100, 250, and 1000 respectively. Since the gain of the transistor is proportional to its gain, the gain can be easily deduced from the reading on the meter. Leakage current is measured by leaving the base open circuit.

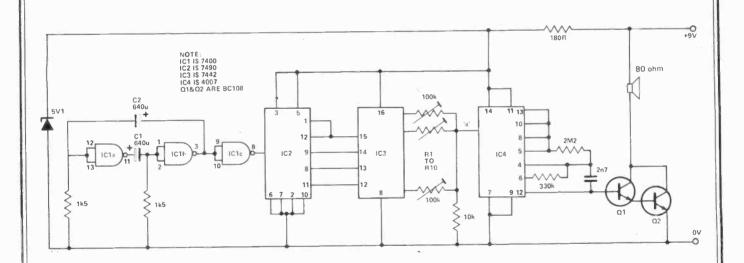
SW2 reverses the polarity of the battery and the meter to allow the testing of both NPN and PNP transistors. R4 and RV1 protect the meter from excessive currents, and do not affect the reading on the meter. RV1 should be adjusted so that the meter



needle just touches the end stop when the collector and emitter terminals are connected together

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P. Reynolds

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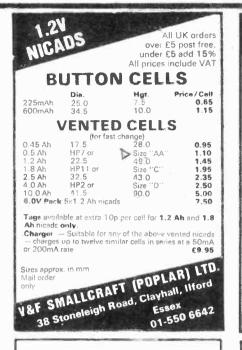
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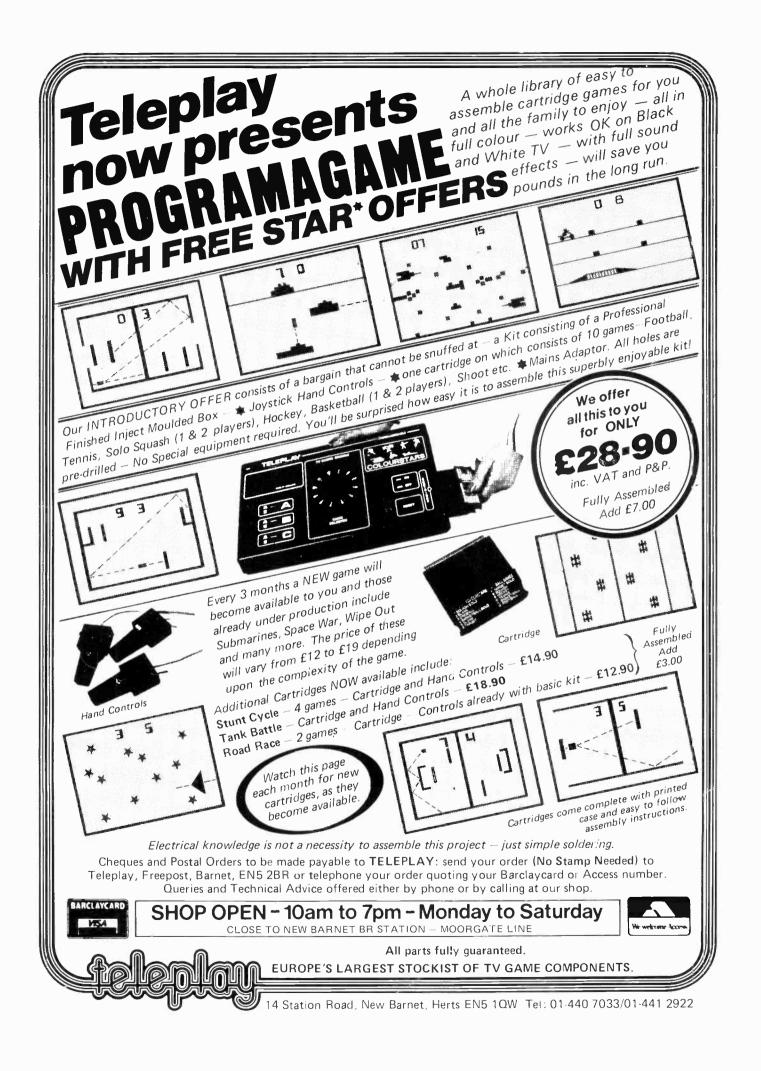
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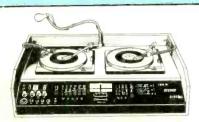
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