
. MEWS . . . .PROJECTS . . . MICROPROCESSORS. . . . AUDIO

## INTERNATIONAL POWERSLAVE 200 watt AMPIIFIER

## POWERTRAN

COMPLETE KIT AS FEATURED IN THIS ISSUE OF E.T.I.
Super-Fi performance for studio/monitoring /hi-fi use with the inherent reliability and ruggedness for the most demanding group / disco applications.
Features * over 200W rms continuous from each of 2 totally independent DC coupled amplifiers - over 800W peak powe
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* independent stabilized power supplies driven by custom designed TOROIDAL transformers
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* protessional quality - metal oxide resistors, cermet adjusters fibre glass boards. sturdy $19^{\prime \prime}$ rack mounting cabinet complete with sleeve and feet for free standing work 100
* easy to build - plenty of working space with ready access to all components, minimal wiring extensive instructions suitable for both experienced constructors and newcomers to electronics - can be purchased one channel at a time
* value for money - quality and performance comparable with ready-built amplifiers costing over $£ 600^{\prime}$


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Powerful stuff . . . . . . . P. 43

COMMODORE PET COMPUTER OP AMPS PART 3 DIGITAL ELECTRONICS PART 7

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## PROJECTS

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| ---: | :--- | :--- |
| GAS MONITOR | $\mathbf{3 2}$ | Sniff out trouble |
| 2OOW AUDIO AMPLIFIER | $\mathbf{4 3}$ | Power and quality! |
| RAIN ALARM | $\mathbf{6 2}$ | Don't be a drip! |

NEWS
NEWS DIGEST $\quad 7$ Who does what and where DATA SHEET 57 Gas sensor examined MICROFILE 73 Board with keys?
ELECTRONICS TOMORROW $\mathbf{8 3}$ Motorola have

| Porsche show . . . . . . . . P. 23 | INFORMATION |  |  |
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## OFFERS

| CBM 5R39 | $\mathbf{2 0}$ | Count on this |
| ---: | :--- | :--- |
| ETI CLOCK | $\mathbf{2 0}$ | Time to get one |
| CHESS CHALLENGER | $\mathbf{2 1}$ | Check this price |


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## TECHNOLOGY:

Double sided epoxy printed circuit board with plated through holes; Dual gate effect transistors; Silvered coils.

FI 2846
IF AMP AND DECODER


TECHNICAL CHARACTERISTICS
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CIRCUIT TECHNOLOGY:
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ALS 1500
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## TECHNOLOGY:

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| 160 | 4 in | 4 in | 11/2in | 62p' |
| 161 | 4 in | 21/4in | P/zins | $620^{\circ}$ |
| 162 | 51/4in | 4 in | 11/2in | 74p. |
| 163 | 4, ${ }^{\text {n }}$ | 21/2in | 2 in | 64* |
| 164 | 31. | 2ın | Yin | $44 \mathrm{p}^{\circ}$, |
| 165 | 7 in | 5 in | $21 / \mathrm{in}$ | ¢1.04, |
| 166 | 8 in | 6 in | 3 in | c1.32' |
| 167 | 6 6 | 4 in | 2 n | $86 p^{\circ}$ |
| INSTRUMENT CASES. In iwo sectione vinyl covered top sides, sluminium bottom, front and back. |  |  |  |  |
|  |  |  |  |  |
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| Ca3l40 | ${ }_{50}{ }_{50} \mathbf{5 0 . 9 0}$. |  | ${ }_{\text {c }}^{\text {¢ }}$ | SN76115 |  |
| LM304 | ¢1.80 | NE555 | ${ }_{\text {E }}$ | SN76660 | ¢0.75* |
| Lм 308 | E1.40* | NE556 | ${ }_{\text {¢0, } 82}$ | Sl414A |  |
| LM3096 |  | NE561 | £3.95* | tAA550] | $\mathrm{cos}^{0.35}$ |
| LM320.5v |  | Ne5628 | ${ }^{\text {E3 } 3.955^{*}}$ | ${ }_{\text {TAA6 }}{ }^{\text {a }}$ |  |
| 320.12 |  | Ne565A | ${ }_{\text {c1. }}^{50}$ | TAD 100 | ${ }_{\text {c1.30 }}$ |
| tm320 |  | NE567 | ${ }^{1.80} 0^{\circ}$ | т8A5400 | E2.20 |
| 20.15 |  | UA702C |  | TBA6418 | E2.25 ${ }^{\circ}$ |
|  | . 50 | 72702 | ${ }^{\text {¢0.46 }}$ | T8A800 | ${ }^{\text {c0. }} 8.80^{\circ}$ |
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| 0-100UA | 1303 | ¢6.70 | 0.100UA | 1308 | $\underline{5} .50$ |
| O-500UA | 1304 | ¢6.70 | 0.500UA | 1309 | £5.50 |
| O-IMA | 1305 | ¢8.40 | O-IMA | 1310 | ¢5:50 |
| 0-50V | 1306 | c8. 40 | 0.50 V | 1311 | ¢5.50 |
|  |  |  | MIN. LEV | ETER |  |
| MR2P TYPE <br> Size $42 \times 42 \times 30 \mathrm{~mm}$ |  |  | Size $23 \times 22 \times 26 \mathrm{~mm}$ |  |  |
|  |  |  | Sensitivity 200UA |  |  |
| Value | No. | Price |  |  |  |
| 0-50UA | 1313 | 65.40 | 1320 |  | £2.80 |
| $\begin{array}{llll}\text { P-IMA } & 1315 & \text { E5.40 }\end{array}$ |  |  |  |  |  |
|  |  |  | Vu METER <br> Size $40 \times 40 \times 29 \mathrm{~mm}$ |  |  |
| balance/tuning |  |  |  |  |  |
|  |  |  | Sensitivity I30UA |  |  |
| Size $45 \times 22 \times 34 \mathrm{~mm}$ |  |  |  |  | Pruce |
| Sensitivity 100/0/100UA |  |  | 1321 |  | £2.00 |
| No. |  | Price |  |  |  |
| 1319 |  | ¢2.00 | MINI MULTI-METER |  |  |
|  |  |  |  |  |  |
|  |  |  |  |  |
| miniature balance/ |  |  | AC VOLTS 0.10.50. 250, 1000 |  |  |
| TUNING METER |  |  | DC VOLTS 0.10, 50. 250.1000 |  |  |
| Size $23 \times 22 \times 26 \mathrm{~mm}$ |  |  | DC CurRe | 1-100m |  |
| Sensivity 100/0/100MA |  |  | Resistance $0-150 \mathrm{~K}$ ohms |  |  |
| No. |  |  | Price |  |  | Price |
| 1318 |  | ¢1.95 | 1322 |  | ¢ 7.50 |

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With two independent secondary windings
No. Type



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| No. | Raung | Price |  |
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| 2032 | 1 amp | E6.60* | P.8.9.86p |
| 2033 | 2 mmp | ¢8.40 | P.\&P £ 1 10 |

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Straight out of Star Wars is a 'Strike Sight' computer from Ferranti.
The further away from the target a tactical aircraft can release a bomb load the greater chance the pilot has of returning safely. In particular, when releasing bombs equal to thousands of tons of TNT, unless the load can be released from an aircraft flying at very high altitudes (most tactical aircraft are not built for this), the only way is to release the load at some distance from the target.

The strike sight computer enables the pilot to do just that. The computer receives signals from and transmits signals to the range and doppler radars, the master reference gyro, and the altimeter. It guides the pilot on the approach to the target. At a specific time from the target the pilot selects whether to release the bomb or to abort the attack and pull out. At a precise interval thereafter, having selected to make the attack, the pull out begins and the computer takes over - the bomb is released automatically.

The only direct decision required by the pilot is whether or not to release the bomb. When he has made the selection the computer computes the release times and the pull-up process is initiated.


Pictured here is the Dawe Sound Level Meter 1405 C . This device has a very wide dynamic range, from 34 to 130 dB - from a whisper to well above the loudest shouting. Dawe Instruments Ltd, Concord Road, Western Avenue, London W3 05D.

## getting abuzz

Wired like an ordinary plug and looking not much larger, the BUZZ PLUG may be fitted as a direct replacement for a standard plug.

The slightly thicker top actually contains some newly designed microcircuitry which continuously monitors the mains supply. In the event of a power failure (due to accidental switch-off, power cut, blown fuse) the plug produces a loud instantaneous warning which will persist until some action is taken.

The plug is powered by a single inexpensive battery which will give up to 60 hours warning. Typical applications include use with freezers, fridges, incubators and equipment which must run constantly or would require recalibration. Abbatal is the manufacturer, and further information is available from Ian Jones on Basingstoke (0256) 56417.

all yer mpus in one chip


Motorola, manufacturers of the 6800 MPU has announced the introduction of a new one chip microprocessor in May. The chip, the MC6801 includes 2 K -bytes of ROM, 128 bytes of RAM, a 16 -bit timer, clock, serial output and 31 input/output lines.

The MC6801 is not a cut-down low end microprocessor but a very large scale integration MPU with an equivalent of 40,000 devices on the chip. The 6801 implements the full 6800 instruction set with the execution time of many key instructions reduced.

## right hook

In a historic ruling, the US Supreme Court has confirmed that private individuals have the right to buy or make their own telephone equipment and connect it to the US telephone network.

Under the ruling it will be legal to hook
up as many devices as the user wishes computer controlled systems, 'phone diverters, memory diallers, picturephones etc, etc. The only restriction is that the various bits must meet the relevant FCC requirements.
Built 5 watt power momplifior Gould-Advance. $\mathbf{4 . 8}$ ohms ouzput, up to 24 V supply 500 mV inio 2 K
inpul Complete with instructions. $115 \times 6 \times 3 \mathrm{~cm} \boldsymbol{£ 3 . 0 0}$. 10 for $\mathbf{E 2 2 . 5 0}$. Suitable power supply for above, in $\mathrm{kn1}$ form $\mathbf{£ 2} 20$.
HONEYWELL Proximity Detector Integral Amplifiter BVDC E2.50 ea. 10 for $£ 22$
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ALMA push-butron read awitchee, push io make High reliability. $18 \times 27 \times 18 \mathrm{~mm} 35 \mathrm{p}$. 25 for £ 7 . 100 for E25. 1000 for $\mathbf{E 2 2 5}$
BURROUGHS 9 digit Pansplox calculator display. 7 segment $0.15^{\prime \prime}$ diguts neon type with red beze sockel and instructions $£ 3.50$. 10 for $£ 30$. 100 for $£ 250$
TV sound converter through your FM turier modula. Complete with instructiona $£ 5.50$ IC Audio Power by TOSMIBA 35 WATT modut. 8 ohms $0 / \mathrm{p}$. 200 mV into 47 K for full output $03 \%$
distortion (max). 60 V power $\$ \mathrm{Supply}$ requred $£ 8.50$. 10 for $£ 75$.


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$21^{2} \mathbf{2}^{\prime \prime} \mathbf{4 0}$ ohm apeaker $\mathbf{2 5 0} \mathbf{~ M . W a t t ~ - ~ i d e a l ~ f o r ~ t h a t ~ s m a l l ~ s p a c e ~} \mathbf{7 5} \mathrm{p}$. 10 for $\mathbf{£ 6}$. 100 for $\mathbf{£ 5 0}$ 3 DIGIT 7 SEGMENT DISPLAYS. C cathode pack of 2 wath data ane or more segments are missing 60p pack. 10 packs for $£ 5$.
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## hammer fet-ish



A new range of low cost VMOS power FETs in plastic have been introduced by Siliconix. These devices are aimed at replacing conventional bipolar transistors in a great many applications. This development in VMOS technology has cut the price of such devices by a third enabling them to compete directly with bipolar devices.

The direct interfacing with most logic families makes VMOS ideal for use in computer peripherals, micro- and mini-computer
systems, process control, etc. In addition to the many existing applications, VMOS should open up many new areas in telephone switching, analogue switching, and unlike conventional FETs or bipolar devices, VMOS transistors are inherently linear and ideally suited to use in linear power amplifiers from audio right through to VHF.

Siliconic Ltd.
Llanllienwen Close
Morriston,
SWANSEA SA6 6NE

## digital dialing

Unilever have recently had a new computer controlled telephone system installed in their Rotterdam headquarters. The system comprises a Philips computer controlled exchange with electronic switching and the latest pushbutton keyphones installed throughout the building. Now each phone has its own 'switchboard' capability and supports many new facilities such as short code dialling, automatic call back and 3 -way conversations

The new system should make telephoning quicker, eașier and more effective (in 1976 the phone bill came to over $£ 200,000$ - more than $£ 200$ per person!).

A similar system will be installed in their London offices in the future.

## bbc get it taped

The $B B C$ and $3 M$ have collaborated to develop a new tape recording system claimed to provide 90 dB noise figure. The system will accommodate 32 tracks on one-inch tape at an undisclosed tape speed.

# digest... <br> <br> lcd giving a hand 

 <br> <br> lcd giving a hand}

A Scottish company, Murrell Dynamics, has designed an LCD watch with an analogue-type 'dial'. The main problem which Murrell has overcome is reducing the number of connections required to address the segments which comprise the 'hands'.

The 100 mm diameter prototype (which Murrell claims can readily be reduced to 25 mm ) utilises two concentric rings of 60 radial lines - the inner ones are addressed uniquely for the hours indication, the two rings together for minutes, and the outer one for seconds

## hpin the day

Hewlett-Packard have announced red and yellow seven-segment displays which can be viewed in direct sunlight or in ambient lighting as bright as 10,000 foot-candles.

## planer sailing

Planer instruments have introduced a new handheld digital voltmeter using a $3^{1 / 2}$ digit display with 11 mm high digits. The device has an input
sensitivity of 10 M and four ranges from 0.1999 volts to 199.9 volts. Price is around £50. Planer Products Ltd, Windmill Road, Sunbury-on-Thames, Middlesex.


ETIPRINTS

In case you have missed out on ETIPRINTS thus far, they are a complete PCB pattern already to rub down in seconds. The patterns are produced from our original artwork so that the results they produce are nice and sharp.

We think that ETIPRINTS are such a good idea that we have patented the system (Patent numbers 1445171 and 1445172 )

ETIPRINTS 005 is now available, and joins 001-004 as part of the regular system.

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Lay down the ETIPRINT and rub over with a soft pencil until the pattern is transferred to the board. Peel off the backing sheet carefully making sure that the resist has transferred. If you've been a bit careless there's even a 'repair kit' on the sheet to correct any breaks!

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> 002 With patterns for the burglar alarm from Jan 78 plus clock board $B$ and the Isv monitor from Dec 77 .
> 003 With patterns for hammer throw and race track from Jan 78 plus the freezer alarm from Dec 77 .
> 004 With patterns for the ultrasonic transmitterreceiver, metronome, $1 B$ metal locater and porch light from Feb.' 78 plus $5 /$ w stereo amplifier Mk. 2 from Jan. '77.
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Readers please note that in earlier ads the contents of 002 and 003 were reversed. Would you please indicate when ordaring from which issue the patterns you require were
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# digest. 

new development
tion was the photon by teal. It's features do not include an on-off switch, batteries, or a charger unit. It operates on pure Solar power, even under "LLL" low light levels.


Audio-wise, we saw a nice combination between the Micro processor and cassette deck by Sharp/Optonica. Some of it's features include auto programme locate which

can skip ahead or backwards, up to 19 songs, and automatically play your choice. Counter memory will stop or play at your own number selection on the counter. An LCD display indicates the function in use, or real time, which may also be used to turn the machine on and off. Approximate price £250.

Fisher offer on their model CR 4025 a cordless pause control with accompanying LED indicator on the deck: Price approximately $\$ 250$.

Blank tape is also a potential money spinning market. Not just the audio cassette field, but the video boom means a prime market place for both types of video cassette formats. One Company who likes to show its products to the full intermag. netics, emphasise their mechanical qualities by utilising transparent housings.

Radio sets has taken a turn for the better With improved audio (maybe even soon stereo A.M.!) and arrays of digital read-outs for almost everything. Even "Star Wars" R2 D2 and CP30 are now available as radios.

Finally a brief look at the up and coming Autosound industry, which is expected to top the billion dollar mark in '78.

We now have a 150 watt per channel power amplifier from AUDIOMOBILE INC. A seven channel stereo graphic equaliser from PYRAMID INDUSTRIES coupled with a multitude of goodies like digital read-outs, ultra small, heavy sounding loudspeakers and a claim most manufacturers make that performance levels are comparable to home audio systems.

The best quote from the show by Nolan Bushnell, the Chairman of Atari TV Games; "Only after you've experienced the sheer joy of slaughtering your best friend, will you know the true meaning of fun.'


It can be a nuisance can't it, going from newsagent to newsagent? "Sorry squire, don't have it - next one should be out soon." Although ETI is monthly, it's very rare to find it available after the first week. If it is available, the newsagent's going to be sure to cut his order for the next issue - but we're glad to say it doesn't happen very often.

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| 27696 | 0.35 | ${ }_{2} 213703$ | 0.15 | 246126 | 0.45 | $\mathrm{BCL}_{159}$ | 0.16 | 80116 | 1.20 | ${ }^{\text {BFXB5 }}$ | 0.35 |  |  |  |  |  | . 00 |
| ${ }^{215697}$ | 0.30 | 243704 | 0.15 | 40361 | 0.50 | 8C160 | 0.35 | 80131 | 0.51 | ${ }_{85 \times 87}$ | 0.30 |  |  |  |  | TMA52 | . 90 |
| 24698 | 0.62 | 233705 | 0.15 | 40362 | 0.55 | 8 Cl 151 | 0.35 | 80132 | 0.54 | $8 \mathrm{~F} \times 88$ | 0.30 |  |  | TS |  | taA550 | 0.60 |
| $2 \mathrm{N699}$ | 0.55 | 213706 | 0.16 | 40363 | 1.30 | ${ }_{8 C 1} 167$ | 0.12 | 80135 | 0.37 | BFx $\times 9$ | 1.25 | CIP |  | 17 |  | тер560 | 1.75 |
| 27706 | 0.28 | 213707 | 0.18 | 40406 | 0.60 | BC158 | 0.12 | 80136 | 0.37 | 8FY50 | 0.25 | ca3020 | 2.00 | LM7408.8 | 0.55 | tans\%0 | 2.30 |
|  | 0.28 | 2 2 37708 | 0.13 | 40407 | 0.52 | BC169 | 0.12 | 80137 | 0.38 | 8FY51 | 0.25 | ca3020] | 279 | L.m748\% | 0.55 | тма6118 | 1.85 |
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| 27709 | 0.50 | 213710 | 0.16 | 40409 | 0.75 | 8 Cl 17 | 0.16 | 80139 | 0.40 | ${ }_{\text {BFFS3 }}$ | 0.34 | ca3028 | 1.01 | UM1808 | t. 92 | tM651a | 1.50 |
| 24718 | 0.27 | 24371 | 0.16 | 40410 | 0.75 | ${ }^{\text {BC }} 172$ | 0.14 | 80140 | 0.40 | BFY90 | 1.20 | C13030 | 1.35 | M1828 | 1.75 | TM46618 | 1.50 |
| 207718 | 0.50 | 213712 | 1.20 | 43411 | 2.85 | ${ }_{8 C 177}$ | 0.20 | 80239 | 0.40 | BHY39 | 0.50 | ca3333A | 2.00 | 1 m 3301 H | 0.85 | TM700 | 3.91 |
| 207204 | 0.80 | 2237113 | 2.30 | 45594 | 0.80 | ${ }^{8 C 178}$ | 0.20 | 80240 | 0.45 | BSX20 | 0.33 | Ca3045 | 1.49 | [m3302\% | 1.40 | TAAS30A | 1.30 |
| $2 \mathrm{HY14}$ | 0. 35 | 214314 | 2.45 | 40595 | 0.90 | ${ }_{\text {BC1 }} 179$ | 0.23 | 80241 | 0.45 | BSx21 | 0.32 | Ca3346 | 0.99 | $1 M^{3} 401$ | 0.70 | тияз308 | 1.30 |
| 24918 | 0,30 | 2 m 3715 | 2.55 | 40673 | 0.75 | 8 Cl 182 | 0.11 | 80242 | 0.50 | But05 | 1.40 | CA3048 | 2.23 | LM3900 | 0.75 | Th0100 | 1.95 |
| 27918 | 0.38 | 243716 | 3.00 | 4.126 | 0.45 | BC182L | 0.14 | 80243 | 0.60 | 8U205 | 2.20 | ca3049 | 1.80 | [M3905 | 1.60 | T8A120 | 0.75 |
| 21929 | 0.25 | 2к3771 | 1.95 | $\sim 127$ | 0.45 | BC189 | 0.11 | 80244 | 0.65 | ME0402 | 0.20 | ca3050 | 242 | LM3909 | 0.68 | төя400 | 200 |
| 24930 | 0.25 | 2 23772 | 2.00 | 4 Cl 12 B | 0.45 | ${ }_{8 C 1835}$ | 0.14 | 80245 | 0.65 | ME 0404 | 0.15 | ca3053 | 1.62 | MC1035 | 1.75 | тва500 | 221 |
| 2413L | 0.30 | 2 213773 | 2.90 | Nasiv | 0.40 | BCIB4 | 0.12 | 80246 | b. 66 | MES0412 | 0.20 | C13080 | 0.75 | MC1303 | 1.03 | т195000 | 2.30 |
| 231132 | 0.37 | 2 243789 | 2.90 | ${ }^{4} 152 \mathrm{~V}$ | 0.50 | BC184i | 0.14 | 88529 | 0.45 | ME4192 | 0.10 | са3080) | 1.88 | MC1304 | 1.40 | t8a510 | 221 |
| 2 W 1813 | 0.30 | 2 3 3790 | 3.10 | $\sim 153$ | 0.55 | 8С207 | 0.16 | 80530 | 0.50 | ME4104 | 0.10 | C13086 | 0.60 | MC1305 | 1.40 | teasiog | 2.30 |
| 221711 | 0.30 | 2213791 <br> 1 | 3.10 | AC153\% | 0.55 | 8С208 | 0.16 | B0Y\% | 1.00 | m. 481 | 1.55 | Са3088 | 1.70 | MC1310 | 1.91 | т8¢520 | 2.21 |
| 2\%1093 | 0.38 | 233792 | 3.50 | AC176 | 0.50 | BC212 | 0.14 | 8F\|15 | 0.38 | M.490 | 1.35 | ca3089 | 252 | MC1327 | 1.54 | T0a5200 | 2.30 |
| 2 L 2102 | 0.98 | 2 213794 | 0.20 | AC176\% | 0.65 | BC212t | 0.17 | 8 F 121 | 0. 55 | M.491 | 1.85 | Ca3090 | 4.00 | MC1330 | 1.00 | гв4530 | 1.98 |
| .212218 | 0.33 | 2 23819 | 0.36 | ${ }_{\text {ach }}$ 187K | 0.60 | $\mathrm{BC}^{\text {c }} 13$ | 0.14 | 9F123 | 0.55 | M, 22955 | 1.25 | ca3130 | 0.98 | WC1350 | 0.90 | TBA5300 | 2.07 |
| 2и2218 | 0.37 | $2 \times 3820$ | 0.38 | AC188K | 0.60 | ${ }_{8 C 213}$ | 0.16 | ${ }_{8 F} 152$ | 0.85 | M, Le340 | 0.58 | [m301a | 0.67 | MC1351 | 1.20 | т8а540 | 2.21 |
| 212219 | 0.35 | 2133823 | 0.80 | 40161 | 1.00 | 8c214 | 0.16 | ${ }_{85153}$ | 0.25 | mi 370 | 0.58 | LM30117 | 0.40 | MC1352 | 1.10 | т8а5400 | 2.30 |
| 2 2r2194 | 0.36 | 2 n 3904 | 0.21 | 20162 | 1.00 | BC2141 | 0.17 | ${ }_{8 F} 154$ | 0.25 | mis 371 | 0.60 | ${ }^{1} 10304$ | 2.45 | MC1458 | 0.91 | тва¢50 | 3.13 |
| 212220 | 0.35 | 2 23906 | 0. 22 | 4106 | 0.55 | $8 \mathrm{BL237}^{2}$ | 0.14 | Bf158 | 0.35 | mues20 | 0.45 | L*3074 | 0.65 | ME555 | 0.40 | тва5500 | 3.22 |
| 222221 | 0.25 | 2 L 4036 | 0.67 | 45109 | 0.75 | 8С238 | 0.12 | BF150 | 0.30 | MJIE521 | 0.65 | 1 U308C | 1.82 | Ne556 | 1.10 | Tan5600 | 3.22 |
| $2 \times 2214$ | 0.25 | 2140337 | 0.55 | $\sim 124$ | D. 65 | 8C239 | 0.15 | 8F161 | 0.60 | MJE2955 | 1.50 | LM309\% | 0.85 | ME565 | 1.30 | T84570 | 1.29 |
| 212272 | 0.25 | 2 24058 | 0.20 | 4125 | 0.65 | ${ }_{8 C 251}$ | 0.15 | BF165 | 0.40 | MUE3055 | 0.95 | 1 m 301 M | 1.85 | ME566 | 1.65 | т¢а5700 | 1.38 |
| 2122224 | 0.25 | 244059 | 0.15 | $\sim 126$ | 0.65 | ${ }_{\text {BC2 }}$ | 0.22 | BF167 | 0.35 | MP8311 | 0.35 | 14317\% | 3.00 | ${ }^{15} 567$ | 1.80 | тви641в | 270 |
| 222368 | 0.25 | 2 m 4050 | 0.20 | ${ }_{\sim}^{\sim} 139$ | 0.69 | BC257A | 0.17 | ${ }_{\text {BF }} 173$ | 0.35 | M.p9112 | 0.40 | 1 m 318 N | 2.26 | SAS560 | 2.50 | тви651 | 2.20 |
| 2 22369 | 0.25 | 244061 | 0.17 | ${ }^{4} 186$ | 0.50 | BC258A | 0.17 | ${ }_{\text {BF }} 177$ | 0.25 | MP8113 | 0.45 | LM323K | 6.46 | SAS570 | 250 | т8а700 | 1.52 |
| 2123694 | 0.25 | $24406{ }^{2}$ | 0.18 | ${ }_{*} 200$ | . 1.20 | BC2598 | 0.18 | BF178 | 0.25 | MPF102 | 0.30 | Lm339\% | 1.40 | S042P | 1.25 | [B/70000 | 1.61 |
| 2 2 2645 | 0.75 | 244126 | 0.17 | AF239 | 0.65 | eczeria | 0.24 | 8F179 | 0.30 | MPSAOS | 0.25 | L3488 | 1.50 | 7600 iN | 1.30 | тва7200 | 2.30 |
| 2 F 2647 | 1.40 | 244289 | 0.20 | 4F240 | 1.14 | BC262B | 0.24 | 8F130 | 0.35 | mpSADS | 0.25 | LM360\% | 2.75 | ${ }^{76003 \%}$ | 220 | тва750 | 1.98 |
| 2012904 | 0.36 | 244519 | 0.65 | ${ }^{4} 279$ | 0.00 | вс2830 | 0. 30 | bF181 | 0,35 | MPSA12 | 0.40 | LM3701 | 2.50 | 76008 K | 1.50 | тва7500 | 207 |
| $223^{272904 A}$ | 0.37 | 244220 | 0.75 | 4280 | 0.85 | - ${ }^{\text {c }} 300$ | 0.40 | ${ }_{8 F 182}$ | 0.35 | MPSAS5 | 0.25 | [m37114 | 1.70 | 76013 K | 1.50 | твавоо | 1.25 |
| 212905 | 0.37 | 2 2 4929 | 0.50 | ${ }^{\text {BCIIP7 }}$ | 0.15 | BC301 | 0.40 | ${ }^{\text {BFF } 183}$ | 0.40 | MPSA56 | 0.25 | Lu3720N | 1.70 | 76013 NK | 1.30 | т8AB10 | 1.25 |
| 2129054 | 0.38 | 2 24922 | 0.55 | $\mathrm{BCL}_{108}$ | 0.15 | BC303 | 0.50 | BF194 | 0.38 | mpsu05 | D. 50 | LM37314 | 2.80 | 76018k | 1.50 | твм820 | 1.25 |
| 2 22906 | 0.28 | ${ }^{214923}$ | 0.70 | ${ }^{\text {BClog }}$ | 0.15 | B6307 | 0.15 | ${ }^{\text {BFF } 185}$ | 0.35 | mpsuob | 0.56 | LIM374 | 3.10 | ${ }^{780233}$ | 1.45 | TTA920 | 2.90 |
| 2129064 | 0.35 | 275190 | 0.60 | $\mathrm{BCO}_{1} 13$ | 0.20 | 8c308 | 0.15 | BF194 | 0.15 | MPSU55 | 0.55 | Lm377\% | 1.75 | 76023100 | 1.26 | ттay200 | 299 |
| 2 W 2307 | 0.25 | 2 N 5191 | 0.70 | $\mathrm{BCl}_{1}$ | 0.20 | BC309C | 0.15 | ${ }^{\text {BF }} 195$ | 0.15 | MPSu56 | 0.60 | LM378\% | 2.25 | 75033 M | 2.20 | Trasio | 1.62 |
| ${ }^{2425074}$ | 0.25 | 2 W 5192 | 0.75 | ${ }^{\text {BCI }} 116$ | 0.19 | ${ }^{\text {BC317 }}$ | 0.14 | BF196 | 0.15 | กР29A | 0.45 | 1 M3795 | 3.95 | ${ }^{761109}$ | 1.18 | TCA1500 | 1.85 |
| 272924 | 0.15 | 205195 | 0.90 | ${ }^{\text {BCl } 1159}$ | 0.20 | BC318 | 0.13 | BF197 | 0.17 | T1P29C | 0.60 | Lm3888 | 0.50 | 76115 N | 1.51 | тCA1608 | 1.61 |
| 212992 | 0.17 | $2 \times 5245$ | 0.34 | ${ }^{8 C 117}$ | 0.22 | в¢327 | 0.20 | 8F198 | 0.18 | прза | 0.49 | LM380\% | 0.98 | 76116 W | 1.66 | тCaz70 | 2.25 |
| 2M3019 | 0.55 | ${ }^{2} 25294$ | 0.40 | ${ }^{\text {BCIIP }}$ | 0.20 | BC328 | 0.19 | BF200 | 0.35 | прзоС | 0.65 | ${ }_{\text {LT3 }}^{\text {cin }}$ | 2.45 | ${ }_{761314}$ | 1.20 | tcaz80A | 130 |
| 2233053 | 0.26 | 245295 | 0.40 | 88119 | 0.30 | вс337 | 0.19 | 8F225d | 0.25 | пр314 | 0.50 | Lm3817 | 1.60 | ${ }^{76226 \mathrm{~N}}$ | 1.56 | tcaz90a | 3.13 |
| 233054 | 0.60 | 255296 | 0.40 | ${ }^{\text {BCII2 }}$ | 0.45 | ${ }_{\text {a }}$ с 338 | 0.21 | 85244 | 0.35 | TiP 3ic | 0.66 | LT382\% | 1.25 | ${ }^{762275}$ | 1.20 | tcaszoa | 1.84 |
| ${ }^{2133055}$ | 0.70 | ${ }^{2} 2752988$ | 0.40 | ${ }^{8 C 132}$ | 0.30 | BC547 | 0.12 | 8F245 | 0.40 | тір32 | 0.55 | $1.9384 \times$ | 1.45 | ${ }^{76228 M}$ | 1.41 | тад730 | 3.22 |
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| 2233939 | 0.16 | 225457 | 0.32 | ${ }^{\text {BCI }} 137$ | 0.20 | вСу31 | 1.00 | 8 8251 | 0.37 | пр34 | 0.90 | $L^{133893}$ | 1.00 | 7654414 | 1.44 | таво00 | 3.13 |
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| 203439 | 0.88 | 2 L 5489 | 0.34 | ${ }^{\text {BC1 }} 142$ | 0.30 | всСү3 $^{\text {¢ }}$ | 1.00 | BF459 | 0.50 | тіР36A | 2.80 | 1 m 709 N | 0.45 | $76550 \%$ | 0.35 | 011 |  |
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## THE CBM PET -

IS IT

## HOUSE TRAINED?

Gary Evans, in conjunction with Graham Wideman and Mark Czerwinski of our Canadian offices, takes a look inside the machine that might do for 1978 what the TV game did for 1977.


Fig. 1. The version of PET that is as far as we know the version that will be available in the UK.

WE FIRST SAW the Commodore PET as long ago as last April and reported on the fact in the July 1977 Microfile. It's taken until now for the final prototype we saw then to be translated into a production model, investments in production facilities completed and at last we have seen the official UK launch of the long awaited machine.

We have been dying to get our hands on a PET for some time and between our Canadian and British offices have now managed to put in some time on various PETS. Before getting down to a detailed description of the hardware just a few words about what will be available, at what price, when and from whom.

Commodore have set up a new division specifically to market PET and associated products (notably KIM 1 A 6502 development kit). Commodore Systems plan to mount a direct marketing operation to retail PET. They hope to locate suitable London showrooms very shortly.

The model available in Britain will initially be assembled in Germany and will feature 8K or user RAM (the 4 K version sold in the States is not to be made available in the UK) and will retail for $£ 695$ (inclusive).

At the launch in early Feburary Commodore Systems were quoting delivery times of about thirty days but expected these to lengthen to perhaps sixty days as demand increased. It might be worth mentioning however that the backlog of orders in the states has meant waits of up to one hundred and twenty days. The thirty or even sixty days quoted by Commodore Systems might be a little optimistic until production reaches target levels.

## The Machine Itself

The machine shown in Fig. 1. is as close to the final production version as makes no odds.

What you get for your money is what you see (CRT, cassette mechanism, calculator style keyboard) all packaged in a neatly styled case, plus the inards (6502 MPU, 12 K or REM containing all that is necessary to operate the hardware plus a fast BASIC interpreter, 8 K or RAM plus other assorted bits shown in Fig. 2.)

## How did we like it

There is no doubt that this package of micro-pieces is well thought out, and designed with the idea that the user needs to know very little about computers to get "into" the PET. Remember the increased popularity of tape recording when cassettes came out? Make it simpler to use, easier for the human being. Same philosophy at work here, and that is what will sell it. Needless to say in spite of the fact we had very little documentation, we were highly impressed.

First of all there is no wiring up to do. When you plug in and switch on the PET, after only a couple of seconds it's ready for action. No BASIC to load from tape, it's already in there. This is one great headache reliever if the program you've written bombs out and writes all over memory in selected vital (of course) locations. No waiting for a 20 minute BASIC reload. 14 K of built-in software is worth ten times its weight in silicon.

So after "power up" you can start programming. (more about that under SOFTWARE) or load from a cassettee the software you want. Presumably in the future, taoes for various useful software packages will be widely available, so you will infrequently need to write your own, but it's lots of fun so you'll actually want to.

## Taped

Upon entering the LOAD instruction, the machine tells you to stick the tape in and press the "PLAY" button. Here's where a compromise has been made. Although the PET can switch the transport motor on and off, it tells you to do the muscle work. Commodoare have saved money by using a standard (cheap) cassette recorder, which is sensible for the consumer. market they are aiming at.

The cassette feature worked quite well, although a couple of demonstration tapes did not load properly (we suspect over-worn tapes due to heavy use). One feature we would have liked, (which could not have been included without a large extra cost) would be a fast forward search. Without it the machine took a long time to find a program if the tape was started at the beginning and the program happened to be near the end. For thic reason it seems most economical (time wise) to only use C30 cassettes (these are thicker and stronger anyway) and then record only one or two programs near the start of each side, unless programs are recorded in some long sequence in which they are to be used, or for storage of little used data etc.

## BASIC

Commodore claims that their full floating point BASIC is the fastest yet implemented on a microcomputer. Here are the statements and features included.

Standard Dartmouth BASIC Statements: LET, READ, PRINT, DATA, IF, THEN, FOR, NEXT, DIM, END, GOTO

Extended BASIC Statements: RESTORE, REM, GET, GOSUB, DEF, RETURN, STOP, STEP, INPUT, FN, ON . . . GOTO, ON . . . GOSUB

Scientific Functions: SGN, INT, ABS, SQR, RND, SIN, COS, TAN, ATN, LOG, EXP, PI

Logical Operators: AND, OR, NOT

Operation Commands: RUN, NEW, CLR, LIST, CONT, FRE

Formatting Functions: $T A B, P O S, S P C$
Machine Level Statements: PEEK, POKE; Allow the user to examine and store at specific memory locations. USR, SYS; Link BASIC to machine language subroutines with parameter passing or developmental subsystems. WAIT; Monitors status of a memory location such as an I/O port until specified bits are set.

String Functions: LEFT\$, RIGHT\$, MID\$: Returns substrings (of specified length and position) of string acted upon. CHRS, ASC: CHR\$ returns a character, given a numeric code. ASC returns a numeric code corresponding to a character. LEN: Returns the length of a string. VAL, STR\$: Convert decimal values to numeric strings and vice-versa.

Extended 1/O Statements: OPEN, CLOSE: Control association of a logical file number to a physical device and, optionally, a file name on the device. SAVE, LOAD, VERIFY: Store and retrieve a program, with optional file name, on a physical device. Load allows for program overlay, VERIFY compares contents of memory to stored program. PRINT\#, INPUT\#, GET\#: Allow communication with logical device numbers other than keyboard or screen. GET\# inputs one character. CMD: Permits communication with multiple devices simultaneously.

## variables

TYPES: Real, Integer (\%), String (\$)
NAMES: Variable names are uniquely given as a letter or a letter followed by a letter or digit.

Special Variables
TI, TIS: Time of day. ST: Status word for I/O operations.

## Key features

Another cost saver is the keyboard, being of calculator type construction and feel. All our reviewers disliked the feel (no feedback, you have to look at the screen to see your entry). On the other hand these reviewers were all


Fig. 2. Our first sight of the insides. It's a real pleasure to open up and discover a "prop" already in place to hold up the top. Notice neat cabling and connecting plugs, making for cheap efficient assembly and service.

Fig. 3. Here we've detached some of the parts from their mountings so we didn't have to draw a block diagram. Video monitor board is above.

Fig. 4 The two PET keyboards. We found it quite handy to have a separate numeric pad.

unfair.
The keyboard arrangement has all the rows of keys lined up instead of offset as in a normal keyboard, which would appear to be a big disadvantage to experienced typists. However, one is unlikely to enter vast quantities
of text, and this turned out to be less annoying that anticiapted.

To solve all these problems, it would be a simple matter to replace the main keyboard with a "proper" one. Since there are electronics inside, it would simply
be a question of configuring the switches the same way. How long will it be until some one comes out with a PET soup-up kit, come on, we've started counting . . .!
The video monitor appears to be quite standard, Fig. 4 is a view in the back. The 9 inch CRT provided is with high resolution for the twenty-five lines of forty characters. A brightness control is located at the rear.

## Electronics

For the electronics enthusiasts (well, you're reading this magazine aren't you?) we've got Fig. 2. Most of the important features are shown on this photo, from which a basic idea of the system configuration can be gleaned. All the big chips are plugged into sockets, something nice to see, especially for ROMs. When's APL coming???)

The keyboard is accessed via a sixteen line Peripheral Interface Adapter ( 6520 like a 6820 for Motorola fans) which we expect uses eight outputs and eight inputs for decoding sixty-four keys, and then also a couple of the PIA 'handshaking" lines for the rest of the work. In other words most of the keyboard decoding is done by software.

The cassette recorder as previously mentioned is a customised standard one, with a new PCB inside full of electronics suitable for data recording. In fact two cassette interfaces are included (second cassette recorder available later) with input, output, and motor on/off lines. This uses six lines of the sixteen on the second 6520 PIA (we assume) leaving ten lines, eight for paralel 0 and two serial, at TTL levels. Both cassettes record at 1000 baud, but using built in software any program is recorded twice in series for reliability, cutting the effective rate of 500 baud. Thus an 8 K program takes about two and a half minutes to load.

In addition, the PET is probably the first popular micro-computer to use the IEEE 488 bus for instrumentation communications, making it compatible with many existing and future digital instruments, printers etc.

A final note of the hardware. Unlike some of the early hobby computer equipment the inside of this machine looks like it's built for business, and for the manufactureer to stay in business. Constructed with quality electronics, and chassis and PCB arrangement for easy assembly and service.

## Software

As stated before, one of the beauties of the PET is its built in ROM - full of software. The 14 K includes 8 K BASIC, 4 K Operating System, 1 K Machine Language and 1 K Diagnostic routine, according to Commodore's literature.

First there's 8K of "extended" BASIC. The correct term is "BASIC interpreter". In simple terms an interpreter is a machine language program which takes your BASIC program as data, "translates" it to machine language subroutines and then executes it. This process actually occurs in a line-by-line translate-execute manner. The term "extended" refers to the fact that many statements are included that are omitted in some versions of $\bar{B} A \overline{S I C}$. Using BASIC means that programming can be conducted in a civilized manner, using statements that are almost readable in English, with base ten numbers. The machine does the work of converting to machine language and eight bit binary arithmetic. As
encouragement to skeptics we've included a description of the language and how (easy it is) to use it.

Commodore claim that thier BASIC is significantly faster than anybody else's which we cannot attest to, but for general use the PET performed admirably as compared to the reasonably typical IBM 360 and 370 interactive, multi-user systems that the reviewers were familiar with. (But we do like APL1/8).

The cassette 1/O, keyboard, video, and other functions are handled by the 8 K operating system. These together with the BASIC are provided in seven $2 \mathrm{~K} \times 8$ ROMs.

Lacking documentation, we can only guess at the other two ROM items provided. The diagnostic routine we assume verifies the operation of the hardware, and uses a LED mounted on the main board as an indicator for this task.

The 1 K machine language we guess refers to an assembly language ("machine language" is what the MPU uses, "assembler" is one step higher, using mnenonics for each instruction, and is generally more readable and useable). There is also a mention of next year's "assembler device", to be plugged into the expansion port for "machine language" (?) programming. The advantage of machine or assembler language programming is increased efficiency of programs over those "interpreted" from BASIC. This could be critical in case of a routine which runs several thousands times in a loop, or for a frequently used function.

## Basic Basic (interactive)

One can think of the PET as operating in one of two modes: "calculator mode" in which the operator asks the machine to execute and give the results of one statement; and "programmed mode" in which the programmer enters a seres of statements, then has the machine execute the entire set at one go. The second mode requires a little more structure for "administrative" purposes.

## Calculator Mode

Known by this name because it is the process of getting a quick answer from a small formula, (similar to calculators), or telling the machine to do one thing.
Example: PRINT 3+4
The machine works out $3+4$ and "prints" the result on the screen.
Another example: LOAD FRED
This causes the machine to obtain a program called "FRED" from the cassette, including giving you instructions about what buttons to push on the recorder.

## Program mode

Problem: Figure out the sum of the integers zero to ten.
Program: $10 \mathrm{~J} \%=0$
20 FOR $1 \%=1$ TO 10
$30 \mathrm{~J} \%=\mathrm{J} \%+1 \%$
40 NEXT
50 PRINT J\%
60 END
The numbers down the side are line numbers which can be any integers, and they keep the lines in order. We have chosen multiples of ten as it makes for easy editing at a later date by inserting line numbers in between if necessary. The variables are $\mathrm{J} \%$ and $1 \%$. The $\%$ signs identify them as integers.

## Graphics

We suspect some pretty creative, intelligent people sat down and worked out the graphics character set on this machine. Figure 4 shows the keyboard and all the characters you can key in

The display system is based upon $8 \times 8$ dot units, which may contain $5 \times 7$ dot upper case letters, or assorted symbols which are so designed to provide very versatile diagram and graphing capabilities. Because the $8 \times 8$ blocks fit together both horizontally and vertically, it means that adjacent lines of text look a little crammed together, but this can be avoided by judicious line spacing if necessary.

The "cursor" is a flashing "element" or chäracter that can be moved about the screen, and it normally indicates the next space to be typed in. After typing in that space the cursor moves to the next adjacent spot and so forth. "RETURN" causes the cursor to move to the first position on the next line, just as a carriage return does on a typewriter. Additional cursor control is provided to move it quickly up, down or sideways. The cursor is useful also for editing and correcting programs already written on screen, with the facility for inserting and deleting characters. When the cursor attempts to move below the bottom of the "page", the screen "scrolls up". That is to say each line moves up one, with the top line disappearing.

In addition to the normal characters, each may be "reversed", that is appear as black on white, which can be used to advantage on same occasions.

Finally, here's our big surprise! If you're been counting keyboard characters, there are a lot less than the 256 possible with the character generator used in PET. Quite by accident we found a wholse set of lower case letters! Very interesting. We're not quite sure what does it but check the pictures for yourself. That still doesn't add up to 256 characters, so there may be yet more we don't know about. In fact keyboards on early prototypes showed a set of graphic symbols that included assorted Greek letters, could they be inside as well? We don't know, but it all depends on what's in the character generator. We wonder if Commodore has lower case letters planned as a future "add-on option?"' For that matter you could always burn your own PROM . . .


## Add-ons

Well that's the basic PET Commodore Systems to intro duce a number of peripherals specifically designed for PET during this year. These include a 24 K memory unit (thought to be dynamic) with its own power supply, a printer and floppy. The fact that PET has the IEE-488 bus and ready access to other data lines means that, with luck, we should see a wide range of add ons at reasonable prices from a wide variety of sources. Many pets (lower case for the furry kind) are bought in an initial burst of enthusiasm but are soon abandoned as sone new copy comes along. More recently this fate has befallen many a TV game

This phenomena is far less likely to apply to a versatile beast like PET but the User's Club Commodore Systems are to set up should go a long way to keeping interest in PET alive well beyond the initial purchase.

The club open to users and, for want of a better phrase, non-users, alike for an annual fee of ten pounds

The club will produce a newsletter and provide a means of exchanging news, information and software on PET type subjects on an international scale

Commodore Systems, although themselves running, the club, hope that the driving force will be the many PET users throughout the world.

## What more can we say?

It looks like computers are starting their march into the homes of the masses, which sounds very science-fiction and scary to some. We hope that the familiarity with computers this may bring to the man/woman/child in the street will reverse the widespread fear of THE COMPUTER as an enemy. Perhaps man will once again feel master over objects. There was after all a time when the automobile was regarded as an evil fire breathing monster, until everybody got one.

Fig. 5. Peeking in the back of the video monitor (left) reveals a fairly standard set of works.
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## BY EXECUTING THE STATEMENT:

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a lternate them in one program.

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\title{
WHITE LNE FOLIOWER
}

ETI's Project Team present here a project for either the modeller who wishes to go electronic, or the electronic enthusiast who wants an excuse for crawling around the carpet all weekend ...


THE IDEA OF A SLOT CAR that doesn't need a slot is not new - - in fact, sophisticated systems based on inductive loops have been used in large factories for some years. This project is at the other end of the complezity scale, and uses a simple light/photocell combination to follow a white line. The electronics involved make up a simple feedback control system - as soon as one photocell sees more light than the other, the differential amplifier applies a correcting voltage to the steering servomotor and so the model steers itself back on to the line.

We are not sure whether to class this project as a toy or as a serious experimental project. Certainly, the basic project makes a great toy, but there is tremendous scope for experimenting and 'tuning' the control circuitry. Like all control systems, this one displays a characteristic called 'damping' - if the system is overdamped, the car will steer sluggishly and will have difficulty following anything, except the smoothest curves. If the control circuitry is under-damped, the car


Figure 1. (Above) Circuit diagram of the white line follower unit.

\section*{HOW IT WORKS}

The sensor used to look for the white line is a pair of light dependent resistors (LDRs) which are aimed at either side of the line so that each sees half white half dark. The line is illuminated by a bulb to ensure that the LDRs have a relatively low resistance. If the car is moved off the centre line one LDR will see more 'white' and its resistance will fall. The two LDRs are connected in series across the supply voltage and so the voltage at the junction will vary as the car moves in relation to the line.

This voltage is compared with that set on RV1 by Q1 and Q2, the error signal driving the servo motor in the correct direction to try to eliminate the error. Negative feedback is provided by R10 to reduce the open loop gain', and dynamic feedback is provided by Cl which is used to reduce overshoot.
will oscillate from side to side on curves - this may also be set-off by small deviations on the straights.

The ideal situation is to have a 'critically damped' system, which has just the right combination of characteristics to respond quickly on curves without oversteering. This can be achieved by theoretical analysis, using techniques like Nyquist's Criterion, but it's mare fun to tune ciy trial and error. The damping is a factor of the photocell spacing, the amplifier gain and the servomotor characteristics.


Figure 2. Component overlay. See text for details of mounting the board and servo motors within the model.

You can have a lot of fun racing these cars, especially since there is quite a bit of scope for tinkering and tuning them. The layout of your race track should include both smooth and tight curves - you may have problems with figure-8's that cross at anything but right angles. And of course you can time races with your ETI stopwatch!

\section*{Construction}

The mechanical side of constructing the car we must leave to the individual reader. As we were more
interested in the electronics side of the project we tended to take short cuts as far as the mechanics go. The car we used was a Tamiya Porsche 935 Turbo, a model specifically designed for radio control applications but ideal for our application. No doubt the model enthusiasts amongst you will know of many other models that would prove suitable.

We cheated with the steering mechanism as well by using a commercial servo motor stripped of its electronics to provide control of \(>\)

\section*{PARTS LIST}


On the right the Porsche with its lid off. The PCB is mounted behind the drive motor, which is sitting between the rear wheels. The servo is sited between the front wheels, and the board upon which we put the LDRs and bulb can be seen on the front of the chassis.

\section*{BUYLINES}

Purchasing the electronic components should prove straightforward. The mechanical parts are, as we say in the main text, a matter for the individual constructor.


\section*{PROJECT:Line Follower}


\begin{abstract}
Above: the eyes of the animal. The bulb lies right between the LDRs. On the right: the foil pattern for the white line follower circuit, shown full size.
\end{abstract}
the steering. Again for those with a mechanical bent, a standard model motor (one that will operate reliably on 1.5 V ) driving the front wheels via suitable links is probably the best bet.

Ideally the sensors should be mounted in front of the wheels and should move with them so that when the wheels turn to the right the sensor does likewise and visa versa (another test of mechanical ingenuity).

The LDRs were housed in short lengths (about 10 mm ) of cardboard tube to act as a shield and were spaced about 15 mm apart (we used a 12 mm wide line) with the bulb mounted between them.

Electrically the components can be built onto the PC board described which can be mounted somewhere in the car. We used separate batteries for the electronics and ran the bulb off the main batteries, to keep the electronics supply more constant.


\section*{Experimenting}

Using different motors/gear ratios some changes to the electronics will probably be found necessary. These would mainly involve C1, R1 and R10. Increasing R10 or reducing R1 increases the DC gain, while increasing C1 increases the dynamic damping to reduce overshoot. Track width may also be experimented with as well as LDR spacing.

En,

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\section*{OP-AMPS PART 3}

\section*{Continuing along Tim Orr's circuit strewn path through op-amps}

\section*{Simple Musical Chime Generator}

The circuit shown is that of a multiple feedback bandpass filter. The preset is used to add some positive feedback and so further increase the 0 factor. The principle of operation is as follows. A short click (pulse), is applied to the filter and this makes it ring with a frequency which is its natural resonance frequency. The oscillations die away exponentially with respect to time and in doing so closely resemble many naturally occuring percussive or plucked sounds. The higher the \(\mathbf{Q}\) the longer the decay time constant. High frequency resonances resemble chimes, whereas lower frequencies sound like claves or bongos. By arranging several of these circuits, all with different tuning, to be driven by pulses from a rhythm generator an interesting pattern of sounds can be produced. There may be some stability problems when high 0 or high frequency operation is involved. To achieve better performance, an op-amp with a greater bandwidth than the 741 should be used. Alternatively, a different structure, such as a state variable fiter could be used. Os of up to 500 can be obtained with this latter circuit.


\section*{Variable Markspace Squarewave Generator with Automatic Level Adjustment}

By putting a triangle wave into one input of a comparator and a manually controlled DC level into the other, it is possible to generate a variable ratio mark/space square wave. However, if the amplitude of the triangle varies then so will the markspace ratio. Alternatively, if you want the manual control to produce a very thin waveform at one end of its travel, then you will probably need a preset and a very stable triangle amplitude. However, this circuit solves these problems. The DC voltage is generated by a peak voltage follower, IC1 driven by the triangle itself. Thus the circuit tracks the peak voltage level. Secondly, only \(97 \%\) of this voltage is ever fed to the comparator, IC2, and so at the end of the markspace pot, a 60:1 ratio square wave is.generated. At the other end of the pot the ratio is 1:1. A 748 is used as the comparator because it has more bandwidth than the 741. As the frequency of the triangle increases, it may be necessary to use an even faster op-amp for IC2 or even a comparator.

\section*{Simple Triangle to Sinewave Converter}

A simple way of converting a triangle to a sinewave is shown. The logarithmic characteristic of the diodes is used to approximate to that of a sine curve. The distortion to be expected is of the order of \(5 \%\). However, the distortion may be tolerable if the sinewave is only used to generate audio tones.



\section*{Noise Generator}

The zener breakdown of a transistor junction is used in many circuits as a noise generator. The breakdown mechanism is random and so generates a small noise voltage. Also this voltage has a high source impedance. By using the op-amp as a high input impedance, high AC gain amplifier, a low impedance, large signal noise source is obtained. The preset is used to set the noise level by varying the gain from 40 to 20 dB .

\section*{Logarithmic Voltage to Voltage Converter}

The output voltage is logarithmically proportional to the input voltage. The difference between this circuit and the previous is that the exponentiator is in the feedback loop of the op-amp and hence the mathematical function has been inverted. The circuit is useful for performing true logarithmic compression or for converting linear inputs into dBs.


\section*{Exponential Voltage to Current/Voltage Converter}

The circuit shown converts a linear input voltage into an exponential current or voltage. This type of circuit is used in music synthesisers to change linear control voltages into musical intervals. That is, if the circuit were used to control an oscillator, input increments of 1 V would change the pitch by one octave. The exponential characteristics of a transistor are exployed to generate the correct transfer function. Q1 and Q2 are a matched pairs of transistors, preferably a transistor dual. IC1 maintains Q1 at a constant current. Thus, the op-amp serves only to bias the emitter of the second transistor Q 2 into a suitable operating region. The purpose of Q1 is to generate this bias voltage. The base emitter junction of a transistor has a high temperature coefficient ( -1.9 \(\mathrm{mV} 1^{\circ} \mathrm{C}\) ) and so the reason for using a matched pair is to use the first transistor, Q1, to provide temperature compensation for the second.

\section*{Musical Envelope Generator and Modulator}

A gate voltage is applied to initiate the proceedings When the gate voltage is in the ON state, Q 1 is turned on, and so the capacitor \(C\) is charged up via the attack pot in series with the 1 K resistor. By varying this pot, the attack time constant can be manipulated. A fast attack gives a percussive sound, a slow attack the affect of 'backward' sounds. When the gate voltage returns to its off state, Q2 is turned on and the capacitor is then discharged via the decay pot and the other 1 K resistor, to ground. Thus the decay time constant of the envelope is also variable.

This envelope is buffered by IC 1 . a high impedance voltage follower and applied to Q3 which is being used as a transistor chopper. A musical tone in the form of a squarewave is connected to the base of Q3. This turns the transistor on or off and thus the envelope is chopped up at regular intervals, the intervals being determined by the pitch of the squarewave.

The resultant waveform has the amplitude of the envelope and the harmonic structure of the squarewave. IC2 is used as a virtual earth amplifier to buffer the signal and D1 ensures that the envelope dies away at the end of a note.


\section*{Transistor Used To Turn An Op Amp On Or Off}

When transistor Q1 is switched off, the circuit behaves as a voltage follower. By applying a positive voltage to the emitter of Q1 via a 10 K resistor, the transistor is made to turn on and go into saturation. Thus the lower end of R4 is shorted to ground. The circuit has now changed into that of a differential amplifier (see fig. 7). but where the voltage difference is always OV. Now as long as the resistor ratios in the two branches around the op amp are in the same ratio then there should be zero output. A 4 K 7 preset is used to null out any ratio errors so that the 'OFF' attenuation is more than 60 dB . The high common mode rejection ratio of a 741 enables this large attenuation to be obtained

\section*{Fast Symmetrical Zener Clamping}

The problem with using two zeners, back to back in series to get symmetrical clamping are one, the knee of the zener characteristics is rather sloppy. Two; charge storage in the zeners causes speed problems and Three: the zeners will have slightly different knee voltages and so the symmetry will not be all that good. The circuit overcomes these problems. By putting the zener inside a diode bridge then the same zener voltage is always experienced. The voltage errors due to the diodes are much smaller than those due to the zener. Also the charge storage of the bridge is much less. Lastly by biasing the zener on all the time, the knee appears to be much sharper.


To be continued

\title{
GAS MONITOR
}

\section*{ETI Project Team present a machine which nose when you're fuming, and which can be suitably alarming. Particularly useful to boat owners, and of value to all needing to keep an eye on the gases.}

PETROL VAPOUR, closed space and electrical sparks are not ideal companions. Many a boat has been destroyed when the owner has switched on the ignition without realising there had been a petrol leak and the vapour content in the engine compartment is at a dangerous level. Unfortunately the circumstances also lead to injury and loss of life. Therefore any system which can prevent this is of great value

This unit is designed to meet this requirement and uses a semiconductor gas detector (TGS cell) to monitor the atmosphere in the engine compartment and either prevent the engine being started or shut it down if a high vapour concentration occurs during operation.

\section*{Construction}

This is relatively easy if the printed circuit board is used and the wiring diagrams are followed. Some precautions should be taken if the unit is to be used in a boat to prevent corrosion. The rear side of the board should be coated with a cellulose spray (dope, nail polish, etc.) and the box, while having to be near the control panel, should be shielded from direct spray. Although we have used a separate box the unit can be mounted behind the control panel if desired.

A small heatsink (about 25 mm square aluminium) should be bolted on to IC1 to keep it cool.

The relay should be capable of handling up to 5A and should have a 12 V coil with a resistance of over 100 ohms.

Obviously the sensor must be mounted in the engine compartment and while it must be in free air it must also be protected against mechanical damage.


\section*{Installation and Adjustment}

The sensor should be mounted in a position where vapour may be expected and should be mechanically protected against damage. The
connection to the sensor should be via a four core cable (on long runs use a shielded cable).
Note that the sensor is symmetrical in layout and also the fact that it will fit into a standard seven pin miniature valve socket.


\section*{HOW IT WORḰS}

This project is designed primarily to monitor the concentration of volatile gases inside the bilge of petrol-engined boats. The circuit provides an electrical cutout which prevents the engine from being started if fumes are present and also will remove all electrical power if fumes become present at any time.

The unit acts as a master switch and due to its warm up requirements, a two minute delay occurs on switch on. Two indicator lights indicate either "safe" or "fail" condition and in the initial warm up period both lights are on. The initial timing is performed by Cl and IC2. With the main switch off there is +12 V across C1. When it is switched on the
capacitor is allowed to discharge through R1. IC2 compares the voltage on Cl with that on pin 3 (about 3 V ). During this period the output of IC2 will be about +2 V .

IC1 is a 5 V regulator and supplies the power for the heater of the sensor. The sensor's resistant element is in series with RVI and this voltage is compared to the voltage set by R4/R5.

The transistor Q1 gives a fall safe operation and if the sensor is not connected this transistor will be off giving +5 V on pin 3 of IC3. Resistor R8 ensures that the voltage on pin 2 will always be slightly less than +5 V .

If vapour is present the sensor resistance will be low and the output of IC3 will be high. During the first two minutes the diodes D1 and D2 prevent the feedback loop (R11) operating. After two minutes if the output goes high the reference voltage on pin 3 of IC3 will go above 5 V and therefore the IC will latch in that position

The relay is operated by Q4 and for it to close the output of IC3 must be low (no vapour) and also the output of IC2 must be high (more than two minutes after switch on). If the unit does switch off, or prevents initial switch on, it must be switched off and then on again (after clearing the fumes) and the two minute delay operates again.

The only adjustment is the sensitivity control and this is set by bringing a small container of petrol near the sensor and ensuring it operates. The adjustment should be as sensitive as possible without giving false operation.

\section*{Other Applications}

By suitable choice of sensor from the TGS range (see data sheet this issue) the basic electronics; with perhaps a few modifications, can be used for a number of other applications - for example switching on an extractor fan if the carbon monoxide level in a garage reaches a dangerous level. No doubt numerous other applications for this project will occur to you.


Fig 2. PCB pattern foil side shown full size (52 \(\times 92 \mathrm{~mm}\) ).

Fig 3. Project component overlay and wiring diagram.

SENSOR

\section*{PARTS LIST}
\begin{tabular}{|c|c|}
\hline \multicolumn{2}{|l|}{RESISTORS} \\
\hline R1 & 3 Mg \\
\hline R2 & 1 M \\
\hline R3 & 6 k 8 \\
\hline P4 & 47k \\
\hline R5,8,11 & 100k \\
\hline R6 \({ }^{\text {a }}\) & 12k \\
\hline R7 & 33k \\
\hline R9 & 15k \\
\hline R10.12 & 8k2 \\
\hline R13 & 5k6 \\
\hline R14,15,16 & 1 k 8 \\
\hline \multicolumn{2}{|l|}{POTENTIOMETERS} \\
\hline RV1 & 100k vert. preset \\
\hline \multicolumn{2}{|l|}{CAPACITORS} \\
\hline \(C 1\) & 22 u 16 V Tantalum \\
\hline C2 & 330n Polyester \\
\hline C3 & 10u 16 V Electrolytic \\
\hline C4 & 100u 25 V Electrolytic \\
\hline \multicolumn{2}{|l|}{SEMICONDUCTORS} \\
\hline IC1 & 7805 \\
\hline IC2,3 & LM301A \\
\hline 01 & BC548 \\
\hline Q2,3,4,5 & BC214 \\
\hline \multicolumn{2}{|l|}{SENSOR} \\
\hline TGS & 812 or 813 \\
\hline \multicolumn{2}{|l|}{LAMPS} \\
\hline LP1. 2 & 12 V 100 mA \\
\hline \multicolumn{2}{|l|}{MISCELLANEOUS} \\
\hline Relay to suit PCB as patter & olication ( min res 100R) box etc. \\
\hline
\end{tabular}

\section*{BUYLINES}

Watford Electronics will supply a kit of parts for this project for \(£ 11.95\) less box which should be selected to suit the constructor's particular application, i.e. the expense of a die cast box would not be warranted unless the device needs to be waterproof.

Watford will also supply all parts separately, e.g. the sensor plus.



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THE 7490 DECADE COUNTĒR is a single-chip counter containing four flip-flops and various gates, which are arranged so that frequency division and decimal counting can be carried out. To make the counter more versatile, one flip-flop is separately connected so that it can be independently used as a scale-of-two counter, and the remaining three flop-flops are gates so that they act as a scale-offive. The two sections of the counter can be connected together in different ways, either as a divide-by-ten circuit, or as a decimal counter with BCD outputs.


Figure 1. Pinout of 7490 Decade counter.


\section*{Twos Into Tens}

BCD - meaning Binary-CodedDecimal - is a form of binary code which is particularly useful if decimal numbers have to be displayed. In a BCD count, each figure of a decimal number is represented by its binary equivalent, so that the number 85 (decimal) becomes 10000101 . binary 8 and binary 5 . Although more convenient, because each BCD counter can then drive a display unit, this form is longer than a pure binary number (binary \(85=1010101\), only seven figures), and BCD numbers are not so simple to add and subtract as pure binary numbers.

\section*{BCD in Practice}

Connect the power supplies to the 7490 with pin 10 to earth and pin 5 to +5 V . Pins 2 and 7 should also be


Figure 2. Connections for frequency division by ten - note that the symbol does not show the true pin positions.
Figure 3. Connections for BCD counting, with reset switch. The reset pin must be kept at logic zero for counting, and taken to logic 1 for reset, so that an inverter must be used along with the pushbutton switch.
earthed for most of the experimental work in this section, although we may use pin 2 later for resetting to zero. Now connect LEDs and their limiting resistors, using the spare pads on the board, to Oa on pin 12 and Od on pin 11. Connect the clock pulse from the slow oscillator to input A (pin 14) and by watching the clock LED and the LED connected to pin 12 (Qa), note the action of this section of the counter.

Switch off, transfer the clock pulse input to input B on pin 1, and switch on again, watching the clock LED and the Od LED on pin 11. Note that the counter will operate only if the reset pins are earthed. There are two pairs of reset pins, each pair being inputs to an AND gate which operates the reset. Pins 2 and 3 are the reset to zero pins, and earthing either of them enables
the counter. If both are allowed to float to logic 1 , or are taken to logic 1 , the counter resets to zero. Pins 6 and 7 also act through an AND gate, but with both high the reset is to BCD 9 (1001) rather than to zero.


Figure 5. Arrangement of segments in a seven-segment display. The decimal point, which may be on the other side of the figure, is always energised separately, often from a range switch.

\title{
BY EXPERIMENT PART 7
}


Figure 4. A terminated count. At some stage in the count, the gate will force the counter to reset. When does this happen?


To use the 7490 as a frequency divider (Fig. 2), we connect Od (pin 11) to INa (pin 14) and take the clock pulse to INb (pin 1). The output will appear at Qa, on pin 12, and the state of this output is monitored by an LED already. Connect up the clock pulse from the slow oscillator on the board, and by counting pulses, confirm that the correct division ratio is being obtained.

For a BCD count, the connections must be changed around (Fig. 3). We now need LED indicators on the Qb (pin 9) and Oc (pin 8) outputs as well
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|c|c|}
\hline & \(\mathbf{A}\) & \(\mathbf{B}\) & \(\mathbf{C}\) & \(\mathbf{D}\) & \(\mathbf{a}\) & \(\mathbf{b}\) & \(\mathbf{c}\) & \(\mathbf{d}\) & \(\mathbf{b}\) & \(\mathbf{f}\) & \(\mathbf{g}\) \\
\hline 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\
\hline 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 \\
\hline 2 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 0 \\
\hline 3 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 \\
\hline 4 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 1 & 0 & 0 \\
\hline 5 & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\
\hline 6 & 0 & 1 & 1 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 \\
\hline 7 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\
\hline 8 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\hline \(\mathbf{9}\) & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\
\hline
\end{tabular}

Figure 6 (left) Truth table for figure and character displays.
Figure 7 (above). Truth table for a common-anode display, figures only.
as on Qa (pin 12) and Qd (pin 11), and the cross-connections are different, with Oa connected to input B and the clock input taken to input A on pin 14. Label the LEDs as A, B, C, and D, and switch on, noting the values at each stage of the count. Use de-bounced switch as a clock supply if the oscillator is too fast to follow. Note that in the circuit of Fig. 3 a reset switch has been used; because we are using push-to-make switches, an inverter must be used as shown.

Because the 7490 is on a single chip it may be convenient to adapt it
for counts of less than 10, rather than use separate flip-flops. This is made easier by the arrangement of the reset lines, connected through AND gates. Ignoring the reset-to-nine pins, we can arrange for pin 2 to be driven by a gate whose output must be zero during the count, rising to 1 at the end of the count. Pin 3 must be kept high, or the count will not be interrupted.

Try the circuit of Fig. 4 - can you work out what the count figure will be? Connect up and try the circuit out.

\section*{Displays}

Though several other forms of display exist, the most convenient type for use with TTL circuits is the sevesegment LED display. The type used for this board, the BI-PAK DL747 (Jumbo) is one of the largest displays of this type available at the time of writing, and has been selected from the point of view of easy reading at a distance. If any other type is substituted, care will have to be taken with the pin connections, since there are several pinout standards for this type of display.

As the name suggests, the sevensegment display consists of seven LEDs made in one chip in the arrangement of a figure-of-eight, as shown in Fig. 5. The letters allocated to the strips are also shown (fortunately these are standardised).

Looking at the arrangement of the segments, we can draw up a table of the segments that will have to be activated (ON) for each number we want to display. Fig. 6 shows such a


Figure 8. Pinout of 7447 BCD to \(7-\) segment decoder-driver.
table for the numbers 0 to 9 , and aiso some of the other characters which can be obtained. We now have to translate this ON / OFF table into terms of logic 1 and 0 .

The next step depends on the type of display that is being used - common cathode or common anode. As the name suggests, the common cathode display has all of the LED cathodes connected together to logic 0 , and each anode must be taken to logic 1 to be illuminated. To prevent excessive current flowing - because the normal forward voltage across the LED is less than the +5 V of the logic circuits - we must wire a limiting resistor in the connection to each anode. We cannot use one single resistor in the cathode lead, as this would cause the brightness of the display to alter according to the number of segments lit.

The other possibility is to connect the anodes of the LEDs together and take the cathodes out to separate pins. In this common-anode type of display, the segments will be lit when their respective cathodes are at logic zero, and once again limiting resistors must be used between each cathode and the TTL driving stage.

The type of display specified for this board is a common anode type, with several of the pins on the display connected to the common anode. Only one of these pins need be connected to the 5 V line.

\section*{Decoders}

To obtain a decimal readout from the BCD output of the 7490 counter, a decoder stage is needed with the truth table shown in Fig. 7.

The type used here is the \(7 \overline{4} \overline{47}\) BCD-to-seven segment decoder/driver, which has output stages of transistor collectors with no loads. In this eay, the combination of LED and limiting resistor acts as load for the collectors of the output transistors in the 7447 :

Care should be taken that the outputs of a 7447 are never connected directly to the +5 V line, as excessive currents could flow if the decoder were operated.

In use, the segment output pins of the 7447 are connected through the limiting resistors to the segment pins of the display. The values of the limiting resistors used will determine the brightness of the display. For the 7447 display we can use 150 R res-
istors, but 470R resistors have been specified on our board to ensure long life and to cut down current consumption. If other displays are used, 470 R should also be suitable - in general the small displays need less current, and so larger values of limiting resistors should be used than with larger displays. If a common cathode display, such as the MAN-3 types, had been used, the 7448 decoder would have been needed.

Now connect up the display and the decoder on your board, noting the connection diagram of Fig. 9. In the


Figure 9. Connection of 7447 to display not that the arrangement on the board is tidy as the diagram would suggest!
prototype boards, the very small resistors used for limiting could be passed under the body of the display, so avoiding long paths around it. The +5 V supply is taken to pin 16 of the 7447, and earth is taken pin 8. The outputs of the 7447, all on the side facing the display, and marked on the circuit diagram with small letters, are taken through the 470 limiters to the correspondingly lettered pins of the display. The inputs indicated by the capital letters \(A, B, C\) and \(D\) on pins \(1,2,7\) and 6 of the 7447 are for the BCD input from a 7490 and should be connected to the appropriate Q outputs from the 7490 counter.

\section*{Testing and Blanking}

Note that pin 3 of the 7447 is labelled "lamp test." Taking this pin to logic 0 illuminates all segments of the display irrespective of what stage the count has reached, and is a useful check on the operation of the display. For example, an operator can check that a steady display of is not just with two segments faulty.

Pins 4 and 5 on the 7447 are for blanking, used mainly when the display is one of a set, to suppress zeros occuring before the first significant figures and after the last one. When pin 4 is low, the display is blanked out, though counting is unaffected.

\section*{To be continued}

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\title{
200W AUDIO AMPLIFIER
}


NO LITTLE TIME AGO we published a project for a 100 W guitar amplifier. It has since proved to be our popular project ever, and shows no signs of lying down to die in a respectable manner.

The main complaint about this machine has been that it is not powerful enough - something which our burst eardrums have repeatedly failed to grasp. In an attempt to please all the people all the time (as usual) we decided to re-do a design completely, uprating the spec to 200 W , and generally improving the quality as far as we could to make the amplifier suitable for high quality domestic usage

The subjective quality of the design is such that it will not be out of place in the highest of hi-fi set-ups, and its power enables it to drive the more inefficient monitors which seem to dominate such spheres.

\section*{SPECIFICATION}


NOTE:
Q1,02 ARE 2N5401
Q3,04 ARE 2N5832
05,06,07 ARE BF460
Q8,09, Q10 ARE 8F463
Q11 IS BC212L
012 IS BC182L
Q13,Q17 ARE 8D420
Q14, Q18 ARE MJ15004
Q15,019 ARE BD419
Q16,Q20 ARE MJ15003
TH1 IS VA1056
IC1 IS 7815
IC2 IS 7915
D1-4 ARE 1 N4 148


R16
1 k 1
15 mA


HOW IT WO

\section*{USE OF DIFF. PAIRS}

Extensive use is made in the design of the 'long-tailed' or differential pair. The basic voltage amplification stage is shown in Fig.2. The symmetrical operation of the long-tailed pairs run with equal current in each arm affords the stage low distortion. Differential operation also possesses high linearity, as any non-linearity in \(V_{k t}\) for either transistors is compensated for by the other.


It is normal to operate differential pairs with a constant-current source in the tail, i.e.

Q8, D2 and R22-24 supply Q9 and Q10, and Q7, D1 and R18-20 supply Q5 and Q6. However, at the input of the amp (Q1 and Q2), only very small changes in tail current occur due to the small voltage swings, and a fairly 'large' value resistor, i.e. R5 will suffice. Similarly for R11 and Q3 and Q4. In the output driver stages very large voltage swings will be present and thus a current source is essential to correct operation.

\section*{OPERATION}

For the purposes of operation the amplifier can be considered in four blocks: Two low gain, low distortion voltage amplification stages formed by Q1 and Q2 and by Q3 and Q4 output drivers Q5 and Q6 with Q9 and Q10 and finally the fully complementary output stages which consist of paralleled darlingtons. Discrete devices are employed for their better operating characteristics and speed 2 MHZ . Q15 and Q16 are paralleled with Q19 and Q20 to form in effect a single output 'transistor'. Q13, Q14, Q17 and Q18 constitute

an identical configuration for the other half of the cycle. Fig.3. shows how the four may be combined into ' Qx '.

\section*{OUTPUT TRANSISTORS}

The output types themselves are 250 watt devices, since for reliability the load line of these transistors must lie within the "safe operating" area (Fig 4). This is simply achieved with a pure resistive load, but not with the reactive component of a loudsspeaker added. For a 4 R load instantaneous dissipation in this design has a max of 144 W , but with a reactive load dissipation can rise to 288 W . For these reasons lower power

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A complete reprint of our superb synthesiser design, published with Maplin Electronics (who also supply the parts) This reprint will
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\section*{READER}

Dear Reader, Can you spare a few minutes to help ETI?
Pages 47 and 48 take the form of a reader survey/questionnaire. We want as many readers as possible to fill in and return these to us. As a token of our appreciation we will be giving away 60 ETI T shirts, the lucky readers will be picked at random on the 10th of April. The only reason we ask for your name and address is to send out the T shirts, if you don't want a T shirt and would prefer to remain anonymous please still fill in and return the page -without your name or address.

We have done several surveys in the past, the most recent being in 5\% of the July 1977 issue. Since this we have made several changes in the magazine's format and organization, a lot of the changes being catalysed by careful analysis of the returned questionaires. Yes, we really do care about what you, the reader, thinks!

Thanks for your help,
May the EMF be with you.


\footnotetext{
Please Note: For commercial reasons the Tandy catalogue is only being inserted in most copies of the magazine for U.K. distribution, and the 50p off coupon is only valid at U.K. Tandy Shops.
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\hline GAS MONITOR & & \[
\sqrt{ }
\] & & & \(\sqrt{ }\) & \\
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devices are not recommended for use, unless more are paralleled to share the load.
Because the amp employs low overall feedback ( 14 dB ) the bias current for Q1 is not obtained via the feedback resistor R52 since this would result in a large DC offset at the output. An independent supply is provided, consisting of RV1, R55 and R4. Temperature compensation is provided by TH1

\section*{QUIESCENT CURRENT}

With the employment of long-tailed pairs setting of quiescent current in the output 'switches' QX is somewhat different to a 'normal' design. The function is affected by RV2, which varies the DC between Q5 and Q6 bases. Q5 base will follow the base of Q6 by differential pair action, and similarly Q9 and Q10 this means that the voltage across R49 and R50, which is proportional to Iq will be 'referred back' to RV2. Temperature effects can be removed by keeping Q5 and Q6 as well as Q9 and Q10 at the same temperature, and to ensure this pair is thermally connected to a (different) heat sink.
Regulators IC1 and IC2 are employed to stabilize the 50 -volt rails with respect to the 65 -volt rail for the constant current sources supplying driver stages Q5 and Q6 and Q9 and Q10.
Diodes D3 and D4 protect IC1 and IC2 from any reverse voltages that might be present at switch on.

\section*{OUTPUT PROTECTION \({ }^{2}{ }^{*}\) 名}

Conventional short circuit protection is employed, using load-line limiting, i.e., refer-

ring to Fig 5. The current through R31 will voltage limit the output, and the current through the parallel combination imposes a current limiting function. If either limit is exceeded, Q12 switches on and closes down the drive signal.


Figure 6. (Above) Circuit diagram for the PSU. Note that the transformer is a special unit wound for the 200 W amplifier by Powertran Electronics. Figure 7. (Below) The circuit diagram for the buffer to increase the input impedance of the unit, and provide variable input sensitivity if required.


\section*{HOW IT WORKS -Buffer \& PSU}

THE power supply is a conventional series spass circuit. The basic circuit elements are *shown below. The circuit is essentially an remitter follower, the voltage at the base of the transistor being some 0 V 6 below that set by the zener in the base. The final circuit shown in Fig. 6 shows that Q101 forms the pass element in the +50 v rail while Q103 forms the complementary device in the negative rail. Q102 and Q104 provide increased drive to the pass elements.

The \(\pm 70 \mathrm{~V}\) rails are dreived from a simple bridge rectifier circuits.


\section*{THE BUFFER}

The buffer is a straightforward circuit formed by Q201-203. The buffer provides an high input impedance and allows for two gains, with RV201 at maximum, +0 dB with SW201 open and +10 dB with R209 in circuit. Changing the value of R209 to 470R will give a 20 dB boost when SW1 is operated.
The buffer is powered from the +50 V rail with ZD201 included to provide a 15 V rail.


The PSU can be seen above. The layout of all the major components can be seen with the buffer (in close up below) just appearing at bottom right.

In addition the bodies of diodes D1 and D2 should be in contact with the heat sink of the appropriate group transistors.

The coil L1 is formed by winding thirty turns of self-fluxing polyurethane covered wire (about 26 SWG) around resistor R56 (4R7), soldering the wire to the leadouts at either end of the resistor.

The interwiring of the various completed units should be undertaken by referring to the overlays and the various views of the amplifier chassis designed to show the layout that should be adopted in te completed unit.

\section*{Testing}

Test of the amplifier should commence with satisfying yourself that the power supply is operating correctly before connecting it to the main amplifier. The voltages shown in Table 1 should be present upon switching on the mains.

\section*{TABLE 1}
\begin{tabular}{lr} 
C102 + to \(0 V\) & \(62 V\) \\
C103- to \(0 V\) & \(-62 V\) \\
C109- to C109 + & \(22 V\) \\
C110- to C110+ & \(22 V\) \\
\hline
\end{tabular}

If all is well switch off and please - discharge the four hefty capacitors via a resistor before doing anything further or else you will be in danger of having a lot of energy make its way to earth via you.

Next connect a 1 k 5 1W resistor between Q101c and \(O \mathrm{~V}\) and a similar resistor between \(\mathrm{Q103c}\) and OV . Switch on and hopefully the voltages will be shown in Table 2

\section*{TABLE 2}
\begin{tabular}{ll} 
Q101c to 0 V & +50 V \\
Q103c to 0 V & -50 V \\
PSU pin 101 to 0 V & +72 V \\
PSU pin 106 to 0 V & -72 V
\end{tabular}

Again if OK, switch off and discharge those Cs.

Next replace the 5A PSu fuses with a 12 V 2 W 4 light bulb in series with a 47R \(1 / 2 \mathrm{~W}\) resistor. This


\section*{PARTS LIST~Buffer \& PSU}


\section*{PARTS LIST~Main Amp.}
\begin{tabular}{|c|c|}
\hline 200 W AMP PARTS LIST & CAPACITORS \\
\hline RESISTORS fall \(2 \%\) OW5 Metal Oxide unless & C1 1upolyester \\
\hline stated) & \(\mathrm{C} 2 \quad 47 \mathrm{u} 10 \mathrm{~V}\) electrolytic \\
\hline & C3.5 - 22063 V electrolytic \\
\hline R1 9k1 & C4, 6, 12, 14, \\
\hline R2 10k & 15,17,18 100n polyester \\
\hline R3. 52 470k & \(\mathrm{C7} \quad 22 \mathrm{p} 160 \mathrm{~V}\) polystrene \\
\hline R4 56k & \(\mathrm{C} 8 \quad 5 \mathrm{p} 663 \mathrm{~V}\) polystrene \\
\hline R5 62k & C9 470u 6V3 electrolytic \\
\hline R6, 7 22k & \(\mathrm{C} 10,11 \quad 100 \mathrm{p} 160 \mathrm{~V}\) polystrene \\
\hline R8 3M9 & C13.16 220 n poltester \\
\hline R9, 10, 13, & \\
\hline 21,53,54 100R & SEMICONDUCTORS \\
\hline R11,19,23 1k & \\
\hline R12 213 k & Q1.2 2N5401 \\
\hline R14, 15, 27, 28 IkI IW Carbon Film & 03.4 2N5382 \\
\hline R16.17 15R & Q5.6.7 BF460 \\
\hline R18, 24 330R & 08, 9, 10 BF463 \\
\hline R20, 22 430R & 011 BC212L \\
\hline R25. 26 15R & Q12 BC182L \\
\hline R29.32 120R & Q13.17 BD420 \\
\hline R30, 31 18k & Q14.18 MJ15004 \\
\hline R33, 34, 41, 42 470R & Q15.19 BD419 \\
\hline R35, 39, 43, 47 750R & Q16.20 MJ15003 \\
\hline R36, 40, 44, 48 OR33 2W5 Wire Wound & Q16. 20 MJI500s \\
\hline R37, 38, 45, 46 47R & IC1 7815 \\
\hline \begin{tabular}{ll} 
R49, 50 & OR22 \\
R51. & 4R7
\end{tabular} & IC2 7915 \\
\hline R55 6k8 & 01.4 1N4148 \\
\hline R56 4R7 2WG Wire Wound & D5,6 IN4002 \\
\hline POTENTIOMETERS & INDUCTOR \\
\hline RV1 5k Cermet & L1 see text \\
\hline RV2 200R Cermet & \\
\hline & MISCELLANEOUS \\
\hline \begin{tabular}{l}
THERMISTOR \\
TH 1 \\
VA1056
\end{tabular} & PCB as pattern, thermal fuse, mica washers connecting wire, etc. \\
\hline
\end{tabular}

\section*{Construction}

With a project of this type, while attempting to keep construction as straightforward as possible, the sheer physical size of many of the components precludes mounting them on a PCB. This inevitably leads to more constructional work and hence to a greater scope for error Care taken during assembly and careful checking at each stage can save time and (plenty of) money later on. With this in mind please read this section thoroughly and study the photographs of the finished unit before starting constructional work.

The first stage in building up the amplifier is to mount all the passive components and links on the three PCBs according to the appropriate overlay. Needless to say all soldering must be or the highest standard and checks should be made to ensure that there are no solder bridges present.

\section*{Power And Its Place}

The four power transistors associated with the amplifier and the two required for the power supply are mounted on cooling brackets as shown in our photographs. Care should be taken to ensure that there are no burrs around the holes in the heat sink that might damage the mica washers that must be fitted to all six transistors. In order to aid heat flow from the transistors silicon grease should be smeared on both sides of these mica washers.

Note that solder tags are required to connect to the cases of the two PSV transistors but not on the main amp's devices where connection is made via the PCB.

\section*{Bracketed Cooling}

The thermal cutout is also fitted to the cooling bracket on which the amplifier's output devices are mounted.

As can be seen in the photos of the amplifier board the following groups of transistors are thermally connected

Q5 Q6 and Q7
Q8, Q9 and Q10
Q13 and Q17
Q15 and Q19


Below can be seen the main circuitry. The heatsinking arrangements of the various output devices can be seen as well as the mounting of C101-C104. Above - an overall view of the amplifier.


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The photograph above shows a version of the amplifier built with power level meters. The circuitry required to provide this facility is shown below.

provides a convenient method of monitoring and limiting the current drawn by the amplifier. Note that if the main amplifier is working correctly you will see an initial surge of light from the bulb which should then die away.

Before connecting power to the amplifier set RV1 to its centre position RV2 fully clockwise and RV201 fully anti-clockwise.

Switch on and check that the voltage between \(O V\) and the R56 and R51 junction is near zero. RV1 should be adjusted until this voltage is exactly zero.

Next RV2 should be rotated until the lamps in the power supply lines start to brighten.

If everything is still going according to plan, replace the power supply fuses and adjust RV2 until the voltage between Q14c and Q16c is 20 mV . Resetting the output to zero volts with RV1 completes setting up.

Leave the amp on for about half an hour and recheck all voltages, making any necessary adjustments connect a speaker to the output and 'test drive' the amp for about an hour. A final check of all voltages and your amplifier is ready to go into service. ET

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\section*{Aleatronicestation} What to look for in the May issue: On sale April 8th

\section*{ROBOT \\ ISSUE}

To many people robots are in the realm of fantasy, forming part of the visionary world of science-fiction. Sci-Fi authors paint scenarios about possible future existences: Dr Peter Sydenham, Electronics Today International special correspondent, and one time robot builder himself, presents the factual side of robots.

Features discuss what robots are, their purpose and structure. An exclusive interview with Britain's robot expert, Professor Thring of Queen Mary College, London, will provoke thought about the future. Dr Mike Larcombe, Head of the Warwick University Robot Laboratory, will explain the state of the art of robot brainpower and combines with Dr Sydenham to provide a guide to constructing professional-quality robots.



In the beginning there was tape phasing, and it was good but cumbersome. Then ETI said, 'Let us use new technology, and it was good - and compact. Next month we publish a state-of-the-art phaser/flanger using the Mullard TDA1022 delay line, which is a charge coupled device. So if you want really good effects, don't miss the May edition of ETI!

\section*{—MK2 ELECTRONIC IGNITION}

The Mk 1 was originally published in September 1973, and many thousands have been successfully built since then. Stirling Sound have taken the design a step further, refining the component values and designing a PCB (instead of the tagboards). So if you missed it first time round, you can investigate the Mk 2 with added vitamin B and marilibone jelly!

\section*{SPOCK RADIO}

Most illogical! How did primative Earthlings design such a machine?" Beam down next month for


\section*{TI59 Review}

The Texas T159 is one of the latest pocket card programmables to challenge the calculator market. With its phenomenal programming power - up to 960 program steps memory position, software in plugin pre-programmed modules and pocketability, this calculator will undoubtedly set the pace for the next few years. Next month we shall be looking critically at the TI59 and bringing you all the facts.

HELPIIG


Quite a while ago, we ran a competition in conjunction with the Royal National Institution for the Deaf - next month we publish the winning entry. Although designed as an integrated system, we think the separate units will find applications in other areas besides remote warning for the deaf.

Articles mentioned here are in an advanced state of preparation but circumstances may affect the final contents.

TGS \(812 / 813\) GAS SENSOR

THE TGS 812/813 are semiconductor gas sensors suitable for general purpose combustible gas detection.

The sensor consists of a ceramic tube onto which is deposited a layer of the semiconductor (mainly tin dioxide) and the electrodes.

In fresh air the resistance of the semiconductor is constant, but as the concentration of the detectable gases increases and comes into contact with the sensor's surface, the resistance of the semiconductor changes.

In order to increase the response time of the device, the sensor is heated to a temperature between 200 and \(400^{\circ} \mathrm{C}\) by a heated coil in the ceramic tube.

The sensitivity characteristics of the TGS sensors are altered by changes in atmospheric temperature and humidity. The detection principle of the sensor is based on chemical absorption and desorption of gases on the sensor's surface, and because these reactions are temperature dependent and water vapour can be considered a gas, the effects of temperature and humidity changes cannot be eliminated from the sensor. These effects can, however, be reduced by suitable circuit design (see Fig 7).


Fig 2. Sensor resistance (Rs) changes with different concentrations of gas.

\section*{The}

TGS812/813
sensors
are
available
from:
Watford
Electronics
33-35
Cardiff
Road
Watford
Herts.


Fig 1. Mechanical structure of types 8128813 sensors, in \(\mathbf{m m}\).
\begin{tabular}{|c|c|}
\hline TYPE NO & TGS 812/813 \\
\hline \begin{tabular}{l}
TEST CONDITION \\
(A) Circuit Voltage (VC) \\
(B) Heater Voltage (VH) Heater Power Dissipation (PH) \\
(C) Load Resistance (RL)
\end{tabular} & \begin{tabular}{l}
10 V (A.C. or D.C.) \\
5.0V (A.C. or D.C.) \\
Approx. 650 mW . \\
4K
\end{tabular} \\
\hline WARM-UP TIME & Approx. 2 min . \\
\hline HEATER RESISTANCE (RH) & \(38 \pm 3\) \\
\hline SENSOR RESISTANCE (RS) & \begin{tabular}{l}
\[
1 \sim 10 \mathrm{~K}
\] \\
in Isobutane 1000ppm/air
\end{tabular} \\
\hline RATIO OF RESISTANCE & RS in Isobutane \(3000 \mathrm{ppm} / \mathrm{air}\) RS in Isobutane \(1000 \mathrm{ppm} /\) air \(=0.63 \pm 0.05\) \\
\hline
\end{tabular}

Fig 3. Sensor characteristics.
\begin{tabular}{|r|l|l|}
\hline \begin{tabular}{c} 
Sensor \\
type
\end{tabular} & Electrical ratings & Applications and Features \\
\hline 812 & \begin{tabular}{l}
\(\mathrm{V}_{r}:(\) Max. \() 24 \mathrm{~V}\) \\
\(\mathrm{~V}_{\mathrm{H}}: 5.0 \mathrm{~V}\)
\end{tabular} & \begin{tabular}{l} 
General purpose combustible gas detection. \\
Carbon Monoxide detection. High CO \\
sensitivity enables most types of smoke to \\
be detected.
\end{tabular} \\
\hline 813 & \begin{tabular}{l}
\(\mathrm{V}_{r}:(\) Max. \\
\(\mathrm{V}_{\mathrm{H}}: 5.0 \mathrm{~V}\)
\end{tabular} & \begin{tabular}{l} 
General purpose combustible gas detection. \\
Methane detection. High CH: sensitivity \\
makes it suitable for Natural Gas detectors.
\end{tabular} \\
\hline
\end{tabular}

Fig 4. Differences in sensitivities between 812 \& 813.


Fig 6. Circuit for use as a gas leak detector.


Fig 5. Basic test and measuring circuit.


Fig 7. Typical circuit for gas triggered fan.


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15 watts R.M.S. Handling
\(50 \mathrm{~Hz}-15 \mathrm{kHz}\) Frequency Response
\(4 \Omega\) Impedance
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Account Number
Name \& Add̄ress
Signature



\section*{Avoid the horrors of heavenly \(\mathrm{H}_{2} \mathrm{O}\) with our Project Teams}

\section*{RAIN ALARM}

MARCH WINDS AND APRIL showers bring forth May flowers not exactly poetry, more accurate reporting of activity on the weather and horticulture fronts at this time of year. Additionally the bit about April showers is likely to bring forth more than the odd May bud if one of these periods of intermittent precipitation disrupts a busy housewife's washday by relegating her almost dry laundry to the ranks of the wet behind the eiderdown brigade.

\section*{It May Rain!}

It's here that the good guys of the ETI project team come to the rescue with their Rain Alarm. This little fellow might well upstage any canine companion as a housewife's best friend, at least on washday, by giving a warning at the first sign of rain, giving plenty of time to get the washing in before it gets too wet.

The rain alarm should be placed out in the open and a length of two core wire run between it and an eight ohm speaker. We used an old
intercom sub-station to provide a home for our speaker but a car extension speaker or indeed any suitably boxed eight ohm device would be fine.

Any rain falling on the sensor track, formed as part of the PCB (neat innit?), will set off the alarm and produce a distinctive, intermittent bleep-bleep.

\section*{Construction}

Construction is straightforward if the PCB layout shown is used and in the case of this project we would recommend that the PCB is used, as this adds to the attractiveness of the project.

Assemble the components according to the overlay, ensuring that the tantalum capacitor is connected the right way round. If you do not use a socket for IC1, solder pins seven and fourteen before the others (this allows the device's internal protection circuitry to function).


In our prototype we used a value of \(4 M 7\) for R1 which acts as a sensitivity adjustment. This value leads to a 'hair trigger' alarm and the value could well be reduced according to the level of sensitivity required.

When construction is complete and the alarm has been tested the area of the PCB that holds the components should be covered with some suitable non-conducting potting compound - epoxy resin should do - to render it waterproof.

\section*{Power To Your}

Power consumption of the unit is so low when the alarm is not triggered that it was not thought necessary to provide an on / off switch.

While this unit is not as effective as a device to control the weather still working on that one - it should at least prevent some of those washday blues.

ET


Fig. 1. Circuit diagram of Rain Alarm.

\section*{HOW IT WORKS}

THE rain alarm is formed by two gated CMOS oscillators and an audio output stage.

The basic CMOS oscillator is shown in Fig. 2. Upon switch on, with \(C\) discharged, the output of inverter \& will be low, the input to A low and its output high. Capacitor \(C\) will now commence to charge towards supply, the voltage level at \(A\) 's output, via resistor \(R\).


We can consider a CMOS gate to be a comparator that will change output state when the level of voltage at its input reaches a specified value, the transfer voltage (Vtr). usually about half supply. Thus as the voltage on \(C\) increases due to the charge current being supplied by \(R\) there will come a point when the voltage on the input of \(A\) will pass its transfer voltage and the output of B to go high.
At this point the charge on C corresponds to a voltage level of approximately half supply.

As the inverters \(A\) and \(B\) change states the end of \(C\) that was held at 0 volts is now at
supply and the end of \(C\) that was connected to supply via \(R\) is now returned to 0 volts via the same resistor.
These changes together with the charge stored on C mean that the potential across \(C\) is now supply plus the transfer voltage of gate A. This is shown in Fig. 3.


Capacitor \(C\) will now discharge via \(R\) until once again the transfer voltage of \(A\) is reached whereupon the outputs of the inverters will assume their original states.

The conditions are not quite the same as at
switch on because, as can be seen in Fig. 3, the Potential across \(C\) is now a negative value equal to \(A\) 's transfer voltage.

From this point \(C\) charges via \(R\) again to repeat the cycle.
The output is shown in Fig. 3 where \(t_{1}=t_{2}=1.1 R C\) (the time taken for \(C\) to charge (discharge) via R to two-thirds of the maximum value of voltage across it)

In practice, due to the protection networks associated with modern CMOS devices, it is necessary to include a resistor ir series with the input of \(A\) in order to ensure that the voltages across \(C\) are allowed to reach the values shown in Fig. X .
The final circuit diagram (Fig. 1) of the Rain Alarm shows that the inverters are in fact formed from the four NAND gates of a 4011 package. In each oscillator, while one gate is configured as a straightforward inverter, the other has one input that can act as a control input, oscillator action being inhibited if this input is held low.
The first oscillator (ICla and IC1b) has this input tied low via a high value resistor (R1) that acts as a sensitivity control. Thus this oscillator will be disabled until the control input is taken high. Any moisture bridging the sensor track will so enable the output which is usquare wave at about 10 Hz . This in turn will gate on and off the 500 Hz oscillator formed by ICle and ICld.
This latter oscillator drives the loudspeaker via R 6 , the Darlington pair formed by Q1 and Q2 and resistor R7.

PARTS LIST


Fig. 2. Overlay of the section of the Rain Alarm PCB that holds the components.


\section*{PROJECT: Rain Alarm}

\section*{BUYLINES}

The PCB will be available from any of the usual suppliers that advertise in ETI. C1 must be a low voltage tantalum but this component should be widely available. All of the other components are likewise obtainable from most electronic retailers.


\title{
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The MK14 National Semiconductor Scamp-based Microprocessor Kit gives you the power and performance of a professional keyboard-addressable unit -for less than half the normal price! For less than \(£ 44.00\) you can have your own microprocessor. One with a specification that makes it perfect for the engineer who needs to keep up to date with digital systems, or for use in school science departments. It's ideal for hobbyists and amateur electronics enthusiasts, too.

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Phil Pittman, Wireless World, Nov. 1977
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\title{
PROJECT CASINGS
} SURVEY

UNLESS YOU ONLY go in for the assembly of commercial electronic kits which come complete with a ready drilled case and all the nuts, bolts and washers required to produce a project with an almost professional finish, you may never have realised that amateur "electronics" is more than just soldering neatly and knowing the difference between a pretty "C280" series capacitor and a liquorice allsort, but that a little "metal bashing" is also required.

\section*{Casing The Joints}

Electronics can often be a very uninteresting subject and a PCB full of the latest chips is not going to be as interesting to a friend (with little or no electronic knowledge) as would be a (say) simple electronic dice built in a nice neat plastic box. Although some people seem to believe that so long as their latest high power electronic rat exterminator works it doesn't matter if it is housed in a tobacco tin held together with insulating tape and chewing gum. Needless to say this is not the attitude we expect from ETI readers.

A little extra money spent on a few basic tools (small drill and assorted bits, files, nuts, bolts, washers) is really essential (making holes in plastic boxes with your soldering iron is not a good idea). This coupled with a little time spent selecting a rase, can turn a mediocre run-of-the-mill project into an impressive piece of electronic sophistication that you won't feel embarrassed to have lying around when the friends/relatives drop in unexpectedly

Although there are many different cases in a multitude of shapes and sizes, the daunting prospect of choosing a suitable case is eased by the parameters governed by the project itself - is screening required? Will it take a PP9 battery, will it fit in your pocket?

Once a box has been chosen the work begins The first essential is a layout of all the controls, sockets cable exits and any other holes which need to be made. Careful attention should be made to details such as not mounting audio switches near mains switches to avoid hum pick up.

Once satisfied that the panel has been designed to be both aesthetic and functional, drilling can begin. This successfully completed leaves only ene more stage before the project can be fitted into its housing, this is inscribing legends for the controls.

This can be done in a variety of ways (typed labels glued on, dymo type labelling strips ..) but the method which gives the most professional appearance is rub down lettering transfers (a set of transfers suitable for panel marking, including potentiometer scales (rotary and slider) is available from ETI - see the advert in this issue.

If using transfers it is best to either use a clear varnish spray or self adhesive plastic film to prevent the lettering from rubbing off with use

All that remains then is to secure the project in its case, stand back and admire


All-plastic 'Gemini' box and lid in black, grey or white. 110x70x30 at 71p. Supplier B.


Folded aluminium boxes with lid and screws. 20 sizes from \(76 \times 51 \times 25\) to \(304 \times 203 \times 76\). Prices from 49 p to \(£ 2.27\). Supplier B

"Classic'" instrument case. Satin-finished anodised aluminium with textured vinyl-coated steel top \& bottom plates. 6 sizes from \(216 \times 127 \times 63\). Price from £7.94. Supplier B.


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Aluminium plate box in two parts.
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Aluminium two part construction case (type PJ) with front and rear vinyl covered. 7 sizes available from \(127 \times 63 \times 57\) to \(305 \times 158 \times 133\). Prices from 65p. Supplier B.


Two colour plastic case with sloping aluminium panel with mounting points for EUROCARD board. Size \(171 \times 121 \times 75\). Type 75-1798K at £4.53. Supplier G.


16 SWG aluminium, silver hammer finish. 3-piece construction. Type U case in 8 sizes from \(76 \times 76 \times 76\) to
\(381 \times 229 \times 229\) from \(£ 1.50\). Supplier A.


Veropak 49 series cases made of heavy guage PVC clad steel in 5 sizes from 277 \(\times 113 \times 219\) with prices from \(£ 9.70\). Supplier G.


Miniature "Minos" cases in black or white ABS with aluminum panel. Guides for PCBs. 4 sizes from \(65 \times 100 \times 50\) to \(130 \times 200 \times 54\) at prices from 52p. Supplier C.


Flip-top polypropylene boxes with aluminium alloy panell in black or yellow. Two sizes \(130 \times 75 \times 34\) and \(196 \times 127 \times 51\) at approx \(£ 2.05\) and \(£ 2.70\). Supplier G.


5000 series boxes in die-cast aluminium in 5 sizes with slotted guides for 1.5 mm PCBs. Sizes from \(100 \times 50 \times 25\) with natural or hammertone. Price from £1.05.


Two vacuum formed cases in black ABS with aluminium panel supplied, for mounting keyboards. Sizes \(220 \times 276\) (about \(£ 8\) ) and \(550 \times 340\) (price TBA). Supplier G.

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"'G range" cases with sloping visor, in 3 sizes with slide on cover. 3 sizes from \(44 \times 134 \times 123\) from \(£ 4.86\). Supplier G.


Hammered finished 20 SWG zinc-coated mild steel cases in two colours and 4 sizes, from \(101 \times 101 \times 155\) to \(177 \times 152 \times 307\). Supplier E.


Alfa range plastic cases in 3 sizes.
Aluminium panel front \& rear. Sizes from \(173 \times 130 \times 35\) to \(173 \times 130 \times 70\) with prices from \(£ 3.46\). Supplier B.


7000 series consoles of all-metal or ABS/metal construction with sizes from \(102 \times 28 \times 140\) to \(356 \times 33 \times 287\). Prices from \(£ 10.18\). Supplier \(F\).


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A range of cases in two colours. Plastic with aluminium front \& rear panels with provision for tilt leg. Sizes from \(85 \times 40 \times 154\) at \(£ 2.76\). Supplier G.


Minicases in mild zinc coated steel sheets with hammertone finish. Sizes from \(165 \times 114 \times 114\). Supplier \(E\).

"Nuova" enclosures in anti-static abs. Transparent red front panel is inclined at \(70^{\circ}\). Contains pillars for PCBs. Four colours and three sizes available, from \(120 \times 50 \times 90\) at \(£ 1.72\). Supplier C.


Samos miniature cases in 7 sizes. Two part construction, holds up to 4 PCBs with special PCB feet. Sizes from \(50 \times 50 \times 100\) to \(75 \times 200 \times 125\). Prices from £1.47. Supplier C.


16 SWG aluminium case, silver hammer finish (type W). 4 sizes from
\(203 \times 152 \times 152\) to \(381 \times 228 \times 203\) with prices from £4.55. Supplier A.


2000 range of boxes in ABS moulded in four colours with slotted guides for 1.5 mm PCBs. 5 sizes from \(100 \times 50 \times 25\) from £1.03. Supplier F.


One size boxes in tin-plated steel popular with some constructors. Boxes come with free contents which should be burnt before use. Price varies.

\section*{SUPPLIER CODES}

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We would like to thank the suppliers for providing photographs and details of their products, and in particular Watford Electronics who rendered us valuable assistance during the compilation of this article.


If it gets much worse the wife'll begin to notice!


That young devil Jenkins is on about lowering the modulus of elasticity of air . . . is he sending me up again Stan?!!

\section*{"STOP PRESS" NEW LOW PRICES}

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\hline \(0.022 \mu \mathrm{f}\) & 5p & \(0.15 \mu \mathrm{f}\) & \(0^{0}\) & 1-0, f & \(28 p\) \\
\hline 0.033 \(\mu \mathrm{f}\) & 5 p & \(0.22 \mu \mathrm{f}\) & Ip & 1-5 \(\mu \mathrm{f}\) & 34p \\
\hline \(0.047 \mu \mathrm{f}\) & 5 p & \(0 \cdot 33 \mu \mathrm{f}\) & 8 p & 2-24i & 40p \\
\hline \multicolumn{6}{|l|}{Mullard Czsil 8 erlea 400vw} \\
\hline 0.0141 & 18p & \(0068{ }^{1} 1\) & 15p & \(0.47 \mu\) & 33 p \\
\hline \(0.015 \mu \mathrm{f}\) & 10p & \(0.10{ }^{\prime \prime}\) & 10p & & \\
\hline \(0.022 \mu \mathrm{f}\) & 10p & \(0.15 \mu \mathrm{f}\) & \(18 p\) & & \\
\hline \(0.033 \mu 4\) & 12p & 0-22 1 ¢ & 25 p & & \\
\hline 0.047 1 & 15p & \(0.33 \mu \mathrm{f}\) & 30 p & & \\
\hline \multicolumn{6}{|c|}{CERAMIC CAPACITORS} \\
\hline \multicolumn{3}{|l|}{Mullard 632 8erlea} & \multicolumn{3}{|r|}{Mullard tat Serles} \\
\hline & & 56pf & & & \\
\hline 2.2pf & 12pi & 68 pf & & & \\
\hline 2.7pt & 15 pf & 82 pf & & & \\
\hline \(3 \cdot 3 \mathrm{pf}\) & 18pf & 100pf & & & \\
\hline 3.9pf & 22 pf & 120pf & & & \\
\hline 4.7 pf & 27 p ! & 150pf & & & \\
\hline 5. 6pf & 33 p f & 180pf & & & \\
\hline 6. 8pf & 39pf & 220 pf & & & \\
\hline 8.2pf & 47pf & 270pf & & & \\
\hline & & & & 8p & \\
\hline Mullard & 6288 & & & & \\
\hline
\end{tabular}


\title{
microfile.
}

\section*{Gary Evans tip toes through the micros}

THE MICROPROCESSOR WAS a boon to those people who like organising conferences. Here was a device about which many people felt a keen "need to know."

News of conferences/seminars/lectures came thick and fast, one could have flitted from one event to the next without drawing breath.

A lot of these activities were aimed at the professional but a fair few were designed to appeal to the amateur. With the initial deluge behind us now it is interesting to note that not only are there fewer events but that the nature of those that take place, at least those aimed at the amateur, is changing

In the early days the subject matter was almost solely concerned with details of the internal architecture of the MPU, max speeds of operation, PSU requirements etc. However, most amateurs, from the word go; thought of the MPU in terms of home computer systems. In this light, a lot of the details presented at these conferences were, essentially, boring. After all, whilst the LSI technology that gave birth to the MPU was essential if any low cost computer was to be envisaged, in essence, a good PSU is just as important to the operation of the overall system and we didn't see many talks on the detailed operation of power supplies - interesting as they might have been

The realisation that what most people are interested in is personal computing means that today we are seeing more and more conferences dealing with this aspect of things.

Early February saw Microsystems 78, a three day event with one day devoted to Personal Computing.

With Dr Chris Evans (must be O.K. with a name like that - Chris I mean) in the chair, delegates were treated to a day of talks on all aspects of personal computing talk of accumulators accumulating and stacks stacking was not to be heard.

Later in the year we have the second of the DIY Computer Shows organised by Online (more of that later) and no doubt there are many other events of this nature in the pipe line.

\section*{Initial PCW}

Early February also saw a new magazine on the market. Personal Computing World is a welcome addition to the ranks of titles that appear on our newsagents shelves every month. Dealing, as it does, almost exclusively with personal computing it should act as some sort of focus for this new hobby - something we need.

While we at ETi hope we have been, and intend to remain with for instance our personal computing supplement in June, involved with personal computing after all the American magazine Popular Electronics has a regular computing section in spite of the Plethora of computing mags - we acknowledge that with the greater space available to them, PCW fill a long felt need in these Islands.

We wish PCW luck.
For further information on the magazine contact: PCW
62A Westbourne Grove London
W2

It should however be available from your local newsagent.

\section*{Video Writer}

The COSMAC VIP (Video Interface Processor) pictured is a new computer kit from RCA

Featuring the CDP 1802 MPU the VIP is specifically designed to allow easy generation of TV graphics and games. The VIP offers a versatile 4 K operating system with facilities for storing and retrieving programs developed in the system's 2K RAM on cassette tape.

Using an interpretive language known as CHIP-8, 2 byte instructions entered via the VIP's hex keyboard, allow patterns to be generated on the screen, the generation of random bytes, the sounding of tones and allow subroutine nesting.

Expansion is possible and the hobbyist manual contains detailed information on use with music synthesizers, relays, printers and ASCII keyboards as well as program listings for twenty video games.

At present available only in a NSTC monochrome standard soon a PAL interface chip and programmable sound capability will be available. Further details from: RCA Solid State-Europe
Sunbury-On-Thames
Middlesex
TW16 7HW


\section*{Be Rat-ional}

If I hear the words Womp Rat once môre I think I shall become an intergalactic hermit.

The response to my offer to provide a BASIC listing of a game program last month has been overwhelming.

I thought that I might, if lucky, get a few requests for the game and so did not bother to ask for an SAE. The result - to avoid being lynched by an overworked reader services department - I had to address a good few envelopes myself. A short Microfile this month because writer's cramp has set in.

It was impossible to give personal replies to the letters - no Microfile if I had, no eating or sleeping either but please anybody who offered us game programs of any sort - take this as a blanket request to send them in to me at ETI.

ET

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\section*{Alpha III Digita Multimeter}

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To enable us to supply the correct interface board and instructions, we must know your television set make and model and, if possible, chassis type


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\section*{A. Kenny}

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> Tech-Tips is an ideas forum and is not aimed at the beginner. We regret we cannot answer queries on these iems.
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\hline 10A & 400 V & TO220 & 120p \\
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Some AC motors judder badly at low speeds when controlled by triacs using phase control. This circuit gives very smooth operation with no RFI

\section*{Increasing Regulator Outputs}
D. Self

It is often necessary to arrange an integrated circuit 3 -terminal voltage regulator to give a higher output voltage than that set by the regulator alone. The normal way of doing this is to connect the "common" terminal to the mid-point of a potential divider hung between the regulated output and ground. The regulator voltage now appears across the top divider resistor; hence, if for exampie equal divider resistors are used, the output voltage is twice that maintained by the regulator between its common terminal and output

The problem with this method is that most IC regulators (eg the 78 series) have a small quiescent current (approx 10 mA ) flowing out of the common terminal to ground. The magnitude of this current is not closely controlled, and hence the total output voltage becomes somewhat unpredictable due to this extra current flowing in the bottom half of the divider. Low divider resistor values help, but there are likely to be the complications of heat dissipation and inefficiency.

The circuit above avoids the problem by using transistor Q1 to generate a low impedence at the regulator common terminal by emitter-follower action, while trans-

Q1 acts as a 'variable resistor' in the mains supply, with diodes D5-8 ensuring unidirectional current flow through the transistor.

Bias to the transistor is supplied by the mains transformer and controlled by RV1. Q1 must be able to withstand peak mains voltage \((-350 \mathrm{~V})\).


\section*{More Ohms Per Volt}

\section*{R. Soar}

This circuit is designed to improve the performance of a low cost \(1 \mathrm{k} /\) volt multimeter on the 0.5 V DC range.

The BC108 emitter follower provides an impedance transformation with a gain of 250 or more, so that the effective input impedance of the multimeter is now \(250 \mathrm{k} / \mathrm{volt}\). The LED provides a fixed reference voltage for the set zero control, which compensates for the voltage drop across the transistor

ferring the voltage derived from a relatively high-resistance divider network. The value of R3 is not critical, but must be low enough to accept the
highest likely quiescent current without causing Q1 to turn off

The circuit shows a practical 24 Volt supply using a 7812 regulator.

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\hline & CODE & PPICE \\
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\hline 50 Hz CRYSTAL TIMEBASE KIt & XTK & E5.45 \\
\hline
\end{tabular}
\begin{tabular}{|c|c|c|c|c|c|c|c|c|c|}
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{CMOS}} & CO4020 & 1.28 & C04040 & 1.11 & C04066 & 0.63 & CO4096 & 1.08 \\
\hline & & CD4021 & 1.04 & CD404 1 & 0.86 & CD4067 & 3.85 & C04097 & 3.85 \\
\hline Mainly & PCA & CO4022 & 0.94 & CO4042 & 0.88 & C04068 & 0.23 & CO4098 & 1.13 \\
\hline CO4000 & 0.17 & CD4023 & 0.23 & CO4043 & 1.01 & C04069 & 0.23 & C04099 & 1.90 \\
\hline CD4001 & 0.18 & CO4024 & 0.80 & CD4044 & 0.96 & CO4070 & 0.51 & CD4502 & 1.24 \\
\hline C04002 & 0.17 & CD4025 & 0.23 & C04045 & 1.45 & C04071 & 0.23 & CO4510 & 1.41 \\
\hline CD4006 & 1.20 & CD4026 & 1.78 & CO4046 & 1.37 & C04072 & 0.23 & C04511 & 1.72 \\
\hline C04007 & 0.18 & C04027 & 0.58 & CD4047 & 1.04 & C04073 & 0.23 & CO4514 & 2.84 \\
\hline CD4008 & 1.00 & CD4028 & 0.92 & CO4048 & 0.58 & CO4075 & 0.23 & CD4515 & 3.24 \\
\hline C04009 & 0.58 & CD4029 & 1.18 & C04049 & 0.58 & CO4076 & 1.34 & CD4516 & 1.40 \\
\hline CO4010 & 0.58 & CD4030 & 0.58 & CO4050 & 0.58 & C04077 & 0.45 & CD4518 & 1.25 \\
\hline C04011 & 0.20 & CO4031 & 2.30 & CO4051 & 0.94 & CO4078 & 0.23 & CO4520 & 1.19 \\
\hline CO4012 & 0.23 & C04032 & 1.02 & CD4052 & 0.94 & C04081 & 0.23 & C04527 & 1.64 \\
\hline CC4013 & 0.58 & CO4033 & 1.44 & CD4053 & 0.94 & CO4082 & 0.23 & CO4532 & 1.39 \\
\hline C04014 & 1.04 & CD4034 & 1.97 & CO4054 & 1.20 & CD4085 & 0.74 & CO4555 & 0.90 \\
\hline CD4015 & 1.04 & CD4035 & 1.22 & CD4055 & 1.36 & CD4086 & 0.74 & C04556 & 0.90 \\
\hline C04016 & 0.58 & CD4036 & 3.29 & CD4056 & 1.36 & CO4089 & 1.60 & MC14528 & 1.22 \\
\hline C04017 & 1.04 & CO4037 & 0.98 & CO4059 & 4.93 & CD4093 & 0.92 & MC14533 & 4.68 \\
\hline CD4018 & 1.03 & CD4038 & 1.10 & CO4060 & 1.15 & CO4094 & 1.94 & IM6508 & 8.05 \\
\hline CO4019 & 0.58 & CO4039 & 3.20 & C04063 & 1.13 & C04095 & 1.08 & & \\
\hline
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MICRÖPMOGES. SORS \\
MEK680002 190.00
\end{tabular}}} & \multicolumn{2}{|l|}{soloercon} & \multicolumn{2}{|l|}{DISPLAYS} & \({ }^{21024-6}\) & 2.05 \\
\hline & & PINS & & TYP & & 21124.4 & 2.90 \\
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1000 & 8. 0.00 & FMC500 C.C. & 1.30 & & \\
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\hline & 28.44 & & & \({ }_{\text {Stil321CA }}\) & 1.49 & 751410 J & 3.36 \\
\hline 280.6 P & & \({ }_{\text {Clack }}\) & & Stor & 4.0 & 7514118 & 4.10 \\
\hline 280-P10 & 12.80 & Mk 50253 & 5.60 & 5.12 MHz & 3.60 & 751239 k & 358 \\
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\section*{Car Voltage Regulator}

\section*{C. Gibbons}

This circuit provides solid state control of battery charging. The field winding of the dynamo is initially energized via the ignition light as in a conventional system. Current flowing down the WL lead passes through Q1 to the F lead then to the field coil Once the engine has started, current from the dynamo passes through D2 to Q1. The ignition light goes out because the WL lead rises in voltage to that of the battery. Current also passes through D5 to the battery. The battery voltage is sensed by ICI , which is wired as a comparator, once the voltage of the non inverting input, rises above that of the inverting input (Held at 4.6 volts by D4) the output goes high. Current then flows through D3 and R2 to the base of Q2 turning it on. This then pulls down the base of Q1 turning it off and cutting off the current to the field winding. The output from the dynamo then drops bringing down the battery voltage. This holds the battery voltage con-

\section*{Organ Modification}
1. Cole

Une of the problems with the ETI Touch Organ (December 76) is that peoples skin resistance varies enormously and with the 4 M 7 pull up resistors as specified, when a person with moist skin touches the keyboard the note is sustained for several seconds even when the finger is removed


This modification provides a solution in the form of a sensitivity control.

The pull up resistors are removed and replaced by 560k resistors stood vertically on the board. The free ends of the resistors were then joined with a piece of rigid wire and connected to the wiper of the "sensitivity" pot.

stant. The battery voltage is adjusted by RV1 to approximately 13.5 volts

Under cold weather starting the battery voltage drops very low. Once the engine has started the internal resistance of the battery is also very low, which would draw excessive current from the dynamo causing possible damage. To limit the current R4 is inserted in the main power lead from the dynamo, the resistance of R4 is chosen so that at maximum current
(Typically 20 amps\() 0.6\) volts is developed across it, this then turns on Q3. When Q3 turns on current flows from the power rail through R2 to the base of Q2 turning it on, which in turn, turns off Q1 and cuts off current to the field winding. The output from the dynamo then drops.

No changes have to be made to the existing wiring. The circuit can be housed in an old regulator box, Q1, Q2 and D5 should be mounted on a heat sink.

Low Current Touch Switch D. Ian


The cost of many CMOS ICs is now lower than a mechanical on/off switch. Using only one half of a 4011 , plus a couple of general purpose transistors, a touch operated switch can be constructed which is ideal for many battery powered projects.

Assuming that the inputs to the remaining half of the 4011 are tied low, the current drawn in the off state
is almost negligible and battery life is hardly affected.

Touching the 'on' contacts with a finger brings pin 3 high, turning on the darlington pair and supplying power to the load (transistor radio etc). Q1 must be a high gain transistor, and Q2 chosen for the current required by the load circuit

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\hline \multicolumn{2}{|l|}{TTLs by TEXAS} & & & \multicolumn{2}{|l|}{C-MOSUCa} & \multirow[t]{2}{*}{DP.AMPS
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\text { CA } 3130
\]} & \multirow[b]{2}{*}{108 p} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{\[
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& \text { NE } 531 \mathrm{~V} \\
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\]} & \multicolumn{4}{|l|}{\multirow[t]{2}{*}{MEMORY I.C.s}} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{\begin{tabular}{l}
MPF \(102 / 3\) \\
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\end{tabular}}} & \multicolumn{2}{|l|}{\multirow[t]{3}{*}{\[
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& \text { 2N2926RB } \% \\
& \text { 2N29260G }
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\]}} & DIODES & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{BRIDGE}} \\
\hline 7400 & 18 p & 74107 & \(36 p\) & 4000 & 21p & & & & & & & & & & & & & & \multirow[t]{2}{*}{\[
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\hline 7401 & 18p & 74109 & 80 p & 4001 & \(21 p\) & CA3140 & \(108 p\) & 709 & & & & & EPR & 775p & MPF104/5 & \[
5^{40 p}
\] & & & & & \\
\hline 7402 & 18p & 74110 & 60 p & 4002 & 21p & CA 3160 & 120p & 733 & & 150 p & 2102.2 & & & 180 p & & 40 p & & 11p & OAB1 15p & &  \\
\hline 74 CO 2 & 25p & 74111 & 75 p & 4006 & 127 p & LM301A & 40 p & 741 & & 25 p & 2112.2 & & & \(300 p\) & MPSA06 & 37p & 2N3053 & 220 & 0485 15p & & \\
\hline 7403 & 18 p & 74112 & 96 p & 4007 & 21p & LM318N & 175 p & 747 & & \(75 p\) & 2114 & & RAM & ¢15 & MPSA12 & 62 & 2N3054 & 65 & OA90 9p & & 500 31p \\
\hline 7404 & 240 & 74116 & 216p & 4008 & 180p & LM324N & 130 P & 748 & & 40 p & 2708 & & EPROM & £27 & MPSA56 & 40p & 2N3055 & 65 & 0491 9p & &  \\
\hline 7405 & 25p & 74118 & 160 p & 4009 & \(67 p\) & LM348N & 130p & 776 & & \(216 p\) & 271 & & EPROM & ¢ 40 & MPSU05 & 72p & 2N3442 & 151p & OA95 9p & 2 A & 200V 55 \\
\hline 7406 & 43 p & 74119 & 225p & 4010 & \(87 p\) & C1458P & 75 P & 3900 & & 70p & 8080A & & CPU & E11 & MPSU06 & 78p & 2N3643 & 54 & oazu0 9p & & 200V 70p \\
\hline 7407 & 43 p & 74120 & 130p & 4011 & \(21 p\) & LINEAMI.Cs & & & & & AY-5.1013 & & UART & 600p & MPSU55 & 90p & 2N3644 & 54 p & 0a202 10p & 3 A 6 & 600 V 80 p \\
\hline 7408 & 22 p & 74121 & 32 p & 4012 & \(23 p\) & AY-1.0212 & & NE562 & & \({ }_{450 p}\) & AY-5-2376 & & Kb Enc & £13 & MPSU56 & 98 & 2N3702/3 & 14p & 1 NG 14 4p & 4 A & 100 V 90 p \\
\hline 7408 & 22 p & 4122 & 52 p & 4013 & 55 p & AY-3-8500 & 775p & NE565 & & 450 p
\(\mathbf{2 0 0 p}\) & MC6800 & & CPAM & \({ }^{\text {£ 14 }}\) & OC28
OC35

cher & 90p & 2N3704/5 & 14 & 1 N916 7p & 4 A & 400 V 96 p \\
\hline 7410 & 18 p & 74123 & 75 p & 4014 & 90p & CA3028A & 112 p & NE566 & & 200p & MC6820 & & RAM & 432 p
848 p & 0 C 35 & 90p & 2N3706/7 & \(14 p\) & 1 N4001 25p & & 50 V 9 p \\
\hline 7411 & 26 p & 74125 & 70p & 4015 & 90 p & CA 3046 & 85 p & NE567 & & 200 p
180 p & MC6820 & & RAM & \({ }_{7588} 848\) & 0071
R20088 & \({ }_{225}^{350}\) & 2N3708/9 & 140 & 1 N 400347 p & 6 A & 100V \\
\hline 7412
7413 & \(25 p\)
40 & 74126
\(74 \% 28\) & \(65 p\)
\(82 p\) & 4016
4017 & 54
100 & CA3048 & 250 p & RC4151 & 1 DN & 480p & R M -3-2513 & & ROM & 758p & R20088
R20108 & \({ }_{2}^{225 p}\) & 2N3773
2N3819 & \({ }^{3209}\) & 1 N4005 \({ }^{\text {7 }}\) \% & & \({ }^{108} \mathrm{p}\) \\
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& 74: 3 \\
& 7414
\end{aligned}
\] & 40p & 74128
74432 & \(82 p\)
\(81 p\) & 4017
4018 &  & CA 3053 & 75 P & SN7271 & & 432p. & RO-3-2513 & & & 756 & R20108 & 225p & 2N3819 & \({ }^{27} \mathrm{p}\) & 1 N4148 4p & 64 & 400 \\
\hline 7416 & 40 p & 74136 & 81 p & 4019 & 57 p & CA3080E & 200 p & SN 7600 & 3N & 275 p & TRANSIS & TORS & BF167 & \(25 p\) & TIP29C & 62 & 2N3823 & 50p & 1N5401/315p & & \\
\hline 7417 & 40 p & 74141 & 8 5p & 4020 & 140p & CA3089E & 250 p & SN7601 & & \(280 p\) & AC125/6 & 20p & 8 F 170 & 25p & IIP30A & 60p & 2N3866 & 970 & & & \\
\hline 7420 & 18 p & 74142 & 300p & 4021 & 120p & CA3090AQ & 255p & SN7601 & 13 N & 175 p & AC127/B & 20p & BF173 & 27p & TIP30C & 72 & 2N3903/4 & 220 & ZEMERS & 254 & \[
{ }^{270} \mathrm{p}
\] \\
\hline 7421 & 43p & 74145 & \(9 \mathrm{5p}\) & 4022 & 140p & ICL7106 & & & & 180 p & AC176 & 20p & BF178 & 30p & TIP31A & 5 & 2N3905/6 & 22 & 27V33V & & 432p \\
\hline 7422 & 28 p & 74147 & 205p & 4023 & 23p & ICL8038 & 400 p & SN7601 & & & AC187/8 & 20p & BF179 & 35p & TIP31C & 68p & 2N4058 & 10p & 400 mW & & \\
\hline 7423 & 36 p & 74148 & \(160 p\) & 4024 & 82 p & LM339N & 175 p & SN7602 & & 175 & AD149 & 60p & F180/1 & 35p & TIP32A & \({ }^{63 p}\) & 2N4060 & 19p & & & \\
\hline 7425 & 33 p & 74150 & 130p & 4025 & 23p & LM377N & 200p & SP8515 & & 760 p & AD161 & 45p & BF 184/5 & 24p & TIP32C & 85 & 2N4123/4 & 22p & & & \\
\hline 7426 & 43 p & 74151 & \(81 p\) & 4026 & 200p & LM380N & 112 p & TAA621 & & & AD162 & 48p & BF194 & 13p & TIP33A & \({ }^{97 p}\) & 2N4125/6 & 22p & Plastic & & \\
\hline 7427 & \(40 p\) & 74153 & \(81 p\) & 4027 & \(64 p\) & LM381N & 180 p & TAA661 & & \(310 p\)
150 p & AF114/5 & 300 & BF195 & 11p & TIP33C & 1200 & 2N4401 & \(34 p\) & & \[
\text { A } 50
\] & 500 V \\
\hline 7428 & 40p & 74154 & 160p & 4028 & 110p & LM389N & 180 p & tbal 20 & & 150 p & AF116/7 & \(30 p\) & BF196 & 17p & TTP34A & 1245 & 2N4427 & \({ }_{97}{ }^{\text {p }}\) & DA 400V & & 160p \\
\hline 7430 & 18 p & 74155 & 97p & 4029 & 120p & LM3911N & 150 p & tbagal & & 300 p & AF127 & 400 & BF197 & 19p & TIP34C & 1600 & 2N4871 & \(60 \%\) & \[
107 p
\] & & \\
\hline 7432 & \(37 p\) & 74156 & 87 & 4030 & \({ }^{67 p}\) & MCi310P & 190 p & tBa651 & & 225 p & AF139 & 400 & BF200 & 40p & TTP35A & 243p
2900 & 2N5179 & \(75 p\) & 6A 500\% \({ }^{\text {a }}\) & 15 A 5 & \(500 \mathrm{~V}^{200 \mathrm{p}}\) \\
\hline 7433
7437 & 43p & 74157
74159 & \(85 p\)
\(250 p\) & 4040 & 150p & MC1351P & 110p & tbaboo & & 112p & \({ }_{\text {AF } 239}\) & 4\%p & \({ }^{\text {BF }} \mathrm{C} 2448 \mathrm{~B}\) & \(3{ }^{34}\) & TIP3 & \({ }_{297}^{298}\) & 2N5245 & \(40 p\) & 120 & & 225p \\
\hline 7438 & & 74160 & 130 p & 4043 & 97p & MCI495L & 490 p & TBAB10 & & 125 p & BC108/8 & & & & TIP36C & & 2N5296 & & & & \\
\hline 7440 & 18 p & 74161 & 130p & 4046 & 150 p & MC3340 & 180 p & tBab20 & & 100p & BC109 & \(10 p\) & BF258 & 39 & TIP41A & 70p & 2N5457 & & ELECTRONICS & LED\% & \\
\hline 744 & 85 p & 74162 & 130p & 4047 & 150p & MC3360P & 160 p & 2Nal4 & & 405p & BC.109/C & 11p & 8F259 & 48p & TIP41C & \(84 p\) & 2N5459 & 40p & & T|L209 Re & Red 14p \\
\hline 744 & 75p & 74163 & 130p & 4049 & 64p & NE540L & \(225 p\) & ZNA24E & & & BC117 & 27p & BF337 & 32p & TIP42A & \(76 p\) & 2N5485 & 45p & ORP 12 130p & T1L211 Gr & Green 38p \\
\hline 7443 & 120p & 74164 & 120p & 4050 & 58 p & NE555 & 40p & ZN425E & & & BC147 & 9 & BFR39 & \(34 p\) & TIP42C & 98 & 2N6107 & 70p & & TlL32 lnt & infrared 81p \\
\hline 7444 & 120 p & 74165 & 150p & 4054 & 120p & NE556 & 97p & \[
2 N 1034
\] & & 218p & BC148 & & BFR40/1 & \(34 p\) & T1F2955 & 760 & 2N6027 & 600 & 2N5777- 40p & 02 " & Red 16p \\
\hline 74 & 108 & 74166 & Op & 4055 & & & & & & & C157 & & & & T1P305 & 60 & 2N6247 & & & & \\
\hline 7446 & 108p & 74167 & 320 p & 4056 & \(145 p\) & voltage r & ULATORS & S-Fixed & & & \({ }^{\text {BC1 } 158 / 9}\) & 13p & BFA80/1 & 37 p & 2N697 & 400 & 2N6254 & 140p & SCR THYRISTO & & \\
\hline 74478 & \(90 p\)
850 & 74170
74172 & 260p
750 p & \[
4060
\] & 130 p
30 p & Plastic-TO220 & -3 Ter- & 62 V & 78162 & 70p & \({ }_{\text {BC1 }} \mathrm{BC172} 5\) & \({ }_{11 p}\) & BFR88 & 37p & \[
\begin{aligned}
& \text { 2N697 } \\
& \text { 2N698 }
\end{aligned}
\] & \[
\begin{aligned}
& 25 p \\
& 430
\end{aligned}
\] & 2N6292 & \({ }^{70 p}\) & 1A 50 V TO5 & & \\
\hline 7450 & 18 p & 74173 & \(190 p\) & 4069 & 30 p & Thanals & & V & 78 L 12 & 70p & \({ }_{8 C 177}\) & 20 & BFX29 & 30 p & 2N706/8 & 220 & 3N140 & \({ }_{97} 9\) & 1 A 400 V TO5 & & 85p \\
\hline 7451 & \(18 p\) & 74174 & 130p & 4071 & 30 p & 5 V . 7805 & 115p & & & 70p & BC178 & \(17 p\) & BFX30 & 34 p & 2N918 & 43p & 3N141 & & 3a 400V STUD & & 120p \\
\hline 7453 & 18p & 74175 & 97 p & 4072 & 30 p & \(6 \mathrm{~V} \quad 7806\) & 115p & 100 ma & ve & TO92 & BC. 179 & 20 & 8FX84/5 & 30 p & 2N930 & 19p & 3 N 187 & \(200 p\) & 16a 400V Plastic & & 220p \\
\hline 7454 & 18p & 74176 & 130p & 4073 & 45p & 8 V 7808 & \(115 p\) & 5 V & 9405 & 80 p & BC182/3 & 12 & 8F×86/7 & 300 & 2N1131/2 & 25p & 3N201 & 120 p & 16a 600 V Plastic & & 270p \\
\hline 7460 & 18 p & 74177 & 130p & 4078 & 30 p & 12 V 7812 & \(115 p\) & & & & BC184 & 14p & 8F×88 & \(30 p\) & 2N1304/5 & 75p & 40360 & 43 p & BT106 1a 700 V & STUD & 130p \\
\hline 7470 & 38 p & 74180 & 160p & 4081 & 30 p & 15 V 7815 & 115p & 12 V & 79 L 12 & 80 p & BC187 & 320 & BFY50 & \(22 p\) & 2N1306/7 & 75p & 40361/2 & 43p & C1080 4 A 400V & Plastic & \({ }^{63 p}\) \\
\hline 7472 & 32p & 74181 & 324p & 4082 & 30 p & 18V 7818 & 115p & 15 V & 79L15 & \(80 p\) & BC212 & & BFY51 & 229 & 2N1613 & 22p & 40409/10 & 75p & MCR101 1/3A 15 & V TO92 & 27p \\
\hline 7473 & \(36 p\) & 74182 & 150p & 4093 & 104p & 24 V 7824 & & & & & BC213 & 12 p & BFY52 & 220 & 2N1711 & 22 p & 40411 & & 2N3525 5A 400 & V 1066 & 20p \\
\hline 7474 & 37 p & 74184 & \(260 p\) & 4510 & 140p & & & LM309K & T03 & 150p & BC214 & & BFY90 & 80p & 2N1893 & 32p & 40594 & & 2N4444 8A 600 & \(V\) Plastic & 200p \\
\hline \(74 \mathrm{C74}\) & 70p & 74185 & 190p & 4511 & 140 p & \(\dagger\) Amp & & LM323K & T03 & 700p & BC461 & & BRY39 & 48 & 2N2102 & 60p & 40595 & 97 p & 2N50800 8A 30 & OV 1092 & \\
\hline 7475 & \(48^{\text {p }}\) & 74186 & 990p & 4516 & 130 p & 5 V - 7905 & & LM327N & Dil & 275p & BC478 & 32p & BS×19/20 & 20 p & 2N2160 & 120 p & 40635 & \({ }^{90} 9\) & 2N50B4 0 8A 20 & 00V T092 & 2 43p \\
\hline 7476 & \(37 p\) & 74190 & 160p & 4518 & 110 p & 12 V 7912 & 160p & MC1468 & & & BC549 & & MJE340 & 70p & 2N2219 & 22 p & 40636 & & & & \\
\hline 7480 & 54 p & 74191 & \(160 p\) & 4528 & 110 p & 15 V 7915 & 160p & TBA6258 & TO5 & 1200 & 8CY70 & & MJ481 & 175p & 2N2222 & \(23 p\) & 40673 & 900 &  & EXAS & \\
\hline 7481 & 108 p & 74192 & 160p & 14433 & £14 & 24 V 7924 & 180 p & 7805 K & T03 & 1500 & BCY7 & & MJ491 & 216p & 2N2369 & 15p & 40841 & & & & \\
\hline 7482 & 90 p & 74193 & \(160 p\) & 14533 & 540 p & & & & & 150 & BD131/2 & & MJ2501 & 250p & 2N2484 & 32 & 40871 & 85 p & \(3 \mathrm{pm} \quad 12 \mathrm{p}\) & 22 pin & \\
\hline 7483 & 99p & 74194 & 160 p & 14583 & 150 p & Heat sink 17 & W 25p & & & & BD139 & & MJ2955 & 130p & 2N2646 & & 40872 & 90 &  & n & \\
\hline 7484 & 108p & 74195 & 110p & Other & & surtable for 1 & TO220 & & & & 80140 & & MJE2955 & 130p & 2N2904/A & 22p & & & P & , & \\
\hline 7485 & 120 p & 74196 & 130p & 9301 & 160p & ( for & 10220 & \[
723
\] & DIL & 145p & BDY56 & & M \(\lrcorner 3001\) & 250p & 2N2905/A & 220 & & & 18 p & 40 pin & \\
\hline 7486 & 38 p & 74197 & 130p & 9302 & . 175 p & & & & & & BF115 & & MJE3055 & 90p & 2N2906/A & 22 p & & & \(20 \mathrm{pin} \quad 33 \mathrm{p}\) & & \\
\hline 7489
7490 & 340p \({ }_{36}\) & 74198
74199 & \({ }_{216 p}^{270 p}\) & 9308
9310 & \(325 p\)
\(275 p\) & \(100 \mathrm{~mA}+\mathrm{ve}\) & 1092 & LM317 & 220 & & & & & & & & & & & & \\
\hline 7491 & 90 p & 75107 & 175p & 9311 & 275 p & & & & & 0 p & & & & & & & & & & & \\
\hline 7492 & 58 p & 75182
75324 & 250p & 9312 & 180p & displars & & & & & MAlL & & & & & & & & & & \\
\hline 7493
7494 & \(38 p\)
\(90 p\) & \[
\begin{aligned}
& 75324 \\
& 75325
\end{aligned}
\] & 400p & 9314 & 175p & 3015F & Minitron & 200p & & & & & & & & & & & & & \\
\hline 7494
7495 & 90p \({ }^{90}\) & 9601 & 400p & 9316
9318 & \(250 p\)
275 & FND500/507
DL704/DL707 & Red
Red & \[
\begin{aligned}
& 1300 \\
& 1600
\end{aligned}
\] & \[
75492
\] & & ORO & & & & & & & & & & \\
\hline 7496 & 80 p & 9602 & 175p & 9321 & 180 p & 0.747 & \({ }_{\text {Red/Green }}\) & & 9368 & 200p & ONLY & & & & & & & & & & \\
\hline 7497 & 290p & & & 9322 & 150 p & TIL312/313 & Red & 120p & & 200p & & & & & & & & & : 0 & 0443 & 43 \\
\hline 74100 & 140p & & & 9324 & 250p & TLL321/322 & Red & 1309 & 74 & 200p & & & & & & & & & elex & 2280 & 00 \\
\hline
\end{tabular}

\title{
electronics tomorrow. by John Miller-Kirkpatrick \\ 
}

\section*{6800 Update}

RECENTLY RECEIVED from a Motorola distributor is an M6800 series shortform catalogue entitled 'Microprocessor News - September 1977'. This catalogue lists Motorola's MPU products ranging from integrated circuits to EXORcisers and Polyvalent Development System components.

In the area of chips Motorla have announced some interesting new goodies. Their \(1024 \times 4\) MCM6614, static RAMs come in 200, 300 and 450 nS versions Motorola's \(128 \times 8\) bit static RAM, the MCM6810 is now also offered in 250,360 or 45 o ns versions. On the subject of memories there is the MCM68708 \(1024 \times 8\) EPROM (basically a 2708) and the pre-announcement of the 2716 product, the MCM68716

\section*{New Interfaces}

Motorola have not been any slower than other MPU manufacturers in getting onto the LSI interface chip bandwagon. LSI technology allows many functions to be integrated on one chip to give high density, low cost interfaces to 1/O devices which would otherwise require multi-TTL chip solutions. An example is the MC6843 Floppy Disk Controller which performs the complex interface between an M6800 system and a range of Floppy Disk drives

The MC6845 is Motorola's CRT controller chip which performs the 6800 to CRT interface with so many programmable options that one set of options is bound to fit your requirements. It features programmable dots per character, lines per character, characters per row, rows per screen, sync generation and cursor generation. It even features an on-chip light pen register to allow the use of presumably some form of photo diode or transistor to recognise a particular character location on the CRT. This would allow the use of a non-keyboard entry device for a low cost MPU system. If the MPU is programmed to display its complete character set at the bottom of the screen whenever it expects a keyboard entry then the light pen can be used to choose the input character from those displayed - System 68 VDU revisited?

Continuing through the catalogue we pass the various auxiliary circuits which include a GPIA General


New Total Development System M68 TDS from Motorola.

Purpose Interface Adaptor, Modems, DMA Controller, Bus transceivers and a thing called a 'Triple Bidirectional Bus Extender/Switch' (TB-DBES?) or MC6881/3449. After this it becomes simple to understand the new two-chip M6800 (MC6802 + MC6846) and the two-board computer (MEK D2 kit).

By now you are prepared for the Polyvalent Development System (PDS) which fills the gap between low-cost and on-a-board microcomputers, and the sophisticated aids for designing microprocessor systems. As its name suggests the PDS is really a family of sub-systems that can be selected to provide the required options and can then be used to analyse the system. This is Motorola's description of their PDS, which looks to me like a rich man's System 68. One version of the PDS is called the Autonomous Development System (you guessed it ADS for short) which includes the PDS master board interfaced to a keyboard and 5 inch CRT monitor, the kit with monitor is the M68ADS 1 or M68ADW1 without the CRT. I am not sure that I could recommend these kits as cost effective MPU tools at over \(£ 900\) if System 68 costs about \(£ 300\) ! But congratulations to Motorola for seeing the problem of high-cost TTYs and trying to overcome the problem.

On page 16 of the catalogue you are in troduced to the MC68CIM 1 (CIM stands for Audio-Cassette Interface Module). This is used to interface any ordinary audiocassette recorder with any of the PDS configurations (SAC, MEB, ADS). The data is recorded on the cassette according to the Kansas City Standard (CUTS), in the playback mode the clock is recovered from the signal and used to clock the receiving ACIA in the PDS, this feature copes with cassette speed variations of up to \(30 \%\) without producing errors. The MC68CIM1 is a module consisting of several chips on a PCB (isn't it about time someone produced a single chip CUTS interface to plug on to any 8 bit data bus?), if anybody knows of one please let us know

ETI

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ELECTRONICS TODAY INTERNATIONAL - APRIL 1978


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