

## achtronicsitial international

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VOL 5. No. 1.

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## Now...the most exciting Sinclair kit ever

## The Black Watch kit At £17.95, it's <br> 

 * practical-easily built by anyone in an evening's straightforward assembly. * complete - right down to strap and batteries.* guaranteed. A correctlyassembled watch is guaranteed for a year. It works as soon as you put the batteries in. On a built watch we guarantee an accuracy within a second a day-but building it yourself you may be able to adjust the trimmer to achieve an accuracy within a second a week. The Black Watch by Sinclair is unique Controlled by a quartz crystal.. powered by two hearing aid batteries... using bright red LEDs to show hours and minutes and minutes and seconds...it's also styled in the cool prestige Sinclair fashion: no knobs, no buttons, no flash.
The Black Watch kit is unique, too. It's rational-Sinclair have reduced the separate components to just four.
It's simple-anybody who can use a soldering iron can assemble a Black Watch without difficulty. From opening the kit to wearing the watch is a couple of hours' work.


## The special features of The Black Watch

Smooth, chunky, matt-black case, with black strap. (Black stainlesssteel bracelet available as extrasee order form.)


Large, bright, red display-easily read at night.
Touch-and-see case-
no unprofessional buttons.


Runs on two hearing-aid batteries (supplied). Change your batteries yourself-no expensive jeweller's service.


## The Black Watch-using the unique Sinclair-designed state-of-the-art IC.

The chip...
The heart of the Black Watch is a unique IC designed by Sinclair and custom-built for them using state-of-the-art technologyintegrated injection logic.
This chip of silicon measures only $3 \mathrm{~mm} \times 3 \mathrm{~mm}$ and contains over 2000 transistors. The circuit includes
a) reference oscillator
b) divider chain
c) decoder circuits
d) display inhibit circuits
e) display driving circuits.

The chip is totally designed and manufactured in the UK, and is the first design to incorporate all circuitry for a digital watch on a single chip.

... and how it works
A crystal-controlled reference is used to drive a chain of 15 binary dividers which reduce the frequency from $32,768 \mathrm{~Hz}$ to 1 Hz . This accurate signal is then counted into units of seconds, minutes, and hours, and on request the stored information is processed by the decoders and display drivers to feed the four 7 -segment LED displays. When the display is not in operation, special power-saving circuits on the chip reduce current consumption to only a few microamps.

Take advantage of this no-risks, money-back offer today!
The Sinclair Black Watch is fully guaranteed. Return your kit within 10 days and we'll refund your money without question. All parts are tested and checked before despatchand correctly-assembled watches are gu'aranteed for one year. Simply fill in the FREEPOST order form and post it-today!
Price in kit form: $£ 17.95$ (inc. black strap, VAT, $\mathrm{p} \& \mathrm{p}$ ).


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## news digest



Wired for sound . . . Strain gauges mounted on flexible metal sensors are used to obtain information on lip and jaw movements that we make while speaking. Honeywell tape and oscillograph instrumentation records the facial movements for analysis at the University of Washington's Speech Physiology Laboratory.

NUCLEAR POWERED PACEMAKER
A nuclear-powered pacemaker, that could bring a new lease of life to thousands of people suffering from certain types of heart trouble, is to be made at the Harwell Atomic Energy Research Establishment. Conventional pacemakers with chemical batteries have to be replaced sugically about every three years. Prolonged trials indicate that the implanted lifetime of the Harwell units could reach 10 or even 20 years.

The Department of Health and Social Security has placed the first production order for 100 of the new nuclear batteries and an order for another 200 batteries is following. Under the present contract Harwell will prepare the heat-producing nuclear sources, each containg less than a fifth of a gram of plutonium oxide, and assemble them, with their miniature thermocouples, in strong metal housings to form the nuclear batteries that power the pacemakers. At the request of DHSS, Harwell has also undertaken for an interim period to assemble 100 pacemakers in which solid-state DC/DC converters and electronics pulse generators (the former also made at Harweil) will be connected to the batteries. The assembly is encapsulated in an epoxy resin known to have lasting compatibility with living tissue.

About 100 pre-production models of the nuclear battery have already been made at Harwell and fitted into pacemakers of similar design. The pacemakers have been subjected to long-term controlled trials at recognised cardiolog-
ical centres under DHSS supervision. First, they were tested in animals and then, during the last five years, in human patients - the earliest of whom is still using hers.

Internationally agreed safety standards require stringent tests on the batteries. The isotope Plutonium 238 has been chosen for the heat source because its power output falls by only one per cent per year, and its radiation, mainly alpha particles, requires the minimum of shielding for implatation. This isotope is specially prepared for the pacemaker batteries and is markedly different from the very long-lived Plutonium isotopes associated with nuclear weapons and nuclear fuel. Electronics and Applied Physics Division, Building 347.7, Harwell, Oxfordshire, OX11 ORA.

## HENRY'S 'PART EXCHANGE SCHEME'

Henry's-Lindair have announced the formation of a new Part Exchange Department. Customers who bring their old equipment into any Henry'sLindair store will be given a 'trade-in' allowance which they may then use against the purchase of new equipment from that particular store. The trade-in equipment will then be passed to the Service Department if necessary, and then sold through one of the Bargain Centres in Edgware Road and Tottenham Court Road with a three month guarantee.

## DATAPAD

Datapad is a unique computer system where freehand printing is directly input and converted into a computer medium. The system enables data capture as a by-product of normal activity such as clerical staff filling out input forms.


The Datapads are attached to miniprocessors which validate the data at source and indicate any errors to the user clerks via a screen. A 'clean' magnetic or paper tape is produced which is read directly into a main frame computer.

The system will completely eliminate the existing punching and verifying load, source validation will improve accuracy since the data will have been 'edited' before leaving the user environment and this elimination of the manual conversion stage will speed up the whole process. Quest Automation Ltd., 26 Cobham Road, Stapehill, Wimborne, Dorset BH21 7NP.

6 $\qquad$

## MICROPROCESSING COUNTER/ TIMERS

Dana Electronics, new 9000 series of microprocessing counter/timers are claimed to be the world's most advanced The front panel of each instrument bears no controls - the microprocessor is shid out of the front panel, and control programmed through the keyboard


Both of the first models in the range to be announced ( 9015 and 9035) work to 100 MHz in measuring frequency, period, period average, time interval, time interval average, frequency ratio and total count. Thanks to the microprocesor direct readout of preset operations, reciprocal calculations, and even answers in engineering units are easily achieved. 9 -digit constants can operate on measure ments through the keyboard.

Trigger levels can be key-set or automatically set: levels are punched into the keyboard, and the processor does the rest, digitally displaying the figures selected for each channel on the integral DVMs. An option is also available to automatically calculate rise/fall times and pulse widths to 10 ns resolution, and a range of interface options are available.

The 9015 microprocessor timer costs $£ 1885.00$ with a delivery of 8-10 weeks. Dana Electronics Ltd, Collingdon Street, Luton, Beds.

## CUNNING LITTLE B*****S

## A recent report by the Assistant

 Master's Association reveals that schoolboys with a knack for elect ronics are making devices to bug teachers' studies. Instances reported include that of a boy who concealed a bug inside a radio, which was then confiscated. The radio was then left in a headmaters's study and the boy was able to hear every word that was said there. While we cannot help but admire the ingenuity that must go into some of these espionage-style projects, we do feel that the talents of such enterprising youths could be much better employed in the construction of International 25 Amplifiers or in writing up some of their more conventional projects for ETI.
## ELECTRONICS HELPS CHILDREN LEARN MATHEMATICS

The Novus "Quiz Kid", is to help children aged from about 5 to 9 years to learn mathematics. It is similar to an electronic calculator and is based on a chip we reported in our July issue. The child uses the keyboard to enter a problem followed by what is thought to be the answer (eg $2 \times 2=4$ ). Then he asks the machine if the answer is correct by touching a question mark key. If it is a 'wise old owl' with red and green eyes will light up its green eye. If the answer is wrong, the red eye comes on.


The Quiz Kid comes with an illustrated Quiz Book containing dozens of mathematical games, riddles and puzzles to help-youngsters explore and have fun with numbers. Price is $£ 11.95$.

## MICROPROCESSOR PRICE CUTS

To encourage more systems engineers to explore the benefits of using M6800 microprocessors in their design, Motorola are cutting the selling price of the basic MEK6800D 1 designers' kit from $£ 177$ to $£ 85$.

The kit contains all the components needed in a basic MPU system plus tech nical information in the form of programming and application manuals The M6800 microprocessor unit is provided with 1 K -Byte random access memories, peripheral interface adaptors (2 off each), an 8 K -bit read only memory containing MIKBUG and the synchronous communications interface adaptor. All the devices are TTL compatible, single level ( 5 Volt ) power driven and use a single bus for memory and input/output transactions. No multiplexing or decoding circuits are required for its operation nor is external buffering for interconnections. The kit is available from approved Motorola distributors.


International-25 (Part 2)
Frequency Counter
200W Guitar Amp
November 1975

To order send 35p for each issue plus P\&P (15p for one, 10p for each subsequent issue). As from 1st January 1976 Back Issues will be 40p each. Back Numbers Dept.,

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stating clearly the issues you require.
We are unable to supply the following: 1972 issues lexcept Oct. and Dec.), February and November 1973, March, September and October 1974 and January, September and December 1975.


## DIGITAL STORAGE <br> OSCILLOSCOPE

The new OS4000 from Gould Advance combines the facilities and performance of a conventional 10 MHz oscilloscope with a digital storage system capable of storing signals up to $450 \mathrm{~Hz}(-30 \mathrm{~dB})$. Digital storage has several advantages over tube storage, including the ability to examine what happens immediately before a trigger signal is received, the simultaneous viewing of stored and real-time displays, absence of deterioration of the stored display over a period of time, flickerfree low-frequency performance, and the alimination of the expensive storage tube.

The OS4000 is ideally suited for viewing transient waveforms - for example, in medical, dynamic testing, vibration or pulse-testing applications. It is also suited to low-frequency measurements, where the incorporation of a 'refresh' mode allows flicker to be eliminated. (The longest sweep time is 200s.) In addition, normal 10 MHz real-time viewing is possible, and comparisons between stored and real-time waveforms are easily made.

The OS4000 measures $178 \times 312 \times$ 417 mm , and weighs 11 kg . The price of the instrument is $£ 1053$ (inc. VAT)

Gould Advance Ltd, Roebuck
Road, Hainault, Essex.

## WATCH/CALCULATOR

Now available in the USA is the Uranus LED Solar Calculator timepiece. This is a five-function watch (hours, minutes, seconds, month, date date), with calculator built in. The keyboard consists of 12 pressbuttons arranged around the face and gives all four arithmetic functions, in chain and constant calculations. The readout is a four digit LED display and as far as we can discover, the watch is solar powered. The Uranus is available in gold or silver plated metal, and can be yours for only $\$ 800$.

## TWO-LAYER TAPE

A new formulation of magnetic recording cassette tape that combines the advantages of both ferric oxide and chromium dioxide tapes has been announced by BASF. Called Ferrochrom, the tape has a two-layer coating which, when used with suitable equipment, gives an improved frequency response for cassette recording. The new tape comprises a polyester base coated with a relatively thick high density ferric oxide dispersion. On top of this is added a carefully controlled layer of chromium dioxide. This composition exploits the simple theory that low frequencies are produced from the whole of the tape coating and high frequencies are reproduced from the top of the coating.


Because of its dual oxide, the optimum bias current for ferrochrom is a value approximately halfway between the current for ferric oxide and that for chromium dioxide. Ferrochrom can still be used on a conventional ferric recorder, but on playback the higher frequencies are unnaturally pronounced, giving an effect similar to those on a Dolbyised cassette played back on a non-Dolby machine. This is easily remedied by a slight adjustment of the amplifier's treble control. The frequency response and dynamic range achieved using Ferrochrom in this way is equal to that of a chromium dioiide cassette played on a machine boased for chromium dioxide. The best results from Ferrochrom are achieved on cassette recorders biased for $\mathrm{Fe}-\mathrm{Cr}$. On these the dynamic range from a VU setting of 0 dB is 1.5 dB greater than that possible from chromium dioxide.

BASF Ferrochrom is available in C60 and C90 length in a new-style cassette housing. The housing has a large window which enables the user to see the tape hubs and BASF's patented tape transport system Special Mechanics (SM). The amount of tape on each hub is accurately shown and any disturbance of or damage to the tape can be seen before problems occur.

## FISH QUALITY METER

An electronic fish quality meter which indicates the freshness quality of wet fish rapidly and accurately is now being produced in Scotland. The GR Torrymeter is manufactured by GR International Electronics Ltd of Perth. It was designed and developed in Perth in collaboration with Aberdeen University and the Torry Research Station, a Government establishment recognised as a world authority on fish handling and processing.

The GR Torrymeter is a simple, hand-held device 200 mm in length and 75 mm in width. The instrument measures an electrical property of the fish tissue; this property changes as fish spoils. Individual readings are displayed in illuminated figures one second after the sensing head has been placed on the fish. The meter, equip ped with long-life rechargeable cells, is housed in a sealed plastic moulding which can be wiped clean or rinsed under running water. Each unit is supplied in an individual wooden storage case incorporating a mainsoperated charger unit. GR International Electronics Ltd, Doman Camberley, Surrey GU15 3DG,

## DIGITAL LENGTH GAUGE

A new miniature measuring instrument in the form of a digital gauge head is announced by Dr. Johannes Heidenhain GmbH. The Metro 1010, surpasses dial test indicators, vernier calipers or analogue measuring heads both technically and economically.


The digital gauge head is suitable for length measurement on smaller machines, micrometer stages, coordinate tables, etc.

The measuring range of the MT10 is 10 mm . The resolution is $1 \mu \mathrm{~m}$. The measuring accuracy of a complete system (digital gauge head with counter) is $\pm 1 \mu \mathrm{~m}$. A genuine incremental design principle and a minimum of mechanical components enables the high degree of accuracy to be maintained.

The measured value is indicated by a five-digit LED display with arith metical sign and decimal point. As an option, the counter can be supplied with a BCD-output (printer output) for electronic processing of measured values. Heidenhain (GB) Ltd., 202 London Road, Burgess Hill, Sussex RH15 9RD.

## T Top 500 semiconductors from the largest range in the UK

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OUR GALAXY contains upwards of' 100000 million stars and their families of planets, and growing evidence suggests that Earth may not be the only life-bearing planet in this galaxy.
Telescope studies have, for example, shown that Earth's basic chemicals are distributed throughout the Universe and organic compounds - life's building blocks - have been detected in inter-stellar space.
But believing that life may exist elsewhere is one thing - attempting to prove it is another.

At our present state of technology the most practical planet on which to search for life is Mars. For if we find that life exists or has existed in the harsh climate of Mars, we will have strong reasons to believe that planets with comfortable climates do support life and that other solar systems are.
inhabited.
Two unmanned, automated Viking spacecraft have recently been launched from Cape Canaveral to begin a 700 million kilometre journey to Mars. Once there the two spacecraft will seek evidence of whether life existed on other planets, and obtain information to improved our understanding of how Earth developed as a life-supporting planet and how we can better protect its environment.
Viking is the most complex mission to be flown by NASA, requiring four highly complicated science stations two orbiters and two landers - to carry out separate co-ordinated operations simultaneously and over an extended period of time.
The 2300 kg orbiter will stay in orbit while the 1090 kg lander descends to the surface, as did the command module and the lunar module during.

VIKING LANDER. This magnificent painting by Charles 0 . Bennett shows the mechanical scoop on the end of the spring-steel arm collecting a sample. Having grasped the material the scoop arm retracts to the spacecraft, swivels and stops over a cylinder covered by a wire grill. The lid of the scoop begins to vibrate and dust and soil particles drop through into a rotating conveyor in the spacecraft's interior which distributes measured quantities to a number of test cells for chemical analysis.
the Apollo missions to the Moon. The Viking lander will use a parachute and retro-rockets to achieve soft landing. It will begin its programme of life-seeking biology experiments on about the 10 th day after landing.
Photos from orbiters and landers and information from the science experiments will be transmitted back
to Earth by radio. Radio signals will require more than. 20 minutes to travel the 320 million kilometres from Mars to , Earth. Spacecraft-tracking and communications will involve the Deep Space Network's stations at Goldstone, California, Madrid, Spain and Canberra, Australia:

## VIKING SCIENTIFIC INVESTIGATIONS

The Viking spacecraft will make basically three investigations during their three-month observation of Mars from orbit and from two sites on the surface:
1.A photographic survey of Mars:
2.A search for forms of life; and
3.An analysis of the physical features and makeup of the planet and its atmosphere.

## PHOTOGRAPHIC SURVEY

Each Viking spacecraft has an orbiter and a lander. Each orbiter carries two high-resolution television cameras, and each lander a pair of facsimile cameras. During the last 180 hours of approach to Mars, each spacecraft will obtain a series of photographs of the planet from progressively closer range. After entering orbit, the spacecraft will remain above the designated lander sites to photomap these regions for a number of days before and after the landers are released for descent to the surface. Then the orbiters will leave their fixed positions above the lander sites to photomap almost the entire surface of the planet.
Orbiter photomaps and thermal and water vapour maps will be used to direct the landers to sites where conditions are most favourable to life and where landing hazards are less extreme. Lander cameras will take high-resolution photos of the ground immediately next to the landers, 360 -degree panoramic views of the terrain and distance features, and long-range photos of Mars' satellites and celestial objects.

The Orbiter cameras will be rapid-sequence vidicon cameras using 475 mm telescopes. Taken from low-point in orbit a photo shows 40 square kilometre area with resolution of 50 metres. Consecutive photos from one orbital pass show a 80 km by 500 km swath. Photos are stored on magnetic tape for playback to Earth. Identical facsimile cameras are mounted one metre apart on top of the lander for stereoscopic black and white, colour, and infrared photos. Cameras can view from the ground beside the lander up to 40 degrees above horizon. Each uses a nodding mirror to scan a scene in tiny increments, requiring 20 minutes for a full scene. Light from the scene increments is converted into digital

information bits which are radioed to Earth and reconstructed into a photograph.

## SEARCH FOR LIFE

Life may exist on Mars in higher forms, like moss or lichens, or in microscopic forms like viruses or bacteria. Or a fich variety of life may have existed at one time but disappeared later in the planet's history.
The two regions chosen for landings are areas where conditions could be conducive to Earth-like life forms. They are relatively low, temperate-zone sites in the northern hemisphere where there are indications of atmospheric moisture now, and of surface moisture at least at some time in the past.
Higher forms of life and fossils, surface burrows or trails, and artifacts could be identified in the lander camera photos of the surface adjacent to the landers. The search for microscopic plant or animal life will be made in Martian soil samples. The samples will be scooped up by the 3 metre lander boom and fed into automated biology test chambers where they will be observed for signs of photosynthesis and metabolism. Chemistry of the organic compounds in the soil will be analysed for indication of whether they were produced by animal or plant life, or could evolve life.
Discovery of life on another planet would have a more profound effect on man's thinking than any other discovery in history.

## TEST FOR PHOTOSYNTHESIS

Photosynthesis is the basic-life sustaining process by which Earth plant life uses light energy to combine basic compounds like carbon dioxide, water, and salts - forming
carbohydrates. Steps in the Viking photosynthesis test are:
(a) Inoculate three soil samples with carbon monoxide and carbon dioxide that bear radioactive tracers.
(b) Inoculate soil and gases under a lamp that simulates Martian sunlight.
(c) Evacuate any remaining free gas.
(d) Heat samples to $590^{\circ} \mathrm{C}$ to vapourize organic materials.
(e) Measure and analyse the vapourised materials.
Liberation of a substantial amount of tracer gas from the samples will be taken as strong evidence that plant-like organisms in the soil consumed the carbon monoxide and carbon dioxide in photosynthesis.

## TEST FOR METABOLIC ACTIVITY

From a science standpoint, the Viking lander is the most complex spacecraft ever built. The biology unit will feed to three soil samples a nutrient or organic compounds like sugar which bear trace chemicals. Instruments will monitor gases given off by the samples over a period of about two weeks.
Steady production of gases by soil samples will be taken as evidence that organisms in the soil consumed the nutrient; steadily increasing production of gases will be taken as evidence of growth by the organisms.

## TEST FOR RESPIRATION

Soil samples will be moistened with nutrients and surrounded in the test chamber with air from the outside, principally carbon dioxide. Constituents of the atmospheric sample will be monitored over a period of about two weeks. Changes in composition of the atmospheric sample will be taken as evidence of


The three-legged lander carries life detection experiments to deter mine if the Martian environment can, or in fact does, support life.
respiration from metabolism of organisms in the soil.

## TEST OF STERILISED SAMPLES

In parallet, a soil sample will be sterilised and subjected to the same tests as further validation of any positive results in the tests.

## BIOLOGY INSTRUMENT

The complete range of experiments planned for Viking, if conducted on Earth with today's standard science instruments, would require thousands of pounds of equipment which would fill several ordinary size laboratory rooms.
The lander biology unit, in 0.25 cubic metre of space, contains: three automated chemical labs, a computer, tiny ovens, counters for radioactive tracers, filters, sun lamp, gas chromatograph to identify chemicals, 40 thermostats, 22000 transistors, 18 000 other electronic parts, and 43 miniature valves.

## THE PLANET AND ITS

## ATMOSPHERE

Instruments of the orbiters and landers will examine the physical features and makeup of the planet and its atmosphere in minute detail. Comparison of the geology and climate of Mars with those of the much more complex Earth and the primitive Moon is expected to resolve many questions about the evolution of Earth and our Solar System.
One landing site is in a valley at the mouth of the giant surface rift, or grand canyon, of Mars. Here, deposits from exposure and erosion of geological features around the chasm are expected to be rich in information about the history and development of the planet.
We still lack a complete understanding of Earth's complex environmental systems; for example, what accounts for the patterns of movement of water vapour and pollutants in our atmosphere. Clues should be found in study of the dynamics of Mars' more rudimentary atmospheric system in the absence of industrialised man.

The Viking Lander. After landing, the Lander's cameras will take pictures of the terrain - some in colour and some emploving both cameras to produce threedimensional stereo pairs. Other instru ments will collect atmospheric and meteorological data, and a seismometer will record Martian quakes and learn about the planet's interior. Surface geology will be examined with the cameras, the soil sampler, the inorganic analysis of soil samples to determine what elements are present.

## GEOLOGY

The orbiters and landers will conduct experiments to study surface geology and planet internal structure, and to determine whether the planet is geologically alive. Orbiter and lander photographs will identify types of land forms, stratification, folds, joints, faults, rocks, erosion, sediments, and soil, and will give indications of mineral and chemical composition. If there are Marsquakes in adequate number, lander seismic readings can determine whether the planet has a molten core, a mantle, and a crust as does Earth, and can allow comparison of the mantles of Mars and Earth.

Lander instruments will identify elements and minerals in the soil. Thermal mapping by the orbiters will allow search for ground frost and evidence of planet internal heat, and will aid in identifying surface structural character from difference in heat conductivity.
Viking radio and radar systems will provide information to improve our knowledge of the planet's size, mass, gravitational field, surface density, and electromagnetic properties, and atmospheric density and turbulence, and will allow study of the solar wind.

## SCIENCE INSTRUMENTS

An infrared radiometer from the orbiter measures heat radiating from the planet's surface. It can record temperatures in both the day and night hemispheres and is accurate within $2^{\circ} \mathrm{C}$.
An X-ray fluorescence spectrometer identifies basic elements in soil by measuring their fluorescence after being exposed to radioactive Cadmium 109 and Iron 55. It can detect elements present in amounts as small as 200 parts per million.

Viking completes a month-long series of rigorous tests designed to qualify it for operating on the surface of Mars in 1976 The tests were conducted in a huge vacuum chamber at the space facilities centre of Martin Marietta Aerospace at Denver, Colorado.

lifent
TITIS
A gas chromatograph-mass spectrometer identifies gases in the atmosphere and organic compounds in the soil.
A seismometer will detect volcanic activity, planet internal structure shifts, and impacts of meteorites on planet's surface. It will be used to determine whether landers are functioning properly by measuring their vibrations.
A soil boom scoop will be used to study characteristics of the soil: cohesiveness, porosity, hardness, particle size. Magnets mounted on the sampler will determine whether soil contains magnetic materials.

## METEOROLOGY

Requirements for the Viking lander are space flight's most demanding: two days of sterilisation baking before launch; ascent gravity and vibration forces atop a 1.4 million pound rocket; 11 months' exposure to harsh space conditions; buffeting from Mars atmospheric entry, parachute opening and retrorocket firing; impact on surface of Mars at a speed of 1.20 metres/sec. Then it must function as a finely tuned, self-sufficient laboratory and data transmission complex for 90 days.
Landers, as they enter Mars' atmosphere, will analyse the ionosphere to determine the effect of the solar wind on the 'planet's atmosphere. As they descend they will record the temperature, pressure, and chemical content of the atmosphere at different altitudes.
The Orbiters will observe the formation and movement of clouds and record their temperatures for analysis of their composition. The lander mass spectrometer will analyse the amounts of carbon dioxide, oxygen, nitrogen, and other gases in the atmosphere at the surface during the first three days, before starting analysis of the soil.

Lander instruments will measure pressure, temperature, windspeed and direction periodically to log daily and season all variations in weather and will record the movement of weather fronts, thermals, and dust devils past the landing sites. Seismometers will record background noise from winds and temperature and pressure changes.


How pictures taken by the television system aboard the Viking Lander will be processed,- transmitted and reconstructed on Earth.

In the meteorology unit transducers will measure temperature and pressure; an anemometer measures windspeed by its cooling effect on a heated wire. Accelerometers and radar altimeter are used to determine atmospheric density and pressure from their drag on the descending lander.
An infrared spectrometer will detect and measure moisture in the atmosphere from the changes in solar radiation, as it reflects from Martian surface through the atmosphere to the orbiter. It can detect water in amounts down to one micron.
A retarding potential analyser will measure the concentration and charge of ions and electrons in the ionosphere as they flow across the analyser's charged grid in lander capsule.
An upper atmosphere mass spectrometer will identify chemical content and concentrations in the upper fringe of atmosphere.
Radio and radar systems consist of S-band Earth-Mars microwave link for commands and data relay; UHF lander-orbiter links; X-band orbiter-Earth link for science use.

## COMMUNICATING WITH VIKING

The Viking Lander Communications subsystem consists of a relay link between the orbiter and lander and a direct link connecting the lander and Earth. UHF radio is employed to relay scientific data to the orbiter, and an S-band transmitter on the lander sends data directly to Earth.
The relay link is the primary means of communicating during descent and landing until a direct link between the lander and Earth can be established on
the surface of Mars. The UHF circuits use compartmentalised, shielded discrete-element wired circuits.
When the lander separates from the orbiter and descends into the Martian atmosphere, UHF radio will begin transmitting real-time data at 4000 bips and at a frequency of 381 MHz to the orbiter. This information will consist of entry science data, such as the pressure and temperature of the Martian ionosphere and atmosphere, and engineering information related to the lander's operating performance.
The subsystem utilises split-phase pulse code modulation and frequency shift keying. A fixed, crossed-dipole antenna radiates signals to the orbiter. Any one of three power modes may be selected.
Prior to separation from the orbiter, the lander will under-go a series of system checks in the lower power mode (1 watt). During descent, the 10 watt mode will be used. After touch-down, the transmitter will be switched to 30 watts power to handle a data stream rate of 16000 bits per second. With succiessful completion of the soft-landing on Mars, the lander will continue to broadcast for about three days via the UHF system before switching over to the direct $S$-band link. Imagery showing the area surrounding the landing site as well as data received from the meteorological and biological sensors contained on the lander can then be transmitted to the orbiter for relay to Earth.
The orbiter will be able to recesve this data from the lander an average of 20 minutes each pass. It will be orbiting Mars once every 24.6 hours.

## S-BAND DIRECT LINK

The lander S-band link directly transmits high-volume scientific and imaging data, Doppler tracking planetary ranging and command reception. The subsystem operates at 2.2 GHz and is coherently locked to Earth frequency references.

Engineering data is transmitted at a rate of $8 \quad 1 / 3$ bits per second and scientific information may be sent at selectable rates of 250500 and 1,000 bits per second. Coded (32/S), phase shift keying-pulse code modulating information is received at 4 bits per second.
A 760 mm parabolic reflector high gain antenna will be used to transmit information from Mars directly to Earth. The high-gain antenna is steerable and utilises a two-axis gimbal. The mast for this antenna is deployed and locked into position immediately upon landing on

Mars. Powered by a d-c driven motor the antenna is stepped to follow the Earth by an open-loop command and control system. A fixed, crossed dipole S-band low-gain antenna will receive the commands from Earth.
Redundant modulator-exciters and a 20 watt travelling wave tube amplifier transmits the telemetry and imaging data at a frequency of 2295 MHz for approximately two hours each day.
Two S-band receivers operate on the same frequency and use the same receiver selector. The receiver selector furnishes signals to the modulatorexciters and the command detectors. One of the receivers is connected to the low-gain antenna and is the primary command receiver. The other receiver is linked to the high-gain antenna to detect the ranging signal. This receiver also serves as a backup command receiver when the high-gain antenna is pointed toward Earth.


Kathy Daniels of Honeywell's aerospace Division examines part of a specialpurpose computer which will control experiments in the Viking. Lander.

## VIKING ANTENNA

The RCA Viking antennas are specially designed and built to operate in the hostile Martian environment. They must withstand harsh surface winds, sand and dust storms, low pressure, and a temperature range from $+111^{\circ} \mathrm{C}$ to $-126^{\circ} \mathrm{C}$.
The UHF low gain antenna is 450 mm high and is located on the Viking Lander Capsule (VLC). It provides for the rapid, high volume transmission of data from the VLC to the Viking Orbiter for relay to Earth. The antenna will operate during both the descent of the VLC to the Martian surface and at programmed times during the mission after touch-down.

An insulating foam designed to minimise power disruptions during transmission in the low pressure Martian atmosphere is enclosed in cylindrical containers on the ends of the antenna elements.

## UHF LOW GAIN ANTENNA Specifications:

Use:
Communications to Earth via orbiter relay.
Operation: During Martian entry and from surface
Type:
Frequency: Crossed dipoles, circularly polarized.

Weight:
The S-Band Low Gain Antenna is' 150 mm . high and is also located on the VLC. It will receive signals from Earth and will operate according to the mission programme after the VLC lands on the planet's surface.

## S-Band LOW GAIN ANTENNA Specifications:

Use:

Operation:
Type:
Frequency:
Weight:

The S-Bànd High Gain Antenna has a 750 mm . parabolic dish of traditional appearance. It will be used to transmit and receive radio signals between the VLC and Earth and will operate after touchdown according to the mission programme. Since Mars, like Earth, rotates on its axis, it is necessary for the S-Band High Gain Antenna to be facing toward Earth for maximum effective transmission. Once the VLC has landed safely on Mars, an on-board computer will calculate the necessary alignment (both azimuth and elevation) for the antenna. The Viking Command and Control, or controller, will transform this information into
commands to motors which will then reposition the antenna relative to Earth. Continuous adjustments will be computed during each transmission. It will take approximately 20 minutes for signals to travel the more than 362 million kilometres between the Viking spacecraft and its home planet.

S-Band High Gain Antenna Specifications:

Use:
Transmit data Mars-to-Earth.
Operation: Martian surface
Dish: $\quad 760 \mathrm{~mm}$ diameter
Frequency: $\quad 2100-2300 \mathrm{mHz}$;
Power: 40 Watts.
Pedestal: Elevation over azimuth
Controller Specifications:
Use: $\quad$ Pedestal drive and encoder processor.
Operation: Martian surface.
Input: Commands from on-board computer.
Output:

Weight:
Size, Max: $\quad 100 \mathrm{~mm} \times 140 \mathrm{~mm} \times$ 178 mm
Configuration: Two 2 -sided four-layer printed circuit boards


Another view of the spectacular 'photo globe' of Mars - the first ever made of any body in the Solar System. It was prepared by the Jet Propulsion Laboratory of California Institute of Technology which managed the Mariner project for NASA.

Electronics Today would like to thank the British Interplanetary Society and the Martin Marienatta Corporation for their assistance with the preparation of this article.

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ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976

Another digital watch? Hardly. The Sinclair 'Black Watch' has as much in common with other digital watches as the calculators of three years ago have with today's products.

There are three distinct differences between this new product and the more common digital watches. Most obvious is the styling - we like it because it is designed for this new technology, it owes nothing to conventional designs. I can however understand those who do not find it appealing but appearance is a very subjective field.

Secondly the case is plastic. Ugh, I hear some of you say but you haven't seen it or felt it. Basically it is a two part case which straps together but the plastic used is unlike any we have seen - Sinclair won't say what it is. It doesn't feel, look or behave like better known plastics. The technology and industrial design behind this case is as exciting as the chip used. The top of the case has two flexible diaphagms incorporated in the moulding only 10 thou thick - this in itself is something that has broken new ground. The case design includes no protruding switches or buttons - the switches for display and for setting are controlled by pressure through the case. Batteries can be replaced in a very short period by removing a malleable plastic plug. Sinclair don't claim the watch is waterproof - but it's certainly water resistant.

The biggest breakthrough is the chip - and only one chip is used. This uses integrated injection logic: I2 L. This brand new technology is only just becoming known to the public. Sinclair believe that their chip - and they themselves designed it - is the first watch chip using I2L. Other companies have been talking about this - but to date it's only talk.

I2L is suited perfectly to digital watch design. Tiny currents are used but this is combined with the ability to supply high peak current when the display is actually used. It also offers high component packing density among other advantages. The chip contains 2,100 active devices in an area $117 \times 123$ thou.

In addition to the chip there is only the crystal - the now near standard 32.768 kHz flexural bar crystal. Fine adjustment of this is obtained by means of a small trimmer. A tiny capacitor connected directly across the battery, is used to prevent very short interruptions in the supply throwing the timing out: this could occur during a violent blow to the watch.

An LED display is used. The poor contrast ratio and doubts about reliability of liquid crystal displays now available led to this choice.

Introducing a digital watch has been the aim of Sinclair ; in fact it was first investigated in 1967. Prototypes were developed but a high frequency crystal ( 4 MHz ) had to be used - this led to high power consumption. LED's weren't around then so neon type displays were used.

In 1972 with the first news of I $^{2} \mathrm{~L}$, the whole programme was turned in this direction. The first semiconductor company decided to pull out leading to a considerable delay. ITT are now making this chip for Sinclair.


When we first saw this watch a: a press launch, we were extremely impressed - so much so that your Editor has placed what may be the first order?

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| 40P2 | PNP | 40 | 40 |
| 90 WATT SILICON |  |  |  |
| Type | Polarity | Gain | vc |
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| 90N2 | NPN | 40 | 40 |
| 90 P 1 | PNP | 15 | 15 |
| 90 P2 | PNP | 40 | 40 |

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[^1]
## Test CMOS and TTL with this versatile instrument.

1

## WARNING:

When using the tester, remember that manufacturers recommend that CMOS ICs should not be inserted or removed from a circuit without first switching off the power supply.

## 匪ivicut <br> 122

EXPERIMENTERS often damage ICs in the process of developing a new circuit and often try a new IC in a circuit that is not working to eliminate that as a possible cause. The result of this is that one usually finishes up with a box full of ICs which are of dubious value. To sort out these ICs one must use a tester that is capable of testing the wide range of differing ICs that are available in the most commonly used families.

Until recently the most commonly used family has been TTL. But CMOS is rapidly gaining widespread usage and any tester, to be of value these days, must be able to test both these families. The ETI Logic Tester is capable of testing both families, and is also capable of being used, to breadboard and test simple circuits based on single ICs.

An LED indicator is associated with each pin of the IC under test and these are arranged around the perimeter of a box representing the IC under test. This allows a small card, which has the

schematic of the particular IC drawn on it, to be fitted to the front of the tester as an aid to the interpretation of the LED test indications.

## CONSTRUCTION

The most expensive single component in the tester, after the transformer, is the case. For this reason we decided to make a wooden case and a plain aluminium front panel. Some people may however wish to mount the unit in a diecast box and for this reason the printed circuit board has been sized to fit in a standard $222 \times 146 \times 51 \mathrm{~mm}$ die-cast box. The following description is for a wooden box specifically, but applies equally well to the metal box.
The printed-circuit board is mounted to the rear of the front panel, copper side to the panel, such that the LEDs and patch pins, mounted on the printed-circuit board, project through the front panel. This greatly simplifies construction as it saves some 48 leads
and solder joints. The switches are secured to the front panel by first glueing two pieces of printed-circuit board to the rear and then soldering the switches to the copper side of the board. This procedure avoids the necessity of a multitude of screws passing through the front panel.
The printed-circuit board should be assembled with the aid of the component overlay by fitting all components with the exception of IC1, 5, 6 and 7, and LEDs 1 through 16, and the patch pins. Check that the ICs are orientated correctly as are also C2, 5, 7, 9 and D1, 2 and 3. Now solder these parts into position using the least amount of heat necessary on ICs 2, 3 and 4.
Position the LEDs and patch pins onto the copper side of the board but do not solder them in place as yet. Now fit the board to the front panel so that the pins and LEDs protrude through the panel evenly. Secure the pins and LEDs in position by using a very small drop of five minute epoxy for each, on the component side of the

## HOW IT WORKS.

The tester consists of four basic sections. The socket for the IC under test, the output level-detect logic, oscillators and switches for the inputs, and the power supply.
The socket for the IC under test has the pins in each row electrically connected to each other. These rows are the groups of five holes which are perpendicular to the central groove on the socket. Each row (ie, each pin on the IC under test) is connected via a 10 megohm resistor to ground to prevent the build up of static charges. The resistors also hold all unconnected inputs at ground potential thus preventing any damage to the IC.
Each row is also connected to a pin
on the front panel. Test connections are made to these pins by patchable links from the oscillator and test switches so that the correct test conditions may be set up.
Resistors R19-26 and R43-R50 connect each row (ie pin) to a logic level detector, ICs 5, 6, and 7. These CMOS hex-inverters buffer each pin and drive an LED to indicate the logic state of the pin. When the logic voltage on a pin is high the LED will be alight. Resistors R19 to R26 and R43 to 50 protect the internal diodes of ICs 5, 6 and 7 against the possibility of a pin being taken above the positive supply voltage or below ground potential. Resistors R11 to R18 and R51 to R58 in conjunction with the five volt supply set the
operating currents for the LEDs.
A 555, IC4, is used as an astable oscillator which initially charges C8 via $R 9$ and R10 until the $2 / 3$ supply threshold is reached. C8 is then discharged via R 9 and pin 7 of the 555 to the lower threshold of $1 / 3$ supply volts. Switch SW6, when operated, puts a larger value of capacitance into the circuit which gives a frequency of about one hertz. This is slow enough so that the eye can follow each logic state transition. The high speed operation is used for checking very long counters and shift registers and can also be used in conjunction with an oscilloscope. The square wave output of the oscillator is made available at. a

patch-pin on the front panel.
There are six further output pins on the front panel three of which, D, E and $F$, are set to negative or positive supply by means of toggle switches. As there is no debounce logic associated with these pins they can ônly be used to set up static conditions and not for clocking counters and shift registers. The remaining three pins are also programmed by switches but these switches are connected to IC1 which contains three RS flip-flops to effectively remove any contact bounce of the switches. This operates as follows. If initially the input of IC $1 / 5$ is earthed by SW2 its output will be high and hence the output of IC
$1 / 6$ will be low. When IC $1 / 6 \mathrm{SW} 2$ is operated again it earths the input of IC $1 / 6$ sending the output of IC $1 / 6$ and input of IC $1 / 5$ high and the output of IC $1 / 5$ low. Since the input of IC $1 / 6$ is connected to the output of IC $1 / 5$ it is held low even if the contacts of SW2 bounce several times when the switch is operated. Thus the output at $A$ is one single transition from high to low (low to high when next the switch is operated). The output of the three debounced switches are labelled on the front panel as $\mathrm{A}, \mathrm{B}$, and C .
In the power supply diodes D1 and D2 full-wave rectify the output from the power transformer. The output from the rectifier is smoothed by C 2
and regulated to five volts by IC3. The resulting five volt supply is used to drive the LED indicators and to power the TTL device under test. Integrated circuit IC2, a type 723, is a regulator the minimum output of which is set to five volts by RV1 and the maximum of 15 volts by RV3. Front panel control RV2 allows the output voltage to be adjusted between five and 15 volts. The current limit on the output is set to 30 mA by means of R8. SW5 selects the high current five volt supply for testing TTL or the low current variable supply for CMOS. Terminal J 1 in the negative supply lead is provided for checking the current drawn by the IC under test.

## LOGIC TESTER

Fig. 2. How the components are mounted on the pc board.



## (Text.continued from page 71)

boards. Do not glue the LEDs to the front panel. Once the glue has set, carefully remove the board from the front panel and then solder the LEDs and pins into position. Fit 250 mm long leads to the board for later connection to the switches and power
transformer and then, using a minimum amount of heat, solder ICs 1,5, 6 and 7 into position.
Solder the leads to the pins on the IC socket - the front panel must be cut out so that these leads may be passed through. Now affix the socket to the front panel and install the printed circuit board. Mount
the transformer into the base of the box and interconnect the board and switches etc.
The wooden box was constructed from 12 mm thick pineboard such that the outside dimensions were 225 x $148 \times 70 \mathrm{~mm}$. We finished our box with coloured high-gloss enamel which


Fig. 3. Wiring diagram of complete unit.

## LOGIC TESTER



TERMINAL POSTS
$\odot \odot \odot \odot \odot \odot \odot$

$\odot \odot \odot \odot \odot \odot \odot \odot \odot \odot \odot$
Fig. 4. Positioning of LEAs and - terminal posts on the copper side of the printed-circuit board.


MAT: 16 GAUGE ALUM
Fig.6. Heatsink for IC3. The IC is mounted (by a screw) through a 3.2 mm hole in the base of the heatsink (see photograph of inside of unit).


Fig. 5. How the front panel and printed-circuit board are assembled.


## LOGIC TESTER



Fig.8. Front panel artwork (shown half-size - full size should be $223 \mathrm{~mm} \times 148 \mathrm{~mm}$ ).
resulted in a very pleasing final appearance.

## DESIGN FEATURES.

There are several design requirements which must be met in a unit which is designed to test both CMOS and TTL devices. These may be summarized as follows.

1) The unit must be capable of correctly testing both types of logic.
2) Simple gate functions should be tested by go/no-go checks and complex functions such as counters and shift registers should also be reliably checked.
3) There should be the least possible chance of damaging the device during testing.
4) CMOS ICs must be testable with a variety of supply voltages.
5) A clock oscillator and a means of setting up the input conditions must be provided.
One of the major design difficulties with a unit such as this is coping with the many different pin configurations of the differing functional requirements (eg a shift register versus a two-input NAND gate) of devices within the one family, as well as those between different families. A multi-way switch could be used for each input pin but would greatly increase the expense of the unit. A good alternative is to use patchable links, and this is the approach that we have chosen to use in our unit. In addition we have used a small breadboard socket as the test socket, rather than a standard 16 pin dual-in-line socket, as this allows us to improvise special test circuits for the
more complex logic ICs, and the means to breadboard simple circuits. The need for a variable power supply for CMOS testing presented two additional problems. The first of these was the danger of plugging a TTL IC into the unit when it is set up for CMOS and for some higher supply voltage than the five volts required for TTL. Secondly the LEDs used for monitoring each pin would draw more current as the supply voltage increased. The current ratio could be as high as four to one and a corresponding variation of LED intensity would occur. To overcome this problem it was decided to provide a second supply of five volts to operate the LEDs which will also provide the higher current required by TTL for its operation. The other supply is a variable one for testing CMOS and is not capable of supplying more than 30 mA . Thus a TTL gate inadvertentl.y connected to this supply would not be damaged.
The regulator used for the five-volt supply is a three terminal IC which has built in current limiting and thermal shutdown. It will not therefore be damaged by a short circuit due to testing a faulty IC. It is not possible to construct a discrete design, as cheaply, that has the same performance.
Next we need a device that will detect the state of each pin on the device under test and drive an LED to indicate that state. The device has to be driven by TTL and CMOS outputs, that is, by voltages anywhere between 5 and 15 volts. A suitable IC is the CMOS 4009 IC which has six inverters in one package. Each inverter will monitor a pin without drawing
appreciable current. The 4009 is also designed to translate. logic levels. Thus we may use it to monitor a 5 to 15 volt input level at its input but provide a five volt signal only at its output.
Switches are provided which have debounce logic associated with them. This is necessary so that single bounce free rise and fall transitions can be generated for the testing of more complex logic. The debounce logic must be capable of operating on 5 to 15 volts and of sinking at least two milliamps for TTL tests. The 4009 IC with its high, output current capability was again considered to be most suitable for this task.
We would also like to have used the 4009 as the oscillator, but RCA do not recommend using CMOS that has a high output capability in a linear mode as the power dissipation of the device may be exceeded. The oscillator must provide pulses that swing between the positive and negative supply rails (in order to drive CMOS) and must be capable of sinking the two milliamps required by TTL. It must also be capable of operating on supply voltages of 5 to 15 volts. Since the standard CMOS devices cannot provide the current requirement it was decided to use a 555 IC as the oscillator.

CMOS devices should not be operated with inputs left floating as some devices may drift into the linear mode and be destroyed by excessive power dissipation. For this reason. a 10 megohm resistor is connected between each pin, on the test socket, and ground. These resistors also conduct away any static charge that may build up.



IF YOU HAVE CONSIDERED buying a scientific calculator recently then you will have been faced with a bewildering choice. One of the larger retail outlets lists 28 different models in the scientific category with prices ranging from about $£ 20$ to over $£ 440$. This survey sets out to cover the more common models with prices up to about $£ 120$ though the models chosen can only be representative of those available - however this survey will enable comparisons to be made with any new or not so common models that may be found.

Early in 1972 Hewlett-Packard introduced the HP-35, one of the first pocket sized scientific calculators, which then cost £199. The HP-35's calculating power was contained in 5 MOS I.C.'s.

Earlier this year, the same company introduced the HP-21, a calculator with a slightly greater calculating power than the HP-35 but selling at only $£ 69$. This dramatic price reduction has been, and still is, common to all calculator manufacturers and thus the calculators covered in this survey range in price from $£ 19.95$ (recommended retail price).

These price reductions have been due to several factors, one of which is obviously competition, as most serious calculator manufacturers now have a scientific in their model range. Another factor has been the steady advance in semiconductor technology during the very recent past. The HP-21 uses more logic circuitry than the original HP-35 yet contains only two I.C.'s against the five of the HP-35.

One new development which appears as exciting as the introduction of the first scientific is the recent introduction of the programmable scientific calculator. There are now at least three major manufacturers offering programmables covering a price range from $£ 29.95$ to $£ 442$. (The cheapest, the Sinclair Scientific

Programmable, was the subject of a review in the November issue of ETII. Programmables are extremely valuable for the solution of repetitive calculators involving a large number of calculating steps having to be performed on many items of separate data. This may well occur when handling experimental data or tabulating a graph for instance. Most manufacturers offer a library of standard programs with their machines. The individual program steps are stored in either a solid state memory or on a magnetic card which is fed into the calculator by a small motor.

There are three tables covering the calculators in this survey, Table 1 deals with those machines which calculate and display in standard floating print (arithmetic) format only, Table 2 covers those calculators which calculate and display in floating point and/or scientific notation (separate mantissa and exponent) and one model which uses scientific notation only. Table 3 covers the programmables.

## choosing a scientific <br> CALCULATOR

With such a vast range of scientifics available in more or less every price range, it is important that the right factors are considered before money is invested in the calculator of your choice. The first decision is which type of calculator will meet your needs - usually the cheaper mpdels do not offer the option of scientific notation and so are limited in their calculating range to numbers between $10^{8}$ and $10-8$. If you want to handle numbers outside this range then a machine which handles full scientific notation will save the bother of keeping a separate note of the powers of 10 involved, this may well happen when handling calculations in electronics with microfarads, nano seconds, giga hertz etc. being involved.

The programmable calculator is at present being hailed as the answer to everybody's dream of infinite, instant calculating power but in general they are considerably more expensive than their non-programmable equivalents.

Having chosen the type pf calculator required then the available range of that type must ne carefully examined for there are several important differences between models of the same basic type.

The most important decision to be made when choosing a scientific is that of which number entry system the calculator should use. The two systems found are Algebraic and Reverse Polish Notation (RPN), sometimes called Post Fixed Operators. The difference can be seen clearly by following the sequence of keystrokes needed to add two numbers together:

ALGEBRIAC

| Key | Display |
| :---: | :---: |
| 2 | 2 |
| + |  |
| 3 |  |
| $=$ | 2 |
|  | RPN |
| Key |  |
| 2 |  |
| enter |  |
| 3 |  |
| + | 2 |
| + | 3 |
|  |  |

This sum requires the same number of keystrokes in each system but the algebraic systems operates the keys in the same order as the sum would be read aloud whereas the RPN system has this mysterious "ENTER" key. Algebraic is certainly a simpler system to operate initially but RPN has a lot going for it, especially in the field of complex scientific calculators. If combined with a working stack of data registers, RPN system gives a very great calculator power with instant access to parentheses without keying in and out of them. A four level stack


TABLE 1 STANDING FLOATING POINT ONLY MACHINES

| MODEL <br> Feature | $\begin{aligned} & \text { ADLER } \\ & \text { 88T } \end{aligned}$ | $\begin{aligned} & \text { DECIMO } \\ & 2001 \end{aligned}$ | HANIMEX ESR100 | Novus $4510$ | ROCKWELL $61 R$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Recommended Price | $£ 49.95$ | £49.95 | £45.00 | £49.95 | £49.95 |
| Rechargable | Extra | Mains adaptor extra | Mains adaptor extra | $\checkmark$ | $\checkmark$ |
| Digits | 8 | 8 | 8 | 8 | 8 |
| Display | LED | LED | Fluorescent | LED | Fluorescent |
| No. Keys | 25 | 25 | 26 | 32 | 20 |
| RPN/ALG | ALG | ALG | ALG. | RPN | ALG. |
| Stack |  |  |  | 3 Level |  |
| Brackets | 1 set |  |  |  |  |
| Ind. Mem. | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Trig | $\checkmark$ | $\checkmark$ | $V$ | $\checkmark$ | $\checkmark$ |
| Radians | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Degree | $\checkmark$ | $\checkmark$ | 1 | $\checkmark$ | $\checkmark$ |
| 1/x | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\checkmark x$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $x^{2}$ |  |  |  | $\checkmark$ |  |
| $\mathrm{M}+{ }^{\text {x }}$ |  |  |  | $\checkmark$ | $\checkmark$ |
| $\pi$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | 1 | $\checkmark$ |
| $\mathrm{e}^{\text {r }}$ | 1 | $V$ | $\checkmark$ | $V$ | $\checkmark$ |
| In $x$ | $V$ | $\checkmark$ | $\checkmark$ | $V$ | $\checkmark$ |
| $\log x$ | $\checkmark$ | $V$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $10^{x}$ |  |  |  |  | $\checkmark$ |
| $\mathrm{x}^{7}$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $V$ |
| $\mathrm{M} \rightarrow \mathrm{x}$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ |
| $X \leftrightarrow y$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $V$ |  |
| Change Sign |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |

is equivalent to three levels of parenthesis (brackets). However the operator of a RPN machine needs to be able to transform a complex equation such as:
$x=\tan ^{-1}\left(\left(3.5^{3}+8 \sin 370\right)+6(\cos 530\right.$ $\left.+2(1+\ln 3.55))^{\frac{1}{3}}\right)$
into a number of 'enter' keystrokes whilst the algebraic machine operator can, if he has sufficient 'brackets' available, enter the problem as written.

A good argument can be made out for each system and the choice must be made on your ability to understand what you are doing. But
do not dismiss RPN as too difficult to understand, have it demonstrated and then try some problems yourself - you might be surprised at the ease with which you can pick up a new system.

One of the arguments advanced in favour of the RPN system is that it is consistent when dealing with the more complex keyboard functions such as the trig functions, logs etc. If you wish to find the 'sin' of $30^{\circ}$ then the key sequence for each type of machine is the same- ' 30 ' 'sin' which indeed is the sequence expected for
a RPN machine, but the reverse of that expected for the algebraic machine - you might find this an inconsistency.

Manufacturers of both types of machines usually offer a separate memory as well as an multi-level stack in the RPN machine or one or more sets of 'brackets' in the algebraic models. Often the choice may be one level parenthesis plus two separate memories or two levels of parenthesis plus one memory.

Having made this decision, which is the most important one, then the next factor to be considered is the type of display and number of digits to be displayed. The most popular type of display is the LED type which is usually red in colour. The cheaper models of calculators use small LED displays with magnifying lenses fixed in front. These lenses tend to narrow the viewing angle (the angle over which the display may easily be read). This is fine in a small pocket calculator but not as satisfactory in an expensive scientific model. The other, not so common type of display is the flourescent type - this is a green display which generally is larger than the LED type and the whole display is built into an evacuated glass envelope. The basic display is more expensive than a LED type and does require more complex additional circuitry (high voltage generators etc) but has the advantage of extremely low power consumption.

Having mentioned power consumption it is a useful next point for consideration; if your choice of calculator is available with or without rechargeable batteries then the extra outlay on the rechargeable version may well be worth considering, the PP3 type battery may be fairly cheap but with the current required by some of the more complex calculators (especially those with LED displays) it will not take all that long to run down a PP3 and so the extra cost of the rechargeable batteries is soon made up.

The next factor for consideration is the keyboard and its layout. The calculators surveyed here range in number of keys from 48 down to a full scientific with only 18 keys. The lesser number of keys, the smaller the calculator and thus the more easily pocketable but with the disadvantage that there are often two keystrokes required for access to a scientific function. Thus if you are going to use the full range of the calculator fairly often then you might well find double function keys awk. ward to use. The only double function keys which are obvious are those of
main text continued on page 31

# SCIENTIFIC CALCULATORS 

1. All quoted prices are the NOTES FOR TABLE 2
. Al quoted prices are the manufacturers. recommended prices, discounts of up to about $40 \%$ are not uncommon on some models.
2. Special type of algif notation only for the input of data and the displaying of results. of the sums of products without using the register which allows the direct evaluation separately accessible.
3. Operates in grads as
. Additional function: Sexagesimal to decimal conversion, i.e. hours, minutes, seconds to EE4 and EE Keys.
4. EEq and EE keys provided.
5. Twelve additional features: $\binom{n}{k},(n) k, \operatorname{Pr}(x), \Gamma(x), \Sigma \pm\|x\|, k \uparrow, k \downarrow, \Sigma \uparrow, \Sigma \downarrow, C l_{\text {grp }}$,
6. Additional features; full arithmetic on all three separately addressable memories, selectable decimal point, live \% key with add on and discounting, \% diffrence between $\times_{1}$ and $\times 2$, linear regression, random number generation, permutations, twenty separate conver-
sions and their inverse. sions and their inverse.

TABLE 2 FLOATING POINT AND SCIENTIFIC NOTATION.

| Model <br> Feature | Casio <br> FX 15 | $\begin{aligned} & \text { C.B.M. } \\ & \text { SR890 } \end{aligned}$ | $\begin{aligned} & \text { C.B.M. } \\ & \text { sRggo } \end{aligned}$ | $\begin{aligned} & \text { C.B.M. } \\ & \text { SR } 6120 R \end{aligned}$ | $\begin{gathered} \text { C.B.M. } \\ \text { SR4148R } \end{gathered}$ | $\begin{aligned} & \text { Decimo } \\ & 2001 E \end{aligned}$ | Dixons <br> Mini | $\begin{gathered} \text { Dixons } \\ \text { SC4001M } \end{gathered}$ | $\begin{aligned} & \text { Hanimex } \\ & \text { ESR } \\ & \text { 1010SN } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Rec. Price (1) | £49.50 | £22.95 | £29.95 | £39.95 | £49.95 | $£ 59.95$ | $£ 19.95$ | $£ 24.95$ | £49.50 |
| Rechargeable | $\begin{gathered} \text { A.C. adaptor } \\ \text { extra } \end{gathered}$ |  |  | $\checkmark$ | $\checkmark$ |  |  | $\underset{\text { extra }}{\text { A. adaptor }}$ | $\mathrm{A}_{\text {extra }}^{\text {adaptor }}$ |
| Digits | $8 \mid 6+2$ | $8 \mid 5+2$ | $8 \mid 5+2$ | $8 \mid 8+2$ | 10\|10+2 | $8 \mid 8+2$ | $8 \mid 5+2$ | $8 \mid 5+2$ | $8 \mid 5+2$ |
| Display | Fluorescent | L.E.D. | L.E.D. | L.E.D. | L.E.D. | L.E.D. | L.E.D. | Fluorescent | Fluorescent |
| No. Keys | 37 | 29 | 39 | 39 | 48 | 35 | 19 | 35 | 35 |
| RPN / Alg. . | Alg. | Alg. | Alg. | Alg. | Alg. | Alg. | Alg. | Alg. | Alg. |
| Stack |  |  |  |  |  |  |  |  |  |
| Brackets |  |  | 1 set | 1 set | 1 set |  |  |  |  |
| Ind. Mem. | $\checkmark$ | 2 | 2 | 2 | 2 | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Trig. | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Radians |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ (4) | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Degree | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ (4) | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Deg./Rad. Conversion |  |  |  |  |  |  |  |  |  |
| $\sqrt{x}$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\times \sqrt{ } \mathrm{Y}$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |  |
| $x^{2}$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |  |
| $x^{y}$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |
| Rect $\leftrightarrow$ Polar Co-ard Conv. |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |  |
| Hyperbolic functions |  |  |  |  |  | $\checkmark$ |  |  |  |
| $\ln x$ | $V$ | $V$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $v$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $e^{x}$ | $\checkmark$ | - | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\log x$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |
| $10^{x}$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |  |
| 1/x | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| X! |  |  |  |  |  |  |  |  |  |
| Mean \& stand deviation |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\cdot$ |  |  |  |
| Sum to mem. | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |  |
| $\mathrm{M} \rightleftarrows \mathrm{x}$ |  |  |  |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| +/-key | - V | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |
| $\mathrm{x} \rightleftarrows \mathrm{y}$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  |  |
| $\pi$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Other Functions | (5) |  | (6) | (6) | (6) |  |  |  |  |



| Hanimex ESR Master | Novus 4520 | Howlet <br> Packard <br> HP 21 | $\begin{gathered} \text { Qualitron } \\ 1448 \end{gathered}$ | Qualitron $1419$ | Qualitron $1420$ | Rockwall $637$ | Sinclair Scientific | Sinclair Oxford 300 | $\begin{aligned} & \text { Toxas } \\ & \text { SR } 50 \end{aligned}$ | Toxas SR 51 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| £49.95 | $£ 72.45$ | $\pm 69.10$ | £27.33 | £31.25 | £61.55 | £59.95 | $\pm 21.55$ | £32.35 | $£ 59.95$ | $£ 89.95$ |
| ${ }_{\text {A }} \mathrm{C}$. adaptor ${ }_{\text {extra }}$ | $\checkmark$ | $\checkmark$ | Extra | Extra | Extra | $\checkmark$ |  | $\begin{gathered} \text { A.C. adaptor } \\ \text { extra } \end{gathered}$ | $\checkmark$ | $\checkmark$ |
| $8 \mid 8+2$ | $8 \mid 8+2$ | $10 \mid 8+2$ | $8 \mid 5+2$ | 10\|10+2 | $10 \mid 10+2$ | $8 \mid 8+2$ | $5+2$ only (2) | $8 \mid 5+2$ | 10\|10+2 | $10 \mid 10+2$ |
| Fluorescent | L.E.D. | L.E.D. | Fluorescent | L.E.D. | L.E.D. | Fluorescent | L.E.D. | L.E.D. | L.E.D | L.E.D. |
| 25 | 35 | 27 | 35 | 35 | 40 | 25 | 18 | 19 | 45 | 45 |
| Alg. | R.P.N. | R.P.N. | Alg. | Alg. | Alg | Alg. | R.P.N. | Alg. | Alg. | Alg. |
|  | 4 level | 4 level |  |  |  |  |  |  |  |  |
| 2 sets |  |  | . |  | 2 sets | 2 sets |  |  | 1 (3) | 1 (3) |
| $v$ | $\checkmark$ | $\checkmark$ | 1 | 1 | 3 | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | 3 |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\checkmark$ |  |  |  |  |  | $\checkmark$ |  |  | $\checkmark$ |  |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |
|  |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
|  |  |  |  |  | $\checkmark$ |  |  |  | $\checkmark$ | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  | $\checkmark$ | $\checkmark$ |
|  |  | $\checkmark$ |  | 4 |  |  |  |  |  | $\checkmark$ |
|  |  |  |  |  |  |  |  |  | $\checkmark$ | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\downarrow$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | 1 |  | $\checkmark$ | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  | $\checkmark$ |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | $V$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |
|  |  |  |  |  | $\checkmark$ | $\checkmark$ |  |  | $\checkmark$ | $\checkmark$ |
|  |  |  |  |  | $\checkmark$ |  |  |  |  | $\checkmark$ |
| $V$ |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | $\checkmark$ |  |
| 1 |  |  | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ |  |  |
| $\checkmark$ | $\checkmark$ | $\checkmark$ | 1 | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |  | $\checkmark$ | $\checkmark$ |
| 1 | $\checkmark$ | $\checkmark$ |  | $\checkmark$ |  | $\checkmark$ |  |  | $\checkmark$ | $\checkmark$ |
| $\dagger$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | $\checkmark$ | 1 | $\checkmark$ |
|  |  |  |  |  | (7) |  |  |  |  | (8) |

# SURUEY OF SCIEMTIFIC CALCULATORS 

## NOTES FOR TABLE

1. Also special engineering notation capability - expressing exponent of number in multiples of three.
2. Also calculates in 'grads' for trig. functions.

Additional features: $\sum+, \sum-$, reçtangular to polar coordinate conversion and viceversa, mean, standard deviation, $\Sigma_{x^{2}}, \Sigma y$. $\Sigma x y$, integer value $x$, fractional portion $x$, absolute value $x, \pi, 1 / x, \%$, change sign, $x \leftrightarrow y$, decimal angle/time $\rightarrow$ angle in degress/hours, minutes, seconds.
4. Not $10^{x}$.
5. Additional features: $M+x, M-x, M+x^{2}, \pi$, change sign, $x \leftrightarrow y$,
6. Not $x^{2}$
7. Additional features: $x \leftrightarrow y, \pi$, change sign.
8. SIn $x, \cos x, \arctan x$ only: key sequences are given for $\arcsin x, \operatorname{arc} \cos x$ and $\tan x$. (sequences of between 9 and 12 keystrokes).


Hewlett-Packard HP25

TABLE 3 PROGRAMMABLE SCIENTIFIC CALCULATORS

| MODEL <br> FEATURE | DIXONS PROGRAM | $\begin{aligned} & \text { HEWLETT } \\ & \text { PACKARD } \\ & \text { HP. } 25 \end{aligned}$ | Novus 4515 | $\begin{gathered} \text { NOVUS } \\ 4525 \end{gathered}$ | QUALITRON 1421 | SINCLAR PROGRAMMABLE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Price | E49.95 | £119 | £99.95 | £119.95 | £45.97 | £29.95 |
| Rechargable |  | $\checkmark$ | $\checkmark$ | $\checkmark$ |  | Mains adaptor supplied. Not re chargable batteries |
| Digits | 8 | $10 / 8+2^{\prime}$ | 8 | $8 / 8+2$ | 8 | $15 \times 2$ |
| Display | L.E.D. | L.E.D. | L.E. D. | L.E.D. | L.E.D. | Fluorescent |
| No. Keys | 36 | 34 | 36 | 39 | 36 | 19 |
| R.P.N./ALG | R.P.N. | R.P.N. | R.P.N | R.P.N. | R.P.N. | R.P.N. |
| Stack/Bracket | 3 level stack | 4 level stack | 3 level stack | 4 level stack | 3 level stack | 2 level stack |
| Ind. Mem. | - V | 8 | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |
| Prog. Mem. (Steps) | 102 | 49 | $>100$ | 100 | 102 | 25 |
| Trig | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $V^{8}$ |
| Rads / Degree | $\checkmark$ | $V^{2}$ | 'V | $\checkmark$ | $\checkmark$ | Radians only |
| Rads/Degree Conversion | $\checkmark$ |  |  |  | $\checkmark$ |  |
| Direct <br> Branching |  | $\checkmark$ |  |  |  |  |
| Conditional Branching |  | $\checkmark$ |  |  |  |  |
| Jests |  | $\begin{aligned} & x \geqslant 0, x<0 \\ & x \neq 0, x=0 \\ & x \geqslant y, x<y \\ & x \neq y, x=y \end{aligned}$ |  |  |  |  |
| Single Step | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |
| Back Step |  | $\checkmark$ |  |  |  |  |
| Pause | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |
| $E^{x}, 1 n x$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |
| $10^{x}, \log x$ | $V^{4}$ | $\checkmark$ | $V^{2}$ | $V^{4}$ | $\checkmark^{4}$ | $\checkmark$ |
| $x^{2}, \sqrt{ } \times$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $V^{6}$ | $\checkmark$ | $\checkmark$ |
| $y^{x}$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ | $\checkmark$ |  |
| Others. | $V^{5}$ | $V^{3}$ | $v^{5}$ | $v^{\prime}$ | $V^{5}$ |  |

## text conintued from page 27

trig and inverse trig function keys. Pressing 'arc' 'sin' seems a good use of the double function key and saves two keys on the keyboard.

Whilst discussing keyboards, it is worth considering the feel of each key and whether there is any 'breakaway' action to a keystroke or whether the key has a 'spongy' feel to it. There is particular advantage to either system and it is really a matter of personal choice, though it does seem as though the more expensive the machine, the more positive the action of the keys. Again the recommendation is to try several different models and see which you like.

In the tables it has been impossible to list every function for each model so the tables have only the main functions and the models which have, extra, less common functions have a separate note at the end of the table.

A recent feature introduced on the latest range of CBM scientifics is worthy of note, especially to electronic enthusiasts, is the provision of two keys marked 'EE4' and 'EE $\downarrow$ '. These keys are used when the machine is being operated in the scientific notation to increase or decrease the value of the exponent whilst shifting the position of the decimal point in the mantissa. This feature appears to be very useful in electronic calculations when we tend to come across component values whose unit values are separated by a factor of $10^{3}$. Thus if a time constant calculation is being carried out and the answer comes out as a required capacitor of ' $3.3 \times 10^{-10}$ ' Farads then by pressing the 'EE $\downarrow$ ' key twice the display will show ' $330 \times 10-12^{\prime}$ or more simply 330 pF .

As may be seen from the tables, several manufacturers are now offering statistical functions on their more expensive models. These are usually the mean and standard deviation of a set of numbers and these are very useful when handling a set of experimental results and trying to analyse some trend or conclusion from them. The one problem with statistical functions in a scientific calculator is that some of them use the calculator memories during the computation of the statistic so those memories are not available for other use during the finding of statistical functions.

Well, good luck in your research for a calculator that will meet your needs. Probably the final piece of advice that we can give is to have a good look at all the models available in your local dealers - try them all out on the same sequence of problems and see which seems to be most convenient to you - and also have a
good look at the handbook supplied with the machine; it might make all the diffrence one day when you are stuck if your machine has a comprehensive handbook.

We would like to thank all those manufacturers who have assisted in the preparation of this article by supplying data and handbooks etc. A list of addresses of the major manufacturers appears at the end of this article.
MANUFACTURERS AND AGENTS CASIO

Automatic Business Machines Ltd,
A.B.M. House,

11 Wyfold Road,
London SW6.
C.B.M.
C.B.M. Business Machines Ltd,

446 Bath Road,
Slough, Bucks, SL1 6BB.
DECIMO
Decimo Limited,
Park House,
96-98 Park Street,
Luton, Beds. LU1 3EX.
DIXONS
Dixons Photographic Limited, Devonshire House,
Pinner Road,
Harrow.
HANIMEX
Hanimex Calculator Division, Hanimex House, Dorcan,
Swindon, Wilts. SN3 5HW.
HEWLETT-PACKARD
Hewlett-Packard Limited,
King Street Lane,
Winneresh, Wokingham,
Berkshire, RG11 5AR.
QUALITRON
Imtech Products Limited,
Imp House,
Ashford Road,
Ashford, Middlesex.

## NOVUS

National Semiconductor (UK) Ltd, The Precinct,
Broxbourne,
Hertfordshire EN10 7HY.
ROCKWELL
Rockwell International,
Anita House,
Rockingham Road,
Uxbridge, Middlesex, UB8 2XL.
SINCLAIR
Sinclair Radionics Limited,
London Road,
St. Ives, Huntingdon,
Cambs. PE17 4HJ.
TEXAS
Texas Instruments Limited, European Calculator Division, 165 Bath Road,
Slough, SL1 4AD,
Buckinghamshire.

## GLOSSARY OF SCIENTIFIC CALCULATORS TERMS

Some of the terms associated with scientific calculators originated in the computer world and are therefore may be now to many engineers. Some of the terms commonly found in calculator handbooks and advertisements are given in this glossary together with brief explanations.
REGISTERS: The names of the memories in which data is stored whilst it is being operated on or used as a longer term store known as a memory. The $X$ register is the register that is used to hold the data that is shown in the display. Thus the $X$ register holds the last keyboard entry during a calculation or the answer when the calculation is terminated.

The $Y$ register is used to store the second number during those operations requiring two variables $\left(+,-, \div, V^{x}\right)$.
STACK: A series of extra data registers, found especially in calculators using Reverse Polish Notation. The stack is used as a 'first-in, last-out' type of memory. Data is shifted into the stack by pressing the enter key, or by entering a new number after pressing an operational key, and is shifted down by pressing an operational key. The lowest registers of the stack are the $X$ and $Y$ registers described above and any subsequent registers become temporary storage registers on a non-randomly accessible basis to allow storage of intermediate results prior to their re-use with a later completing operation. Thus, access to parenthesis (brackets) is automatic upon pressing of the 'enter' key. A four level stack ( $X, Y, Z, t$ ) has the capability of three levels of parenthesis (three sets of brackets).
MEMORY: One or more sets of data registers all of which are randomly accessible. It is often possible to act upon the data contained in the memory with the data in the $X$ register by using keys such as $M+, M-M \times, M \div$ and sometimes data in the memory and data in the $X$ register can be interchanged by using the $M \leftrightarrow X$ kev. $X \leftrightarrow Y$ is the key which directly interchanges the contents of registers $X$ and $Y$. This key is mainly used when using the function $X Y$. although it can also be used when interchanging the terms in a division or a subtraction.
STANDARD DEVIATION: A statistical measure used most often when analysing experimental data. The standard deviation of a set of data is the measure of the dispersion of data values about the mean.
LINEAR REGRESSION: This is a statistical function used again when handling experimental data. It is especially used when using an experiment to find a mathematical relationship between two variables. Linear regression is the name of the procedure which is used to find the line which best fits the set of data points which have been found experimentally. The procedure usually finds the equation of the straight line and also a parameter called the correlation coefficient which indicates how well the data fits the line.
RADIANS: A measure of angle like the degree; 1 radian $=57.30$ approx, and $2 \pi$ radians $=360^{\circ}$. Many problems use, or give results directly in radians. Thus a calculator capable of handling degrees and radians is extremely useful as is an easy conversion between the two.


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# $50+50$ WATT  

Details of the basic amplifier used in the Crossover amplifier. The module can be used to boost the output of a low power stereo to 50 W per channel!


THE CROSSOVER AMPLIFIER described in this issue uses the power amplifier module out of the International 422 amplifier - described in the August 1974 issue of Electronics Today. For new readers who do not have this issue we are publishing details here.
Most of the electronics is mounted on either the printed circuit-board or on the heatsinks. The board may be
assembled in accordance with the component overlay diagram given. Note that capacitors C25, 26, 27 and 28 do not have holes provided for them and they are therefore mounted directly across resistors R33, 34, 41 and 42 respectively.
The heatsink should be assembled as shown in the photograph and the drawing. The transistors Q13 and 14 should each be epoxied into a hole in
one of the heatsinks to ensure good thermal contact. Also secure all leads to the heatsink with epoxy. The interconnections between the printed circuit board and the heatsink should be carried out in accordance with the wire numbering on the diagrams. Final wiring details are given in the respective separate projects.
text continues on page 36 .

## HOW IT WORKS -

The input signal is fed via Cl and R1 to the base of Q3 which, with Q7, forms a differential pair. Transistor Q5 is a constaint current source where the current is 15.6 V (ZD1) -0.6 (Q5)]/2700 (R7) - that is about 2 mA . This current is shared by Q3 and Q7. Transistor Q9 is also a constant current source supplying about 10 mA which, if no input signal exists, flows through Q13 and Q11. The differential pair controls Q11 and thus the voltage at its collector.
The resistors R19 and R21, together with potentiometer RV1, control the voltage across Q13 and maintain it at about 1.9 volts. But as Q13 is mounted on the heatsink, this voltage will vary with heatsink temperature. Assuming that the voltage af points 5 and 9 is equally
spaced about zero volts (ie $\pm 0.95$ volts), the current will be set at about 12 mA through Q15 and Q17. The voltage drop across the 47 ohm resistors (R25 and R31) will be enough to bias the outpht transistors, Q19 and Q20, on slightly to give about 10 mA quiescent current. This quiescent current is, adjustable by means of potentiometer RVI.
Local afeedback is applied to the output stage by the network R33, R35, R39 and R41, giving the oufput stage a voltage gain of about four. The overall feedback resistor, R15, gives the required gain control.
Protection to the amplifier, against shorted output leads, is provided by fuses in the positive and negative supply rails to both amplifiers.
Temperature stability is obtained by mounting Q13 on the heatsink. Q13 will thus automatically adjust the bias voltage. Frequency stability
is ensured by C9/R13, C5, C7, C11, C25 and C27.
Although the power amplifier itself does not produce a thump in the loudspeakers on switch on, the preamplifier used may. To reduce any thump to an acceptable level,Q1 is used to short the input for about two seconds on switch-on and immediately after switch-off.
The power supply is a conventional full-wave bridge with centre tap, providing +40 volts and -40 volts. Diode D1 is used to rectify a second negative supply which is used to control the FETs. Due to the resistance in series with the diode, the charge of C24 is slow. In addition, during the charge period, C23 is also being charged increasing the delay. On switch off, however, C23 cannot assist the voltage on C24 and the off-timing is much shorter than the on-timing.

Fig. 3. Component overlay.



Fig. 4. Printed circuit lavout for the amplifier. Full size $255 \times 97 \mathrm{~mm}$.



Fig. 5. How the heatsinks are assembled.


The only adjustment required is that of the bias current. This is normally done with an ammeter in the power-supply lead to the output stage BINDERS and, with the speaker disconnected and no signal, $\mathrm{R} \vee 1$ is adjusted to obtain a current of about 20 milliamps. However if a major fault
exists, or occurs, with the above method the meter as well as the output transistors may be damaged.
To obviate this we recommend a different approach as follows. Take out the fuses and temporarily connect a 220 ohm half-watt resistor across the fuse holder. Adjust RV1 to obtain about four volts across these resistors. If a major fault exists these resistors will get hot and possibly burn out. However no other damage will occur to the amplifier as the resistors limit the maximum current that can flow. After bias adjustment these resistors are removed and the fuses replaced.

It may be found that the voltage across the resistor in the positive lead is slightly different from that across the resistor in the negative lead. This is due to a slight offset in the output voltage but as long as the average is about four volts it will be satistactory.


You only have to ask! In response to those readers who wrote in to ask, we have arranged for binders to be designed for ETI. Each binder can hold up to 13 issues in such a way as to not only protect them and keep them in order but to make them look attractive on your bookshelf. Finished in black leather-look plastic with gold lettering, the binders make ideal Christmas presents (along with a subscription, perhaps?) at $£ 2$ each, including VAT and carriage.

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# CROSSOVER AMPLIFIER 



## One approach to an electronic crossover system.

LAST month we published the details of active-crossover boards for use with high-fidelity speaker systems. This month we give details of a typical complete system. The system described is not intended to be considered as the only possible way. nerely as an example of the way in which a system may be built.
The system described uses two 422 power-amplifier modules, but any other amplifier could equally well be used eg, the 440 amplifier. A two-way system is described, that is, one amplifier is used for the high frequencies and one amplifier is used for the low frequencies for each speaker system. Thus only two two-way crossover boards are used together with the two amplifier boards and the power supply to make the complete system.

## CONSTRUCTION

We build our prototype inte two pieces of channel aluminium as may be seen in the photographs. The aluminium channel used had dimensions of five inches by two inches and we used a piece 380 mm long for each side. Readers may have difficulty in getting hold of this but an equivalent can readily be made from
1.6 mm aluminium bent up as required. We suggest that you make your chassis about 430 mm long as we found ours to be a little cramped.

The heatsinks used were the Mullard 35D $\times 75 \mathrm{~mm}$ and these were assembled as detailed in the 422 amplifier section. The printed circuit boards were also assembled as detailed in that section. Printed-circuit pins should be used for all connections to the board as this makes interconnection of the unit much easier.

The location of the individual modules and components can be seen from the internal photograph of the unit. If construction similar to ours is used, with the transformer close to one end of the printed circuit boards, some trouble with hum may be encountered in the main amplifiers closest to the transformer. We overcame this problem by using these amplifiers for the high channels and by reducing their bass response by changing C4 from 100 microfarad to 2.2 microfarad. With this modification the response of the amplifier will drop off below 300 Hertz, thus reducing hum, but will still be adequate for high channel use. If the high channel
response is required to be lower than 300 Hertz then the transformer must be mounted further away from the amplifier modules.
Some care must be taken to prevent earth loops causing problems. The wiring of the power cables is as shown in Fig. 1, the most important being the zero volt line. The zero volt lines of both boards are linked by a heavy cable and the common side of the transformer is joined to the centre of this link. The common for the speakers is also joined to this same point. Make sure that this junction is insulated so that a short does not occur when the unit is closed up. The plus and minus 40 volts are taken to the crossover board which has the regulator on it, and the plus and minus 7.5 volts is linked between the two boards. The zero volt line for the crossover boards is taken via the signal output leads to the appropriate amplifier board.
Due to the power dissipation in the regulator for the crossover, a heatsink must be used. We simply bolted the crossover boards onto the end panel by means of a piece of angle aluminium, and bolted the transistors onto the end panel using insulating washers. We used a piece of cardboard

between the two boards to prevent any shorts occuring between the two boards. We also installed cardboard under each of the power amplifier boards similarly to protect them.
Coaxial cable was used to connect the inputs from the potentiometers to the main amplifiers but only twisted pairs from the crossover boards to the potentiometers. Coaxial cable could have been used here but was found to be not necessary as these leads are a long way from the power transformer.
Finally, a word about the power transformer. We have only used a single power transformer, as used in the 422 amplifier, to power the two complete 422 amplifier boards. But remember that the frequency spectrum is split up between the high and low channels and hence each amplifier, although called upon to provide the same peak power, only has to handle half the average power. The transformer is thus quite capable of handling the total load as the system is still nominally 50 watts per channel.

## PARTS LIST -

## CROSSOVER AMPLIFIER

Two complete sets of components as detailed for the 422 power amplifier module except that only one transformer and rectifier bridge is required for domestic use especially if the crossover is above 2 kHz .
Two ETI 433A boards with the following exceptions. The transformers, D1 to D4, C1I and Cl2
are not required for either, and Cl 3 , C14, R19, R20, Q1, Q2, ZD1 and ZD2 are not required on the second board. The value of R19 and R20 on the first board should be 8.2 k .
Switch SWI, if required, should be a double pole switch and RV1 and RV2 should be dual gang linear potentiometers.
Chassis.
Input and output sockets as required.

| $4^{32} 1$ | $4^{3} 21$ |  |
| :---: | :---: | :---: |
|  |  | in |
| 10 | 10 |  |
| 20 | 20 |  |
| BASS CUT dB | TREBLE CUT dB | BASS EQ. |

Fig. 3. Artwork for the level control panel. Full size $73 \times 50 \mathrm{~mm}$.

# COMPARATOR MODULE 

of any Q time, to exclude 'difference' outputs which are due to switching times, etc.

The Q output of a J-K Flip-Flop (4027 (A)) is set to ' 1 ' half-way through Q0 time by QO and C 1 '. During one word (00 time to 09 time) it will be reset if there are any. differences between the numbers set by the BCD switches and the contents of the clock module (SR). If, however, a whole word passes. with no differences between the two sets of data, O will still be set halfway through the following QO time, and Q of Flip-Flop (B), the comparator alarm output, will be clocked high. This alarm output is cleared when power is first applied, and may be cleared or disabled by closing the 'clear' switch.

If the jumper connection J 1 is made (indicated by a dotted line), when the alarm output goes high it will stay high until the middle of the next QO time, i.e. it will be high for
one word time. If this output is connected to the clock module reset connection it will reset the clock module to zero, and leave it counting. This can be used to provide a programmed pulse generator with a range of milliseconds to hundreds of hours. Any number of digits between 1 and 9 can be used to 'set' the comparison time. Unspecified digits will be treated by the comparator as being zero's. As mentioned earlier, any number of comparator modules may be connected to one clock module.

Any enquiries about the three timing modules can be answered by Sintel, of 53a Aston Street, Oxford. Sintel can supply pcbs and components for these projects.

A final note about the clock module described last month. The .4020 first-stage divider should be selected for operation at 5.12 MHz . All the Fairchild devices are suitable.

```
PARTS LISTS
CMOS-
        4 0 0 1
        4 0 2 7
        4 0 5 0
        4 0 7 0
        4 0 7 2
RESISTORS-
    Six 22k 1/4W
CAPACITORS-
```

    Five 10 nF ceramic
    one \(4.7 \mu \mathrm{~F}\), tantalum
    DIODES
up to $25 \times 1 \mathrm{~N} 914$

ALSO: pcb ETI553, three 14 pin DIL sockets, two 16 pin DIL sockets, DIL 16 pin header, optional 22 pin LSI socket and plug, 20 -way cable ( $6^{\prime \prime}$ or so), optional I/O plug, socket and cable for BCD switches.


## LOW DIFFERENTIAL THERMOSTAT

This circuit evolved as a result of the need for a more satisfactory method of controlling the temperature in our paint heaters which operate at 1700 F . The differential of conventional mechanical thermostats was too wide, both in actual rating and in \% accuracy, so that severe overheating occurred when the demand for paint momentarily lapsed. The result was poor finish and in a number of cases the destruction of the thermometer (at approx $£ 10$ a time).

The introduction of the new thermostat completely eliminated the problem. The circuit consists of a GEC J5G424 Zero Voltage Switch in IC form together with a Mullard 232264090004 which is plastic encapsulated giving it both mechanical and electrical protection. It is available

in four sizes with a temperature cover-: The J5G424 is, by nature of its design age from -30 to $+200^{\circ} \mathrm{C}$.
The RC network, $0.1 \mathrm{mF}+100$ ohms, prevents self latching of the Triac.
free of RFI. The type of Triac em ployed will depend on the loading. We were using 6 and 15 amp loads.


This pcb is available from Ramar or Crofton (see Ad. index) for $76 p$ including $V A T$ \& $P$ and $P$.

## 30V REGULATORS

Three-terminal voltage regulators are available in $5,9,12,15,18$ and 24 V types. If you require a 30 V supply use a 24 V regulator with a 6.2 V zener diode in the earth lead as shown. This increases the volatge to 30 V . A $0.1 \mu \mathrm{~F}$ capacitor should be connected across the zener diode as shown.

The zener should be of suitable wattage rating. In a similar manner for 27 V use a 3.3 V zener or for 33 V a 9.1 V zener.


## techtips

## AUDIO-RF SIGNAL TRACER <br> PRE-AMP

This economical signal tracer is very useful for servicing and alignment work in receivers and low power transmitters. It is easily constructed on a small piece of matrix board which can be mounted inside a commercially-available probe case or homemade probe. The slide switch can be mounted on the probe housing. A miniature toggle switch could be used as a substitute.
When switched to RF, the modulation on any signal is detected by the diode and amplified by the FET. A twin-core shielded lead can be used to connect it to an amplifier and to feed 6 volts to it.


## GO/NO-GO DIODE/TRANSISTOR CHECKER

A diode can be checked by connecting it between C and E . If LED 1 lights the diode is OK and its anode is connected to C. If LED 2 lights its cathode is connected to C. If both light it is a short circuit suitable only as a link!
To check transistors with known pin connections, set VR1 at maximum resistance and connect the transistor. Advance VR1 until one LED lights. If LED 1 lights it is NPN, PNP if LED 2 lights. If both light you have a three-legged link. If neither light you have a three-legged fuse!
To check transistor connections, if unknown, short two of its leads together and check as for a diode

making note of which lead/leads respond as anodes. Short two other
leads together and do it again. Refer to diagrams above.

## TINNING WITH SOLDER WICK

Do not discard the lengths of solder saturated solder wick. Further use can be made of them to plate printed-circuit boards by pre-tinning the joints, prior to inserting components and soldering.

The simple operation is as follows - place the saturated solder wick on the printed board and apply a heated soldering iron to melt the solder in the wick. At the same time, move the wick and iron along sections or joints requiring tinner. A neat plated copper print will result.

## PRECISE AUDIO CLIPPER

A differential amplifier makes an excellent audio clipper and can provide precise, symmetrical clipping. The circuit shown commences clipping at an input of 100 mV . The output commences clipping at $\pm 3 \mathrm{~V}$. Matching 07 and Q2 is necessary for good symmetrical clipping, however, if some asymmetry can be tolerated this need not be done.


## high-power zenered voltage FROM LOW POWER SOURCES

A power transistor can be used to provide a high power zellered voltage from a low wattage zener. A 400 mW zener can be used where a 10 watt zener is required or a 1 W zener can be used where a 50 to 80 watt zener is required, by using appropriate transistors for O 1 and O 2 in the circuits shown.
Where low rating is required Q1 would be a ASZ 15 (germanium) or an AY9140 (silicon). Q2 could be a 2N3054 (silicon). For higher powers Q1 could be an ASZ18 (germanium) or a 2N2955 (silicon) and Q2 a 2N3055 (silicon) or an AY8149 (silicon).
A heatsink on the transistor is

required. The circuit in (a) has the advantage that power transistors can
be bolted directly on to a chassis which may serve as a heatsink.

## VARIABLE, HIGH VOLTAGE REGULATOR

This regulator is ideal for SSB linear amplifier tube screens. It would also have application in the repeller supply for a reflex-klystron microwave oscillator. CRO deflection amplifier supply is another possible application. Regulation is about $0.5 \%$. The output transistor will need to be mounted on a small, insulated heatsink. A BF459 is preferred ( 30 V Vceo) as the BF458 is sailing a bit close to the wind when the output is down to 50 V .


## DOORCHIMES DELAY



Ever get tired of people who repeatedly press your doorbell?
With values shown, this simple circuit
will permit one operation every 10 seconds or so. Capacitor C1 charges through R1 when the button is released. Making R1 larger will increase the delay.

## FLICKER-FREE STARTING

Here is an extremely simple, yet effective modification which will eliminate the annoying flickering when a fluorescent lamp is first switched on.

The modification consists of inserting a diode (P.I.V. about 600 V )
in series with the starter. This results in a fairly heavy current on initial switch-on, which heats the filaments quickly. When the starter contacts open again, the lamp fires immediately.

NOTE: The effectiveness of the modification, depends largely upon the characteristics of the starter; try and find one that is quick -acting.

My original unit has been working successfully in my desk lamp for the past three years, and l've had no problems with dc magnetisation of the ballast, or excessive power consumption on switch-on.

## techtips

MULTIPHASE CLOCK GENERATOR

 Out (1)
(1)
(2)

Whenever sequential logic operations are to be performed, a multiphase clock generator is often required. The circuit shown, which uses only two CMOS ICs, was designed by Michel Burri of Motorola's Geneva applications laboratories. It will produce a pulse on each of the four output lines in turn. These pulses do not overlap one another.
Operation of the circuit is self-evident from an examination of

the schematic; however, it is interesting to note that the power supply of the MC14001 is derived from the clock input. The maximum operating speed of this circuit is about 1 MHz .

## CRYSTAL CHECKER



For checking fundamental HF crystals on a 'Go-No-Go' basis, the above circuit works quite well, An untuned Colpitts oscillator drives a voltage multiplier rectifier and a
current amplifier. If the crystal oscillates, 02 conducts and the LED lights. A3 or $6 \mathrm{~V}, 40 \mathrm{~mA}$ bulb could be substituted for the LED.

## ZENER BOOSTS OUTPUT VOLTaGE OF REGULATOR




In this circuit the zener diode raises all voltages - with respect to earth by the zener voltage, i.e.
$\operatorname{Vin}(\max ) \bumpeq$ voltage regulator Vin (max) + zener voltage

Vin (working min) $\bumpeq$ voltage regulator V in ( min ) + zener voltage

Vout $=$ voltage regulator Vout + zener voltage

As the voltage regulator dissipates all excess power while the zener merely clamps the output voltage above its own voltage, a low wattage zener ( 250 mW ) should be adequate unless lower voltage taps are used, as in the second example in which the total output is one amp.

For other value zeners, wattages can be worked out by the formula W $=$ zener voltage $X$ current.

## COLOUR CODING COMPONENTS

The resistor colour code can be extended for use in codifying all manner of other components.
Zener diodes for example can be thus coded once their parameters have been established. Similarly it assists when building a unit to mark the leads of transformers, coils transistors etc with short lengths of coloured insulating spaghetti. If for example one has a centre-tapped transformer then from the top of the winding inwards the code could be top = brown, centre = red, bottom = orange.
With a transistor base $(B)=2=$ red; collector (C) = $3=$ orange; emitter ( E ) $=5=$ green. Just follow a numerical sequence equating numbers with letters of the alphabet.

## SENSITIVE RF VOLTMETER



This device will measure RF voltages beyond 200 MHz and up to about 5 V with the components as indicated. The diode etc should be mounted in a remote probe, close to the probe tip. Sensitivity is excellent and voltages less than 1 V peak can be easily measured. The unit can be calibrated by connecting input to a known level
of RF voltage, such as a calibrated signal generator and setting the calibrate control. The output indicates in RMS. As it is it reads about 2 V

RMS full scale. This can be increased to 20 V or more by increasing $\mathrm{R1}$ to 20 M (two 10 M in series). The $100 \mu \mathrm{~A}$ meter could be a multimeter if desired.

## TRANSFORMERLESS INVERTER



This transformerless inverter chops the dc supply voltage then rectifies the
resulting square wave using a cenventional voltage doubling circuit.

The square wave source can be a simple IC multivibrator which is used to drive phase splitter Q1 through coupling diode D1. Base bias is established through R1 and the collector and emitter load resistors are R2 and R3 respectively.
The output power transistors Q2 and Q3 in conjunction, with diode D2, serve as a simple high level switch developing a square wave whose peak to peak amplitude is near that of the dc supply voltage.
The output square-wave is coupled via C1 (which must be a suitably chosen high-value electrolytic) and fed into a voltage doubler circuit thus producing a dc output of reverse polarity.
Capacitor C2 is the output ripple filter.
Resistor values are dependent on the original supply voitage - the drive frequency is not critical but signals in the kilohertz region are preferred (2-6 kHz ).

## HOME-MADE LDR

I had to find a cheap and easy way to improvise for an LDR or photo-transistor. 1 took several medium power silicon transistors from my surplus box and carefully cut off with a saw and filed the top part of the can, exposing only the silicon chips.

They worked quite well as substitutes for LDRs or photo-transistors.
For use as an LDR use only the collector and emitter legs. As a photo-transistor use all three legs emitter base, collector.
In some circuits the component values need not be changed, while in
others a change is necessary for the circuit to work.
High powered silicon transistors also give a similar effect but they are costly unless found in the surplus box. Germanium transistors cannot be used because of the 'grease' present in them.

## techtips

## measuring rms value of ac with a dc dvm



The above circuit may be used for measuring the RMS value of ac (sinewave) with a dc digital voltmeter. It has a frequency response to beyond 10 kHz and will measure signals as low
as 400 mV . The error rises at low frequencies, somewhat below 50 Hz , to about 4\% mean. The LM301 supply may be as low as $\pm 4 \mathrm{~V}$. or up to $\pm 15 \mathrm{~V}$, if desired, with reduced
sensitivity at the lower voltage.
The DVM input must be floating and a differential input is required. To increase the input range a step attenuator may be used.

## 'BUCKET' REGULATOR

There are a number of applications where a simple cheap form of reg. ulated power supply, giving a supply regulation of the order of $5-10 \%$, is useful. One such application is the class B audio amplifier. The cost of the additional components required to achieve regulation is more than offset by the saving in cost and size of the electrolitic capacitors alone.
Fig. 1. shows the circuit of the regulated supply. The only additional components required to affect regulation are the SCR, R1, and the zener.

At switch on the the reservoir C1 is discharged and the cathode of the SCR is at zero volts. The positive going output from the bridge rectifier will cause gate current to flow via R1 triggering the SCR. The reservoir C1 starts to charge. At the end of the halfcycle the SCR will turn off.

The following half-cycles will repeat the process charging C1 until the supply voltage approaches the zener voltage. However the maximum positive potential at the SCR gate is determined by the zener, so there comes a time when the reservoir will have charged to the point where the SCR gate cannot be driven positive with respect to its cathode. At this stage the SCR will stop firing and no further charging current will be delivered to the reservoir. The reservoir will dis-

charge via the load, whatever power is being supplied, until the gate is once more positive, when the SCR will fire again. One or more half cycles are sufficient to raise the reservoir voltage sufficiently to prevent further firing.

Thus the SCR fires as necessary to keep the reservoir "topped up" and it is this topping up action which gave the regulator its name. The number of times it fires in any particular interval being dependant on the load current taken from the supply.

There are two particularly attractive features of this type of supply. First its efficiency is high, there are none of the power losses associated with either series or shunt regulators.

The second is that it is possible to obtain very simply an indication of the current being delivered, This may
be obtained by connecting a LED (in series with a current limiting resistor R3) across the main current limiting resistor R2. The LED will flash each time the SCR conducts and hence the rate at which the flashes occur will depend on load current, the flash rate varying from once every few seconds when only leakage is being made good - to continuously under full load conditions.

As an indication of circuit values the following where used for a 25 V , 1.5A supply: $\mathrm{R} 1=1.2 \mathrm{k}, \mathrm{R} 2=2 \Omega, \mathrm{R} 3=$ $330 \Omega, \mathrm{C} 1=5000 \mu \mathrm{~F}, 25 \mathrm{~V}$, Transformer 30 V .

The SCR and bridge rectifier should be rated at full load current, but for many music and speech applications the transformer can be derated as much as $50 \%$.

## SOUND EFFECT GENERATOR



FIG 1


The waveshape generator shown in this circuit will interest those readers experimenting with sound effects.

Basically the circuit is a slow
running oscillator with variable attack and decay. A variable amplitude (high impedance) output is available via the 2 meg potentiometer. Figure 2 shows
an add-on circuit which should be used if a low impedance output is required

Some of the output waveforms that can be produced are shown in Fig 3.

## INTRUDER ALARM



Here two gates of a 7400 are used to provide photoelectric control in con junction with an ORP 12 photocell. When light falls on PC1 the potential is applied to the trigger circuit consist ing of $1 / 2$ the 7400 . The feedback pro vided ensures a positive output change at pin 6. The output, whilst PC1 is under illumination, is equal to the supply voltage. R1 enables a small 9 V battery to be used. If PC1 is shaded the output at pin 6 is $O V$ This may now be used to trigger a relay for an intruder alarm. If this is the case it is wise to use a small mains supply and to incorporate a diode across the relay coil, to prevent high back EMF from destroying the IC.

## GOOD/NO GOOD BATTERY TESTER

This is a simple tester for use with a PP3 or similar battery.

It is wired to a PP3 battery clip remembering that red is connected to -ve of battery and black to the +ve. It uses 3 small LEDs of the same size: one red, one green. Due to the fact that the green LED needs a far greater current, the green will glow only if the battery is in reasonable condition. The red will glow even if battery is down. If the red glow is very faint the battery is no good


## techtips

## ELECTRONIC LOCK

- REPRESENTS REED SWITCHES THAT HAVE
TO BE TURNED ON IE. MAGNE TISED


REED SWITCHES ARE SHOWN IN ONE ROW IN CIRCUIT DIAGRAM BUT SHOULD BE WIRED AS SHOWN ABOVE TO AVOID CROSS TRIGGERING BY CLOSE REED MAGNE TS


DIAGRAM OF KEY NEEDED TO OPEN THIS LOCK

COMBINATION REQUIRED TO


NOTE G1 = SN7430 8 INPUT NAND GATE I.C. $\mathrm{G} 2, \mathrm{G} 3, \mathrm{G4}, \mathrm{G5}=\mathrm{SN} 7400$ QUAD 2 INPUT NAND GATE I.C.

This device enables a solenoid to be switched on by means of an electronic key. If the correct key is used the circuit will latch, but if an incorrect key is inserted a warning tone rings until the correct key is used. The circuit has automatic switching to turn it on, but this can be replaced by a conventional ON-OFF switch if desired.
-The main element of the circuit is G1 the eight-input NAND gate. If all inputs of the NAND gate are high (achieved by closing the right combination of reed switches) the output will be low. The low output is fed to G2 forcing its output high which turns on TR1 energising the solenoid coil. At the same time a low is fed, back from TR1's collector to G2's other input latching it. Thus once the solenoid is energised the key may be removed. C1 $(0.1 \mu \mathrm{~F})$ ensures that TR1 is always 'off' on switch-on of the circuit.

TR 1's collector is also connected to

G3's input which along with G4 forms a multivibrator. When TR1's output is low the multivibrator is dis-enabled. However if an incorrect key is used TR1's output will be high and the multivibrator will oscillate. G5 acts as a buffer to drive a loudspeaker.

R9 with RL1 forms the automatic switch on circuit. When R9 is closed RL1 is energised pulling in it contacts to permanently connect the supply. If R 9 is opened after this the circuit continues to operate. This means if an incorrect key is used and R9 is closed the alarm tone will continue to ring even if the key is removed.

Nine reed switches are used in the circuit. One to switch on the circuit and the other eight to provide the correct input combination. The lock opens only if all eight inputs to the NAND gate are high. To do this the circuit is wired so that some reed switches must be on and some must be off. The eight reed switches give $2^{8}$ $=256$ possible combinations.

Reed switches that have to be turned on are wired like this


If the reed switch is not closed the $470 \Omega$ will pull gate input to low and the lock will not open.

Reed switches that have to be 'eft open are wired like this


If the reed switch is closed it will put gate input to low and the lock will not open.

If preferred the reed switches could be replaced by simple ON/OFF switches then the circuit would act as a combination lock, like a tumbler lock.

The construction of the circuit is in no way critical. Veroboard provides a relatively cheap mounting. The connections of Vcc and gnd to the two ICs should not be forgotton!
With many LED TTL logic probes it is

## AUDIBLE TTL PROBE


difficult to watch the LEDs and the circuit one is testing. The following circuit is an attempt to produce a new sort of probe.

When the probe is in contact with a TTL low (0) the probe emits a low note. With a TTL high (1) a
high note is emitted.
The whole circuit uses only two TTL ICs and several auxiliary components. This low component count makes it easy to miniaturise the unit. The power is supplied by the circuit under testing so no battery is required.


The circuit shown is a simple amplifier, the gain of which can be switched between two precisely controlled values by the application of a signal voltage: $V$. If $V$ is such that $Q 1$ is saturated, then the voltage gain of the circuit is simply $-\frac{R 2}{R 1}$. If the transistor is cut off. then the voltage gain be-
comes $\left(1+\frac{R 3}{R 4}\right) /\left(\frac{R 3}{R 4}-\frac{R 1}{R 2}\right)$.
One obvious application of the circuit is if the resistors are adjusted such that the two gains are equal in magnitude, but opposite in sign, (e.g. $\mathrm{R} 1=20 \mathrm{k}, \mathrm{R} 2=10 \mathrm{k}, \mathrm{R} 3=50 \mathrm{k}$, $R 4=10 \mathrm{k}$ gives voltage gainśs of +2 and -2 ) Then the circuit could be
used as a chopper for the input of a DC amplifier.

The value of R5 is largely arbitrary, depending on the magnitude of the chopper signal, V . Its sole purpose is to prevent excess current being drawn by the base of Q1. Suitable components for the op amp and transistor are the 741 and BC 182 L .

Tech-Tips is an ideas forum and is not aimed at the beginner. Ne regret we cannot answer queries on these items.
ETt is prepared to consider circuits or ideas submitted by readers for this page. All items used will be paid for. Draw. ings should be as clear as possible and the text should prefer ably be typed. circuits must not be subject to copyright Electronics Today International, 36 Ebury Street, Lorvon swiw olw.

## 741 TIMER

The circuit shows a very simple timer based on a 741 op amp.

R1 and R2 hold the inverting input at half supply voltage. R4 applies some feedback to increase the input impedance at pin 3, but its value is such that negligable damping of pin 2's voltage occurs. Pin 3, the non inverting input, is connected to the junction of R3 and C. After S1 is opened and C charges via R3. When the capacitor has charged up sufficiently for the potential at pin 3 to exceed that at pin 2 the output abruptly changes from OV to positive line potential. If reverse polarity operation is required simply transpose R3 and C.

R3 and C can be any values and time delays from a fraction of a second to several hours can be obtained

by judicious selection. The time delay is 0.7 CR seconds where C is in Farads
and R in ohms and hence is completly independent of supply voltage.
$100,000 \mathrm{M} \Omega$ DC PROBE?


Most multimeters used for transistor work have an input impedance of 20,000 2 /V

Occasionally, especially when measuring potentials on high impedance equipment, this sensitivity is sufficient. The circuit shown, however, presents neglible loading on the circuit under test.

A 741 op amp is used with $100 \%$ $A C$ and DC feedback to provide a typical input impedance of $10^{11} \Omega$ and unity gain (or so the contributor, Ed.)

Due to the possibility of hum and RF pickup the input leads should be kept as short as possible and the circuit should be mounted in a smatl earthed case.

The output leads may be as long as required since the output impedance of the circuit is a fraction of an ohm.

With no input the output level is indeterminate. This state of affairs can be changed by including R1 in the circuit through this lowers the input impedance to $22 \mathrm{M} \Omega$.

## MAKING SLOW LOGIC PULSES AUDIBLE



For monitoring slow logic pulses a Schmitt trigger is connected as an oscillator. The trimpot controls the pitch of the output. Very useful as a keying monitor or digital clock alarm. When the input goes high, the 7413 will oscillate.

## DIL DRILLING

Drilling holes in a pcb for 14 and 16 DIL ICs is quite difficult and if the holes are slightly off centre it is tricky to fit the IC. An easy way to get it right use a small piece of 0.1 matrix veroboard as a template.



## 



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VC 1 Single Less Switch C3 Tandem Less Switch $\begin{array}{ll}\text { VC } 4 & 1 \mathrm{~K} \text { Lin Less Switch } \\ \text { VC } 5 & 100 \mathrm{~K} \text { Log anti-Log }\end{array}$

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## AL 10/AL 20/AL 30

The AL10, AL20 and AL30 units are similar in their appearance and in their general specification. However, careful selection of the plastic power devices has resulted in a range of output powers from 3 to 10 watts R.M.S.
The versatility of their design makes them ideal for use in record players, tape recorders, stereo amplifiers and cassette and cartridge tape players in the car and at home.

AL10 £2.30, AL20 £2.65, AL30 £2.95


ELÉCTRONICS TODAY INTERNATIONAL-JANUARY 1976

## Anne E. Crump looks at the development of dice and suggests a technological innovation-

## ELECTRONIC

THE CUBICAL DIE is the oldest game known to man. The dice our ancestors rolled more than two thousand years ago had pretty well the same configuration of dots as the ones we use today. Even before he gambled with them, primitive man considered them to be magical devices, and by their fall he divined the future.

It wasn't very long, of course, before he cottoned on to the possibility of making special dice with which he could cheat! We have found crooked dice along with the treasures of the tombs of the ancient Egyptians, which leads to at least two intriguing possibilities. Did they intend going on into the next world armed with slightly more than a fair sporting chance, or did they take their dice along for old times sake, having used them to acquire all that treasure in the first place? On either count, that's really no way to get to heaven!

But times have changed. Modern man produces hand-made "perfect" dice for his casinos. Sawn from extruded plastic rods and produced to a tolerance of $1 / 5,000 \mathrm{in}$, these perfect dice have spots drilled approximately 17/1,000in into each face and filled with a paint of the exact same weight as the plastic which has been removed. Buffed and polished so that no recesses remain, they are then ready for the gambling tables of the world.

Now, after 2,000 years, electronic technology has caught up with the oldest game in the world, and electronic dice have been proven to be even more random than their predecessors. The dice described in this article has been designed with minimisation of cost in mind.

The complete circuit uses 74 series integrated circuits only, plus a few discrete components, and is divided into three main areas, i.e. the pulse generator, the pulse counter, and the final decoding electronics.

## THE PULSE COUNTER

The Pulse Counter is a simple arrangement utilising two 2 -input NAND gates with the output of the second gate connected to the inputs of the first - a straight forward and convenient way of producing a low-cost oscillator. The frequency of oscillation is determined by the capacitor and resistance values selected, the actual frequency being relatively unimportant so long as it is as high as possible in order to avoid any possibility of the user being able to predetermine his 'throw." A figure of 120 kHz is suitable. The oscillator output is connected directly to a 7492 counter, which is used in the "divide by six" mode, and in this way a full scan of counts one to six occurs 20,000 times each second.

## THE DECODER

The BCD output of the 7492 is connected to an arrangement of gates which decode the BCD information into suitable drive signals for the LEDs. The decoding arrangements and the diode drive combination have been derived in such a way that the final illuminated display appears in the same dot configuration as a conventional die. The Boolean equations for the gating system are shown on the circuit diagram.

## THE PULSE GENERATOR

In order to obtain the maximum degree of randomness, it is essential that the dwell time should be equal on each of the six counts. The main factors affecting these requirements are the stability of the oscillator and also the spread of output reactances around the counter. If, for example, the frequency of the oscillator shifts during the one to six count cycle, the length of time spent on some of the counts will be greater, and the
"throw" will therefore be biased in favour of those numbers. In practice, however, the simple oscillator shown gives adequate stability over the significant period of six pulses, long-term drift being of little importance. A further important point to be borne in mind is the parasitic reactances around the counter area, which could also be instrumental in biasing the "throw," and a good layout of the final unit is therefore essential. A ready-made printed circuit board, complete with assembly and testing instructions, is currently available, and its use is recommended.

The actual degree of randomness of the die is easily obtained by pressing the operating button a number of times, and recording the amount of times each number appears. In order to get a reasonable accurate picture, it is necessary to record at least 3,000 "throws," from which an average should be worked out. The results obtained are excellent, and in tests have shown the random distribution figures of the electronic dice to be in most instances superior to that of the conventional dice. In any group of 10 or 20 "throws" there will always tend to be a predominance of one or two numbers which does tend to be discouraging when first encountered. It is therefore essential to remember that this also happens when using ordinary dice!

## CONSTRUCTION

There are a number of ways in which an electronic die can be activated. The method chosen for the particular unit described here is for the unit to display the last throw until play resumes and the operating switch is pressed again. The last "throw" remains clearly visible for all to see, this avoiding any danger of arguments which might arise if the "throw" was only displayed while the operator's finger remained on the button. Two microswitches


|  | 1 | 2 | 3 | 4 | 5 | 6 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $A:$ | $H$ | $L$ | $H$ | $L$ | $H$ | $L$ |
| $B:$ | $L$ | $L$ | $L$ | $L$ | $H$ | $H$ |
| $C:$ | $L$ | $H$ | $H$ | $L$ | $L$ | $L$ |

Fig. 3. Logic states at $A, B \& C$ for each number.
are shown on this circuit. One is sufficient, but it is useful to have a number of switches in series so that people at different parts of the table can operate the unit by pushing a local button.

Power for the circuit is provided by four $1 \frac{1}{2}$ volt transistor radio cells, and the randomness of the counter is unimpaired so long as the brightness of the LEDs is reasonable. As soon as the LEDs begin to dim, the batteries should be changed immediately, as deterioration in the random quality of the circuitry may occur. It is important that the battery supply should not be allowed to fall below 4.5 V or to exceed 7 V . The voltage adversly affects the random operation and the higher voltage is the maximum safe ceiling at which TTL can be run. With LEDs and solid-state electronics used throughout, the reliability of the dice is excellent, and the switches should be first to wear out.

The finished product is very much to the design of the individual
himself. Double or even treble dice, operating from a single switch or two or three individual buttons, can be made. A buzzer could be added, to be triggered each time a single, double or treble six is "thrown. This is easily done by adding the necessary gating to the outputs of the $A, B$ and $C$ outputs of the 7492 so that the buzzer is activated each time the appropriate combination appears. Some form of sound, even if only the click of the microswitch, is essential in order to prove to other players that the button has actually been pressed, and a further improvement could be a brief audible tone emanating from the dice when activated

It is important to note that in addition to their obvious leisure uses, electronic dice have other varied applications, particularly where statistical calculations are involved, an excellent example being their use for quality assurance in factories. Here, by their use, a truly random selection of the production run of a factory can be
obtained for testing purposes. Perhaps if the Romans had had electronic dice, human nature being as it is, the selection of Christians going to the lions would have been a trifle more random? And Nero would still have fiddled while Rome burned - but chances are he'd have been fiddling with an electronic die!

## PARTS LIST

| R1 | Resistor | $680 \Omega$ |
| :--- | :--- | :--- |
| R2-R6 |  | $4 \mathrm{k} 7^{*}$ |
| R7-R10 | " | $100 \Omega$ |
| C1 | Capacitor | $0.022 \mu \mathrm{~F}$ |
| D1,D2, D3 | Diodes | 1 N 4148 |
| LED 'A'-LED 'G' | TIL409 |  |
| IC1, IC4 | 7400 or $7403^{*}$ |  |
| IC2 | 7492 |  |
| IC3 | 7404 |  |
| SW1 SW2 | microswitches |  |

SW1,SW2 microswitches
NB: R2-R6 are not used with the 7400 gates - only with open - collector gates, such as 7403

# Electronic timekeeping Sintel delivers: designs - kits - components 

## Parts for the ETI Stopwatch System:

- See this month's article


## STOPWATCH

## h without Latch

Conents: Verobox 75/410J - Red perspex front panel - Manganese batteries - clips - Transistors - Diodes - Wiring Pins - Screws - Sockets - 14 pin Pin-Header displays _- Full instructions As above except for Displays and Display PCB £27.82 (for other display options see below)

## STOPWATCH with one LATCH

Complete Kit for Stopwatch with one Latch ( Split-display freeze) facility
With two sets of $3 \times D L 33 \mathrm{MMB}$ displays. etc With one set of $3 \times$ DL33MB displays 46.32

Without Displays (see other options below)
£ 39.75

## LARGE DISPLAY OPTIONS

The Stopwatches may be used with larger displays - DL704E (0.3") or FND500 $\left(0.5^{\prime \prime}\right)$. N.B. When using larger displays we recommend that a segment current boosting circuit be used to obtain higher brightness: details supplied with any parts ordered

Displays
$\begin{array}{llll}\text { (each):DL704E85p } & \text { For DL704Es } & \mathbf{£ 1 . 3 5} \\ \text { FND500 } & £ 1.50 & \text { For FND500s } & \mathbf{£ 1 . 3 5}\end{array}$

Note that only one of these display PCBs, together with displays, can fit in the 7510 J Verobox supplied with the complete kits

## FURTHER APPLICATIONS

For those who want to vary or extend the Stopwatch design components are available from us in various sub-divisions - see box by article for 'PCB Module Kits' Send a targe s.a.e for fuller list. Some possibilities suggested are

## Multiple Latch Systems <br> Alarm Systems <br> - Mains Power Supplies

- Chess Clocks

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## SINTEL

53a Aston Street, Oxford Tel. 086543203<br>Telex: 837650 A/B ELECTRONIC OXFD

## ALARM CLOCK KITS

Comple kivs 6 digit alarm clocks with bleep alarm (C) and LED displays. Kits also include PCBs - active and passive components -- IC socket - min. transformer - switches -
flat cabla
6 dit Alarm Clo Kit with $0.3^{\prime \prime}$ DL704E displays

$$
\begin{aligned}
& £ 22.80 \\
& £ 26.70
\end{aligned}
$$

## CLOCK CRYSTAL TIMEBASE

32.768 kHz Quartz Cfystal: High accufacy/stability for clock and watch time bases Can be used with CMOS divider and logic 10 provide stable 50 cps E3.60 ' 50 cps Kit' - will provide 50 cps for clock Cs , giving time accurate to within a few Ceconds a month, contains smaleal for car or boat clocks.

## IC's

MK50253 12 or 24 hour, 4 or 6 digit alarm clock with alarm tone on chip. snooze, reset to zero; proven high reliability
MK50250 identical to MK50253 except that it is 24 hr only
MK5030M CMOS LED watch IC with date and seconds
AY1202 4 digtt clock with simplest interiace $\quad$ E3.66 MM5314 4 or 6 digit clock
$\mathrm{c4.44}$

## DISPLAYS

FND 500 Very attractive $0.5^{\prime \prime}$ Red Common Cathode LED - only $\quad$ £1.50 L704E 3 Cathode LED $85 p$ MAN3 0.13 C Cathode LED LIXCO $31 / 2$ digit $0.5^{\prime \prime}$ Liquid Crystal display. with socket
DISPLAY PCBs - each of the four display PCBs below is $£ 1.35$
or clocks with $\mathbf{4}$ or $\mathbf{6}$ displays: PCB for DL704E's; PCB for FND500's
For counters: up to 8 digits: PCB for DL704E's; PCB for FND500's

## VEROBOXES

Wo trom the range of the attractive and functional cases from Vero: $70 \times 75 \mathrm{~mm} \mathbf{£ 2 . 9 4}$ Red or Green Perspex for front panel of Box J 30p; Red for box'D 40p.

## ETI DVM and Frequency Meter

MC145i1CP $\underset{\text { CA31 }}{\mathbf{E 1 . 9}} \quad \mathbf{8 8 p}$
MC14518CP £1.03

## CMOS

CMOS from the leading manufacturers only

| CD4000AE | 0.17 | CD4027AE | 0.46 | CO4051AE 0.77 | C04081BE | 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CD4001AE | 0.17 | CD4028AE | 0.74 | CD4052AE 0.77 | CD4082AE |  |
| CD4002AE | 0.17 | CD4029aE | 0.94 | CD4053AE 0.77 | BE | 59 |
| CD4006AE | 0.97 | CD4030AE | 0.46 | CD4054AE 0.95 | CD4086BE | 9 |
| CD4007AE | 0.17 | CD4031AE | 1.82 | CD4055AE 1.08 |  | \% |
| CD4008AE | 0.79 | CD4032AE | 0.88 | CD4056AE 1.08 | CD4093BE | 66 |
| CD4009AE | 0.46 | CD4033AE | 1.14 | CD4057AE 20.35 | E | . 53 |
| CD4010AE | 0.46 | CD4034AE | 1.56 | CD4059AE | BE | 0.86 |
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| C04015AE | 0.83 | CD4039AE | 2.86 | CO4066AE 0.58 |  |  |
| CD4016AE | 0.46 | CD4040AE | 0.88 | CD4067BE 2.95 | CP |  |
| CD4017AE | 0.83 | CD404 ${ }^{\text {AE }}$ | 0.69 | CD4068BE 0.18 | C1451 | 1.95 |
| CD4018AE | 0.83 | CD4042AE | 0.69 | CD4069BE 0.18 | C14516CP | 1.03 |
| CD4019AE | 0.46 | CD4043AE | 0.83 | CD4070BE 0.18 |  |  |
| CD4020AE | 0.92 | CD4044AE | 0.77 | CD4071BE 0.18 | MC14520CP |  |
| CD4021AE | 0.83 | CD4045AE | 1.15 | CD4072BE 0.18 | MC14553CP | 4.07 |
| CD4022AE | 0.79 | CD4046AE | 1.10 | CD4073BE 0.18 | MC14555CP | 0.74 |
| CD4023AE | 0.17 | CD4047AE | 0.74 | 0.18 1.27 | MC14556CP | 0.74 |
| CD4024AE | 0.64 | CD4048AE | 0.46 | CD4076BE 1.27 | MC14566CP | 1.21 |
| CD4025AE | 0.17 | CD4049AE | 0.46 | CD4077BE 0.18 | MC14585CP | 1.45 |

RCA 1976 CMOS Databook: 400 pages of data sheets and 200 pages of
 New Motoroia McMOS Databoo

## HARDWARE

SOLDERCON OIL SOCKETS for IC's displays, CMOS, TTL
The sensible low cost way for providing sockets. As they can be soldered on the conductor side of the board, they are ideal for use with double-sided PCB. Strip of 100 pins for $\mathbf{5 0 p}$. 400 for $\mathbf{£ 2}$. 1,000 for $\mathbf{£ 4} .3,000$ for $\mathbf{£ 1 0 . 5 0}$ 20 -way Colour Coded Flexible Flat Cable $\quad £ 1$ per metre: $\mathbf{£ 8 . 5 0}$ per 10

## ETI DATA SHEET

TCA280A TRIGGER INTEGRATED CIRCUIT

The TCA280A is a monolithic IC designed will thyristor and triac control circuits. It will operate in phase control, synchronous mof switching and time proportional
Fig. 3 gives a circuit for a typical application as a synchronous on/off switching controller. The synchronous switch gives triggering around the zero crossings of the mains voltage with a typical pulse duration of $160 \mu \mathrm{~S}$. The values of $\mathrm{Rd}, \mathrm{Rg}$ and Cl are chosen for triacs requiring a gate current, la, typ. of soe the manufacturer's full data sheet. a single
trigger pulse bursts is shown circuit using used in conjunction with a triac this will INTERNAL CIRCUIT
provide full-wave a.c. control and if used in conjunction with a thyristor, gives a controlled half-wave rectifier. Once again
component values are given for the SC450 component values are given for the SC450 graphs in the manufacturer's data.

| SPECIFICATION - typical values | 161514131211109 |
| :---: | :---: |
| Supply voltage 14V |  |
| Output trigger pulse $\quad 250 \mathrm{~mA}$ |  |
| Output voltage drop $\mathrm{V}_{11}-10<2.8 \mathrm{~V}$ |  |
| PACKAGE 16 pin DIL <br> For pin functions see Figs. 3 \& 4. |  |



SL415A \& SL414A 3W \& 5W AUDIO AMPLIFIERS

The SL415A and SL414A are robust high gain audio power ampliflers each with a separate pre-amplifler. The ctrcults, which have been optimized to give maximum reliabllity, have guaranteed power outpuits of 5 W and $3 W$ respectively under the conditions specified.



## OPERATING NOTES

Each circuit has built in resistance to damage caused by voltage overload or short circuits across the output

For high inductive loads a high frea. uency load of 47 nF and $22 \Omega$ connected in series may be required between pins 10 and 1 to enhance stability. When connecting the circuit shown in Figs. 5,9 and 10 is recommended that:
(a) The capacitor between Pins 9 and 1
should be kept close to the integrated
circuit.
(b) The earth path between Pins 3 and

1 should be kept short.
(c) The earth connection from the supply should be connected to Pin 1 not The P . o/p DC bias point half way between supply and earth. Any variations in this level can be adjusted with the use of the circuit shown in Fig. 4.

The devices should be used with a suitable heat sink of approximately $20 \mathrm{~cm}^{2}$ ( 3 tn 2 ). Typical advertised prices are $S$. 414 A 2.00 , SL415A E2.75.

## CHARACTERISTIC

Supply voltage
SL414A

SL415A
Power into $15 \Omega$ load
SL414A
SL415A
3.8 W

Pre. amp. voltage gain 24 dB
Main amp. voltage gain 26dB
D.C. input current

Preamp. 100nA
Main amp. $\quad 100 \mathrm{nA}$
Main amp. o/p impedance $\quad 0.2 \Omega$
Distortion
Preamp. $0.1 \%$
Main amp. $\quad 0.3 \%$
Noise level $\quad-75 \mathrm{~dB}$
Ripple rejection 30dB
Input impedance
Pre amp 20M $\Omega$
Main amp $\quad 100 \mathrm{M} \Omega$
ABSOLUTE MAXIMUM RATINGS
Supply voltage
SL414A +20V
SL415A +25V
Max. peak loud current 1.4A
Storage temp.
Operating temp (with suitable heat sink)
$-20^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$
$-10^{\circ} \mathrm{C}$ to $+70^{\circ} \mathrm{C}$


## CONDITIONS

30W r.m.s. o/p Load $=7.5 \Omega$
5 W r.m.s. o/p THD $=5 \%$
$V_{\text {supply }}=18 \mathrm{~V} \quad 5 \%$ THD
$V_{\text {supply }}=24 \mathrm{~V}$

Pre amp. o/p $=0.9 \mathrm{~V}$ r.m.s.
$f=400 \mathrm{~Hz}$, Pout $=1 \mathrm{~W}$
Source impedance $=1 \mathrm{M} \Omega$, gain $=26 \mathrm{~dB}$
with pin 7 decoupled

(NOT ACTUAL SIZE)
In the 'minimum components' application (which is the simplest possible configuration for the SL414/415) the preamplifier provides a temperature-compensated input bias to the power amplifier (pin 4). The circuit has an input impedance of $100 \mathrm{k} \Omega$. With an input of 250 mV r.m.s. and a $7.5 \Omega$ load, typical power outputs of 2.5 W (SL414, 18 V supply) and 4.0 W (SL415, 24 V supply) are obtained. The
supply rejection is 6 dB .


The M252 is a PMOS IC which will supply any of fifteen rhythms to drive eight sound generators. A down-beat output is provided and a version (M252 BlAA) is available with standard music content.

Rhythm is selected by binary coded input (see table 1) 8 or 9 instrument generators are required as well as a variable clock gener ator operating at TTL levels.


* This output must be connected so as to
drive the "snare drum" when the rhythms drive the "snare drum" when the rhythms from 1 to 9 (see rhythms selection) are sel ected, and the "claves" when the rhythms from 10
selected.
$* *$ This pin generates a down - beat trigger which can be used to drive an external lamp to indicate the start of each measure. Typical Advertised Price: $£ 7.50$

Output voltage vs. external supply voltage $\left(V_{E \times T}-V_{S S}\right)$

$\begin{array}{lllllll}12 & 13 & 14 & 15 & 16 & 17 & V_{G G}-V_{S S}(-V)\end{array}$ Output voltage vs. supply voltage $\left(V_{G G}-V_{S S}\right)$




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## MHI KIT SYSTEM

## MH1-5039 (UNIVERSAL COUNTER)

Uses a new counter chip from MOSTEK (MK50395) and will count up or down at speeds of up to 1 MHz with a toial system speed of 400 kHz . Count and compare registers can be loaded from logic ICS or BCD switches, features count inhibit, display latch, display decode. Oulputs 6 digit drives, BCO and 7 -segment. count = compare. for tele-cine, batch counters, repeatable "pill" counters. etc
interfaces with any six digit MHI display kit

## MHI-5378 (DIGITAL CAR CLOCK KIT)

Uses the new National MM5378 Auto-Clock chip. The Chip has full car/boat clock facilities with a voltage range of 9.20 v with no-loss-oi-time down to 5 v . Timing source is a 2.097152 MHz Quartz Crystal which is driven and divided by the chip. Facilities include: (i) display on/oft switching with ignition leaving the clock running at all times (draws about 5 mA ) (iii) display brightness control. MM5738 ktI kkt CA 308 , 2 MHz .
and Trimmers. P. . E 15.10 +VAT. (Interfaces with MHI tour-digit display kits.)

## MHI-5314 (BASIC CLOCK)

Uses National MM5314 chip to give a four or six digit clock with $12 / 24$ hour readout from $50 / 60 \mathrm{~Hz}$ supply. This kit and chip are so simple that no previous electronics experience is really necessary to have an electronic clock working within a couple of hours $\varepsilon 6.60 \times$ VAT

## MHI-5025 (ALARM CLOCK)

For a digital bedroom clock with accurate alarm time. snooze facility and display brightness control. Six digit output in $50 \mathrm{~Hz}, 24$-hour format. Alarm tone oscillator is on-chip and will drive small loudspeaker with single transistor interface. Very simple to assemble £9. $35+$ VAT

## MHI-7001 (ALARM/DATE/TIMER)

A six digit clock with optional display of date. Has switched alarm output and a switched timer (clock/radio. "sleep") output. Apart from being a very unusual clock this kit can be used for remote switching of tape recorders, etc. We advise the use of a six digit readout with this kit. $\mathrm{E} 10.00+$ VAT

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## ETI PCB's



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R/C 'Gxc2' Channel Splitting Unit $1000 \propto 2000 \mathrm{~Hz}$ $£ 8.34$
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DHF/UHF Antennea Amplifier £8.87

LITRONIX DISPLAYS DL704

Cathode
(See ETI Digital Frequency Meter in this issue)
£1.80 each 4 for $£ 7$
DL707 (Common Anode)
£1.80 each 6 for $£ 10$

MACK'S ELECTRONICS FOR A MOTOROLAMcMOS

| MC14001CP Ousd 2 Inpur NOR . |  |  |
| :---: | :---: | :---: |
| MC14002CP | Dual 4 Input NOR | p |
| MC14009CP | Hex Invertor Buffer | p |
| MC14011CP | Quad 2 Input NAND | p |
| MC14015CP | Dual 4 Bit Static Shift R | 26 |
| MC14017CP | Decade Counter | £1.22 |
| MC14021CP | 8 Bit Static Shift Register | 26 |
| MC14023CP | Triple Three Input NAND | 0p |
| MC14027CP | Dual J-K Flip Ftop | 8 p |
| MC14042CP | Quad Larch | 26 |
| MC14046CP | Phase Locked Loop | E1 |
| MC14510CP | BCD Up-Down Counter | £1.40 |
| MC14511CP | BCD 7 Segment Latch | Decoder |
| Driver |  |  |
| MC14528C | ual Monostab |  | ALL PRIGES INGLUDE V.A.T. The revolutionary SUPERTESTER ICE 680R



FAIRCHILD 723

| 14 Pin Dif var. vollage Regutator FAIRCHILD 741 | 55 p |
| :---: | :---: |
| 8 Pin Dill Op Amp | 30p |
| SIGNETICS NE555v |  |
| 8 Pin Dil Timer | 60p |
| FERRANTI ZN 414 |  |
| Hadio Chip | £1.25 |
| MOTOROLA MC I310P |  |
| 14 Pin Dill Coiless Stereo Decoder | £2.10 |
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75p
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14 Pin Dill 2 Watt Audio Amp ..... £1.20
NATIONAL SEMICONDUCTOR
LM380-8
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| :--- | ---: |
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| MM5314 |  |
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55p
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LAST MONTH the basic theory of operational amplifiers was given. This month we give constructional details of, firstly, a dual power supply suitable for running all the units to be described in this series, followed by the description of a sine wave audio oscillator. The oscillator is the first of a series of small projects designed to give practical experience with op-amps.

## THE POWER SUPPLY

The limitation when attempting to reduce the size of any small piece of equipment is, with the present state of the art, the dimensions of the mains operated power supply required to drive it. Thus although an oscillator can be constructed with one IC and a few passive components, the companion ac power supply, by comparison, is extremely bulky. Thus it is fairly pointless to attempt to construct the power supply in such a way as to minimize its total volume. This is not rea!ly a disadvantage, however, as the power supply can be used to power other circuits and the diminutive oscillator can, of course, be powered by separate, small, batteries when that is desirable.
Small size may not be a feature of the power supply, but it does have several important characteristics, namely, automatic short circuit protection and good voltage stabilisation. A brief specification is given in Table 1, while the full circuit diagram appears in Fig. 1.

Consider first that part of the circuit above the dotted line.
Diodes D1 and D2 full-wave rectify the transformer output and charge C1 positively. Capacitor C 1 has a sufficiently large value to filter out nearly all the 100 Hz ripple component and provide a smooth dc to the regulator.
The constant potential across the Zener diode D3 maintains the base of Q1 similarly constant. Should the mains voltage vary, or the load current alter, then the output voltage will tend to change too; however, that voltage is fed, via the diode D4, to 01 emitter.

where it is compared with the Zener voltage at Q1 base. Thus, the collector current of Q1, and hence the base current of Q2, will alter, so effectively changing the emitter-collector impedance of Q2 in such a way as to correct for the original variation. The voltage drop across D4. D8 compensates for the drop across 01 and Q3 thus ensuring the output voltage is the same as the zener voltage.
Such a configuration will give good load regulation but very large variations in mains voltage will not be counteracted as well as is done in some other circuit designs. This is because the Zener diode is fed from an unstabilised supply. Improvement in output voltage stability - by the order of a factor of five or so - can be achieved by modifying the circuit to drive the Zener from the output, rather than the input, of the regulator, but this would not permit the incorporation of short-circuit protection components, in an arrangement now to be described. Short circuit protection of the supply is vital in experimental work, especially on integrated circuits, where the small size and close spacing of the connecting leads printed circuit tracks can so easily result in unwanted shorts which may overload and possibly damage the power supply.
Consider what happens when the output of the voltage regulator is connected directly to the earth line. The germanium diode D4 is no longer forward biased, for its anode,

|  | TABLE 1 |
| :--- | :--- |
| Input | $220-240 \mathrm{~V} 50 \mathrm{~Hz}$ |
| Output voltage | $1 .-12 \mathrm{~V}$ and +12 V <br> or <br> or |
| Output current $3 .+24 \mathrm{~V}$ |  |
| Protection | Automatic constant current <br> limiting $(210 \mathrm{~mA})$ on short <br> circuit |
| Regulation | Better than 80 mV variation, no <br> load to full load <br> Less than 3 mV |
| Hum and noise |  |

## TABLE 2

Frequency range $120 \mathrm{~Hz}-1.2 \mathrm{kHz}$
$1.2 \mathrm{kHz}-12 \mathrm{kHz}$
Output level
1 V rms maximum, continuously variable

Output impedance 70 ohms
Min. load at 1 Vrms 1.5 k
Power supply needed 3 mA at +12 V
2 mA at -12 V

connected now to earth, is not more positive than its cathode; accordingly, D4 can be considered to be absent and the effective circuit arrangement is as in Fig. 2. The base voltage of Q 1 is still fixed (by the voltage across the Zener) at 12 V with its emitter taking up a voltage about 0.7 V less. This fixed voltage appears across R2, which means that a fixed current flows through R2 and Q2 into the base of Q1 and hence, the emitter current of Q1 is fixed also. The emitter current of Q2 will be larger than its base current by a factor equal to the current gain of transistor Q 2 . The emitter current of Q 2 is the load current however, so that it can be seen that, under short circuit conditions, a constant current of a magnitude determined by R2 flows into that short circuit.

A suitable value of $R 2$ must be selected to obtain the desired short circuit current. Here, it is chosen so that 210 mA flows in short circuit conditions. It is under these conditions that the greatest power dissipation occurs in Q2 and accordingly it has been ensured that a continuous short circuit will not give rise to overheating of that transistor.

In point of fact, about 3.4 watts are then dissipated in Q2, a value well within the capability of the transistor type employed. It is bolted to, but insulated from, the die-cast box in which the power supply is housed, so that it is thereby provided with a very large heatsink. Accordingly, in normal use all components run with hardly any temperature rise, and even when running into a short circuit, the combination of current limiting and large heatsink ensures that the power supply is not damaged.
What has been discussed so far is a
single power supply, giving an output of 12 V . The actual unit contains two such supplies, as Fig. 1 shows, of similar circuit configuration, but, in the second case, a PNP transistor is used instead of an NPN and vice versa, and with a negative supply voltage, derived from D5 and D6, fed to it. The Zener diode and its electrolytic capacitor, as well as the germanium diode, are all connected with the opposite polarity from before. Therefore, a stabilised and protected -12 volts appears at the output, relative to the centre supply terminal.
As quoted in the specification, this
permits the output from the power supply unit to be connected in any one of three configurations:-

1. +12 V and -12 V relative to earth
2. +24 V relative to earth
3. -24 V relative to earth

This is achieved by connecting the two supplies in series, with the common point brought out as an external connection. Further, this comrnon point is not earthed, but a separate earth terminal provided. The three different modes of operation are then obtained by means of the appropriate external connections - see Fig. 3.


## OpAmps

For use with the type of operational amplifier dealt with here, the first mode i.e. $\pm 12 \mathrm{~V}$ will usually be employed.
The rated output, of 100 mA from either side, will be found to be more than adequate for the intended use, since type 709 and 741 op . amps. draw less than 5 mA each, unloaded.
A suggested layout and constructional technique for the dual power supply is given in Fig. 4, but the layout is by no means critical and the constructor may employ any alternative method. Nevertheless, a robust housing is required and the best is probably a die-cast box - any small extra expense incurred, to obtain such a convenient and easily worked case, is well worthwhile.

## THE OSCILLATOR

Now for the first constructional project using an operational amplifier. As mentioned earlier, this is a sine wave audio oscillator. The circuit is given in Fig. 5 and its specification in 7 able 2.
The oscillator makes use of the well known Wien-bridge network to set the frequency of operation. A resistor (in this case RVia and R1) and a oarallel capacitor (either C1 or C2) are connected to further resistors (RV1b. RV3 and R4) in series with a further capacitor (either C3 or C4). It is a property of the Wien network that the junction of the two RC arms, has, at a single frequency only, a voltage in phase with, but smaller than, that applied to the whole network. Since, in the oscillator, this in-phase voltage is fed to the non-inverting terminal of the op. amp. it constitutes positive feedback, and thus oscillations will occur and be maintained at one specific frequency - a frequency determined by the values of the resistors and capacitors employed in the Wien network.
So much for the frequency of ascillation. What of its amplitude?
Consider for a moment what would happen if, with the oscillator already giving a sine wave output, the output amplitude should increase for some reason. If it continues to do so, eventually the voltage will become so large that it will be limited by the supply rails and a clipped sine wave will result. Conversely, if the amplitude of oscillation should decrease, then oscillations will eventually die away to nothing.
Such variations in amplitude can easily arise due to temperature changes etc., and will in any case occur as the frequency is altered, due to tolerances


Fig. 4 (a). Suggested layout of major components in the box. Note that the Veroboard should be insulated from the box by inserting cardboard between it and the box.


Fig. 4 (b) Suggested front panel layout.
in the capacitor values and tracking errors in the twin-gang potentiometer.
Thus, some means of automatic gain control is essential in order to maintain a constant output amplitude.

It will be recalled that the signal voltage applied to the op. amp. non-inverting input was smaller than the output voltage due to the attenuation in the Wien network. To maintain oscillation the op. amp. must have a gain equal or exceeding this attentuation - which is in fact $\times 3$. The desired gain is obtained by selecting the ratio of feedback resistance to input resistance of the inverting input (RV2 + R3)/R2.
If the overall gain, including feedback, exceeds unity the circuit will produce sine wave oscillation at a frequency set by the Wien network.

Stabilisation of the gain is brought about by the action of diodes D1 and D2.

When the instantaneous output voltage is close to zero, neither diode conducts, since even a germanium diode requires 0.4 volts or so forward voltage to bias it on. Consequently, the negative feedback loop is open
(giving maximum gain) and, under the action of the positive feedback via the Wien network, oscillations build up rapidly. As soon as their amplitude is sufficient to bias on either D1 or D2 (depending on the polarity of the output voltage swing), then R2, R3 and RV2 provide negative feedback, so limiting oscillations to a convenient level.
Re-inforcement of such oscillations takes place close to each zero crossing when D1 and D2 are open i.e. non-conducting; the setting of RV2 determines the final amplitude.
This method of stabilisation does give rise to a very small amount of crossover distortion, but the effect of this can be minimised by setting VR2 for the largest possible sine wave without clipping. In any event, some distortion is a small price to pay for such a simple, easy-to-get-working sine wave oscillator and, further, it is a low level of distortion - some class $B$ audio amplifiers are worse!

Range switching is confined to a choice of two ranges, in the interest of simplicity and cheapness, but more ranges could easily be provided if the constructor is so inclined.


The frequency ranges mentioned in the specification are a little unusual, in that most audio generators provide ranges starting and ending at $1 \mathrm{kHz}, 10$ kHz and so on. However, in the present case, the selection of easily available components having standard values produced the ranges shown and these were, in fact, found to be convenient in practice.
A simple emitter-follower output stage completes the unit, with a logarithmic potentiometer as a level control, enabling the output to be set from 1 V rms down to 10 mV rms or so.
Suitable compensation components R5, C5 and C6 are required for the type 709 op. amp. A layout found
suitable for the oscillator is given in Fig. 6. Notice that this calls for a box of only $100 \times 75 \times 30 \mathrm{~mm}$, which, whilst making the oscillator quite small and neat, does not result in cramping of the layout, which is straightforward and easily followed.
With wiring up completed and thoroughly checked, switch on and, if possible, monitor the output on an oscilloscope. No 'scope? Then a pair of headphones, of reasonably high impedance, can be used instead. Set RV4 about half way, S1 to "low" and RV1 about half way. If there is no output, adjust RV2; clockwise rotation should give increased output.
With an ac meter, measure the signal level at the junctron of D1, D2 and

RV2. Adjust RV2 for 3 volts rms. This will ensure the highest output level (thus reducing the effect of crossover distortion) consistent with sine wave operation (no clipping). This should provide about one volt rms at the output.
It was found on the prototype that changing to higher frequency range gave a slightly reduced output doubtless due to the use of $10 \%$ tolerance capacitors in the Wien network. Closer tolerance capacitors are, of course, more expersive but the amplitude difference may be overcome by adding small capacitors to either C1 and C3 or C2 and C4 whichever reduces the amplitude difference.
Variation of output level as RV1 is


## OpAmps

| PARTS LIST - AUDIO OSCILLATOR |  |  |
| :---: | :---: | :---: |
| IC1 Q1 | Integrated Circuit Transistor | $\begin{aligned} & 709 \\ & \mathrm{BCl}^{207,108} \end{aligned}$ |
|  |  | or similar |
| D1,D2 | Diode | OA95 |
|  | Resistor | $\begin{aligned} & \text { 1k } 1 / 8 \mathrm{~W} \\ & \text { or } 1 / 4 \mathrm{~W} \end{aligned}$ |
| R2, R3 | " | 4.7 k |
| R4 | ". | $4701 / 8 \mathrm{~W}$ |
| RS |  |  |
| R6 | ",', | 15k 3 ',', |
| R7 | " | 330k $3 .$, |
| R8 $\mathrm{Cl} 1, \mathrm{C} 3$ | Capacitor | 3.3 k $0.01 \mu \mathrm{~F}$ |
| C1, C 3 | Capacitor | 0.01 $\mu \mathrm{F}$ |
| C2, C4, c7 | " | $0.1 \mu \mathrm{~F}$ |
| C5C6C8 | " | polyester |
|  | " | 15pF ${ }^{220 \mathrm{pF}}$, |
|  | - | $10 \mu \mathrm{~F}$ |
| C8 |  | electrolytic |
| RV1 | Potentiometer | 10k + 10k |
| VR2,RV2 | " | 5 k pre-set |
| VR3,RV3 | , | $\left(\begin{array}{l}\text { horizontal) } \\ \text { l } \\ \text { pre-set }\end{array}\right.$ |
|  |  | (horizontal) |
| VR4,RV4 | " | 10 k |
| S1 Switch |  | DPDT |
| Aluminium box, $100 \times 75 \times 40 \mathrm{~mm}$ |  |  |
| 8 pin ic nolader |  |  |
|  |  |  |

rotated, due to tracking errors between RV1 sections, can be minimised by adjustment of RV3. To do this, set RV1 close to its high frequency end. Adjust RV3 to give about the same level of output as with RV1 at mid-travel. If the twin gang potentiometer RV1 is particularly poor in its tracking an alteration to the value of R4 may be called for.

Calibration of the frequency scale is always a problem with any home constructed audio equipment such as this oscillator. Comparison with other audio signals is one method, either by ear, oscilloscope or frequency counter. Ideally comparison . with another oscillator will allow the frequency scale to be marked up accurately.
Regarding alternative components: a 741 in place of a 709 will function well, except for some slew rate limiting at the higher frequencies, leading to distortion. If a 741 is used
delete C5, C6 and R5. A type 301 may also be used; in this case C5, C6 and R5 are deleted as before and a 10 pF capacitor is fitted hetween pins one. and 8. Almost any silicon NPN transistor will be satisfactory for Q1, but some alteration in R7 value may be necessary. Silicon diodes for D1 and D2 give rise to much greater levels of crossover distortion, due to their greater forward voltage drop.
This little oscillator will be found to give a sufficiently pure sine wave to assist in the testing of almost any audio equipment, its restricted frequency range being no great drawback for that work.
A. truly portable oscillator can be made by replacing the dual 12 volts batteries by two small nine volt batteries. If this is done, however, some re-adjustment of RV2 and a reduction in the value of R6 to give a sine wave free from clipping, will be required.

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6. LIGHT DEPENDENT RESISTOAS
7. GAS FILLED DISCHARGE TUBE
8. FIELD EFFECT TAAMSISTOAS
9. MPN TAANSISTOR
10. Capacition
11. LF. CHOKE 12. MICROPHDNE 13. THERMO-COUPLE 14. AERIAL (GENERAL).
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13. NEON
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15. TETRODE VALVE
16. PNP TRANSISTOR
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[^3]
# ELECTRONICS -it's easy! Last word on filters 

WE CONTINUE our discussion of active filter design following our analysis of single and multiple feedback systems. The most common active filter is the multiple feedback path design shown last month. Other options open to us are to use an op-amp set up as either a controlled source with added elements - see Fig. 1, or as the negative-impedance convertor shown schematically last month. These can offer certain advantages over the voltage-amplifier design but suffer some disadvantages. NIC devices, for instance, do not give the ideal zero output impedance. Stages must be buffered to retain designed performance for example, when they are cascaded to obtain higher orders. One the good side is the small number of passive elements needed. Fig. 2 compares the four alternatives showing that no one type is exclusively the best choice.
At this stage we can only suggest that details of designs can be found in the many text books and application notes now available. Very few people would attempt (or even could) design an active filter from basic theory today. There are now available many well-prepared circuit design guides we heartily recommend the Burr-Brown "Handbook of Operational Amplifier Active RC Networks". This contains twelve basic circuits, along with quite manageable design procedures for each, in which desired values are put in formulae to arrive at circuit values for low-pass, band-pass and high-pass requirements.


|  |  | Realiza | Technique |  |
| :---: | :---: | :---: | :---: | :---: |
| Property | InfiniteGain SingleFeedback | InfiniteGain MultipleFeedback | Controlled Source | Negativeimpedance Converter |
| Minimal number of network elements | - | + | + | + |
| Ease of adjustmen: of characteristics | - | 0 | $0^{\circ}$ | + |
| Stability of characteristics | + | + | - | - |
| Low output impedance | + | + | + | - |
| Presence of summing inpuit | + | - | - | - |
| Relatively high gain available | + | - | + | + |
| Low spread of element values | + | - | + | + |
| High- $Q$ realizations possible | + | - | + | + |
| + indicates the realization is superior for the indicated property |  |  |  |  |
| 0 indicates the realization is average for the indicated property |  |  |  |  |
| - indicates the realization is inferior for the indicated property |  |  |  |  |

Fig. 2. Comparison table for various kinds of active filter realisations Ifrom Burr-Brown handbook).

## FILTER CHARACTERISTIC TERMINOLOGY

The ideal edge on a filter characteristic is usually a sharp "square" response with attenuation occuring instantly as the frequency passes through the corner point. It should also have a constant response level at all points in the pass-band regions. As well as the rudimentary RC filter characteristic which falls off at $20 \mathrm{~dB} /$ decade from a breakpoint, two other kinds of response are commonly encountered. These are Butterworth and Chebyshev 'responses. Both derive their names from persons who developed the mathematics involved (Butterworth designed filters around 1930. Chebyshev developed certain mathematical theory in his study of steam-engine linkages around 1850).

The Butterworth response is said to be maximally flat (that is as flat as possible) in the pass-band region. It has the optimum constancy possible with a given number of available peaking resonances (the complex passive or active filter circuits can be regarded as a group of staggered-tuned resonating sections, each arranged to peak just aside of the others, thereby, providing a broadened response band and a reject region). Fig. 3 shows the kind of Butterworth responses obtainable. Note that each passes through the 3 dB , down half power, point. The order (a mathematical term denoting the number of resonances available) of the filter is denoted ' $n$ ' in the chart. A typical roll-off rate is 20 $\mathrm{ndB} /$ decade so a fourth order Butterworth response filter (which can

## ELECTRONICS -it's easy!



Fig. 3 Butterworth filter responses for various orders used.
be realised by either passive or active methods) will attenuate at around 80 dB/decade.
Whereas the pass-band response is reasonably constant, the rate of roll-off is not as good as can be obtained if the resonating sections are staggered differently. Other criteria of staggering the resonances can provide
higher roll-off rates but only by introducing "ripples" in the pass-band response. When these individual ripples have equal amplitude across the pass-band response Chebyshev polynomials describe the shape, thus giving the name to an alternative response situation. As with Butterworth designs the higher the
order the better the roll-off rate as can be seen diagrammatically in Fig. 4. The depth of ripple that can be tolerated also influences the roll-off rate - the smaller the variation that can be allowed the less the roll-off rate. (This can be readily seen by sketching in the required number of ripples of given depth at the appropriate scale).
Normally Butterworth or Chebyshev response filters will be of order 1 to 4 but higher orders are possible. These two forms are not the only sophisticated filter responses available: other mathematical criteria could be used to set up workable mathematical equations for designing other networks. These two will, however, meet most demands required and all filter design, as we have seen, is dominated by need to compromise between what is needed and what can be handled mathematically.

## PHASE SHIFT AND DELAY FILTERS

These act to provide a phase shift to a signal without selectively attenuating the frequency content. They are sometimes called all-pass filters. The amount of phase shift of practical circuits, however, usually varies with the frequency of the signal even though the amplitude response is invariant. Constant time-delay or linear-phase filters have a reasonably straight (linear) phase response as shown in Fig. 5. The so-called Bessel filter approximates this response using a workable mathematical formulation. Fig. 6 gives the general configuration of such a method realised as an active filter design.

## COMPONENTS TO USE

Resistors - In non critical applications the normal 20\% tolerance carbon composition resistor may be acceptable. If tighter filter characteristics are needed then one must resort to more expensive resistors such as 5\% or closer tolerance carbion composition. Even better, use



Fig. 5 Phase response of phase


metal-film or wirewound types. It is sometimes permissible to hand choose values from wide tolerance groups in order to produce specific values, but it must not be forgotten that wide tolerance resistors often lack the same degree of time and temperature stability as the more expensive types.

Capacitors - Ceramic disc capacitors can be employed but they are best avoided. Nylon film, polystyrene and Teflon capacitors are much the better to use. When especially long-period filters are needed the capacitance value will be large. In such cases the leakage current due to losses in the dielectric is extremely critical and this rules out, in the majority of cases, using electrolytics.
Op-amp - It is easy to assume all op-amps will provide good active filters but this is not so. The main factor is a low offset current, this being especially important in long-period filters. As a general rule the more critical the need the better the op-amp should be. When op-amp filters also add gain they should have an open-loop gain at least 50 times the filter gain. Many active-filter design procedures enumerate the requirements of the op-amp.

## RESPONSE TO TRANSIENTS

Filters of second order and higher invoke the characteristics of resonating circuits for their operation. In passive filters we can readily identify the inductance and capacitance; in active circuits these may not be so obvious, the mathematical expression showing that resonances do occur.
When a step change in signal is applied to a resonant circuit, the circuit 'rings', that is, the output rises rapidly but then oscillates with decreasing amplitude to the final value as indicated in Fig. 7. The extent to which a resonant circuit rings is decided by the damping provided the higher the Q of the resonant configuration the greater the ringing effect.
It is not hard to see that higher order filters, therefore, will tend to ring more than the lower order designs when transient signals appear at their
input terminals. Transients occur in practice as noise spikes, switching spikes, sudden signal appearance and departure.

## THE S-PLANE, POLES AND ZEROS <br> (For the advanced reader)

## S-Notation

The above study of filters can only act as a guide to filter selection. From there one must turn to the many articles and books available for details. To make good use of such material it is necessary to have a basic understanding of the mathematical methods used. This section is given to assist the more advanced reader. It is possible to get by without this information, provided a suitable configuration and design procedure can be located. Therefore do not be concerned if you are unable to understand this section.
Scanning through even basic, well-organised books on filter (and feedback amplifier) design the terms transfer function, s-plane, poles, zeros and root-locus will be encountered. Sadly, most books omit to provide the background explaining what this is all about. The concepts are not difficult to grasp, any confusion arising almost certainly from the number of synonomous terms used and the fact that the concepts are, perhaps, quite alien to begin with.
We have seen how reactive elements (capacitance and inductance) have apparent resistances of $2 \pi \mathrm{fL}$ for inductance and $1 / 2 \pi \mathrm{Fc}$ for capacitance. These terms, however, do not provide information about the phase changes produced with these reactance elements.
Electronic circuit designers use the operator symbol j (mathematicians use i) to denote a phase change of $90^{\circ}$ hence, $\mathrm{j} 2 \pi \mathrm{fL}$ represents both the reactance value and the phase change. Furthermore $\mathbf{j}=\sqrt{ }-1$. For capacitive reactance the complete notation is $-\mathrm{j} 2 \pi \mathrm{Fc}$, as the capacitor introduces a $90^{\circ}$ phase shift of opposite sign to inductance. Resistance, having no phase shift, nor being frequency dependent is merely $R$. We can be a little more basic still and use $\omega$ instead
of $2 \pi \mathrm{f} . \quad \omega$ is the angular frequency being expressed in radians $\mathrm{sec}^{-1}$. (There are $2 \pi$ radians in one cycle).
When reactance and resistance are mixed we represent the value as a complex number as, for example, $R+$ $j \omega L$. The left-hand part is known as the Real part, the other (that after j) the Imaginary part, the whole forming what is called a complex number.
Where the circuit element is only reactive the complex number representing the impedance reduces to $j \omega L$ or $-j \omega C$ for which the symbol 's' is used instead of $\mathrm{j} \omega$. (In some books ' $p$ ' is used instead of ' $s$ '). A trap can occur here for the -j of $-\mathrm{j} \omega \mathrm{C}$ indicates a $180^{\circ}$ phase shift over $j$, not a negative quantity in the normal way. To avoid confusion we rewrite -j $\omega \mathrm{C}$ as $1 / \mathrm{j} \omega \mathrm{C}$ (which is valid - it comes from multiplying both numerator and denominator $-\mathrm{j} \omega \mathrm{C}$ by j . Hence we obtain SL and $1 / \mathrm{sC}$ as the shortnand way of writing inductive and capacitive reactance in which frequency dependency and phase information are both retained.
Once these terms and concepts are mastered it becomes much more straightforward to write down the transfer function for a frequency dependent network. For example, consider finding the impedance presented by a series, lossless, resonant circuit shown in Fig. 8a.
$Z=s L+1 / s C=\frac{L\left(s^{2}+1 / L C\right)}{s}$
(The individual components of the expression are put on a common denominator, dividing out to get the $s^{2}$ terms with unity coefficients).
For the series resonant lossy circuit of Fig. 8b.
$Z=s L+1 / s C+R=\frac{s^{2}+R / L \cdot s+1 / L C}{s \cdot 1 / L}$
Again, for the parallel $L$ and $R$ circuit of Fig. 8c.
$1 / Z=1 / s L+1 / R$ from which

$$
\begin{aligned}
& \text { om which }=\frac{R \cdot s}{(s \leftrightarrows R / L)}
\end{aligned}
$$


in text.


It is these forms of expression that are quoted in circuit design books. The
torm of expression is not restricted to two terminal networks - it applies for
all frequency dependent reactive networks. Fig. 9 gives the circuit of a low-pass multiple feedback active filter along with its derived transfer function expressed in ' $s$ ' notation form.
As these complex numbers possess two parts we must use a two-axis graph to represent them in which the two axes are mutually perpendicular. Thus complex-number quantities need a plane rather than a line to depict a unique number. This plane is known as the s-plane (see Fig. 10). The two axes are usually labelled Re, R or $\sigma$ for the Real axis and Im, I or $\mathrm{j} \omega$ for the Imaginary axis, each pair being used respectively.

## POLES AND ZEROS

We have seen above how a network of passive elements lactive designs also apply) produces a mathematical expression in terms of s notation. As s merely represents $j \omega$ and j denotes only phase information we can, whenever s appears, substitute $\omega$ (or $2 \pi \mathrm{f}$ ) to see how the expression varies in magnitude with varying frequency. Consider the case where a function is given by the numerical example:

$$
\frac{(s+1)(s+2+j 1)(s+2-j 1)}{s^{3}(s+3)(s+5)}
$$

When $s=-1,-2-j 1$ or $-2+j 1$, the numerator becomes zero for one of the bracketed terms becomes zero. Hence at each of these frequency values the expression becomes zero. We say it has 'zeros' at these points. Zeros also exist when the singular s term goes to infinity in the denominator. When $\mathrm{s}=0$ (three times, as it is from $\mathrm{s}^{3}=$ s.s.s.), -3 or -5 , we get the reverse situation for at all of these values of $\omega$ the denominator goes to zero making the function rise to infinity. These frequency points are called 'poles'.
Thus the poles and zeros express the peaks and hollows of the function. The position of these can be plotted on the s-plane diagram as shown in Fig. 11. 0 is used for zeros, a cross $X$ for poles. In realisable networks there must be as many poles as zeros including those at zero and infinity.
Another way to imagine the network characteristic is to draw a topographical representation giving relative height to poles and zeros on the s-plane placed horizontally as shown by the example of Fig. 12. This makes the terms poles and zeros more meaningful in a physical sense.
In the numerical example we avoided, in that case, using a quadratic or higher order term such as $s^{2}+4 s+$ 5. When these are encountered they must be factorized by finding the roots of the expression - giving the two terms $s+(2+j 1)$ and $s+(2-j 1)$
in this case. These are the individual roots, i.e., poles and zeros, of the expression. Note that quadratic elements involving an Imaginary part form mirror image pole or zero pairs called a conjugate pair. If these are lossless (no Real part) they lie on the imaginary axis, if lossy (with Real part) they will be displaced out into the s-plane depending upon the resistive value. Positive values of resistance result in displacement into the left-hand plane, negative resistance gives poles or zeros in the right-hand plane, these halves being denoted LHP and RHP respectively.
Mathematics of complex numbers show that resonant systems with roots lying in the L.HP are stable systems, their oscillations die down because to be in the LHP they must contain resistive damping. If the roots lie on the Imaginary axis itself the system is marginally stable - transients will undoubtedly create unstable situations at times even though the system is not absolutely unstable. Note that this situation only arises if the resistive component occurs as negative resistance - oscillators create this condition by the use of an active element.

## ROOT LOCUS

When considering the behaviour of feedback systems, such as amplifiers, controllers and active filters, it is highly valuable to plot the changes in position on the s-plane of the closed-loop poles of the system transfer function as the open-loop gain changes. The path traced, by the movement of the poles in this way is called the root-locus. These are often referred to in amplifier and other feedback-mechanism designs and it is, therefore, helpful to at least appreciate what they are. It is, however, not a simple matter to produce them from an original expression; lots of experience is vital.
By way of example the root-locus for a relatively simple transfer function is given in Fig. 13. This tells us that an open-loop gain in excess of 48 places some of its poles in the RHP establishing an unstable situation. The value of the root-locus is that we can "see" the behaviour of the system as the gain is increased and, more importantly, what we should do to the position of the poles most influencing an unstable situation. By altering the transfer function we can place the locus in more favourable situations. This is done by altering original component values where possible or by adding other networks that reduce the effect of the dominant poies those lying close to the RHP.
Next month we look at digital elect. ronics.


Baker Group 25.3.8, or 15 ohm Baker Group 35.3.8 or 15 ohm Baker Deluxe, 8 or 15 ohm
Baker Major, 3,8 or 15 ohm Baker Regent. 8 or 15 hm Baker Superb, 8 or 15 ohm
Celestion PSTR.(for Unilex)
Celestion MH HoOO horn. 8 or 15 hm
EMI $13 \times 8,150 \mathrm{~d} / \mathrm{c}, 8 \mathrm{ohm}$
EMI $13 \times 8350.8$ or 15 ohm
EMI $13 \times 25$ watt bass
EMI 21/4" tweeter 8 ohm
EMI $8 \times 5$. 10 watt, d/c, roll/s 8 ohm
Elac 59RM 10915 ohm .59 RM 1148 ohm
Elac $61 / 2^{\prime \prime} \mathrm{d} / \mathrm{c}$ roll/s 8 ohm
Elac TW4 4" ${ }^{\prime \prime}$ roll/s 8 ohm
Elac TW4 $4^{\prime \prime}$ tweeter
Fane Pop 15 watt $12^{\prime \prime}$
Fane Pop 15 watt $12^{\prime \prime}$
Fane Pop $25 \mathrm{~T} 12^{\prime \prime} 8 \mathrm{ohm}$
Fane Pop 25T $12^{\prime \prime} 8 \mathrm{ohm}$
Fane Pop 55, 12" 60 watt 8 ohm
Fane Pop 60 watt. $15^{\prime \prime} 8 \mathrm{ohm}$
Fane Pop 100 watt. 8 ohm
Fane Crescendo 15.8 or 15 ohm
Fane Crescendo 18.8 or 15 ohm
Fane 807T $8^{\prime \prime} \mathrm{d} / \mathrm{c}$. rolls/s. 8 or 15 ohm Fane $801 \mathrm{~T} 8^{\prime \prime} \mathrm{d} / \mathrm{c}$ roll/ $/ 58 \mathrm{hm}$ Goodmans 8P 8 or 15 ohm Goodmans 10 P 8 or 15 ohm Goodmans 12P 8 or 15 ohm Goodmans 12P-D 8 or 15 ohms
Goodmans 12P-G 8 or 15 ohms
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Goodmans Axiom 4028 or 15 oh Goodmans Axiom 4028 or 15 ohm
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Kef T15
Kef B1 10
Kef 8200
Kef B139
Kef DN8
Kef DN 12
Kef DN 12
Kef DN 13
Kef DN 13
Richard Allan CG8T 8" d/c roll/s $^{\prime 2}$
STC 4001 G super tweeter
Goodmans Merzo Twinkır, paır
Goodmans DIN 20, 4 ohm, each
Helme XLK 25, pair
Helme XLK 30 , parr
Helme XLK 50, parr
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## Efectranics 

## MORE MONEY-SAVING IDEAS

Yet again we bring you news of how to get more from a clock chip for . less money! National Semiconductor have now released the MM5318 clock chip as a separate chip. You may remember that we mentioned it a couple of months ago as being connected to the TV time display chip. The IC has all of the features of the MM5311 - six digits, seven segment and BCD outputs, 12/24 hour and $50 / 60 \mathrm{~Hz}$ operation. The outputs are multiplexed but controlled. Whereas the MM5311 outputs were under the control of an internal oscillator and multiplexer, the MM5318 outputs are demanded by the data input on three pins. To operate the MM5318 you need a simple oscillator and a BCD divide-by- 6 counter, about $£ 1$, or less, in cost. (In addition to the displays and drivers you would need with the MM5311). 'How is this a saving?', I hear you ask - the answer is quite simply that the recommended price of the MM5311 is $£ 5.69$, but the MM5318 is only $£ 3.36$, a staggering difference of $£ 2.33$ ! Thus for about $£ 4.50$ you can build a $£ 5.69$ clock chip. . I can only assume that National have sold such vast quantities of these chips to TV manufacturers that the production costs are very low and that these low costs are being passed on to the low volume market. Large industrial sales can mean that there are no low volume chips available but it could be worth trying to get one if you want to save some money.

To those of you who build computers, data recorders and similar bit processing systems, it is interesting to note that the MM5318 can be strobed by any $B C D$ data highway and deliver its digit data in BCD and seven segment data to any TTL or MOS system. Much to my own personal annoyance I have recently completed the building of a system to feed a pen plotter with a time signal in $B C D$ format for printing, the unit was built in TTL and took many hours and many chips. It could have

been built using a MM5318 and a couple of other chips - less time, fewer chips and less money.

## DON'T MISS A THING!

Whilst on the subject of TV clocks and tuners we bring you news of the latest in the 'let's get more ICs in TVs' onslaught. The most elaborate channel-selection device is due on the market from Toshiba early in 1976. No more clock watching so that you don't miss Magic Roundabout, no more boring changing channels when News at Ten starts. With this new chip you settle down with the evening paper spralled open at the TV page and program the TV to change channels at predetermined times, then you can ease back in your armchair to an evening's uninterrupted viewing. The chip displays your selection and lists the time and channel as you choose them, up to 16 programs with the on and off time for each channel. The clock operates from 4.5 MHz (presumably a crystal) and has full power-failure back-up facilities. It would seem that the chip could also be used outside of a television set for programming central-heating, anti-burglar lights and any number of other useful things in the home, do not even bother to start thinking of its applications in the industrial area, and yet this chip has come to life as a chip to make a boring life
even more boring.
Programming chips are next year's IN product: microprocessors at under $£ 200$, TV programmers, oven programmers, central-heating, etc. The latest is another National chip for programming calculators, or how to put new life into last year's calculator. The MM5765 calculator programmer provides a convenient and inexpensive way of adding a 'Learn mode' to virtually any calculator chip. It memorizes any combination of key entries during the Learn mode, then automatically plays back the programmed sequence as often as required when in the Run mode. Up to 102 characters or operations can be stored in multiprogram sequence blocks. Each program can be executed individually or the operator can make the decision to branch to specific programs.

The MM5765 is coupled to the calculator by paralleting the digit drives and key inputs between the calculator chip and the programmer chip. The calculator encodes a key depression by sensing which digit drive was active when an input was received by one of its ' $K$ ' inputs. Most calculators have 6, 8, 10 or 12 digit drives and up to 4 ' K ' inputs; thus a six-digit calculator with 3 ' $K$ ' inputs can recognise 18 different key inputs and a 12 -digit machine with 4 ' $K$ ' inputs can have up to 48 kevs. The MM5765 can
handle 47 different key positions by having 12 digit inputs and 4 ' K ' drives. In operation a calculator key is pressed and this will connect one of the digit drives to one of the ' $K$ ' inputs, at the same time the digit drive is connected to the digit input of the programmer and to the ' K ' input of the programmer. The programmer stores this information. as a binary number in six bits (up to decimal 64) as a number 0-46, other non-keyboard entries such as SKIP and HALT give codes in the series 47-63. When the program is put into the RUN mode the programmer will decode this number into a high level output on one of the ' $K$ ' lines when the same digit drive is strobed again, in this way the calculator chip is fooled into thinking that a keyboard depression has caused that digit drive to be directed to that ' $K$ ' input and will thus execute that function. As we are fooling the calculator chip we can enter any valid key depression or series of depressions and on a scientific machine (such as the NOVUS) the radius of a circle of known area could be -

HALT Enter known area
START start programmer
$\begin{array}{ll}\div & \text { Divide } \\ 3 & \text { by Pi }\end{array}$

## 1

$\stackrel{4}{=}$
LOG
Give Result 1
$\div \quad$ Divide
$2 \quad \log$ by 2
=
ALOG Compute Antilog
HALT with approx result.
If you are satisfied with this result you can SKIP to program 2 and use it in. a calculation there, otherwise you can continue with program 1 which might now reverse the computation in order to check its accuracy -

| START | Cont PGM 1 with pre- |
| :--- | :--- |
| vious |  |
| result |  |
| $X$ | Multiply result by |
| $=$ | itself |
| $X$ | and then by |
| 3 | Pi |

$$
1
$$

4
-
$=\quad$ to give comparison with orig area.
If the calculator has memories then it would have been possible to save the original area in a memory and calculate the percentage error in program 1 before continuing with program 2.

The applications of this chip do not end at calculators, many clock
chips use the Digit/'K' method of inputting data and a MM5765 plus a CT7001 could probably make a Lunar Module Command Computer look sick. If the Mostek cooker timer MK50206 is ever fully released it could be combined with the MM5765 to cook a meal or range of meals, all programmed as switch on and off times. You could even use the same combination to set up a selection of TV programmes and on and off times! The price of the MM5765 calculator programmer chip is $£ 9.97$, data from National.

## CT7001 et al

The CT7001 series of clock chips and the CT6002 series of watch chips from Caltex were so widespread at the time of the financial crisis of the company that supplies did not start to dry up for some time. Eventually no CT7004s could be found, then 7002s, 6003s, 6002 s , and now even the CT7003 and Big Daddy the CT7001 are becoming scarce. This chip caused a great stir some time after its announcement and when its capabilities became known; but just at its peak two things happened. First a lot of faulty chips came onto the market, and secondly Caltex had financial problems. The company finally stopped trading last spring and the future of the 7001 looked grim indeed. The last batch of full-spec devices had just been delivered to the UK and there were no more to come - that batch number 7448 indicated that the 7001s had been made in 1974, it is probable that none were made in 1975 at all.

If you are a TIME/DATE/ ALARM fanatic fear not, because the CT7001 has now become the HCM7001 from Fairchild, who bought the Caltex plants, a few CT7001s are still around at $£ 7.30$ (Bywood, Walter Scott and some other retailers) but nobody yet knows the availability or the price of it now that it is the HCM7001. Fairchild also seem to intend to produce the HCM 7002,3 and 4 as well as introducing the HCM 7010 which seems to incorporate most of the family features, further details on the 7010 as soon as Fairchild let me have them.

## PERSONAL

Two personal things from me, firstly I would like to buy a cheap teletype terminal to interface to a microprocessor - if you know of one please let me know. Secondly, I would like to wish you all the compliments of the season and an electrifying 1976


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## PROIV SELECTOR $\operatorname{INALLCHART}$

Intel have published an attractive wallchart which has been designed to aid the engineer in the selection of programmable read only memories. It lists the significant characteristics of 39 different PROM types manufactured by Intel. These are divided into three groups on the chart: Schottky bipolar, silicon gate EPROMs (ultra-violet erasable) and silicon gate MOS

For each Intel PROM there is a pin-for-pin compatible mask programmable ROM shown on the chart; therefore, one a design has passed from the development cycle into production, the PROMs can be replaced with ROMs without the . need for any circuit or printed board changes.

Also featured on the chart is a comprehensive PROM equivalents guide which lists the important char acteristics of PROMs from many manufacturers and gives the Intel equivalent, if one exists. Intel Corporation, Broadfield House, 4 Between Towns Road, Cowley, Oxford OX4 3NB.

## RCA TO BUILD METEROLOGICAL SPACECRAFT

RCA has announced receipt of a \$ 45.6 million NASA contract to design and build eight third generation meteorological spacecraft known as TIROS-N These are expected to make possible weather forecasts two days in advance with accuracy equal to present day predictions. The first launch is scheduled for 1978.

TIROS-N will weigh about 1,400 pounds in orbit and will stand 17 feet tall. They will be advanced polar orbiting satellites that will carry out not only weather forcásting but also worldwide oceanographic and hydrological services.

The TIROS-N spacecraft will provide day and night cloud cover imagery andwill take atmospheric and sea surfacetemperature and water vapour readings. The device will also measure proton, electron and alpha particle activity around the planet.

## ADDENDA

3600 Synthesiser, ETI July '76 The pcb design shown alongside is for the Voltage Controlled Filter.

MOTOROLA SEMICONDUCTOR DATA

Motorola have published a sevenvolume Semiconductor Data Library with a combined thickness of about nine inches giving specifications for all their semiconductor devices. The library also lists the function and the significant characteristics of all EIA registered semiconductor devices.

The product range is divided into

## COS-B SATELLITE

The first satellite to be launched by the new European Space Agency was recently successfully put into orbit by a Delta 2913 rocket, from the Western Test Range in California.

This scientific satellite, called COS-B, is a remotely-controlled astronomical observatory which will detect, locate and enable scientists to study extraterrestrial gamma radiation, the most energetic and penetrating form of radiation known.

Gamma-rays come from certain radio-active atoms, from nuclear explosions and from the depths of outer space. They provide a knowledge of the structure of matter, and possibly of the origins of the Universe. Highly energetic gamma radiations from space is detectable even in coal mines after penetrating the earth's rocky crust, but it is found most freely outside the earth's atmosphere in space itself. The gamma radiation which COS-B will detect may be the remnants of exploding stars or quasars, pulsars and other radio and $X$-ray sources.

The cylindrically shaped $\operatorname{COS}-B$
six volumes ( $1 \mathrm{~N}-2 \mathrm{~N}, 3 \mathrm{~N}-4 \mathrm{~N}$, in-house type numbers, MECL, CMOS and linear ICs). The seventh volume functions as an index and an equivalents guide, and it contains outline drawings and an index of application notes.

The Semiconductor Data Library is available from Motorola Distributors for $£ 15$. Individual prices include the MECL Data Book for $£ 2$, the CMOS book for $£ 1.50$ and the Linear Data Book for $£ 2.50$.
weighs 278 Kg . including the payload, which is a single experiment assembled from five primary and two subsidiary experiment units supplied by six institutes in four European countries (France, Germany, Italy and Netherlands). COS-B will have an oper ational life of two years during which time it will study 24 targets for approximately one month each.


After the successful launching and initial performance checks, the Satellite very rapidly commenced its scientific observation programme by looking at the Crab Nebula. Approximately 14 days after launching a genuine Gamma event was observed and with all systems functioning perfectly COS-B is giving every indication of fulfilling its two year mission lifetime successfully.


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Normally special offers are open from the date of publication to the end of the mionth on the cover of the issue. Usually the filing of orders and the despatch of goods is handled by the company suppiying the products. In this case queries should be addressed to the company and not to EII. The PULSAR offer is being run by ETI but we have had problems in getting hold of extra supplies to meet the unpredictably high demand.

## T-SHIRTS

ETI T-shirts are available in Large, Medium, or Small sizes. They are yellow cotton with black printing and cost $£ 1.50$ each. Send orders to ETI SOFTWARE Dept

## PCBs

PCBs are available for our projects irom companies advertising in the magazine, such as Ramar and Crotton, who do an excellent service.

## EDITORIAL QUERIES

Written queries can only be answered when accompanied by an SAE, and the reply can take up to three weeks. These must relate to recent articies and not involve ETI staff in any research. Mark your letter ETI QUERY.. Telephone queries can only be answered when technical staff are free. and never before 4 pm .

## NON-FUNCTIONING PROJECTS

We cannot solve the problems faced by indiwidual readers building our projects uniess they are concerning interpretation of our aricies. When we know of any error we print a correction as soon as possible at the end of News Digest. Any useful addenda to a project will be similarly dealt whi. We cannot advise readers on modifications to our projects.

## CONTRIBUTIONS

Before submitting any material for publication contact the editor who will advise on suitability (except for letters, news \& Tech-Tips).

## NEWS DIGEST

We receive 20 times more news than we have space for. If you have an interesting item we will be pleased to consider it along with the rest. The statement must be brief and preferably accompanied by a large photograph.

## TECH-TIPS

We pay for items printed in this section: send ideas for submission to ETI TECH-TIPS . . . Drawings must be as clear as possible and the text should be typed or clearly written on alternate lines. Circuits must not have been previously published and must not be subject to copyright. We cannot answel queries on published Tech-Tips.

## LETTERS FOR PUBLICATION

We do nol pay for letters published and we only print them if they are very interesting or important. They should be addressed to the Editor.

## FAN MAIL

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