

CECTONES INTERNATIONAL

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Now...the most exciting Sinclair kit ever

The Black Watch kit At £17.95, it's

9

75

*** practical** – easily built by anyone in an evening's straightforward assembly.

*** complete** – right down to strap and batteries.

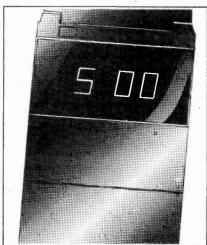
*guaranteed. A correctlyassembled watch is guaranteed for a year. It works as soon as you put the batteries in. On a built watch we guarantee an accuracy within a second a day-but building it yourself you may be able to adjust the trimmer to achieve an accuracy within a second a week. The Black Watch by Sinclair is unique. Controlled by a quartz crystal... powered by two hearing aid batteries...using bright red LEDs to show hours and minutes and minutes and seconds...it's also styled in the cool prestige Sinclair fashion: no knobs, no buttons, no flash.

> The Black Watch kit is unique, too. It's rational-Sinclair have reduced the separate components to just four.

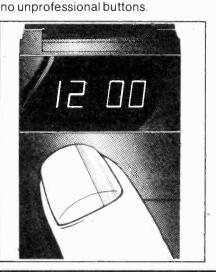
It's simple – anybody who can use a soldering iron can assemble a Black Watch without difficulty. From opening the kit to wearing the watch is a couple of hours' work.

The special features of The Black Watch

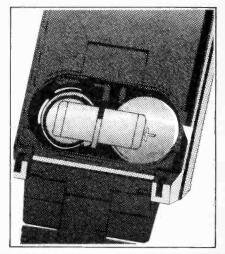
Smooth, chunky, matt-black case, with black strap. (Black stainlesssteel bracelet available as extrasee order form.)



Large, bright, red display-easily read at night. Touch-and-see case -



Runs on two hearing-aid batteries (supplied). Change your batteries yourself-no expensive jeweller's service.



The Black Watch-using the unique Sinclair-designed state-of-the-art IC.

The chip...

The heart of the Black Watch is a unique IC designed by Sinclair and custom-built for them using state-of-the-art technologyintegrated injection logic.

This chip of silicon measures only 3 mm x 3 mm and contains over 2000 transistors. The circuit includes

- a) reference oscillator
- b) divider chain
- c) decoder circuits
- d) display inhibit circuits
- e) display driving circuits.

The chip is totally designed and manufactured in the UK, and is the first design to incorporate *all* circuitry for a digital watch on a single chip.

...and how it works

A crystal-controlled reference is used to drive a chain of 15 binary dividers which reduce the frequency from 32,768 Hz to 1 Hz. This accurate signal is then counted into units of seconds, minutes, and hours, and on request the stored information is processed by the decoders and display drivers to feed the four 7-segment LED displays. When the display is not in operation, special power-saving circuits on the chip reduce current consumption to only a few microamps.



ET7.95 The kit contains 1. printed circuit board

Complete kit

- 2. unique Sinclair-designed IC
- 3. encapsulated quartz crystal
- 4. trimmer
- 5. capacitor
- 6. LED display
- 7. 2-part case with window in position
- 8. batteries
- 9. battery-clip
- black strap (black stainlesssteel bracelet optional extrasee order form)
- 11. full instructions for building and use.

All you provide is a fine soldering iron and a pair of cutters. If you've any queries or problems in building, ring or write to the Sinclair service department for help.

Trimmer **** Quartz crystal

Batteries

Take advantage of this no-risks, money-back offer today!

The Sinclair Black Watch is fully guaranteed. Return your kit within 10 days and we'll refund your money without question. All parts are tested and checked before despatchand correctly-assembled watches are guaranteed for one year. Simply fill in the FREEPOST order form and post it-today!

Price in kit form: £17.95 (inc. blackstrap, VAT, p&p).



Sinclair Radionics Ltd, London Road, St Ives, Huntingdon, Cambs., PE174HJ. Tel: St Ives (0480) 64646. Reg. no: 699483 England. VAT Reg. no: 213 8170 88.

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Please send me	
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Total £

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...... (qty) Sinclair Black Watch kit(s) at £17.95 (inc. black strap, VAT, p&p). (qty) black stainless-steel bracelet(s) at £2.00 (inc. VAT, p&p). * I enclose cheque for £.....made out to Sinclair Radionics Ltd and crossed.
 * Please debit my *Barclaycard/Access/ American Express account number

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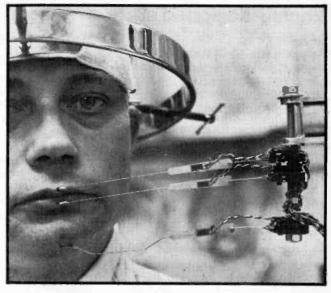
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ETI 1

news digest.





Wired for sound . . . Strain gauges mounted on flexible metal sensors are used to obtain information on lip and jaw movements that we make while speaking. Honey well tape and oscillograph instrumentation records the facial movements for analysis at the University of Washington's Speech Physiology Laboratory.

NUCLEAR POWERED PACEMAKER

A nuclear-powered pacemaker, that could bring a new lease of life to thousands of people suffering from certain types of heart trouble, is to be made at the Harwell Atomic Energy Research Establishment. Conventional pacemakers with chemical batteries have to be replaced sugically about every three years. Prolonged trials indicate that the implanted lifetime of the Harwell units could reach 10 or even 20 years.

The Department of Health and Social Security has placed the first production order for 100 of the new nuclear batteries and an order for another 200 batteries is following. Under the present contract Harwell will prepare the heat-producing nuclear sources, each containg less than a fifth of a gram of plutonium oxide, and assemble them, with their miniature thermocouples, in strong metal housings to form the nuclear batteries that power the pacemakers. At the request of DHSS, Harwell has also undertaken for an interim period to assemble 100 pacemakers in which solid-state DC/DC converters and electronics pulse generators (the former also made at Harwell) will be connected to the batteries. The assembly is encapsulated in an epoxy resin known to have lasting compatibility with living tissue.

About 100 pre-production models of the nuclear battery have already been made at Harwell and fitted into pacemakers of similar design. The pacemakers have been subjected to long-term controlled trials at recognised cardiological centres under DHSS supervision. First, they were tested in animals and then, during the last five years, in human patients — the earliest of whom is still using hers.

Internationally agreed safety standards require stringent tests on the batteries. The isotope Plutonium 238 has been chosen for the heat source because its power output falls by only one per cent per year, and its radiation, mainly alpha particles, requires the minimum of shielding for implatation. This isotope is specially prepared for the pacemaker batteries and is markedly different from the very long-lived Plutonium isotopes associated with nuclear weapons and nuclear fuel. Electronics and Applied Physics Division, Building 347.7, Harwell, Oxfordshire, OX11 ORA.

HENRY'S 'PART EXCHANGE SCHEME'

Henry's-Lindair have announced the formation of a new Part Exchange Department. Customers who bring their old equipment into any Henry's-Lindair store will be given a 'trade-in' allowance which they may then use against the purchase of new equipment from that particular store. The trade-in equipment will then be passed to the Service Department if necessary, and then sold through one of the Bargain Centres in Edgware Road and Tottenham Court Road with a three month guarantee.

DATAPAD

Datapad is a unique computer system where freehand printing is directly input and converted into a computer medium. The system enables data capture as a by-product of normal activity such as clerical staff filling out input forms.



The Datapads are attached to miniprocessors which validate the data at source and indicate any errors to the user clerks via a screen. A 'clean' magnetic or paper tape is produced which is read directly into a main frame computer.

The system will completely eliminate the existing punching and verifying load, source validation will improve accuracy since the data will have been 'edited' before leaving the user environment and this elimination of the manual conversion stage will speed up the whole process. Quest Automation Ltd., 26 Cobham Road, Stapehill, Wimborne, Dorset BH21 7NP.

MICROPROCESSING COUNTER/ TIMERS

Dana Electronics, new 9000 series of microprocessing counter/timers are claimed to be the world's most advanced. The front panel of each instrument bears no controls – the microprocessor is slid out of the front panel, and control programmed through the keyboard.



Both of the first models in the range to be announced (9015 and 9035) work to 100MHz in measuring frequency, period, period average, time interval, time interval average, frequency ratio and total count. Thanks to the microprocesor direct readout of preset operations, reciprocal calculations, and even answers in engineering units are easily achieved. 9-digit constants can operate on measurements through the keyboard.

Trigger levels can be key-set or automatically set: levels are punched into the keyboard, and the processor does the rest, digitally displaying the figures selected for each channel on the integral DVMs. An option is also available to automatically calculate rise/fall times and pulse widths to 10ns resolution, and a range of interface options are available.

The 9015 microprocessor timer costs £1885.00 with a delivery of 8–10 weeks. Dana Electronics Ltd, Collingdon Street, Luton, Beds.

CUNNING LITTLE B****

A recent report by the Assistant Master's Association reveals that schoolboys with a knack for electronics are making devices to bug teachers' studies. Instances reported include that of a boy who concealed a bug inside a radio, which was then confiscated. The radio was then left in a headmaters's study and the boy was able to hear every word that was said there. While we cannot help but admire the ingenuity that must go into some of these espionage-style projects, we do feel that the talents of such enterprising youths could be much better employed in the construction of International 25 Amplifiers or in writing up some of their more conventional projects for ETI.

ELECTRONICS HELPS CHILDREN LEARN MATHEMATICS

The Novus "Quiz Kid", is to help children aged from about 5 to 9 years to learn mathematics. It is similar to an electronic calculator and is based on a chip we reported in our July issue. The child uses the keyboard to enter a problem followed by what is thought to be the answer (eg $2 \times 2 = 4$). Then he asks the machine if the answer is correct by touching a question mark key. If it is a 'wise old owl' with red and green eyes will light up its green eye. If the answer is wrong, the red eye comes on.



The Quiz Kid comes with an illustrated Quiz Book containing dozens of mathematical games, riddles and puzzles to help-youngsters explore and have fun with numbers. Price is £11.95.

MICROPROCESSOR PRICE CUTS

To encourage more systems engineers to explore the benefits of using M6800 microprocessors in their design, Motorola are cutting the selling price of the basic MEK6800D1 designers' kit from £177 to £85.

The kit contains all the components needed in a basic MPU system plus technical information in the form of programming and application manuals. The M6800 microprocessor unit is provided with 1 K-Byte random access memories, peripheral interface adaptors (2 off each), an 8 K-bit read only memory containing MIKBUG and the synchronous communications interface adaptor. All the devices are TTL compatible; single level (5 Volt) power driven and use a single bus for memory and input/output transactions. No multiplexing or decoding circuits are required for its operation nor is external buffering for interconnections. The kit is available from approved Motorola distributors.

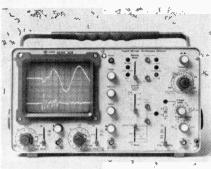


March, September and October 1974

and January, September and Dec-

ember 1975.

news digest



DIGITAL STORAGE OSCILLOSCOPE

The new OS4000 from Gould Advance combines the facilities and performance of a conventional 10MHz oscilloscope with a digital storage system capable of storing signals up to 450Hz (-30dB). Digital storage has several advantages over tube storage, including the ability to examine what happens immediately before a trigger signal is received, the simultaneous viewing of stored and real-time displays, absence of deterioration of the stored display over a period of time, flickerfree low-frequency performance, and the alimination of the expensive storage tube.

The OS4000 is ideally suited for viewing transient waveforms – for example, in medical, dynamic testing, vibration or pulse-testing applications. It is also suited to low-frequency measurements, where the incorporation of a 'refresh' mode allows flicker to be eliminated. (The longest sweep time is 200s.) In addition, normal 10MHz real-time viewing is possible, and comparisons between stored and real-time waveforms are easily made.

The OS4000 measures $178 \times 312 \times 417$ mm, and weighs 11kg. The price of the instrument is £1053 (inc. VAT).

Gould Advance Ltd, Roebuck Road, Hainault, Essex.

WATCH/CALCULATOR

Now available in the USA is the Uranus LED Solar Calculator timepiece. This is a five-function watch (hours, minutes, seconds, month, date date), with calculator built in. The keyboard consists of 12 pressbuttons arranged around the face and gives all four arithmetic functions, in chain and constant calculations. The readout is a four digit LED display and as far as we can discover, the watch is solar powered. The Uranus is available in gold or silver plated metal, and can be yours for only \$800.

TWO-LAYER TAPE

A new formulation of magnetic recording cassette tape that combines the advantages of both ferric oxide and chromium dioxide tapes has been announced by BASF. Called Ferrochrom, the tape has a two-layer coating which, when used with suitable equipment, gives an improved frequency response for cassette recording. The new tape comprises a polyester base coated with a relatively thick high density ferric oxide dispersion. On top of this is added a carefully controlled layer of chromium dioxide. This composition exploits the simple theory that low frequencies are produced from the whole of the tape coating and high frequencies are reproduced from the top of the coating.



Because of its dual oxide, the optimum bias current for ferrochrom is a value approximately halfway between the current for ferric oxide and that for chromium dioxide. Ferrochrom can still be used on a conventional ferric recorder, but on playback the higher frequencies are unnaturally pronounced, giving an effect similar to those on a Dolbyised cassette played back on a non-Dolby machine. This is easily remedied by a slight adjustment of the amplifier's treble control. The frequency response and dynamic range achieved using Ferrochrom in this way is equal to that of a chromium dioiide cassette played on a machine boased for chromium dioxide. The best results from Ferrochrom are achieved on cassette recorders biased for Fe-Cr. On these the dynamic range from a VU setting of OdB is 1.5dB greater than that possible from chromium dioxide.

BASF Ferrochrom is available in C60 and C90 length in a new-style cassette housing. The housing has a large window which enables the user to see the tape hubs and BASF's patented tape transport system – Special Mechanics (SM). The amount of tape on each hub is accurately shown and any disturbance of or damage to the tape can be seen before problems occur.

Continued on page 84

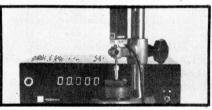
FISH QUALITY METER

An electronic fish quality meter which indicates the freshness quality of wet fish rapidly and accurately is now being produced in Scotland. The GR Torrymeter is manufactured by GR International Electronics Ltd of Perth. It was designed and developed in Perth in collaboration with Aberdeen University and the Torry Research Station, a Government establishment recognised as a world authority on fish handling and processing.

The GR Torrymeter is a simple, hand-held device 200mm in length and 75mm in width. The instrument measures an electrical property of the fish tissue; this property changes as fish spoils. Individual readings are displayed in illuminated figures one second after the sensing head has been placed on the fish. The meter, equipped with long-life rechargeable cells, is housed in a sealed plastic moulding which can be wiped clean or rinsed under running water. Each unit is supplied in an individual wooden storage case incorporating a mainsoperated charger unit. GR International Electronics Ltd, Doman Camberley, Surrey GU15 3DG,

DIGITAL LENGTH GAUGE

A new miniature measuring instrument in the form of a digital gauge head is announced by Dr. Johannes Heidenhain GmbH. The Metro 1010, surpasses dial test indicators, vernier calipers or analogue measuring heads both technically and economically.



The digital gauge head is suitable for length measurement on smaller machines, micrometer stages, coordinate tables, etc.

The measuring range of the MT10 is 10mm. The resolution is 1μ m. The measuring accuracy of a complete system (digital gauge head with counter) is $\pm 1\mu$ m. A genuine incremental design principle and a minimum of mechanical components enables the high degree of accuracy to be maintained.

The measured value is indicated by a five-digit LED display with arithmetical sign and decimal point. As an option, the counter can be supplied with a BCD-output (printer output) for electronic processing of measured values. Heidenhain (GB) Ltd., 202 London Road, Burgess Hill, Sussex RH15 9RD.



A. Marshall (London) Ltd Dept ETI 42 Cricklewood Broadway London NW2 3ET Tel: 01-452 0161/2 Telex: 21492 & 85 West Regent St Glasgow G2 2QD Tel: 041-332 4133 & 1 Straits Parade Fishponds Bristol BS16 2LX Tel: 0272 654201/2 & 27 Rue Danton Issy Les Moulineaux Paris 92 Tel: 642 2985 Catalogue price 25p

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2N706A	0.16	2N3415	0.10	3N139	1.42	AF280	0.79	BC261	0.25	BF184	0.30	14DIL	0.38	SL621C	2.60	£23.50
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2N916	0.28	2N3703	0.12	40394	0.56	BC115	0.17	BC 307	0.17	8F200	0.40	LM7815	2.50	SN76033N	2.92	
2N918	0.32	2N3704	0.15	40395	0.65	BC116	0.17	BC308A	0.15	8F225J	0.23	LM7824	2.50	ST2	0.20	P.C. M
2N929	0.37	2N3705	0.15	40406	0.44	BC116A	0.18	BC309C	0.20	BF244	0.21	MC1303	1.50	TAA300	1.80	400M
2N930	0.22	2N3706	0.15	40407	0.35	BC 117	0.21	BC327	0.27	BF245	0.45	MC1310	2.92	TAA263	1.10	IC Soc
2N1302	0.19	2N3707	0.18	40408	0.50	BC118	0.14	BC328	0.28	BF246	0.58	MC13300P MC135IP	0.80	TAA 350	2.10	20p.
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2N1306	0.31	2N3711	0.15	40594	0.74	BC132	0.23	BCY32	1.15	BF258	0.53	ME0402	0.20	TBA6418	2.25	VAT
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2N2219	0.24	2N3819	0.37	AC153	0.35	BC149	0.15	BD116 BD121	0.75	BFX88 BFX89	0.25	MJE520 MJE521	0.00	TIP41A	0.79	
2N2219A	0.26	2N3820	0.38	AC153K	0.40	BC153 BC154	0.18	8D123	0.82	BFY50	0.23	MJE2955	1.20	TIP42A	0.90	
2N2220	0.25	2N3823	1.42	AC154	0.25	BC157	0.16	BD124	0.67	BFY51	0.23	MJE3055	0.75	TIP29c	0.80	
2N2221	0.18	2N3904	0.27	AC176 AC176K	0.30	BC158	0.16	BD131	0.40	BFY52	0.21	MPB111	0.32	TIP30c	0.85	Cmc
2N2221A	0.21	2N3906	0.27	AC176K	0.40	BC160	0.60	BD132	0.50	BFY53	0.18	MP8112	0.40	TIP31c	1.00	4000
2N2222	0.20	2N4036 2N4037	0.67	AC188K	0.35	BC167B	0.15	BD135	0.43	BFY90	0.75	MP8113	0.47	T1P32c	1.25	4000
2N2222A	0.25	2N4037	0.42	ACY18	0.24	BC168B	0.15	BD136	0.49	BRY39	0.48	MPF102	0.30	TIP33c	1.45	4002
2N2368	0.25	2N4059	0.15	ACY19	0.27	BC168C	0.15	BD137	0.55	BSX 20	0.21	MPSA05	0.25	TIP34c	2.60	4006
2N2369 2N2369	0.20	2N4060	0.15	ACY20	0.22	BC1698	0.15	BD138	0.63	BSX20	0.21	MPSA06	0.31	TIP41c	1.40	4007
2N2646	0.22	2N4061	0.15	ACY21	0.26	BC169C	0.15	BD139	0.71	BSX21	0.29	MPSA12	0.35	TIP42c	1.60	4008
2N2647	0.98	2N4062	0.15	ACY28	0.20	BC170	0.15	BD140	0.87	BU104	2.00	MPSA55	0.26	TIP49 ·	0.70	4009
2N2904	0.22	2N4126	0.21	ACY30	0.58	BC171	0.16	BD529	0.80	BU105	2.25	MPSA56	0.31	TIP53	0.53	4010
2N2904A	0.24	2N4289	0.34	A0142	0.57	BC172	0.17	BD530	0.80	C106D	0.65	MPSU05	0.65	TIP2955	0.98	4011
2N2905	0.25	2N4919	0.95	AD143	0.68	BC 177	0.28	BFR39	0.24	CA3180A	0.85	MPSU06	0.58	TIP3055	0.50	4012
2N2905A	0.26	2N4920	1.10	AD149V	1.20	BC178	0.27	BFR79	0.24	CA3020A	1.80	MPSU55	0.63	TIS43	0.28	4013
2N2906	0.19	2N4921	0.80	AD150	0.63	BC179	0.30	BDY20	1.05	CA 3028A	0.79	MPSU56 NE555V	0.80	ZTX300	0.13	4014
2N2906A	0.21	2N4922	0.83	AD161	1.15	BC182	0.12		0.36	CA3035	1.37	NE555V NE556	1.30	ZTX301	0.13	4015
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2N2926	0.20	2N5192	1.24	AF106	0.40	BC184 BC184L	0.13	BF125 BF152	0.35	CA3089E	4.23	OC23	1.35		0.18	
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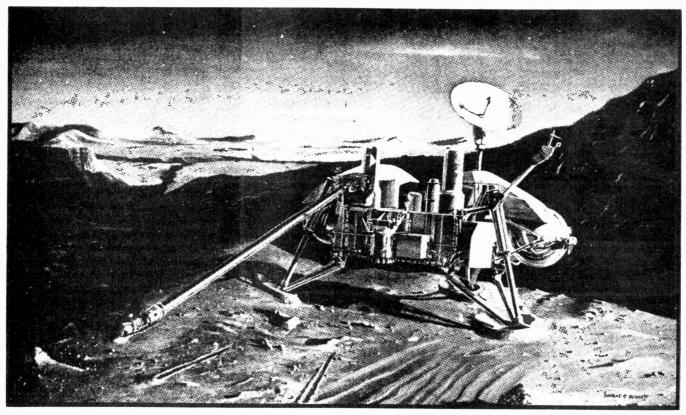
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ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976

62.04

Latest Viking space mission specifically seeks for life on Mars



OUR GALAXY contains upwards of 100 000 million stars and their families of planets, and growing evidence suggests that Earth may not be the only life-bearing planet in this galaxy.

Telescope studies have, for example, shown that Earth's basic chemicals are distributed throughout the Universe – and organic compounds – life's building blocks – have been detected in inter-stellar space.

But *believing* that life may exist elsewhere is one thing – attempting to *prove* it is another.

At our present state of technology the most practical planet on which to search for life is Mars. For if we find that life exists or has existed in the harsh climate of Mars, we will have strong reasons to believe that planets with comfortable climates do support life and that other solar systems are inhabited.

Two unmanned, automated Viking spacecraft have recently been launched from Cape Canaveral to begin a 700 million kilometre journey to Mars.

Once there the two spacecraft will seek evidence of whether life existed on other planets, and obtain information to improved our understanding of how Earth developed as a life-supporting planet and how we can better protect its environment.

Viking is the most complex mission to be flown by NASA, requiring four highly complicated science stations – two orbiters and two landers – to carry out separate co-ordinated operations simultaneously and over an extended period of time.

The 2300 kg orbiter will stay in orbit while the 1090 kg lander descends to the surface, as did the command module and the lunar module during. VIKING LANDER. This magnificent painting by Charles O. Bennett shows the mechanical scoop on the end of the spring-steel arm collecting a sample. Having grasped the material the scoop arm retracts to the spacecraft, swivels and stops over a cylinder covered by a wire grill. The lid of the scoop begins to vibrate and dust and soil particles drop through into a rotating conveyor in the spacecraft's interior which distributes measured quantities to a number of test cells for chemical analysis.

the Apollo missions to the Moon. The Viking lander will use a parachute and retro-rockets to achieve soft landing. It will begin its programme of life-seeking biology experiments on about the 10th day after landing.

Photos from orbiters and landers and information from the science experiments will be transmitted back

to Earth by radio. Radio signals will require more than 20 minutes to travel the 320 million kilometres from Mars to, Earth. Spacecraft-tracking and communications will involve the Deep Space Network's stations at Goldstone, California, Madrid, Spain and Canberra, Australia.

VIKING SCIENTIFIC INVESTIGATIONS

The Viking spacecraft will make basically three investigations during their three-month observation of Mars from orbit and from two sites on the surface:

- 1.A photographic survey of Mars:
- 2.A search for forms of life; and
- 3.An analysis of the physical features and makeup of the planet and its atmosphere.

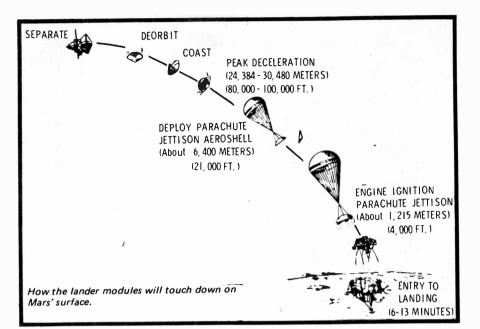
PHOTOGRAPHIC SURVEY

Each Viking spacecraft has an orbiter and a lander. Each orbiter carries two high-resolution television cameras, and each lander a pair of facsimile cameras.

During the last 180 hours of approach to Mars, each spacecraft will obtain a series of photographs of the planet from progressively closer range. After entering orbit, the spacecraft will remain above the designated lander sites to photomap these regions for a number of days before and after the landers are released for descent to the surface. Then the orbiters will leave their fixed positions above the lander sites to photomap almost the entire surface of the planet.

Orbiter photomaps and thermal and water vapour maps will be used to direct the landers to sites where conditions are most favourable to life and where landing hazards are less extreme. Lander cameras will take high-resolution photos of the ground immediately next to the landers, 360-degree panoramic views of the terrain and distance features, and long-range photos of Mars' satellites and celestial objects.

The Orbiter cameras will be rapid-sequence vidicon cameras using 475 mm telescopes. Taken from low-point in orbit a photo shows 40 square kilometre area with resolution of 50 metres. Consecutive photos from one orbital pass show a 80 km by 500 km swath. Photos are stored on magnetic tape for playback to Earth. Identical facsimile cameras are mounted one metre apart on top of the lander for stereoscopic black and white, colour, and infrared photos. Cameras can view from the ground beside the lander up to '40 degrees above horizon. Each uses a nodding mirror to scan a scene in tiny increments, requiring 20 minutes for a full scene. Light from the scene increments is converted into digital



information bits which are radioed to Earth and reconstructed into a photograph.

SEARCH FOR LIFE

Life may exist on Mars in higher forms, like moss or lichens, or in microscopic forms like viruses or bacteria. Or a rich variety of life may have existed at one time but disappeared later in the planet's history.

The two regions chosen for landings are areas where conditions could be conducive to Earth-like life forms. They are relatively low, temperate-zone sites in the northern hemisphere where there are indications of atmospheric moisture now, and of surface moisture at least at some time in the past.

Higher forms of life and fossils. surface burrows or trails, and artifacts could be identified in the lander camera photos of the surface adjacent to the landers. The search for microscopic plant or animal life will be made in Martian soil samples. The samples will be scooped up by the 3 metre lander boom and fed into automated biology test chambers where they will be observed for signs of photosynthesis and metabolism. Chemistry of the organic compounds in the soil will be analysed for indication of whether they were produced by animal or plant life, or could evolve life.

Discovery of life on another planet would have a more profound effect on man's thinking than any other discovery in history.

TEST FOR PHOTOSYNTHESIS

Photosynthesis is the basic-life sustaining process by which Earth plant life uses light energy to combine basic compounds like carbon dioxide, water, and salts – forming carbohydrates. Steps in the Viking photosynthesis test are:

- (a) Inoculate three soil samples with carbon monoxide and carbon dioxide that bear radioactive tracers.
- (b) Inoculate soil and gases under a lamp that simulates Martian sunlight.
- (c) Evacuate any remaining free gas.
- (d) Heat samples to 590°C to vapourize organic materials.
- (e) Measure and analyse the vapourised materials.

Liberation of a substantial amount of tracer gas from the samples will be taken as strong evidence that plant-like organisms in the soil consumed the carbon monoxide and carbon dioxide in photosynthesis.

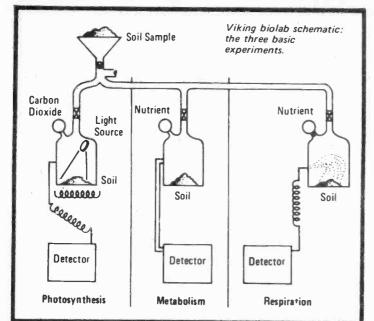
TEST FOR METABOLIC ACTIVITY

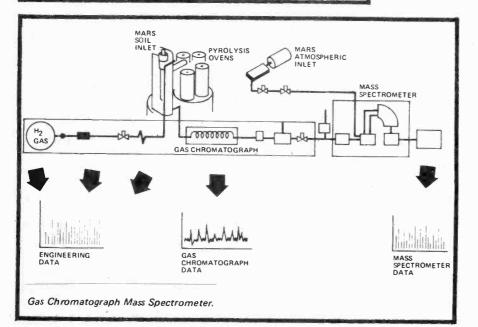
From a science standpoint, the Viking lander is the most complex spacecraft ever built. The biology unit will feed to three soil samples a nutrient or organic compounds like sugar which bear trace chemicals. Instruments will monitor gases given off by the samples over a period of about two weeks.

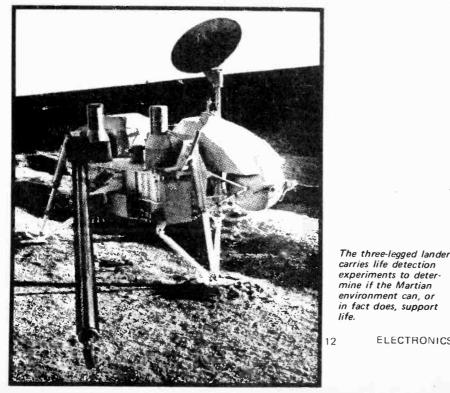
Steady production of gases by soil samples will be taken as evidence that organisms in the soil consumed the nutrient; steadily increasing production of gases will be taken as evidence of growth by the organisms.

TEST FOR RESPIRATION

Soil samples will be moistened with nutrients and surrounded in the test chamber with air from the outside, principally carbon dioxide. Constituents of the atmospheric sample will be monitored over a period of about two weeks. Changes in composition of the atmospheric sample will be taken as evidence of







mars?

> respiration from metabolism of organisms in the soil.

TEST OF STERILISED SAMPLES

In parallel, a soil sample will be sterilised and subjected to the same tests as further validation of any positive results in the tests.

BIOLOGY INSTRUMENT

The complete range of experiments planned for Viking, if conducted on Earth with today's standard science instruments, would require thousands of pounds of equipment which would fill several ordinary size laboratory rooms.

The lander biology unit, in 0.25 cubic metre of space, contains: three automated chemical labs, a computer, tiny ovens, counters for radioactive tracers, filters, sun lamp, gas chromatograph to identify chemicals, 40 thermostats, 22 000 transistors, 18 000 other electronic parts, and 43 miniature valves,

THE PLANET AND ITS ATMOSPHERE

Instruments of the orbiters and landers will examine the physical features and makeup of the planet and its atmosphere in minute detail. Comparison of the geology and climate of Mars with those of the much more complex Earth and the primitive Moon is expected to resolve many questions about the evolution of Earth and our Solar System,

One landing site is in a valley at the mouth of the giant surface rift, or grand canyon, of Mars. Here, deposits from exposure and erosion of geological features around the chasm are expected to be rich in information about the history and development of the planet.

We still lack a complete understanding of Earth's complex environmental systems; for example, what accounts for the patterns of movement of water vapour and pollutants in our atmosphere. Clues should be found in study of the dynamics of Mars' more rudimentary atmospheric system in the absence of industrialised man.

The Viking Lander. After landing, the Lander's cameras will take pictures of the terrain – some in colour and some employing both cameras to produce threedimensional stereo pairs. Other instruments will collect atmospheric and meteorological data, and a seismometer will record Martian quakes and learn about the planet's interior. Surface geology will be examined with the cameras, the soil sampler, the inorganic analysis of soil samples to determine what elements are present.

GEOLOGY

The orbiters and landers will conduct experiments to study surface geology and planet internal structure, and to determine whether the planet is geologically alive. Orbiter and lander photographs will identify types of land forms, stratification, folds, joints, faults, rocks, erosion, sediments, and soil, and will give indications of mineral and chemical composition. If there are Marsquakes in adequate number, lander seismic readings can determine whether the planet has a molten core, a mantle, and a crust as does Earth, and can allow comparison of the mantles of Mars and Earth.

Lander instruments will identify elements and minerals in the soil. Thermal mapping by the orbiters will allow search for ground frost and evidence of planet internal heat, and will aid in identifying surface structural character from difference in heat conductivity.

Viking radio and radar systems will provide information to improve our knowledge of the planet's size, mass, gravitational field, surface density, and electromagnetic properties, and atmospheric density and turbulence, and will allow study of the solar wind.

SCIENCE INSTRUMENTS

An infrared radiometer from the orbiter measures heat radiating from the planet's surface. It can record temperatures in both the day and night hemispheres and is accurate within 2°C.

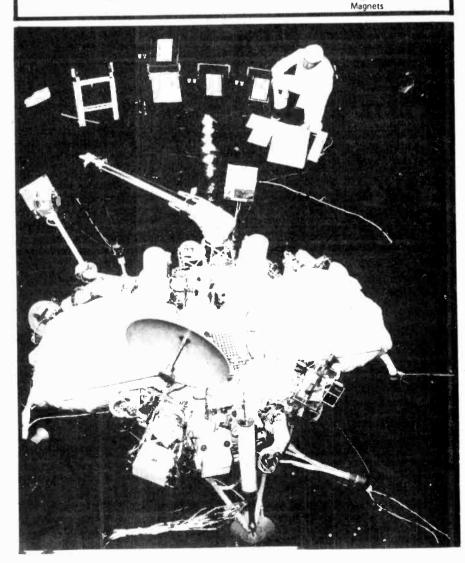
An X-ray fluorescence spectrometer identifies basic elements in soil by measuring their fluorescence after being exposed to radioactive Cadmium 109 and Iron 55. It can detect elements present in amounts as small as 200 parts per million.

Viking completes a month-long series of rigorous tests designed to qualify it for operating on the surface of Mars in 1976. The tests were conducted in a huge vacuum chamber at the space facilities centre of Martin Marietta Aerospace at Denver, Colorado.

Radioisotope thermoelectric S-band high-gain generator coolant lines (Nonfunctioning in antenna (Lander to landed configuration) DSN direct link) Propulsion fill and N₂ makeup lines Magnifying mirror Radioisotope thermoelectric Radar altimeter generator wind cover (2) electronics number 1 Camera (2) Meteorology sensors Seismometer Radioisotope thermoelectric generator power source Meteorology (under cover) (2) boom assembly JItrahigh frequency antenna (relay) Annet and camera test target Temperature sensor Leg 2 Roll engine (4) S-band low-gain Gas chromatograph-mass antenna spectrometer processor Terminal descent View propellant tank (2) Biology processor mirror (2) Terminal descent landing Furlable boom radar (underside of X-ray fluorescence experiment funnel Lander structure) Leg 3 Terminal descent Collector head engine (3)

Radar altimeter antenna

(18-nozzle configuration)





A gas chromatograph-mass spectrometer identifies gases in the atmosphere and organic compounds in the soil.

A seismometer will detect volcanic activity, planet internal structure shifts, and impacts of meteorites on planet's surface. It will be used to determine whether landers are functioning properly by measuring their vibrations.

A soil boom scoop will be used to study characteristics of the soil: cohesiveness, porosity, hardness, particle size. Magnets mounted on the sampler will determine whether soil contains magnetic materials.

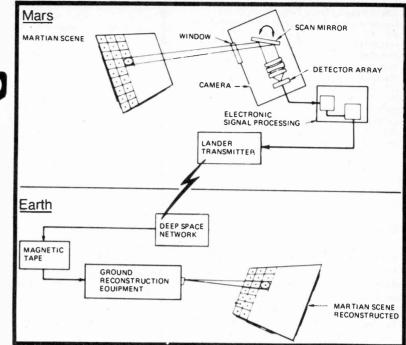
METEOROLOGY

Requirements for the Viking lander are space flight's most demanding: two days of sterilisation baking before launch; ascent gravity and vibration forces atop a 1.4 million pound rocket; 11 months' exposure to harsh space conditions; buffeting from Mars atmospheric entry, parachute opening and retrorocket firing; impact on surface of Mars at a speed of 1.20 metres/sec. Then it must function as a finely tuned, self-sufficient laboratory and data transmission complex for 90 days.

Landers, as they enter Mars' atmosphere, will analyse the ionosphere to determine the effect of the solar wind on the planet's atmosphere. As they descend they will record the temperature, pressure, and chemical content of the atmosphere at different altitudes.

The Orbiters will observe the formation and movement of clouds and record their temperatures for analysis of their composition. The lander mass spectrometer will analyse the amounts of carbon dioxide, oxygen, nitrogen, and other gases in the atmosphere at the surface during the first three days, before starting analysis of the soil.

Lander instruments will measure pressure, temperature, windspeed and direction periodically to log daily and season all variations in weather and will record the movement of weather fronts, thermals, and dust devils past the landing sites. Seismometers will record background noise from winds and temperature and pressure changes.



How pictures taken by the television system aboard the Viking Lander will be processed, transmitted and reconstructed on Earth.

In the meteorology unit transducers will measure temperature and pressure; an anemometer measures windspeed by its cooling effect on a heated wire. Accelerometers and radar altimeter are used to determine atmospheric density and pressure from their drag on the descending lander.

An infrared spectrometer will detect and measure moisture in the atmosphere from the changes in solar radiation, as it reflects from Martian surface through the atmosphere to the orbiter. It can detect water in amounts down to one micron.

A retarding potential analyser will measure the concentration and charge of ions and electrons in the ionosphere as they flow across the analyser's charged grid in lander capsule.

An upper atmosphere mass spectrometer will identify chemical content and concentrations in the upper fringe of atmosphere.

Radio and radar systems consist of S-band Earth-Mars microwave link for commands and data relay; UHF lander-orbiter links; X-band orbiter-Earth link for science use.

COMMUNICATING WITH VIKING

The Viking Lander Communications subsystem consists of a relay link between the orbiter and lander and a direct link connecting the lander and Earth. UHF radio is employed to relay scientific data to the orbiter, and an S-band transmitter on the lander sends data directly to Earth.

The relay link is the primary means of communicating during descent and landing until a direct link between the lander and Earth can be established on the surface of Mars. The UHF circuits use compartmentalised, shielded discrete-element wired circuits.

When the lander separates from the orbiter and descends into the Martian atmosphere, UHF radio will begin transmitting real-time data at 4 000 bps and at a frequency of 381 MHz to the orbiter. This information will consist of entry science data, such as the pressure and temperature of the Martian ionosphere and atmosphere, and engineering information related to the lander's operating performance.

The subsystem utilises split-phase pulse code modulation and frequency shift keying. A fixed, crossed-dipole antenna radiates signals to the orbiter. Any one of three power modes may be selected.

Prior to separation from the orbiter, the lander will under-go a series of system checks in the lower power mode (1 watt). During descent, the 10 watt mode will be used. After touch-down, the transmitter will be switched to 30 watts power to handle a data stream rate of 16 000 bits per second. With successful completion of the soft-landing on Mars, the lander will continue to broadcast for about three days via the UHF system before switching over to the direct S-band link. Imagery showing the area surrounding the landing site as well as data received from the meteorological and biological sensors contained on the lander can then be transmitted to the orbiter for relay to Earth.

The orbiter will be able to receive this data from the lander an average of 20 minutes each pass. It will be orbiting Mars once every 24.6 hours.

S-BAND DIRECT LINK

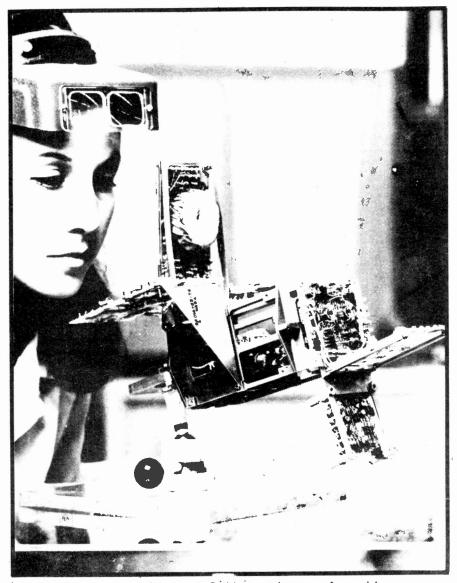
The lander S-band link directly transmits high-volume scientific and imaging data, Doppler tracking planetary ranging and command reception. The subsystem operates at 2.2 GHz and is coherently locked to Earth frequency references.

Engineering data is transmitted at a rate of 8 1/3 bits per second and scientific information may be sent at selectable rates of 250 500 and 1,000 bits per second. Coded (32/S), phase shift keying pulse code modulating information is received at 4 bits per second.

A 760 mm parabolic reflector high-gain antenna will be used to transmit information from Mars directly to Earth. The high-gain antenna is steerable and utilises a two-axis gimbal. The mast for this antenna is deployed and locked into position immediately upon landing on Mars. Powered by a d-c driven motor, the antenna is stepped to follow the Earth by an open-loop command and control system. A fixed, crossed dipole S-band low-gain antenna will receive the commands from Earth.

Redundant modulator-exciters and a 20 watt travelling wave tube amplifier transmits the telemetry and imaging data at a frequency of 2295 MHz for approximately two hours each day.

Two S-band receivers operate on the same frequency and use the same receiver selector. The receiver selector furnishes signals to the modulatorexciters and the command detectors. One of the receivers is connected to the low-gain antenna and is the primary command receiver. The other receiver is linked to the high-gain antenna to detect the ranging signal. This receiver also serves as a backup command receiver when the high-gain antenna is pointed toward Earth.



Kathy Daniels of Honeywell's aerospace Division examines part of a specialpurpose computer which will control experiments in the Viking Lander.

ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976

VIKING ANTENNA

The RCA Viking antennas are specially designed and built to operate in the hostile Martian environment. They must withstand harsh surface winds, sand and dust storms, low pressure, and a temperature range from $+111^{\circ}$ C to -126° C.

The UHF low gain antenna is 450 mm high and is located on the Viking Lander Capsule (VLC). It provides for the rapid, high volume transmission of data from the VLC to the Viking Orbiter for relay to Earth. The antenna will operate during both the descent of the VLC to the Martian surface and at programmed times during the mission after touch-down. An insulating foam designed to minimise power disruptions during transmission in the low pressure Martian atmosphere is enclosed in cylindrical containers on the ends of the antenna elements.

UHF LOW GAIN ANTENNA Specifications:

Use:	Communications to Earth <i>via</i> orbiter relay.
Operation:	During Martian entry
	and from surface
Type:	Crossed dipoles,
	circularly polarized.
Frequency:	400 MHz.
Power:	60 Watts.
Weight:	1.3 kg.

The S-Band Low Gain Antenna is 150 mm. high and is also located on the VLC. It will receive signals from Earth and will operate according to the mission programme after the VLC lands on the planet's surface.

S-Band LOW GAIN ANTENNA Specifications:

Use:	Receive commands
	from Earth.
Operation:	Martian Surface.
Type:	Broad pattern, circularly
	polarized.
Frequency:	2100 mHz.
Weight:	0.11 kg.

The S-Band High Gain Antenna has a 750 mm. parabolic dish of traditional appearance. It will be used to transmit and receive radio signals between the VLC and Earth and will operate after touchdown according to the mission programme. Since Mars, like Earth, rotates on its axis, it is necessary for the S-Band High Gain Antenna to be facing toward Earth for maximum effective transmission. Once the VLC has landed safely on Mars, an on-board computer will calculate the necessary alignment (both azimuth and elevation) for the antenna. The Viking Command and Control, or controller, will transform this information into

commands to motors which will then reposition the antenna relative to Earth. Continuous adjustments will be computed during each transmission. It will take approximately 20 minutes for signals to travel the more than 362 million kilometres between the Viking spacecraft and its home planet.

S-Band High Gain Antenna Specifications:

Transmit data
Mars-to-Earth.
Martian surface.
760 mm diameter.
2100-2300 mHz;
40 Watts.
Elevation over azimuth
ecifications:
Pedestal drive and
encoder processor.
Martian surface.
Commands from
on-board computer.
Drives pulses to two
stepper motors sine/
cosine from two
resolvers.
0.22 kg.
100mm x 140mm x
178mm
: Two 2-sided four-layer

printed circuit boards

Another view of the spectacular 'photo globe' of Mars – the first ever made of any body in the Solar System. It was prepared by the Jet Propulsion Laboratory of California Institute of Technology which managed the Mariner project for NASA.

Electronics Today would like to thank the British Interplanetary Society and the Martin Marienatta Corporation for their assistance with the preparation of this article.

		Contract of participants and the						
L I MANA		DALL	10		DIGITAL DISPLAYS & LED'S			
LYNX ELECTRONICS (LONDON) LTD					DL704 99p DL747 1-75 -2 RED LED ONLY 13p			
			DO (LOND)		DL707 99p DL750 1-75 GREEN CLEAR 15p			
	AND STREET, MARKET MARKET				THYRISTORS 8A 1A 3A 6A 8A 10A			
Transistors	BC183 10p*	BF337 32p	CRS1-10 25p	2N697 12p 2N706 19p	(TO92) (TO5) (C106 type) (TO220) (TO220) (TO220)			
AC126 15p AC127 16p	BC183L 10p* BC184 11p*	BFW60 17p* BFX29 26p	CRS1-20 35p CRS1-40 40p	2N929 14p	50 20 25 35 41 42 47 100 25 25 46 47 48 54			
AC128 13p	BC184L 11p*	BFX30 30p	CRS1-40 40p	2N930 14p	100 25 25 40 47 48 54 200 27 35 45 58 60 68			
AC128K 25p	BC207B 12p	BFX84 23p	CRS3-05 34p	2N1131 15p	400 30 40 50 87 88 98			
AC141 18p	BC212 11p*	BFX85 25p	CRS3-10 45p	2N1132 16p	600 65 70 1.09 1.19 1.26			
AC141K 28p	BC212L 11p*	BFX88 20p	CRS3-20 50p	2N1304 20p 2N1305 20p	TRIACS (PLASTIC TO-220 PKGE, ISOLATED TAB)			
AC142 18p AC142K 28p	BC213 12p* BC213L 12p*	BFY50 20p BFY51 18p	CRS3-40 60p CRS3-60 85p	2N1305 20p 2N1711 18p				
AC176 16p	BC214 14p*	BFY52 19p	MJ480 80p	2N2102 44p	4A 6:5A 8:5A 10A 15A (a) (b) (a) (b) (a) (b) (a) (b) (a) (b)			
AC176K 25p	BC214L 14p*	BFY64 35p	MJ481 £1-05	2N2369 14p	(a) (b) (a) (b) (a) (b) (a) (b) (a) (b) 100 V 0.60 0.60 0.70 0.70 0.78 0.83 0.83 1.01 1.01			
AC187 18p 1	BC237 16p*	BFY90 65p	MJ490 90p	2N2369A 14p	200 V 0.64 0.64 0.75 0.75 0.87 0.87 0.87 0.87 1.17 1.17			
AC187K 25p	BC238 16p*	BR100 20p	MJ491 £1-15	2N2484 16p 2N2646 50p	400 V 0.77 0.78 0.80 0.83 0.97 1.01 1.13 1.19 1.70 1.74			
AC188 18p AC188K 25p	BC300 34p BC301 32p	BRY39 40p BSX19 16p	MJE340 40p* MJE371 60p	2N2905 18p	600 V 0-96 0-99 0-87 1-01 1-21 1-26 1-42 1-50 2-11 2-17			
AD140 50p	BC323 60p	BSX20 18p	MJE371 60p MJE520 45p	2N2905A 22p	N.B. Triacs without internal trigger diac are priced under column (a). Triacs with internal trigger diac are priced under column (b). When ordering please indicate			
AD142 50p	BC327 18p*	BSX21 20p	MJE521 55p	2N2926R 10p*	clearly the type required.			
AD143 46p	BC328 16p*	BSY95A 12p	OA5 50p*	2N29260 9p*				
AD149 45p	BC337 17p*	BT106 £1.00	OA90 8p	2N2926Y 9p* 2N2926G 10p*	74 TTL mixed prices			
AD161 35p AD162 35p	BC338 17p* BCY70 12p	BT107 £1.60 BT108 £1.60	0A91 8p 0C41 15p	2N3053 15p	$\begin{array}{cccccccccccccccccccccccccccccccccccc$			
AL102 950	BCY71 18p	BT109 £1.00	OC41 15p OC42 15p	2N3054 40p	The set of the set of set of and and and			
AL103 93p	BCY72 12p	BT116 £1-00	OC44 12p	2N3055 50p	7401 14p 12p 10p 7447 81p 75p 65p 7493 45p 40p 32p 7402 14p 12p 10p 7448 75p 62p 50p 7495 67p 55p 45p			
AF114 20p	BD115 55p	BU105 £1-80*	OC45 10p	2N3440 56p	7403 15p 121p 10p 7447A 95p 83p 67p 74100 £1.08 89p 72p			
AF115 20p	BD131 36p	BU105/	OC70 10p	2N3442 £1.20	7404 16p 13p 11p 7470 30p 25p 20p 74107 35p 28p 22p			
AF116 20p AF117 20p	BD132 40p BD135 36p	02 £1-90*	OC71 10p	2N3570 80p 2N3702 10p*	7493 16p 13p 11p 7472 25p 21p 17p 74121 34p 28p 23p			
AF118 50p	BD136 39p	BU126 £1.60* BY206 15p*	OC72 22p OC84 14p	2N3703 10p*	7409 16p 13p 11p 7473 30p 25p 20p 74122 47p 39p 31p 7410 16p 13p 11p 7474 32p 26p 21p 74141 72p 63p 53p			
AF139 33p	BD137 40p	BY207 20p*	SC40A 73p	2N3704 10p*	7410 16p 13p 11p 7474 32p 26p 21p 74141 78p 63p 53p 7413 29p 24p 20p 7475 47p 39p 31p 74145 68p 58p 48p			
AF239 37p	BD138 48p	BYX36-300	SC40B 81p	2N3705 10p*	7417 27p 22 p 20p 7476 32p 26p 21p 74154 £1.62 £1.48 86p			
BC107 14p	BD139 8p	12p*	SC40D 085	2N3706 10p* 2N3707 10p*	7420 16p 13p 11p 7482 75p 62p 50p 74174 £1.00 83p 67p			
BC107B 16p BC108 13p	BD181 86p BD182 92p	BYX36-600 15p*	SC40F 855	2N3707 10p* 2N3714 £1·05	7427 27p 221p 18p 7485 £1-30 £1-09 87p 74180 £1-06 88p 71p			
BC109 140	BD183 97p	BYX36-900	SC41A 65p SC41B 55p	2N3715 £1-15	7430 16p 13p 11p 7486 32p 26p 21p 74181 £3 20 £2 50 £1 90 7432 27p 221p 18p 7486 32p 26p 21p 74181 £3 20 £2 50 £1 90			
BC109C 16p	BD232 60p	180*	SCALD 70P	2N3716 £1-25	7437 27p 221p 18p 7489 £2.92 £2.80 £2.10 74192 £1.35 £1.14 99p			
BC117 19p*	BD233 48p*	BYX36-1200	SCALE 85P	2N3771 £1-60	7441 75p 62p 50p 7490 49p 40p 32p 74193 £1 35 £1 14 90p			
BC125 18p*	BD237 55p	21p*	60p ST2 20p	2N3772 £1-60	7442 65p 55p 43p 7491 65p 55p 45p 74196 £1 64 £1 34 99p			
BC126 20p* BC141 28p	BD238 60p	BYX38- 300 50p	TIP29A 44p	2N3773 £2-10 2N3819 28p*	LINEAR IC's			
BC141 200 BC142 230	BD184 £1 20 BDY20 800	BYX38-	TIP30A 52p	2N3904 16p*	301A 8 pin DiL 35* 3900 14 pin DIL 78* 565 14 pin DIL £2*00*			
BC143 23p	BDY38 60p	600 55p	TIP31A 54p	2N3906 16p*	307 55* 709 8/14 pin DIL 35* 566 8 pin DIL £1.50*			
BC144, 30p	BDY60 66p	BYX38-	TIP32A 64p	2N4124 14p+	309K 1.60 741 8/14 pin DIL 28* 503 0 pin DIL 21 30			
BC147 9p*	BDY61 65p	900 60p	TIP34 £1-05 TIP41A 68p	2N4290 12p+ 2N4348 £1-20	380 14 pin DiL 90* 748 8 pin DiL 36* 507 8 pin DiL £2.00*			
BC148 9p* BC149 9p*	BDY62 55p	BYX38- 1200 65p	TIP42A 720	2N4870 35p*	381 14 pin DIL 1-60* 555-8 pin DIL 45 CA3046 14 pin DIL 50*			
BC152 25p*	BF178 28p BF179 38p	BZX61 series	IN2069 14p	2N4871 35p*	Matching charge 20p per pair. P & P 20p. Overseas 80p. CA3045 85'			
BC153 18p*	BF194 10p*	zeners 20p	IN2070 16p*	2N4919 70p*	NATIONAL CLOCK CHURS			
BC157 9p*	BF195 10p*	BZX83 or	IN4001 4p*	2N4920 50p*	NATIONAL CLOCK CHIPS			
BC158 9p* - BC159 9p*	BF196 12p*	BZX88 series zeners 11p*	IN4002 5p* IN4003 6p*	2N4922 58p* 2N4923 64p*	MM 5316 . £3.75 MM 5316 £5.25			
BC159 9p* BC160 32p	BF197 12p* BF224J 18p*	zeners 11p* C106A 40p	IN4004 7p*	2N5060 20p*	(Basic clock chip giving 6 digit display) (Sophisticated device including calendar			
BC161 38p	BF244 17p*	C106B 45p	IN4005 8p*	2N5061 25p*	alarm, similar to CT 7001)			
BC168B 9p*	BF257 30p	C106D 50p	IN4006 9p*	2N5062 27p*	HIGHAM MEAD, CHESHAM, BUCKS. Tel. (02405) 75151			
BC182 11p*	BF258 35p	C106F 35p	IN4007 10p*	2N5064 30p*				
BC182L 11p*		CRS1-05 25p	2N696 14p	2N5496 65p	VAT-Please add 8% except items marked * which are 25%			

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Another digital watch? Hardly. The Sinclair 'Black Watch' has as much in common with other digital watches as the calculators of three years ago have with today's products.

There are three distinct differences between this new product and the more common digital watches. Most obvious is the styling – we like it because it is designed for this new technology, it owes nothing to conventional designs. I can however understand those who do not find it appealing but appearance is a very subjective field.

appealing but appearance is a very subjective field. Secondly the case is plastic. Ugh, I hear some of you say but you haven't seen it or felt it. Basically it is a two part case which straps together but the plastic used is unlike any we have seen – Sinclair won't say what it is. It doesn't feel, look or behave like better known plastics. The technology and industrial design behind this case is as exciting as the chip used. The top of the case has two flexible diaphagms incorporated in the moulding only 10 thou thick – this in itself is something that has broken new ground. The case design includes no protruding switches or buttons – the switches for display and for setting are controlled by pressure through the case. Batteries can be replaced in a very short period by removing a malleable plastic plug. Sinclair don't claim the watch is waterproof – but it's certainly water resistant.

The biggest breakthrough is the chip – and only one chip is used. This uses integrated injection logic: 1^2L . This brand new technology is only just becoming known to the public. Sinclair believe that their chip – and they themselves designed it – is the first watch chip using 1^2L . Other companies have been talking about this – but to date it's only talk.

date it's only talk. I²L is suited perfectly to digital watch design. Tiny currents are used but this is combined with the ability to supply high peak current when the display is actually used. It also offers high component packing density among other advantages. The chip contains 2,100 active devices in an area 117x123 thou.

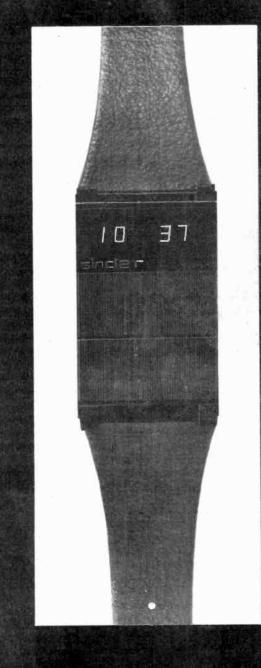
In addition to the chip there is only the crystal – the now near standard 32.768kHz flexural bar crystal. Fine adjustment of this is obtained by means of a small trimmer. A tiny capacitor connected directly across the battery, is used to prevent very short interruptions in the supply throwing the timing out: this could occur during a violent blow to the watch.

An LED display is used. The poor contrast ratio and doubts about reliability of liquid crystal displays now available led to this choice.

Introducing a digital watch has been the aim of Sinclair; in fact it was first investigated in 1967. Prototypes were developed but a high frequency crystal (4MHz) had to be used — this led to high power consumption. LED's weren't around then so neon type displays were used.

In 1972 with the first news of I^2L , the whole programme was turned in this direction. The first semiconductor company decided to pull out leading to a considerable delay. ITT are now making this chip for Sinclair.

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When we first saw this watch at a press launch, we were extremely impressed - so much so that your Editor has placed what may be the first order!



Test CMOS and TTL with this versatile instrument.



schematic of the particular IC drawn on it, to be fitted to the front of the tester as an aid to the interpretation of the LED test indications.

CONSTRUCTION

The most expensive single component in the tester, after the transformer, is the case. For this reason we decided to make a wooden case and a plain aluminium front panel. Some people may however wish to mount the unit in a diecast box and for this reason the printed circuit board has been sized to fit in a standard 222 x 146 x 51 mm die-cast box. The following description is for a wooden box specifically, but applies equally well to the metal box.

The printed-circuit board is mounted to the rear of the front panel, copper side to the panel, such that the LEDs and patch pins, mounted on the printed-circuit board, project through the front panel. This greatly simplifies construction as it saves some 48 leads and solder joints. The switches are secured to the front panel by first glueing two pieces of printed-circuit board to the rear and then soldering the switches to the copper side of the board. This procedure avoids the necessity of a multitude of screws passing through the front panel. The printed-circuit board should be assembled with the aid of the component overlay by fitting all components with the exception of IC1, 5, 6 and 7, and LEDs 1 through 16, and the patch pins. Check that the ICs are orientated correctly as are also C2, 5, 7, 9 and D1, 2 and 3. Now solder these parts into position using the least amount of heat necessary on ICs 2, 3 and 4.

Position the LEDs and patch pins onto the copper side of the board but do not solder them in place as yet. Now fit the board to the front panel so that the pins and LEDs protrude through the panel evenly. Secure the pins and LEDs in position by using a very small drop of five minute epoxy for each, on the component side of the

WARNING:

£

When using the tester, remember that manufacturers recommend that CMOS ICs should not be inserted or removed from a circuit without first switching off the power supply.

eiproject 122

EXPERIMENTERS often damage ICs in the process of developing a new circuit and often try a new IC in a circuit that is not working to eliminate that as a possible cause. The result of this is that one usually finishes up with a box full of ICs which are of dubious value. To sort out these ICs one must use a tester that is capable of testing the wide range of differing ICs that are available in the most commonly used families.

Until recently the most commonly used family has been TTL. But CMOS is rapidly gaining widespread usage and any tester, to be of value these days, must be able to test both these families. The ETI Logic Tester is capable of testing both families, and is also capable of being used to breadboard and test simple circuits based on single ICs.

An LED indicator is associated with each pin of the IC under test and these are arranged around the perimeter of a box representing the IC under test. This allows a small card, which has the

HOW IT WORKS.

The tester consists of four basic sections. The socket for the IC under test, the output level-detect logic, oscillators and switches for the inputs, and the power supply.

The socket for the IC under test has the pins in each row electrically connected to each other. These rows are the groups of five holes which are perpendicular to the central groove on the socket. Each row (ie, each pin on the IC under test) is connected via a 10 megohm resistor to ground to prevent the build up of static charges. The resistors also hold all unconnected inputs at ground potential thus preventing any damage to the IC.

Each row is also connected to a pin

on the front panel. Test connections are made to these pins by patchable links from the oscillator and test switches so that the correct test conditions may be set up.

Resistors R19-26 and R43-R50 connect each row (ie pin) to a logic level detector, ICs 5, 6, and 7. These CMOS hex-inverters buffer each pin and drive an LED to indicate the logic state of the pin. When the logic voltage on a pin is high the LED will be alight. Resistors R19 to R26 and R43 to 50 protect the internal diodes of ICs 5, 6 and 7 against the possibility of a pin being taken above the positive supply voltage or below ground potential. Resistors R11 to R18 and R51 to R58 in conjunction with the five volt supply set the

operating currents for the LEDs.

1.

A 555, IC4, is used as an astable oscillator which initially charges C8 via R9 and R10 until the 2/3 supply threshold is reached. C8 is then discharged via R9 and pin 7 of the 555 to the lower threshold of 1/3 supply volts. Switch SW6, when operated, puts a larger value of capacitance into the circuit which gives a frequency of about one hertz. This is slow enough so that the eye can follow each logic state transition. The high speed operation is used for checking very long counters and shift registers and can also be used in conjunction with an oscilloscope. The square wave output of the oscillator is made available at, a

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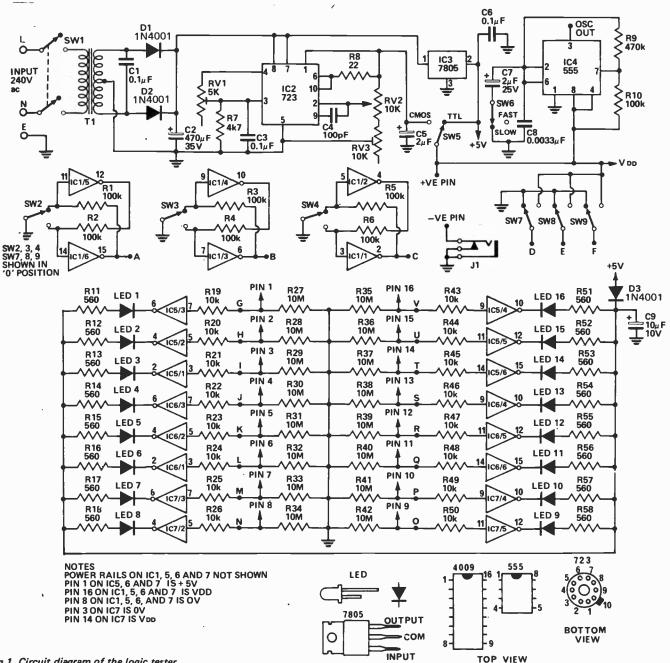


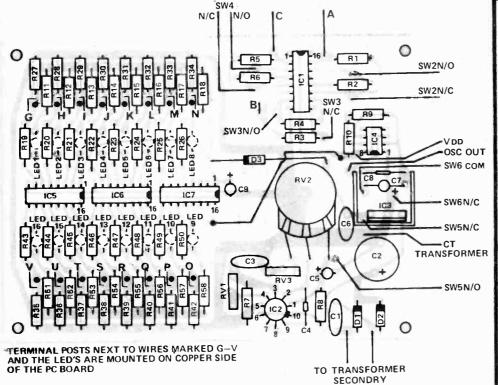
Fig. 1. Circuit diagram of the logic tester.

patch-pin on the front panel.

There are six further output pins on the front panel three of which, D, E and F, are set to negative or positive supply by means of toggle switches. As there is no debounce logic associated with these pins they can only be used to set up static conditions and not for clocking counters and shift registers. The remaining three pins are also programmed by switches but these switches are connected to IC1 which contains three RS flip-flops to effectively remove any contact bounce of the switches. This operates as follows. If initially the input of IC 1/5 is earthed by SW2 its output will be high and hence the output of IC 1/6 will be low. When IC 1/6 SW2 is operated again it earths the input of IC 1/6 sending the output of IC 1/6and input of IC 1/5 high and the output of IC 1/5 low. Since the input of IC 1/6 is connected to the output of IC 1/5 it is held low even if the contacts of SW2 bounce several times when the switch is operated. Thus the output at A is one single transition from high to low (low to high when next the switch is operated). The output of the three debounced switches are labelled on the front panel as A, B, and C.

In the power supply diodes D1 and D2 full-wave rectify the output from the power transformer. The output from the rectifier is smoothed by C2 and regulated to five volts by IC3. The resulting five volt supply is used to drive the LED indicators and to power the TTL device under test. Integrated circuit IC2, a type 723, is a regulator the minimum output of which is set to five volts by RV1 and the maximum of 15 volts by RV3. Front panel control RV2 allows the output voltage to be adjusted between five and 15 volts. The current limit on the output is set to 30 mA by means of R8. SW5 selects the high current five volt supply for testing TTL or the low current variable supply for CMOS. Terminal J1 in the negative supply lead is provided for checking the current drawn by the IC under test.

Fig. 2. How the components are mounted on the pc board.



LOGIC TESTER

PARTS LIST - ETI 122								
R8 Resistor R11,18 " R51,58 " R7 " R19,26 "	22Ω ¼W 5% 560 """ 4 k7 "" 10 k """							
R43,50 " R1,6 " R10 " R9 " R27,42 "	10 k " " 100 k " " 100 k " " 470 k " " 10 M " "							
RV1 Potentiomet RV3 '' RV2 ''	ter5k Trim type 10k """" 10k Linear							
C4 Capacitor C8 '' C1,3,6 '' C5,7 '' C9 '' C2 ''	100 pF Ceramic 0.0033/JF polyester 0.1/JF 2/JF 25V electro 10/JF 10V " 470/JF 35V "							
D1,2,3 Diode 1 LED 1 - LED 16	N4001 or similar Light Emitting Diodes							
IC1,5,6,7 Integra IC2 '' IC3 '' IC4 ''	ted Circuit 4009 (CMOS) 723 (metal can case) 7805 (TQ-220 case) 555							
J1 Jack sr	nall earpiece type							
SW1 DPST SW2-SW9 miniatu	toggle 240V rated pre slider switch 2 pole 2 position							
PC BOARD ETI	122							
IC Socket Wooden case see text Transformer 240 V primary 30 V CT secondary or 2 x 15 V windings								
25 patching Pin f	eed throughs							
front panel 3 core flex and p heatsink for IC3	lug (see Fig.6)							
12								

(Text continued from page 71)

boards. Do not glue the LEDs to the front panel. Once the glue has set, carefully remove the board from the front panel and then solder the LEDs and pins into position. Fit 250 mm long leads to the board for later connection to the switches and power transformer and then, using a minimum amount of heat, solder ICs 1, 5, 6 and 7 into position.

Solder the leads to the pins on the IC socket — the front panel must be cut out so that these leads may be passed through. Now affix the socket to the front panel and install the printed circuit board. Mount

the transformer into the base of the box and interconnect the board and switches etc.

The wooden box was constructed from 12 mm thick pineboard such that the outside dimensions were 225 x 148 x 70 mm. We finished our box with coloured high-gloss enamel which

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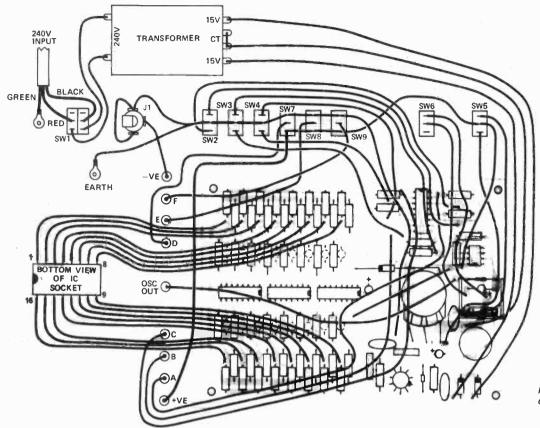
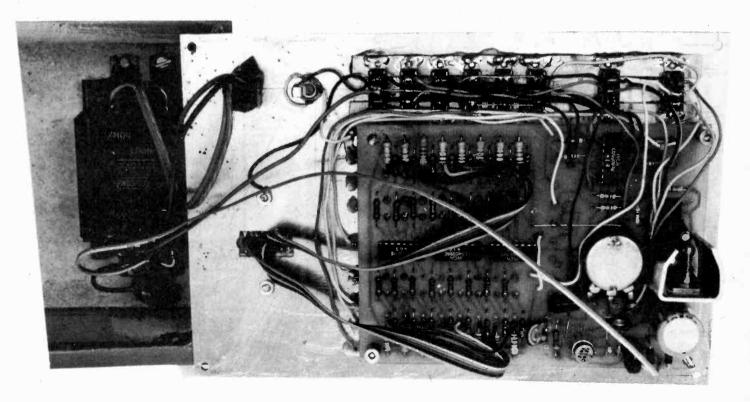
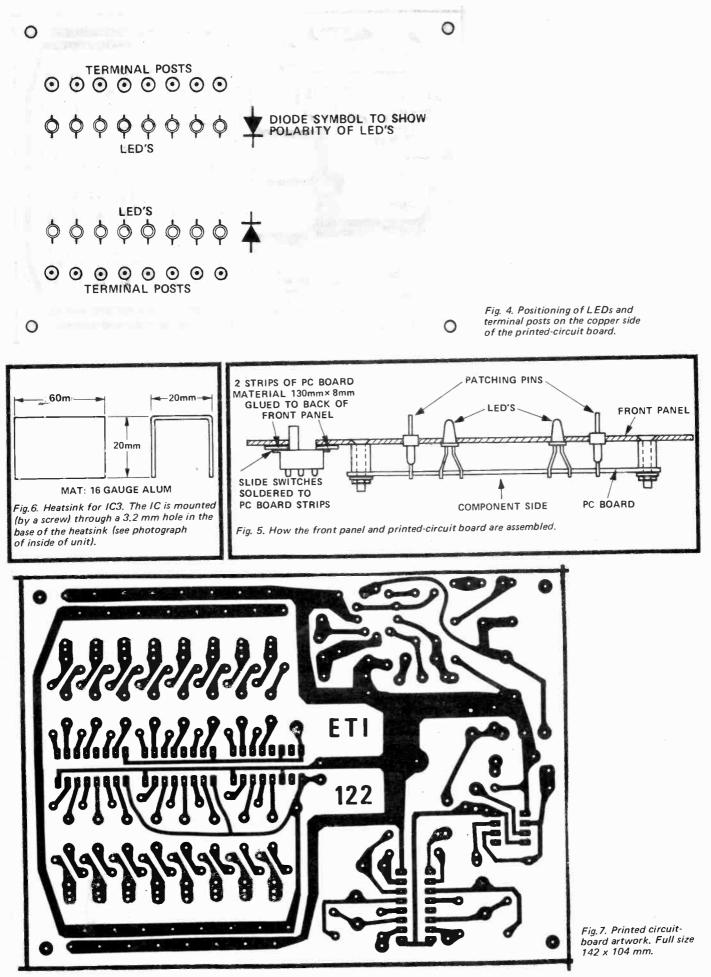


Fig. 3. Wiring diagram of complete unit.

LOGIC TESTER





LOGIC TESTER

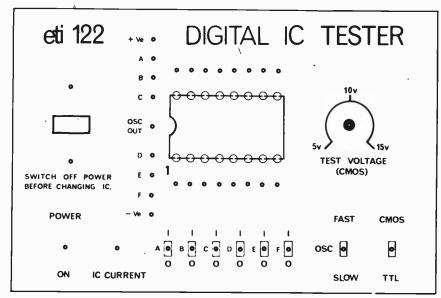


Fig.8. Front panel artwork (shown half-size - full size should be 223 mm x 148 mm).

resulted in a very pleasing final appearance.

DESIGN FEATURES.

There are several design requirements which must be met in a unit which is designed to test both CMOS and TTL devices. These may be summarized as follows.

1) The unit must be capable of correctly testing both types of logic. 2) Simple gate functions should be tested by go/no-go checks and complex functions such as counters and shift registers should also be reliably checked.

3) There should be the least possible chance of damaging the device during testing.

4) ČMOS ICs must be testable with a variety of supply voltages.

5) A clock oscillator and a means of setting up the input conditions must be provided.

One of the major design difficulties with a unit such as this is coping with the many different pin configurations the differing functional of requirements (eg a shift register versus a two-input NAND gate) of devices within the one family, as well as those between different families. A multi-way switch could be used for each input pin but would greatly increase the expense of the unit. A good alternative is to use patchable links, and this is the approach that we have chosen to use in our unit. In addition we have used a small breadboard socket as the test socket. rather than a standard 16 pin dual-in-line socket, as this allows us to improvise special test circuits for the

more complex logic ICs, and the means to breadboard simple circuits.

The need for a variable power supply for CMOS testing presented two additional problems. The first of these was the danger of plugging a TTL IC into the unit when it is set up for CMOS and for some higher supply voltage than the five volts required for TTL. Secondly the LEDs used for monitoring each pin would draw more current as the supply voltage increased. The current ratio could be as high as four to one and a corresponding variation of LED intensity would occur. To overcome this problem it was decided to provide a second supply of five volts to operate the LEDs which will also provide the higher current required by TTL for its operation. The other supply is a variable one for testing CMOS and is not capable of supplying more than 30 mA. Thus a TTL gate inadvertently connected to this supply would not be damaged.

The regulator used for the five-volt supply is a three terminal IC which has built in current limiting and thermal shutdown. It will not therefore be damaged by a short circuit due to testing a faulty IC. It is not possible to construct a discrete design, as cheaply, that has the same performance.

Next we need a device that will detect the state of each pin on the device under test and drive an LED to indicate that state. The device has to be driven by TTL and CMOS outputs, that is, by voltages anywhere between 5 and 15 volts. A suitable IC is the CMOS 4009 IC which has six inverters in one package. Each inverter will monitor a pin without drawing appreciable current. The 4009 is also designed to translate logic levels. Thus we may use it to monitor a 5 to 15 volt input level at its input but provide a five volt signal only at its output.

Switches are provided which have debounce logic associated with them. This is necessary so that single bounce free rise and fall transitions can be generated for the testing of more complex logic. The debounce logic must be capable of operating on 5 to 15 volts and of sinking at least two milliamps for TTL tests. The 4009 IC with its high output current capability was again considered to be most suitable for this task.

We would also like to have used the 4009 as the oscillator, but RCA do not recommend using CMOS that has a high output capability in a linear mode as the power dissipation of the device may be exceeded. The oscillator must provide pulses that swing between the positive and negative supply rails (in order to drive CMOS) and must be capable of sinking the two milliamps required by TTL. It must also be capable of operating on supply voltages of 5 to 15 volts. Since the standard CMOS devices cannot provide the current requirement it was decided to use a 555 IC as the oscillator.

CMOS devices should not be operated with inputs left floating as some devices may drift into the linear mode and be destroyed by excessive power dissipation. For this reason a 10 megohm resistor is connected between each pin, on the test socket, and ground. These resistors also conduct away any static charge that may build up.

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SURVEY OF SCIENTIFIC CALCULATORS

IF YOU HAVE CONSIDERED buying a scientific calculator recently then you will have been faced with a bewildering choice. One of the larger retail outlets lists 28 different models in the scientific category with prices ranging from about £20 to over £440. This survey sets out to cover the more common models with prices up to about £120 though the models chosen can only be representative of those available — however this survey will enable comparisons to be made with any new or not so common models that may be found.

Early in 1972 Hewlett-Packard introduced the HP-35, one of the first pocket sized scientific calculators, which then cost £199. The HP-35's calculating power was contained in 5 MOS I.C.'s.

Earlier this year, the same company introduced the HP-21, a calculator with a slightly *greater* calculating power than the HP-35 but selling at only £69. This dramatic price reduction has been, and still is, common to all calculator manufacturers and thus the calculators covered in this survey range in price from £19.95 (recommended retail price).

These price reductions have been due to several factors, one of which is obviously competition, as most serious calculator manufacturers now have a scientific in their model range. Another factor has been the steady advance in semiconductor technology during the very recent past. The HP-21 uses more logic circuitry than the original HP-35 yet contains only two I.C.'s against the five of the HP-35.

One new development which appears as exciting as the introduction of the first scientific is the recent introduction of the programmable scientific calculator. There are now at least three major manufacturers offering programmables covering a price range from £29.95 to £442. (The cheapest, the Sinclair Scientific

Programmable, was the subject of a review in the November issue of ETI). Programmables are extremely valuable for the solution of repetitive calculators involving a large number of calculating steps having to be performed on many items of separate data. This may well occur when handling experimental data or tabulating a graph for instance. Most manufacturers offer a library of standard programs with their machines. The individual program steps are stored in either a solid state memory or on a magnetic card which is fed into the calculator by a small motor.

There are three tables covering the calculators in this survey, Table 1 deals with those machines which calculate and display in standard floating print (arithmetic) format only, Table 2 covers those calculators which calculate and display in floating point and/or scientific notation (separate mantissa and exponent) and one model which uses scientific notation only. Table 3 covers the programmables.

CHOOSING A SCIENTIFIC CALCULATOR

With such a vast range of scientifics available in more or less every price range, it is important that the right factors are considered before money is invested in the calculator of your choice. The first decision is which type of calculator will meet your needs - usually the cheaper mpdels do not offer the option of scientific notation and so are limited in their calculating range to numbers between 10⁸ and 10-8. If you want to handle numbers outside this range then a machine which handles full scientific notation will save the bother of keeping a separate note of the powers of 10 involved, this may well happen when handling calculations in electronics with microfarads, nano seconds, giga hertz etc. being involved.

The programmable calculator is at present being hailed as the answer to everybody's dream of infinite, instant calculating power but in general they are considerably more expensive than their non-programmable equivalents.

Having chosen the type pf calculator required then the available range of that type must ne carefully examined for there are several important differences between models of the same basic type.

The most important decision to be made when choosing a scientific is that of which number entry system the calculator should use. The two systems found are Algebraic and Reverse Polish Notation (RPN), sometimes called Post Fixed Operators. The difference can be seen clearly by following the sequence of keystrokes needed to add two numbers together:

Al	GEBRIAC
Key	Display
2	2
+	3
3	3
=	5
	RPN
Key	Display
2	2
enter	2
3	3
+	5

This sum requires the same number of keystrokes in each system but the algebraic systems operates the keys in the same order as the sum would be read aloud whereas the RPN system has this mysterious "ENTER" key. Algebraic is certainly a simpler system to operate initially but RPN has a lot going for it, especially in the field of complex scientific calculators. If combined with a working stack of data registers, RPN system gives a very great calculator power with instant access to parentheses without keying in and out of them. A four level stack

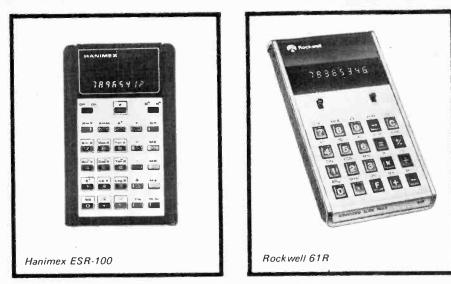


TABLE 1 STANDING FLOATING POINT ONLY MACHINES

MODEL Feature	ADLER 88T	DECIMO [®] 2001	HANIMEX ESR100	NOVUS 4510	ROCKWEL
Recommended Price	£49.95	£49.95	£45.00	£49.95	£49.95
Rechargable	Extra	Mains adaptor extra	Mains adaptor extra	V	V
Digits	8	8	8	8	8
Display	LED	LED	Fluorescent	LED	Fluorescent
No. Keys	25	25	26	32	20
RPN/ALG	ALG.	ALG.	ALG.	RPN	ALG.
Stack				3 Level	
Brackets	1 set				
Ind. Mem.	V	V .	V	V	V
Trig	V	V		V	V.
Radians	V	V		V	V
Degree -	V	V	1	V	V
1/x	V	V	V	V	V
V x	V	V	V	V	V
x ²				V	
M+*2				V	V.
111 1	V	V	V	V	V
ei	V	V	V	V.	
In x	V	V	V	- V	V.
log x	V	V	V	V	V
10 ^x					V
xy	V	V .	V	V.	V
M⇔x	V	V	V		V
Х↔у	V	V	V	V	
Change Sign		V	V	V	V

is equivalent to three levels of parenthesis (brackets). However the operator of a RPN machine needs to be able to transform a complex equation such as:-

 $x=\tan^{-1}((3.53 + 8 \sin 370)+6(\cos 530 + 2(1+ \ln 3.55))^{\frac{1}{3}})$

into a number of 'enter' keystrokes whilst the algebraic machine operator can, if he has sufficient 'brackets' available, enter the problem as written.

A good argument can be made out for each system and the choice must be made on your ability to *understand* what you are doing. But do not dismiss RPN as too difficult to understand, have it demonstrated and then try some problems yourself - you might be surprised at the ease with which you can pick up a new system.

One of the arguments advanced in favour of the RPN system is that it is consistent when dealing with the more complex keyboard functions such as the trig functions, logs etc. If you wish to find the 'sin' of 30° then the key sequence for each type of machine is the same- '30' 'sin' which indeed is the sequence expected for a RPN machine, but the reverse of that expected for the algebraic machine – you might find this an inconsistency.

Manufacturers of both types of machines usually offer a separate memory as well as an multi-level stack in the RPN machine or one or more sets of 'brackets' in the algebraic models. Often the choice may be one level parenthesis plus two separate memories or two levels of parenthesis plus one memory.

Having made this decision, which is the most important one, then the next factor to be considered is the type of display and number of digits to be displayed. The most popular type of display is the LED type which is usually red in colour. The cheaper models of calculators use small LED displays with magnifying lenses fixed in front. These lenses tend to narrow the viewing angle (the angle over which the display may easily be read). This is fine in a small pocket calculator but not as satisfactory in an expensive scientific model. The other, not so common type of display is the flourescent type - this is a green display which generally is larger than the LED type and the whole display is built into an evacuated glass envelope. The basic display is more expensive than a LED type and does require more complex additional circuitry (high voltage generators etc) but has the advantage of extremely low power consumption.

Having mentioned power consumption it is a useful next point for consideration; if your choice of calculator is available with or without rechargeable batteries then the extra outlay on the rechargeable version may well be worth considering, the PP3 type battery may be fairly cheap but with the current required by some of the more complex calculators (especially those with LED displays) it will not take all that long to run down a PP3 and so the extra cost of the rechargeable batteries is soon made up.

The next factor for consideration is the keyboard and its layout. The calculators surveyed here range in number of keys from 48 down to a full scientific with only 18 keys. The lesser number of keys, the smaller the calculator and thus the more easily pocketable but with the disadvantage that there are often two keystrokes required for access to a scientific function. Thus if you are going to use the full range of the calculator fairly often then you might well find double function keys awkward to use. The only double function keys which are obvious are those of

main text continued on page 31

SURVEY OF SCIENTIFIC **CALCULATORS**

- 1. 2.
- 4. 6.
- **NOTES FOR TABLE 2** All quoted prices are the manufacturers recommended prices, discounts of up to about 40% are not uncommon on some models. Operates in scientific notation only for the input of data and the displaying of results. Special type of algebraic calculator with extra register which allows the direct evaluation of the sums of products without using the memory although the 'brackets' are not separately accessible. Operates in grads as well as degrees and radians for trig. functions. Additional function: Sexagesimal to decimal conversion, i.e. hours, minutes, seconds to decimal hours etc. EE4 and EE4 keys provided. n Twelve additional features: (k), (n)k, Pr(x), $\Gamma(x)$, $\Sigma \pm ||x||$, K4, K4, $\Sigma4$, $\Sigma4$, $\Sigma4$, C1grp, Item count. Additional features; full arithmetic on all three separately addressable memories, selectable decimal point, live % key with add on and discounting, % difference between x1 and x2, linear regression, random number generation, permutations, twenty separate conversions and their inverse. 8.



TABLE 2 FLOATING POINT AND SCIENTIFIC NOTATION.

Model Feature	Casio FX 15	C.B.M. SR890	C.B.M. SR990	C.B.M. SR6120R	C.B.M. SR4148R		Dixons Mini	Dixons SC4001M	Hanimex ESR 1010SN
Rec. Price (1)	£49.50	£22.95	£29.95	£39.95	£49.95	£59.95	£19.95	£24.95	£49.50
Rechargeable	A.C. adaptor extra			V	V			A.C. adaptor extra	A.C. adaptor extra
Digits	8 6+2	8 5+2	8 5 + 2	8 8+2	10 10+2	8 8 + 2	8 5+2	8 5+2	8 5+2
Display	Fluorescent	L.E.D.	L.E.D.	L.E.D.	L.E.D.	L.E.D.	L.E.D.	Fluorescent	Fluorescent
No. Keys	37	29	39	39	48	35	19	35	35
RPN/Alg.	Alg.	Alg.	Alg.	Alg.	Alg.	Alg.	Alg.	Alg.	Alg.
Stack			-						
Brackets			1 set	1 set	1 set				
Ind. Mem.	V	2	2	2	2	V	V	V	V
Trig.	V	V	V	V	V	V	V	· ·	V
Radians		V	V	V	V	√ (4)	V	V	V
Degree	V	. /	V	V	V	√ (4)	V	V	V
Deg./Rad. Conversion									
√x	V		V	V	V	V	V	V	
×√Y			V	V	V			- · ·	
\mathbf{x}^2	V		V	V	V				
x ^y	V	V	V	V	V	V			
Rect⇔Polar Co-ord Conv.			V	V	V				
Hyperbolic functions						V			
ln x	V	- V	V	· · · · ·	V	V	V	V	V
e ^x	V		V	V	V	V	V	V	V
log x	V .	V	V	V	V	V ·			
10 [×]			V	V	V				
1/x	V		- V	V	V	V	V	, v∕	V
X!									
Mean & stand deviation			V	. V.	V				
Sum to mem.	V		V.,	V	V.				
M ≓x						V	V *	V	V
+/ key	- 1	V	1	· V	V	V			
x ⋛ y			V	V	V	r i			
π	V		V	V	V	V	V	- V	V
Other Functions	(5)		(6)	(6)	(6)				



Hanimex ESR Master	Novus 4520	Hewlett Packard HP 21	Qualitron 1448	Qualitron 1419	Qualitron 1420	Rockwell 63R	Sinclair • Scientific	Sinclair Oxford 300	Texas SR 50	Texas SR 51
£49.95	£72.45	£69.10	£27.33	£31.25	£61.55	£59.95	£21.55	£32.35	£59.95	£89.95
A.C. adaptor extra	V	V	Extra	Extra	Extra	V		A.C. adaptor extra	· V	V
8 8+2	8 8 + 2	10 8+2	8 5 + 2	10 10 + 2	10 10+2	8 8 + 2	5+2 only (2)	8 5+2	10 10+2	10 10+2
Fluorescent	L.E.D.	L.E.D.	Fluorescent	L.E.D.	L.E.D.	Fluorescent	L.E.D.	L.E.D.	L.E.D.	L.E.D.
25	35	27	35	35	40	25	18	19	45	45
Alg.	R.P.N.	R.P.N.	Alg.	Alg.	Alg.	Alg.	R.P.N.	Alg.	Alg.	Alg.
	4 level	4 level								-
2 sets					2 sets	2 sets			1 (3)	1 (3)
V	V	V	V		3	V		V	V	3
V	V	V	V	V	V	V	V	/	V	V
V		V .	V	V	V .	V	V	/	V	V
V		V	V	V	V	V		V	V	V
V						V			V	
V	· V	V	V	V	V	V		V	V	V
									V	V
					V				V	V
V	V	V		V	V	V			· / ·	V
		V		√						V
									V	
	V	V	V	V	V	V		V	V	V
	V	V	V	V	V	V		V	V	V
V V	V	V		V	V	V	- V			V
V	V	V			V	V	V			V.
V	V	V	V	V	V	V		V	V	V
V		1			V	V			- V	V
				1	V					V
V		V	V	V	V	V		V	V	
V			V	V	V	V		V V		
V	V	V	V	V	V	V			V	V
V	V	V		V		V			V	V
F.	V	V	V	V	(7)	V		V		(8)

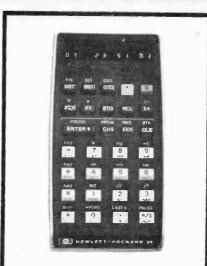
SURVEY OF' SCIENTIFIC **CALCULATORS**

NOTES FOR TABLE 3

- Also special engineering notation capability expressing exponent of number in multiples of three, 1.
- multiples of three.
 Also calculates in 'grads' for trig. functions.
 Additional features: Σ+, Σ-, rectangular to polar coordinate conversion and vice-versa, mean, standard deviation, Σx², Σy. Σxy, integer value x, fractional portion x, absolute value x, π, 1/x, %, change sign, x → y, decimal angle/time → angle in degress/hours, minutes, seconds.
 Not 10^X.
- Additional features: M + x, M − x, M + X², π, change sign, x→y.
 Not x²
- 7.

Additional features: $x \leftrightarrow y$, π , change sign. Sin x, cos x, arc tan x only: key sequences are given for arc sin x, arc cos x and tan x. (sequences of between 9 and 12 keystrokes). 8.

TABLE 3 PROGRAMMABLE SCIENTIFIC CALCULATORS



Hewlett-Packard HP25

MODEL	DIXONS PROGRAM	HEWLETT PACKARD HP.25	NOVUS 4515	NOVUS 4525	QUALITRON 1421	SINCLAIR PROGRAMM- ABLE
Price	£49.95	£119	£99.95	£119.95	£45.97	£29.95
Rechargable		V	V	V		Mains adaptor supplied. Not re- chargable batteries
Digits	8	10/8+21	8	8/8+2	8	/5×2
Display	L.E.D.	L.E.D.	L.E.D.	L.E.D.	L.E.D.	Fluorescent
No. Keys	36	34	36	39	36	19
R.P.N./ALG	R.P.N.	R.P.N.	R.P.N.	R.P.N.	R.P.N.	R.P.N.
Stack / Bracket	3 level stack	4 level stack	3 level stack	4 level stack	3 level stack	2 level stack
Ind. Mem.	· 1/	8	V/	V	· · · · ·	V
Prog. Mem. (Steps)	102	49	> 100	100	102	25
Trig	V	V	V	V	V	1 8
Rads / Degree	V	V ²	1	1/	V	Radians only
Rads/Degree Conversion	V				V	
Direct Branching		V				
Conditional Branching		V				
Ţests		$x \ge 0, x < 0$ $x \ddagger 0, x = 0$ $x \ge y, x < y$ $x \ddagger y, x = y$				
Single Step	V	V	V	V	V	
Back Step		V				
Pause	V	V	V	V		
E ^x , 1n x	V	V	V		1	
10 ^x , log x	\checkmark^{4}	V	V 4	v ⁴	V ⁴	V
x^2 , \sqrt{x}	V	V	V	V ⁶	V	V
×	V I	V	V	V	V	P
Others.	√ 5	V ³	V ⁵	V 7	√ 5	

text conintued from page 27

trig and inverse trig function keys. Pressing 'arc' 'sin' seems a good use of the double function key and saves two keys on the keyboard.

Whilst discussing keyboards, it is worth considering the *feel* of each key and whether there is any 'breakaway' action to a keystroke or whether the key has a 'spongy' feel to it. There is particular advantage to either system and it is really a matter of personal choice, though it does seem as though the more expensive the machine, the more positive the action of the keys. Again the recommendation is to try several different models and see which you like.

In the tables it has been impossible to list every function for each model so the tables have only the main functions and the models which have, extra, less common functions have a separate note at the end of the table.

A recent feature introduced on the latest range of CBM scientifics is worthy of note, especially to electronic enthusiasts, is the provision of two keys marked 'EE+' and 'EE+'. These keys are used when the machine is being operated in the scientific notation to increase or decrease the value of the exponent whilst shifting the position of the decimal point in the mantissa. This feature appears to be very useful in electronic calculations when we tend to come across component values whose unit values are separated by a factor of 10^3 . Thus if a time constant calculation is being carried out and the answer comes out as a required capacitor of '3.3 x 10^{-10} ' Farads then by pressing the 'EE i' key twice the display will show '330 x 10-12' or more simply 330pF.

As may be seen from the tables, several manufacturers are now offering statistical functions on their more expensive models. These are usually the mean and standard deviation of a set of numbers and these are very useful when handling a set of experimental results and trying to analyse some trend or conclusion from them. The one problem with statistical functions in a scientific calculator is that some of them use the calculator memories during the computation of the statistic so those memories are not available for other use during the finding of statistical functions.

Well, good luck in your research for a calculator that will meet your needs. Probably the final piece of advice that we can give is to have a good look at all the models available in your local dealers — try them all out on the same sequence of problems and see which seems to be most convenient to you — and also have a good look at the handbook supplied with the machine; it might make all the diffrence one day when you are stuck if your machine has a comprehensive handbook.

We would like to thank all those manufacturers who have assisted in the preparation of this article by supplying data and handbooks etc. A list of addresses of the major manufacturers appears at the end of this article.

MANUFACTURERS AND AGENTS CASIO Automatic Business Machines Ltd, A.B.M. House, 11 Wyfold Road, London SW6. C.B.M. C.B.M. Business Machines Ltd, 446 Bath Road, Slough, Bucks, SL1 6BB. DECIMO Decimo Limited, Park House, 96-98 Park Street, Luton, Beds. LU1 3EX. DIXONS **Dixons Photographic Limited,** Devonshire House. Pinner Road. Harrow. HANIMEX Hanimex Calculator Division, Hanimex House, Dorcan, Swindon, Wilts. SN3 5HW. HEWLETT-PACKARD

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QUALITRON Imtech Products Limited, Imp House, Ashford Road, Ashford, Middlesex.

NOVUS National Semiconductor (UK) Ltd, The Precinct, Broxbourne, Hertfordshire EN10 7HY.

ROCKWELL Rockwell International, Anita House, Rockingham Road, Uxbridge, Middlesex, UB8 2XL.

SINCLAIR Sinclair Radionics Limited, London Road, St. Ives, Huntingdon, Cambs. PE17 4HJ.

TEXAS

Texas Instruments Limited, European Calculator Division, 165 Bath Road, Slough, SL1 4AD, Buckinghamshire.

GLOSSARY OF SCIENTIFIC CALCULATORS TERMS

Some of the terms associated with scientific calculators originated in the computer world and are therefore may be new to many engineers. Some of the terms commonly found in calculatorhandbooks and advertisements are given in this glossary together with brief explanations.

REGISTERS: The names of the memories in which data is stored whilst it is being operated on or used as a longer term store known as a memory. The X register is the register that is used to hold the data that is shown in the display. Thus the X register holds the last keyboard entry during a calculation or the answer when the calculation is terminated.

The Y register is used to store the second number during those operations requiring two variables $(+, -, \div, \gamma^X)$.

STACK: A series of extra data registers, found especially in calculators using Reverse Polish Notation. The stack is used as a 'first-in, last-out' type of memory. Data is shifted into the stack by pressing the enter key, or by entering a new number after pressing an operational key, and is shifted down by pressing an operational key. The lowest registers of the stack are the X and Y registers described above and any subsequent registers become temporary storage registers on a non-randomly accessible basis to allow storage of intermediate results prior to their re-use with a later completing operation. Thus, access to parenthesis (brackets) is automatic upon pressing of the 'enter' key. A four level stack (X,Y,Z,t) has the capability of three levels of parenthesis (three sets of brackets).

MEMORY: One or more sets of data registers all of which are randomly accessible. It is often possible to act upon the data contained in the memory with the data in the X register by using keys such as M+, M-,Mx, $M\div$, and sometimes data in the memory and data in the X register can be interchanged by using the $M \Longrightarrow X$ key. $X \Longrightarrow Y$ is the key which directly interchanges the contents of registers X and Y. This key is mainly used when using the function XY, although it can also be used when interchanging the terms in a division or a subtraction.

STANDARD DEVIATION: A statistical measure used most often when analysing experimental data. The standard deviation of aset of data is the measure of the dispersion of data values about the mean.

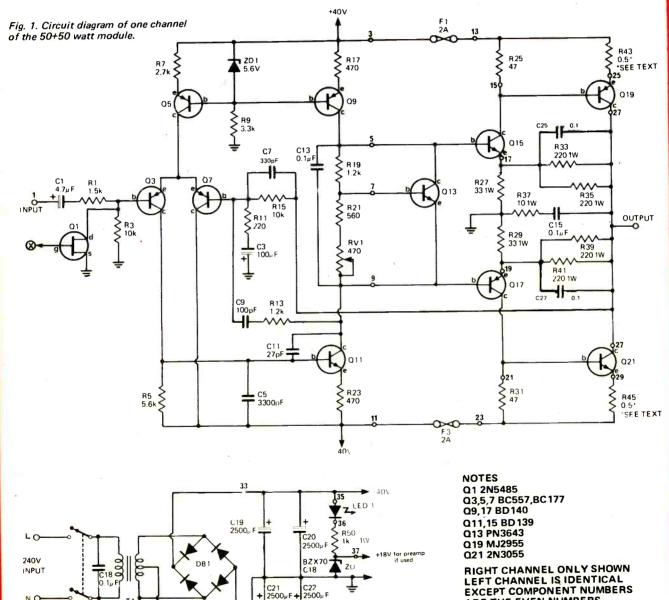
LINEAR REGRESSION: This is a statistical function used again when handling experimental data. It is especially used when using an experiment to find a mathematical relationship between two variables. Linear regression is the name of the procedure which is used to find the line which best fits the set of data points which have been found experimentally. The procedure usually finds the equation of the straight line and also a parameter called the correlation coefficient which indicates how well the data fits the line.

RADIANS: A measure of angle like the degree;1 radian = 57.3° approx, and 2π radians = 360°. Many problems use, or give results directly in radians. Thus a calculator capable of handling degrees and radians is extremely useful as is an easy conversion between the two.



50+50 WATT 422 POWER MODULE

Details of the basic amplifier used in the Crossover amplifier. The module can be used to boost the output of a low power stereo to 50W per channel!



40V

EXCEPT COMPONENT NUMBERS ARE THE EVEN NUMBERS ie, R 16 IS THE SAME AS R15

COMPONENTS D1,2 Q1,2 R47,48,49, C23,24 ARE ARE FOR THE DETHUMP CIRCUITRY AND MAY NOT BE REQUIRED

LED 1, R50 AND ZD3 ARE USED ONLY IF THE 422 PREAMPLIFIER IS USED

ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976

D2 1N91

R48 47k

R49 100k

BOTH CHANNELS COMMON

220

D1 1N914

3

EO

THE CROSSOVER AMPLIFIER described in this issue uses the power amplifier module out of the International 422 amplifier — described in the August 1974 issue of Electronics Today. For new readers who do not have this issue we are publishing details here.

Most of the electronics is mounted on either the printed circuit-board or on the heatsinks. The board may be

HOW IT WORKS -

The input signal is fed via C1 and R1 to the base of Q3 which, with Q7, forms a differential pair. Transistor Q5 is a constant current source where the current is [5.6 V (ZD1) - 0.6 (Q5)]/2700 (R7) - that is about 2 mA. This current is shared by Q3 and Q7. Transistor Q9 is also a constant current source supplying about 10 mA which, if no input signal exists, flows through Q13 and Q11. The differential pair controls Q11 and thus the voltage at its collector.

The resistors R19 and R21, together with potentiometer RV1, control the voltage across Q13 and maintain it at about 1.9 volts. But as Q13 is mounted on the heatsink, this voltage will vary with heatsink temperature. Assuming that the voltage at points 5 and 9 is equally assembled in accordance with the component overlay diagram given. Note that capacitors C25, 26, 27 and 28 do not have holes provided for them and they are therefore mounted directly across resistors R33, 34, 41 and 42 respectively.

The heatsink should be assembled as shown in the photograph and the drawing. The transistors Q13 and 14 should each be epoxied into a hole in

spaced about zero volts (ie ± 0.95

volts), the current will be set at about

12 mA through Q15 and Q17. The

voltage drop across the 47 ohm resistors (R25 and R31) will be

enough to bias the output transistors, Q19 and Q20, on slightly to give

about 10 mA quiescent current. This quiescent current is, adjustable by means of potentiometer RVI.

Local ofeedback is applied to the

output stage by the network R33, R35, R39 and R41, giving the output

stage a voltage gain of about four. The overall feedback resistor, R15,

Protection to the amplifier, against

shorted output leads, is provided by

fuses in the positive and negative supply rails to both amplifiers.

Temperature stability is obtained by mounting Q13 on the heatsink.

Q13 will thus automatically adjust

the bias voltage. Frequency stability

gives the required gain control.

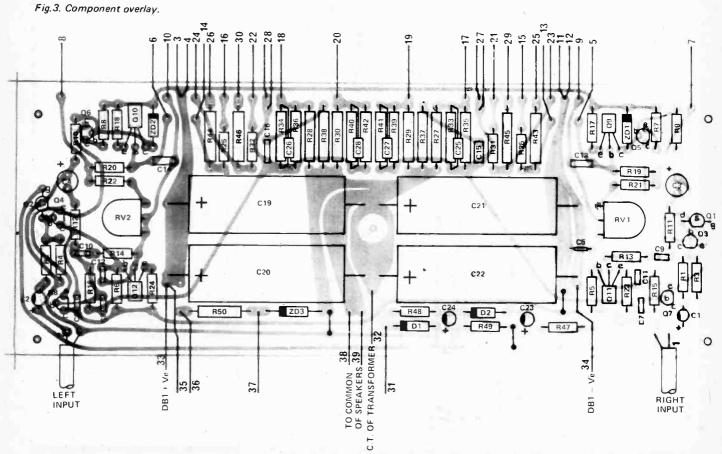
one of the heatsinks to ensure good thermal contact. Also secure all leads to the heatsink with epoxy. The interconnections between the printed circuit board and the heatsink should be carried out in accordance with the wire numbering on the diagrams. Final wiring details are given in the respective separate projects.

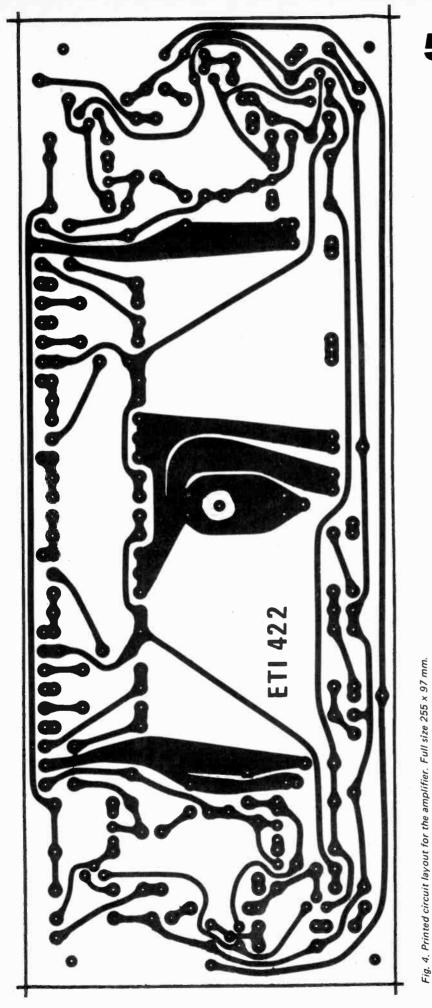
text continues on page 36.

is ensured by C9/R13, C5, C7, C11 C25 and C27.

Although the power amplifier itself does not produce a thump in the loudspeakers on switch on, the preamplifier used may. To reduce any thump to an acceptable level, QI is used to short the input for about two seconds on switch-on and immediately after switch-off.

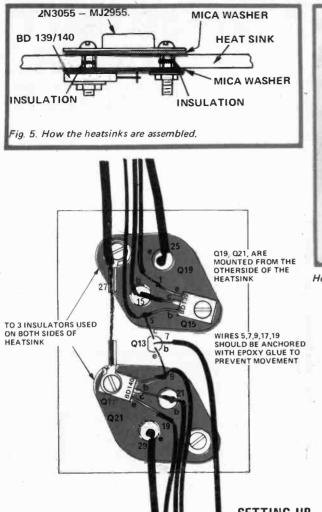
The power supply is a conventional full-wave bridge with centre tap, providing + 40 volts and -40 volts. Diode D1 is used to rectify a second negative supply which is used to control the FETs. Due to the resistance in series with the diode, the charge of C24 is slow. In addition, during the charge period, C23 is also being charged increasing of the delay. On switch off, however, C23 cannot assist the voltage on C24 and the off-timing is much shorter than the on-timing.

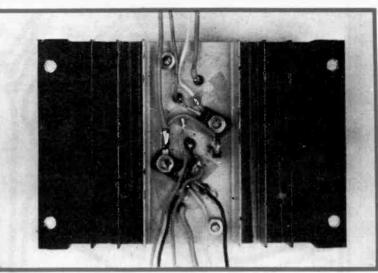




50+50 WATT POWER MODULE

PA	RTS LIST	
R43,44,45,46 R R37,38 R27,28,29,30 R25,26,31,32 R11,12	esistor 0.5 0	hm 2W* 1W 5%
R25,26,31,32 R11,12	220	
R33,34,35,36 R39,40,41,42 R17,18,23,24 R21,22 R13,14,19,20	" 560 '	; 1,₩ ;; , 1/2,₩ ;; , , , , , , , , , , , , , , , , , ,
R,13,14,19,20	" 1 k2 " 1 k5	
R1,2 R7,8 R9,10 R5.6	" 1 k5 " 2 k7 " 3 k3 " 5 k6 " 10 k	·· ·· ··
R ³ ,4,15,16 *If difficult to a may be fabricat of electric fire e is sufficient for around a 1 wath higher) and solo	bbtain, these ed from a sh lement—abc each. Wind s resistor (10 er into place	e resistors fort length but 90mm securely 10 ohms or 2.
RV1,2 Potentic	meter 4/0 c	onm Trim
C11.12 Capacit C9,10 " C7,8 " C5,6 " C13,14,15,16"	or 27 pF 100 pF 330 pF 0.0033 μ 0.1 μF	ceramic ceramic ceramic JF polyester "
C25,26,27,28 " C18 " C1,2 " C3,4 C19,20,21,22 '	0.1 μF 0.1 μF 2 4.7 μF 1 100 μF 1 2500μF	" 50 Vac 0V Electro- 10V lytic 50V "
Q1,2 T Q3,4,5,6,7,8 Q9,10,17,18 Q11,12,15,16 Q13,14 Q19,20 Q21,22 *with mountin ZD1 2 Zener d	ransistor 2N BC: BD BD BD BD BD BD BD BD BD BD BD BD BD	5485 177,2N3645 140 139 177 2955* 3055*
201,2 20101 0		(DIV) IEA
DB1 Diode brid	ige min 100 122	V PIV, 1.5A
DB1 Diode brid PC Board ETI- F1-F4 Chassis and 2 Amp. F T1 Transforme Heatsinks 20ff	nounting Fu uses. r 56V CT 1. 75mm of 35	ise holders 5A iD or 40D
the following c	omponents.	required add
R47 Resistor2		5.0/:
R47 Resistor R48 '' R49 ''	220 k ½W 47 k " 100 k "	5% "
D1,2 Diode Q1,2 Transist C23 Capacito C24 "	1N914 or 2N548 or 10μF 25 1μF 25\	v electrolytic
NB Heatsinks' sinks are availa Road, Maltby, Ref. 40D N007 (includes P&P a pieces (2 pieces	The Mullard ble from Sya Yorks S66 8 50A100 and Ind VAT) fo , £1.64; 1 p	40D Heat- ice of Blyth HZ. Quote i send £3.24 ir four 75mm iece 85p).





How the completed heatsink assembly appears.

SETTING UP

The only adjustment required is that of the bias current. This is normally done with an ammeter in the power-supply lead to the output stage and, with the speaker disconnected and no signal, RV1 is adjusted to obtain a current of about 20 milliamps. However if a major fault exists, or occurs, with the above method the meter as well as the output transistors may be damaged.

To obviate this we recommend a different approach as follows. Take out the fuses and temporarily connect a 220 ohm half-watt resistor across the fuse holder. Adjust RV1 to obtain about four volts across these resistors. If a major fault exists these resistors will get hot and possibly burn out. However no other damage will occur to the amplifier as the resistors limit the maximum current that can flow. After bias adjustment these resistors are removed and the fuses replaced.

It may be found that the voltage across the resistor in the positive lead is slightly different from that across the resistor in the negative lead. This is due to a slight offset in the output voltage but as long as the average is about four volts it will be satisfactory.

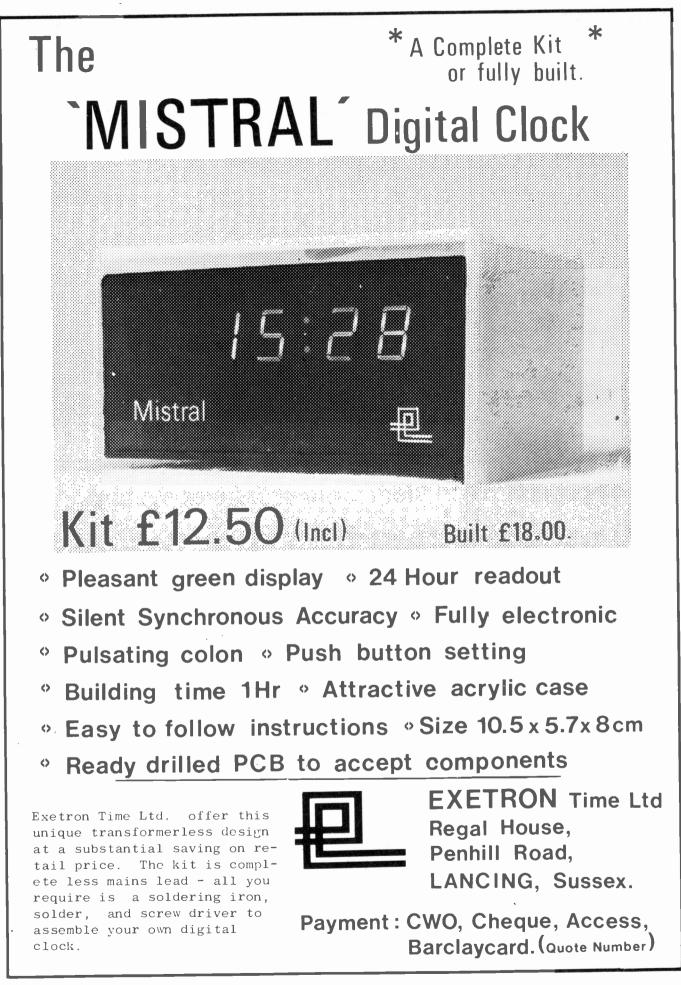
You only have to ask! In response to those readers who wrote in to ask, we have arranged for binders to be designed for ETI. Each binder can hold up to 13 issues in such a way as to not only protect them and keep them in order but to make them look attractive on your bookshelf. Finished in black leather-look plastic with gold lettering, the binders make ideal Christmas presents (along with a subscription, perhaps?) at £2 each, including VAT and carriage.

Send your order to ETJ Binders, 36 Ebury Street, London SW1W 0LW.

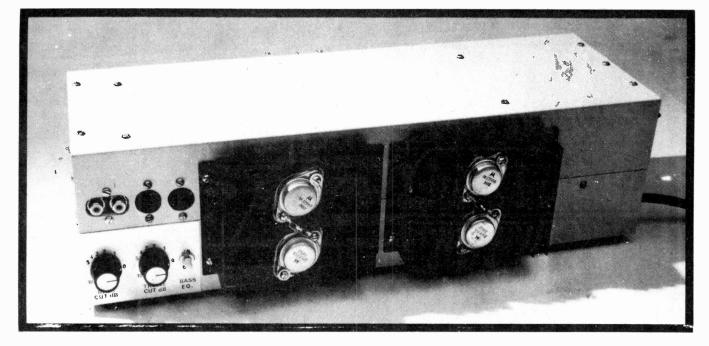
ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976



BINDE



CROSSOVER AMPLIFIER



One approach to an electronic crossover system.

LAST month we published the details of active-crossover boards for use with high-fidelity speaker systems. This month we give details of a typical complete system. The system described is not intended to be considered as the only possible way, merely as an example of the way in which a system may be built.

The system described uses two 422 power-amplifier modules, but any other amplifier could equally well be used eg, the 440 amplifier. A two-way system is described, that is, one amplifier is used for the high frequencies and one amplifier is used for the low frequencies for each speaker system. Thus only two two-way crossover boards are used together with the two amplifier boards and the power supply to make the complete system.

CONSTRUCTION

We build our prototype into two pieces of channel aluminium as may be seen in the photographs. The aluminium channel used had dimensions of five inches by two inches and we used a piece 380 mm long for each side. Readers may have difficulty in getting hold of this but an equivalent can readily be made from 1.6mm aluminium bent up as required. We suggest that you make your chassis about 430 mm long as we found ours to be a little cramped.

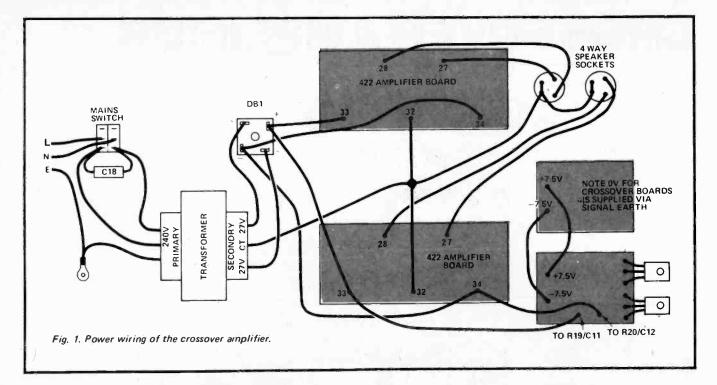
The heatsinks used were the Mullard $35D \times 75$ mm and these were assembled as detailed in the 422 amplifier section. The printed circuit boards were also assembled as detailed in that section. Printed-circuit pins should be used for all connections to the board as this makes interconnection of the unit much easier.

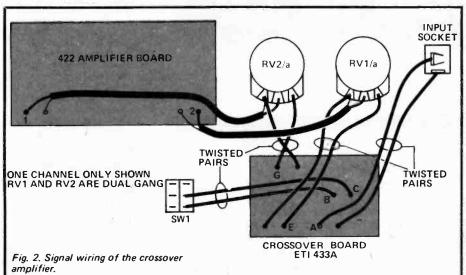
The location of the individual modules and components can be seen from the internal photograph of the unit. If construction similar to ours is used, with the transformer close to one end of the printed circuit boards, some trouble with hum may be encountered in the main amplifiers closest to the transformer. We overcame this problem by using these amplifiers for the high channels and by reducing their bass response by changing C4 from 100 microfarad to 2.2 microfarad. With this modification the response of the amplifier will drop off below 300 Hertz, thus reducing hum, but will still be adequate for high channel use. If the high channel

response is required to be lower than 300 Hertz then the transformer must be mounted further away from the amplifier modules.

Some care must be taken to prevent earth loops causing problems. The wiring of the power cables is as shown in Fig. 1, the most important being the zero volt line. The zero volt lines of both boards are linked by a heavy cable and the common side of the transformer is joined to the centre of this link. The common for the speakers is also joined to this same point. Make sure that this junction is insulated so that a short does not occur when the unit is closed up. The plus and minus 40 volts are taken to the crossover board which has the regulator on it, and the plus and minus 7.5 volts is linked between the two boards. The zero volt line for the crossover boards is taken via the signal output leads to the appropriate amplifier board.

Due to the power dissipation in the regulator for the crossover, a heatsink must be used. We simply bolted the crossover boards onto the end panel by means of a piece of angle aluminium, and bolted the transistors onto the end panel using insulating washers. We used a piece of cardboard





between the two boards to prevent any shorts occuring between the two boards. We also installed cardboard under each of the power amplifier boards similarly to protect them.

Coaxial cable was used to connect the inputs from the potentiometers to the main amplifiers but only twisted pairs from the crossover boards to the potentiometers. Coaxial cable could have been used here but was found to be not necessary as these leads are a long way from the power transformer. Finally, a word about the power transformer. We have only used a single power transformer, as used in the 422 amplifier, to power the two complete 422 amplifier boards. But remember that the frequency spectrum is split up between the high and low channels and hence each amplifier, although called upon to provide the same peak power, only has to handle half the average power. The transformer is thus quite capable of handling the total load as the system is still nominally 50 watts per channel.

PARTS LIST – CROSSOVER AMPLIFIER

Two complete sets of components as detailed for the 422 power amplifier module except that only one transformer and rectifier bridge is required for domestic use – especially if the crossover is above 2 kHz.

Two ETI 433A boards with the following exceptions. The transformers, DI to D4, C11 and C12

are not required for either, and C13, C14, R19, R20, Q1, Q2, ZD1 and ZD2 are not required on the second board. The value of R19 and R20 on the first board should be 8.2 k.

Switch SW1, if required, should be a double pole switch and RV1 and RV2 should be dual gang linear potentiometers.

Chassis.

Input and output sockets as required.

4 3 2 4 3 2 1 1 5 IN 5 OUT 10 10 20 20 BASS TREBLE BASS CUT dB EQ. CUT dB

Fig. 3. Artwork for the level control panel. Full size 73 x 50 mm.

ETI TIMING MODULES:

COMPARATOR MODULE

of any Q time, to exclude 'difference' outputs which are due to switching times, etc.

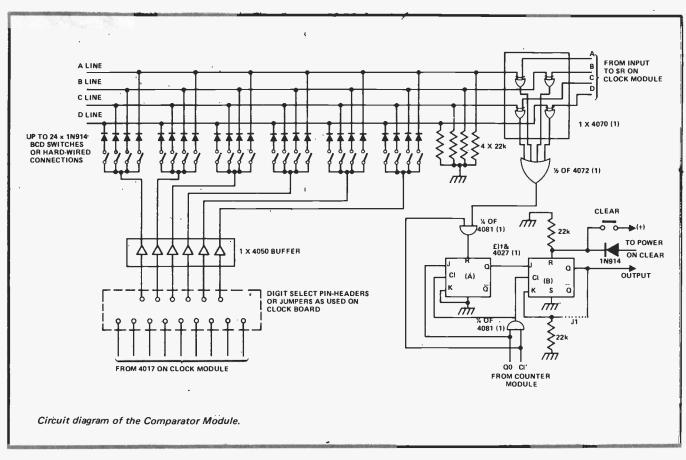
The Q output of a J-K Flip-Flop (4027 (A)) is set to '1' half-way through QO time by QO and C1' During one word (QO time to Q9. time) it will be reset if there are any differences between the numbers set by the BCD switches and the contents of the clock module (SR). If, however, a whole word passes with no differences between the two sets of data, Q will still be set halfway through the following QO time, and Q of Flip-Flop (B), the comparator alarm output, will be clocked high. This alarm output is cleared when power is first applied, and may be cleared or disabled by closing the 'clear' switch.

If the jumper connection J1 is made (indicated by a dotted line), when the alarm output goes high it will stay high until the middle of the next Q0 time, i.e. it will be high for one word time. If this output is connected to the clock module reset connection it will reset the clock module to zero, and leave it counting. This can be used to provide a programmed pulse generator with a range of milliseconds to hundreds of hours. Any number of digits between 1 and 9 can be used to 'set' the comparison time. Unspecified digits will be treated by the comparator as being zero's. As mentioned earlier, any number of comparator modules may be connected to one clock module.

Any enquiries about the three timing modules can be answered by Sintel, of 53a Aston Street, Oxford. Sintel can supply pcbs and components for these projects.

A final note about the clock module described last month. The 4020 first-stage divider should be selected for operation at 5.12MHz. All the Fairchild devices are suitable. $\begin{array}{l} \textbf{PARTS LISTS} \\ \textbf{CMOS-} \\ 4001 \\ 4027 \\ 4050 \\ 4070 \\ 4072 \\ \textbf{RESISTORS-} \\ Six 22k 1/4W \\ \textbf{CAPACITORS-} \\ Five 10nF ceramic \\ one 4.7 \mu F, tantalum \\ \textbf{DIODES-} \\ up to 25 x 1N914 \\ \end{array}$

ALSO: pcb ETI553, three 14 pin DIL sockets, two 16 pin DIL sockets, DIL 16 pin header, optional 22 pin LSI socket and plug, 20-way cable (6" or so), optional I/O plug, socket and cable for BCD switches.

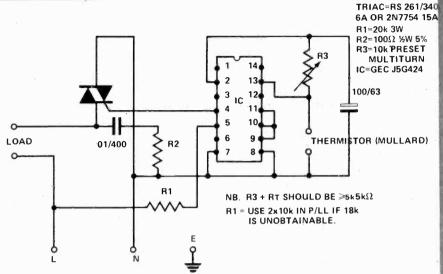




LOW DIFFERENTIAL THERMOSTAT

This circuit evolved as a result of the need for a more satisfactory method of controlling the temperature in our paint heaters which operate at 170°F. The differential of conventional mechanical thermostats was too wide, both in actual rating and in % accuracy, so that severe overheating occurred when the demand for paint momentarily lapsed. The result was poor finish and in a number of cases the destruction of the thermometer (at approx £10 a time).

The introduction of the new thermostat completely eliminated the problem. The circuit consists of a GEC J5G424 Zero Voltage Switch in IC form together with a Mullard 2322 640 90004 which is plastic encapsulated, giving it both mechanical and electrical protection. It is available

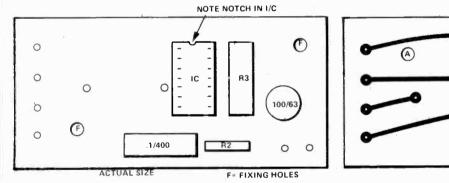


in four sizes with a temperature coverage from -30 to $+200^{\circ}$ C.

The RC network, 0.1mF + 100ohms, prevents self latching of the Triac.

The J5G424 is, by nature of its design free of RFI. The type of Triac employed will depend on the loading. We were using 6 and 15 amp loads.

A



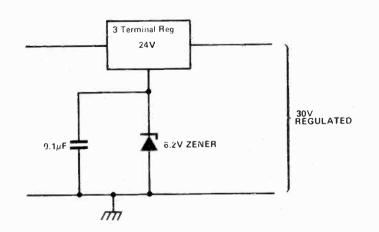
This pcb is available from Ramar or Crofton (see Ad. index) for 76p including VAT & P and P.

ACTUAL SIZE

30V REGULATORS

Three-terminal voltage regulators are available in 5,9,12,15,18 and 24V types. If you require a 30V supply use a 24V regulator with a 6.2V zener diode in the earth lead as shown. This increases the volatge to 30V. A 0.1μ F capacitor should be connected across the zener diode as shown.

The zener should be of suitable wattage rating. In a similar manner for 27V use a 3.3V zener or for 33V a 9.1V zener.



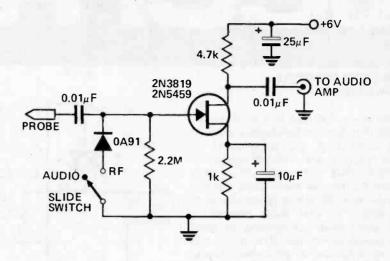
TECH-TIPS SPECIAL - JANUARY 1976

tech-tips

AUDIO-RF SIGNAL TRACER PRE-AMP

This economical signal tracer is very useful for servicing and alignment work in receivers and low power transmitters. It is easily constructed on a small piece of matrix board which can be mounted inside a commercially-available probe case or homemade probe. The slide switch can be mounted on the probe housing. A miniature toggle switch could be used as a substitute.

When switched to RF, the modulation on any signal is detected by the diode and amplified by the FET. A twin-core shielded lead can be used to connect it to an amplifier and to feed 6 volts to it.

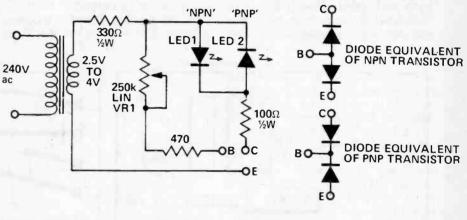


GO/NO-GO DIODE/TRANSISTOR CHECKER

A diode can be checked by connecting it between C and E. If LED 1 lights the diode is OK and its anode is connected to C. If LED 2 lights its cathode is connected to C. If both light it is a short circuit suitable only as a link!

To check transistors with known pin connections, set VR1 at maximum resistance and connect the transistor. Advance VR1 until one LED lights. If LED 1 lights it is NPN, PNP if LED 2 lights. If both light you have a three-legged link. If neither light you have a three-legged fuse!

To check transistor connections, if unknown, short two of its leads together and check as for a diode



making note of which lead/leads respond as anodes. Short two other

leads together and do it again. Refer to diagrams above.

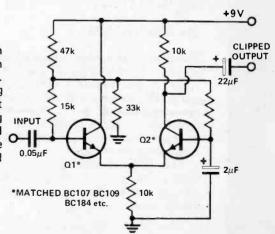
TINNING WITH SOLDER WICK

Do not discard the lengths of solder saturated solder wick. Further use can be made of them to plate printed-circuit boards by pre-tinning the joints, prior to inserting components and soldering.

The simple operation is as follows – place the saturated solder wick on the printed board and apply a heated soldering iron to melt the solder in the wick. At the same time, move the wick and iron along sections or joints requiring tinner. A neat plated copper print will result.

PRECISE AUDIO CLIPPER

A differential amplifier makes an excellent audio clipper and can provide precise, symmetrical clipping. The circuit shown commences clipping at an input of 100 mV. The output commences clipping at ± 3 V. Matching Q7 and Q2 is necessary for good symmetrical clipping, however, if some asymmetry can be tolerated this need not be done.



HIGH-POWER ZENERED VOLTAGE FROM LOW POWER SOURCES

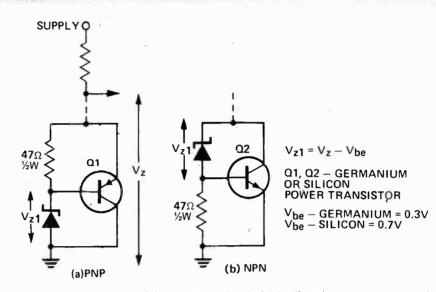
A power transistor can be used to provide a high power zenered voltage from a low wattage zener. A 400 mW zener can be used where a 10 watt zener is required or a 1 W zener can be used where a 50 to 80 watt zener is required, by using appropriate transistors for Q1 and Q2 in the circuits shown.

Where low rating is required Q1 would be a ASZ 15 (germanium) or an AY9140 (silicon). Q2 could be a 2N3054 (silicon). For higher powers Q1 could be an ASZ18 (germanium) or a 2N2955 (silicon) and Q2 a 2N3055 (silicon) or an AY8149 (silicon).

A heatsink on the transistor is

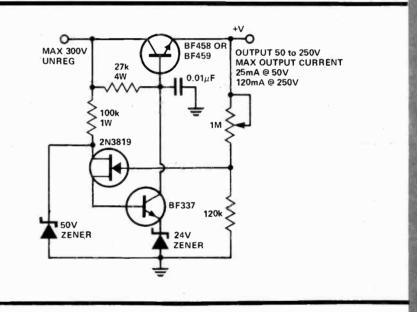


This regulator is ideal for SSB linear amplifier tube screens. It would also have application in the repeller supply for a reflex-klystron microwave oscillator. CRO deflection amplifier supply is another possible application. Regulation is about 0.5%. The output transistor will need to be mounted on a small, insulated heatsink. A BF459 is preferred (30V Vceo) as the BF458 is sailing a bit close to the wind when the output is down to 50 V.

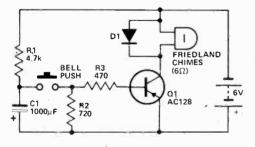


required. The circuit in (a) has the advantage that power transistors can

be bolted directly on to a chassis which may serve as a heatsink.



DOORCHIMES DELAY



Ever get tired of people who repeatedly press your doorbell? With values shown, this simple circuit will permit one operation every 10 seconds or so. Capacitor C1 charges through R1 when the button is released. Making R1 larger will increase the delay.

FLICKER-FREE FLUORESCENT STARTING

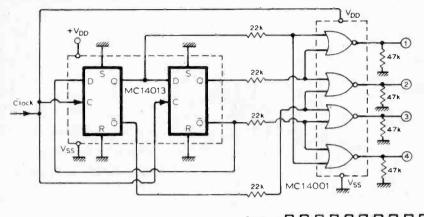
Here is an extremely simple, yet effective modification which will eliminate the annoying flickering when a fluorescent lamp is first switched on. The modification consists of inserting a diode (P.I.V. about 600 V) in series with the starter. This results in a fairly heavy current on initial switch-on, which heats the filaments quickly. When the starter contacts open again, the lamp fires immediately.

NOTE: The effectiveness of the modification, depends largely upon the characteristics of the starter; try and find one that is quick-acting.

My original unit has been working successfully in my desk lamp for the past three years, and I've had no problems with dc magnetisation of the ballast, or excessive power consumption on switch-on.

tech-tips

MULTIPHASE CLOCK GENERATOR



schematic; however,

interesting to note that the power

supply of the MC14001 is derived

from the clock input. The maximum

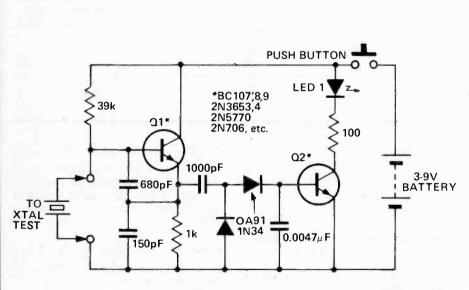
operating speed of this circuit is about

it is

Whenever sequential logic operations are to be performed, a multiphase clock generator is often required. The circuit shown, which uses only two CMOS ICs, was designed by Michel Burri of Motorola's Geneva applications laboratories. It will produce a pulse on each of the four output lines in turn. These pulses do not overlap one another.

Operation of the circuit is self-evident from an examination of

CRYSTAL CHECKER

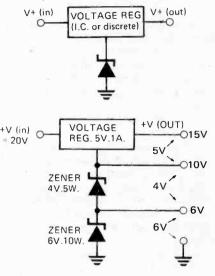


the

1 MHz

For checking fundamental HF crystals on a 'Go-No-Go' basis, the above circuit works quite well, An untuned Colpitts oscillator drives a voltage multiplier rectifier and a current amplifier. If the crystal oscillates, Q2 conducts and the LED lights. A3 or 6V, 40mA bulb could be substituted for the LED.

ZENER BOOSTS OUTPUT VOLT-AGE OF REGULATOR



In this circuit the zener diode raises all voltages — with respect to earth — by the zener voltage, i.e.

Vin (max) ≏ voltage regulator Vin (max) + zener voltage

Vin (working min) ≏ voltage regulator Vin (min) + zener voltage

Vout = voltage regulator Vout + zener voltage

As the voltage regulator dissipates all excess power while the zener merely clamps the output voltage above its own voltage, a low wattage zener (250 mW) should be adequate – unless lower voltage taps are used, as in the second example in which the total output is one amp.

For other value zeners, wattages can be worked out by the formula W = zener voltage X current.

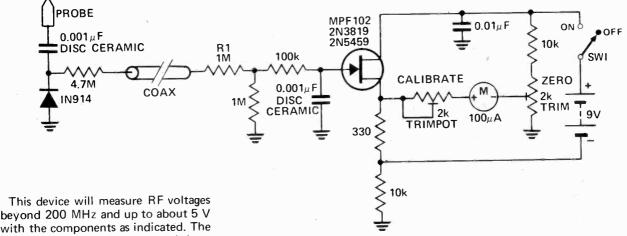
COLOUR CODING COMPONENTS

The resistor colour code can be extended for use in codifying all manner of other components.

Zener diodes for example can be thus coded once their parameters have been established. Similarly it assists when building a unit to mark the leads of transformers, coils transistors etc with short lengths of coloured insulating spaghetti. If for example one has a centre-tapped transformer then from the top of the winding inwards the code could be top = brown, centre = red, bottom = orange.

With a transistor base (B) = 2 = red;collector (C) = 3 = orange; emitter (E) = 5 = green. Just follow a numerical sequence equating numbers with letters of the alphabet.

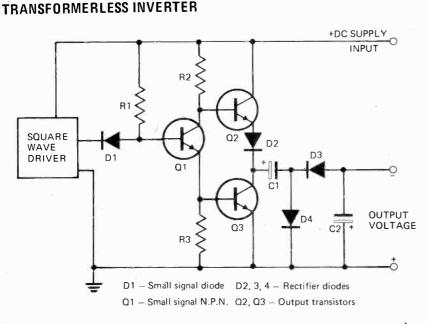
SENSITIVE RF VOLTMETER



with the components as indicated. The diode etc should be mounted in a remote probe, close to the probe tip. Sensitivity is excellent and voltages less than 1 V peak can be easily measured. The unit can be calibrated by connecting input to a known level

of RF voltage, such as a calibrated signal generator and setting the calibrate control. The output indicates in RMS. As it is it reads about 2 V

RMS full scale. This can be increased to 20 V or more by increasing R1 to 20 M (two 10 M in series). The 100 μ A meter could be a multimeter if desired.



This transformerless inverter chops the dc supply voltage then rectifies the

resulting square wave using a conventional voltage doubling circuit.

They worked quite well as substitutes

for LDRs or photo-transistors. For use as an LDR use only the collector and emitter legs. As a photo-transistor use all three legs – emitter base, collector.

In some circuits the component values need not be changed, while in

The square wave source can be a simple IC multivibrator which is used to drive phase splitter Q1 through coupling diode D1. Base bias is established through R1 and the collector and emitter load resistors are R2 and R3 respectively.

The output power transistors Q2 and Q3 in conjunction, with diode D2, serve as a simple high/level switch developing a square wave whose peak to peak amplitude is near that of the dc supply voltage.

The output square-wave is coupled via C1 (which must be a suitably chosen high-value electrolytic) and fed into a voltage doubler circuit thus producing a dc output of reverse polarity.

Capacitor C2 is the output ripple filter.

Resistor values are dependent on the original supply voltage — the drive frequency is not critical but signals in the kilohertz region are preferred (2-6 kHz).

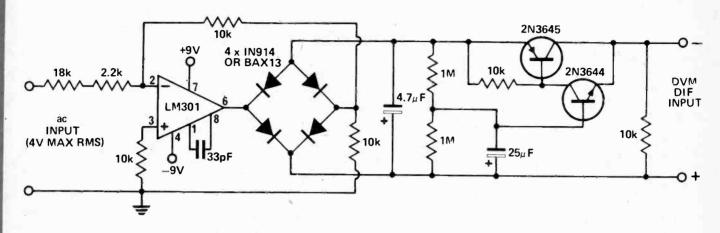
HOME-MADE LDR

I had to find a cheap and easy way to improvise for an LDR or photo-transistor. I took several medium power silicon transistors from my surplus box and carefully cut off with a saw and filed the top part of the can, exposing only the silicon chips. others a change is necessary for the circuit to work.

High powered silicon transistors also give a similar effect but they are costly unless found in the surplus box. Germanium transistors cannot be used because of the 'grease' present in them.

tech-tips

MEASURING RMS VALUE OF AC WITH A DC DVM



The above circuit may be used for measuring the RMS value of ac (sinewave) with a dc digital voltmeter. It has a frequency response to beyond 10 kHz and will measure signals as low as 400 mV. The error rises at low frequencies, somewhat below 50 Hz, to about 4% mean. The LM301 supply may be as low as $\pm 4V$. or up to $\pm 15V$, if desired, with reduced

sensitivity at the lower voltage.

The DVM input must be floating and a differential input is required. To increase the input range a step attenuator may be used.

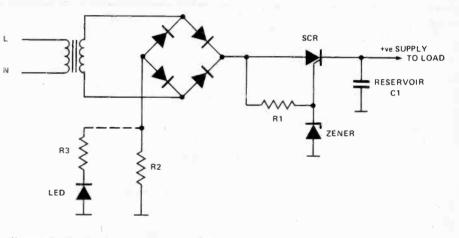
'BUCKET' REGULATOR

There are a number of applications where a simple cheap form of regulated power supply, giving a supply regulation of the order of 5-10%, is useful. One such application is the class B audio amplifier. The cost of the additional components required to achieve regulation is more than offset by the saving in cost and size of the electrolitic capacitors alone.

Fig. 1. shows the circuit of the regulated supply. The only additional components required to affect regulation are the SCR, R1, and the zener.

At switch on the the reservoir C1 is discharged and the cathode of the SCR is at zero volts. The positive going output from the bridge rectifier will cause gate current to flow via R1 triggering the SCR. The reservoir C1 starts to charge. At the end of the halfcycle the SCR will turn off.

The following half-cycles will repeat the process charging C1 until the supply voltage approaches the zener voltage. However the maximum positive potential at the SCR gate is determined by the zener, so there comes a time when the reservoir will have charged to the point where the SCR gate cannot be driven positive with respect to its cathode. At this stage the SCR will stop firing and no further charging current will be delivered to the reservoir. The reservoir will dis-



charge via the load, whatever power is being supplied, until the gate is once more positive, when the SCR will fire again. One or more half cycles are sufficient to raise the reservoir voltage sufficiently to prevent further firing.

Thus the SCR fires as necessary to keep the reservoir "topped up" and it is this topping up action which gave the regulator its name. The number of times it fires in any particular interval being dependant on the load current taken from the supply.

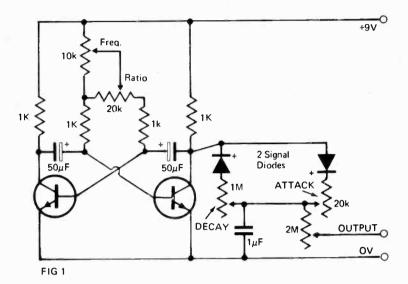
There are two particularly attractive features of this type of supply. First its efficiency is high, there are none of the power losses associated with either series or shunt regulators.

The second is that it is possible to obtain very simply an indication of the current being delivered, This may be obtained by connecting a LED (in series with a current limiting resistor R3) across the main current 4 imiting resistor R2. The LED will flash each time the SCR conducts and hence the rate at which the flashes occur will depend on load current, the flash rate varying from once every few seconds when only leakage is being made good - to continuously under full load conditions.

As an indication of circuit values the following where used for a 25V, 1.5A supply: R1=1.2k, R2= 2Ω , R3= 330Ω , C1= 5000μ F, 25V, Transformer 30V.

The SCR and bridge rectifier should be rated at full load current, but for many music and speech applications the transformer can be derated as much as 50%.

SOUND EFFECT GENERATOR



The waveshape generator shown in this circuit will interest those readers experimenting with sound effects. Basically the circuit is a slow running oscillator with variable attack and decay. A variable amplitude (high impedance) output is available via the 2 meg potentiometer. Figure 2 shows an add-on circuit which should be used if a low impedance output is required. Some of the output waveforms that can be produced are shown in Fig 3.

AFTER MODULATION

BC108

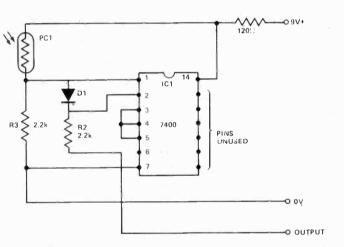
+ 900

100

μF

010

INTRUDER ALARM



Here two gates of a 7400 are used to provide photoelectric control in conjunction with an ORP 12 photocell. When light falls on PC1 the potential is applied to the trigger circuit consisting of 1/2 the 7400. The feedback provided énsures a positive output change at pin 6. The output, whilst PC1 is under illumination, is equal to the supply voltage. R1 enables a small 9V battery to be used. If PC1 is shaded the output at pin 6 is OV. This may now be used to trigger a relay for an intruder alarm. If this is the case it is wise to use a small mains supply and to incorporate a diode across the relay coil, to prevent high back EMF from destroying the IC.

GOOD/NO GOOD BATTERY TESTER

This is a simple tester for use with a PP3 or similar battery.

It is wired to a PP3 battery clip remembering that red is connected to -ve of battery and black to the +ve. It uses 3 small LEDs of the same size: one red, one green. Due to the fact that the green LED needs a far greater current, the green will glow only if the battery is in reasonable condition. The red will glow even if battery is down. If the red glow is very faint the battery is no good.

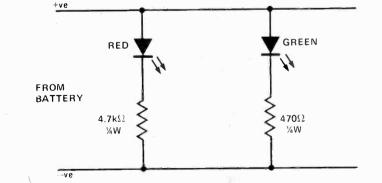


FIG. 2

10M

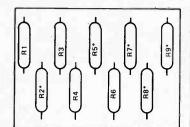
I/P

FIG. 3

tech-tips

ELECTRONIC LOCK

* REPRESENTS REED SWITCHES THAT HAVE TO BE TURNED ON I.E. MAGNETISED



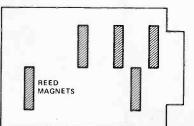
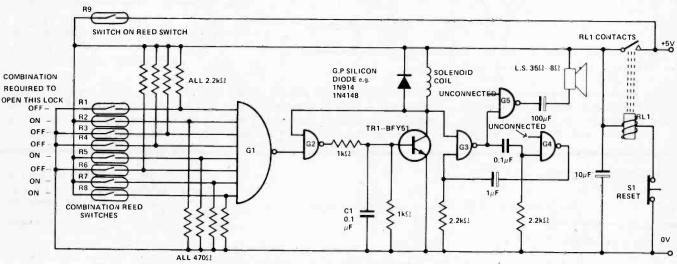


DIAGRAM OF KEY NEEDED TO OPEN THIS LOCK

REED SWITCHES ARE SHOWN IN ONE ROW IN CIRCUIT DIAGRAM BUT SHOULD BE WIRED AS SHOWN ABOVE TO AVOID CROSS TRIGGERING BY CLOSE REED MAGNETS



NOTE G1 = SN7430 8 INPUT NAND GATE I.C. G2;G3,G4,G5 = SN7400 QUAD 2 INPUT NAND GATE I.C.

This device enables a solenoid to be switched on by means of an electronic key. If the correct key is used the circuit will latch, but if an incorrect key is inserted a warning tone rings until the correct key is used. The circuit has automatic switching to turn it on, but this can be replaced by a conventional ON-OFF switch if desired.

The main element of the circuit is G1 the eight-input NAND gate. If all inputs of the NAND gate are high (achieved by closing the right combination of reed switches) the output will be low. The low output is fed to G2 forcing its output high which turns on TR1 energising the solenoid coil. At the same time a low is fed, back from TR1's collector to G2's other input latching it. Thus once the solenoid is energised the key may be removed. C1 (0.1μ F) ensures that TR1 is always 'off' on switch-on of the circuit.

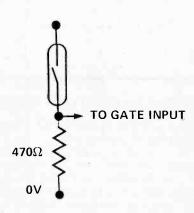
TR1's collector is also connected to

G3's input which along with G4 forms a multivibrator. When TR1's output is low the multivibrator is dis-enabled. However if an incorrect key is used TR1's output will be high and the multivibrator will oscillate. G5 acts as a buffer to drive a loudspeaker.

R9 with RL1 forms the automatic switch on circuit. When R9 is closed RL1 is energised pulling in it contacts to permanently connect the supply. If R9 is opened after this the circuit continues to operate. This means if an incorrect key is used and R9 is closed the alarm tone will continue to ring even if the key is removed.

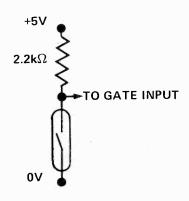
Nine reed switches are used in the circuit. One to switch on the circuit and the other eight to provide the correct input combination. The lock opens only if all eight inputs to the NAND gate are high. To do this the circuit is wired so that some reed switches must be on and some must be off. The eight reed switches give 2^8 = 256 possible combinations.

Reed switches that have to be turned on are wired like this



If the reed switch is not closed the 470 Ω will pull gate input to low and the lock will not open.

Reed switches that have to be 'eft open are wired like this

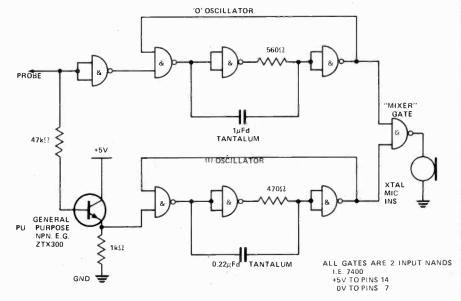


If the reed switch is closed it will put gate input to low and the lock will not open.

If preferred the reed switches could replaced by simple ON/OFF be switches then the circuit would act as a combination lock, like a tumbler lock.

The construction of the circuit is in no way critical. Veroboard provides a relatively cheap mounting. The connections of Vcc and gnd to the two ICs should not be forgotton!

With many LED TTL logic probes it is



difficult to watch the LEDs and the circuit one is testing. The following circuit is an attempt to produce a new sort of probe.

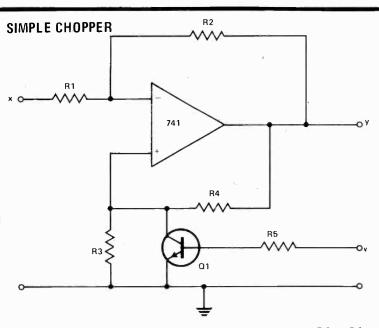
AUDIBLE TTL PROBE

When the probe is in contact with a TTL low (0) the probe emits a low note. With a TTL high (I) a

x

high note is emitted.

The whole circuit uses only two TTL ICs and several auxiliary components. This low component count makes it easy to miniaturise the unit. The power is supplied by the circuit under testing so no battery is required.



y

The circuit shown is a simple amplifier, the gain of which can be switched between two precisely controlled values by the application of a signal voltage: V. If V is such that Q1 is saturated, then the voltage gain of the circuit is simply $\frac{R^2}{R^1}$. If the transistor is cut off, then the voltage gain be-

comes $(1+\frac{R_3}{R_4})/(\frac{R_3}{R_4}-\frac{R_1}{R_2})$. One obvious application of the circuit is if the resistors are adjusted such that the two gains are equal in magnitude, but opposite in sign, (e.g. R1 = 20k, R2 = 10k, R3 = 50k, R4 = 10k gives voltage gains of +2 and -2) Then the circuit could be used as a chopper for the input of a DC amplifier.

The value of R5 is largely arbitrary, depending on the magnitude of the chopper signal, V. Its sole purpose is to prevent excess current being drawn by the base of Q1. Suitable components for the op amp and transistor are the 741 and BC182L.

tech-tips

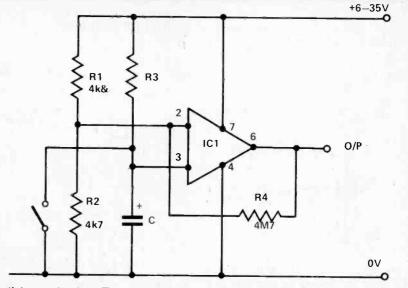
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741 TIMER

The circuit shows a very simple timer based on a 741 op amp.

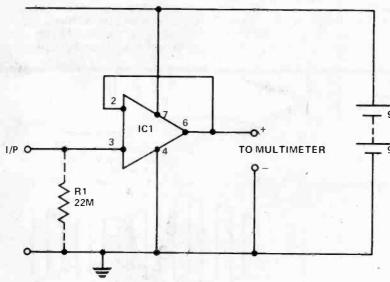
R1 and R2 hold the inverting input at half supply voltage. R4 applies some feedback to increase the input impedance at pin 3, but its value is such that negligable damping of pin 2's voltage occurs. Pin 3, the non inverting input, is connected to the junction of R3 and C. After S1 is opened and C charges via R3. When the capacitor has charged up sufficiently for the potential at pin 3 to exceed that at pin 2 the output abruptly changes from OV to positive line potential. If reverse polarity operation is required simply transpose R3 and C.

R3 and C can be any values and time delays from a fraction of a sec- by judicious selection. The time delay ond to several hours can be obtained is 0.7CR seconds where C is in Farads



and R in ohms and hence is completly independent of supply voltage.

100,000MΩ DC PROBE?



Most multimeters used for transistor work have an input impedance of 20,000Ω/V.

Occasionally, especially when measuring potentials on high impedance equipment, this sensitivity is sufficient. The circuit shown, however, presents neglible loading on the circuit under test.

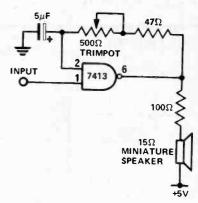
A 741 op amp is used with 100% AC and DC feedback to provide a typical input impedance of $10^{11}\Omega$ and unity gain (or so the contributor, Ed.)

Due to the possibility of hum and RF pickup the input leads should be kept as short as possible and the circuit should be mounted in a small earthed case.

The output leads may be as long as required since the output impedance of the circuit is a fraction of an ohm,

With no input the output level is indeterminate. This state of affairs can be changed by including R1 in the circuit through this lowers the input impedance to $22M\Omega$

MAKING SLOW LOGIC PULSES AUDIBLE



For monitoring slow logic pulses a Schmitt trigger is connected as an oscillator. The trimpot controls the pitch of the output. Very useful as a keying monitor or digital clock alarm. When the input goes high, the 7413 will oscillate.

DIL DRILLING

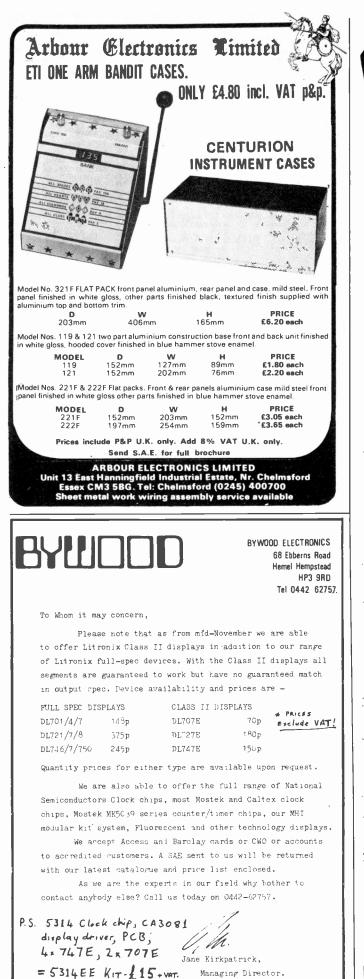
Drilling holes in a pcb for 14 and 16 DIL ICs is quite difficult and if the holes are slightly off centre it is tricky to fit the IC. An easy way to get it right use a small piece of 0.1 matrix veroboard as a template.





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)К,	approx. count by weight .60	PS 7 D.I.N. 7 Pin	0.17
.14	C3 50 Precision Resistors mixed values .60	PS 9 Jack 3.5mm Plastic	0.17
.26	C4 751/8th width Resistors mixed preferred values .60		0.17 0.14
.14	C5 5 Pieces assorted Ferrite Rods .60 C6 2 Tuning Gangs. MW/LW .69	PS 12 Jack ¼" Screened	0.20
	VHF	PS 14 Phono	0.33 0.09
	colours .60		0.14 0.14
2K, 1.	C8 10 Reed Switches .60 C9 3 Micro Switches .60	INLINE SOCKETS	
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kΡ	C13 20 Electrolytics Trans. types .60 C14 1 Pack assorted Hardware-		0.15 0.28
15p 18p	Nuts/Bolts, Grommets, etc60 C15 5 Mains Slide Switches, 2 Amp .60	PS 28 Jack ¼" Screened PS 29 Jack Stereo Plastic	0.32 0.28
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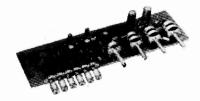
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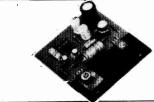
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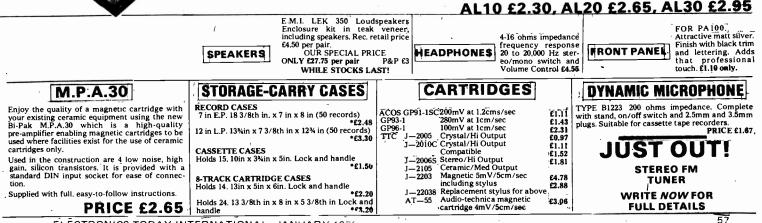
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ELECTRONICS TODAY INTERNATIONAL-JANUARY 1976

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Anne E. Crump looks at the development of dice and suggests a technological innovation—

ELECTRONIC DICE

THE CUBICAL DIE is the oldest game known to man. The dice our ancestors rolled more than two thousand years ago had pretty well the same configuration of dots as the ones we use today. Even before he gambled with them, primitive man considered them to be magical devices, and by their fall he divined the future.

It wasn't very long, of course, before he cottoned on to the possibility of making special dice with which he could cheat! We have found crooked dice along with the treasures of the tombs of the ancient Egyptians, which leads to at least two intriguing possibilities. Did they intend going on into the next world armed with slightly more than a fair sporting chance, or did they take their dice along for old times sake, having used them to acquire all that treasure in the first place? On either count, that's really no way to get to heaven!

But times have changed. Modern man produces hand-made ''perfect' dice for his casinos. Sawn from extruded plastic rods and produced to a tolerance of 1/5,000in, these perfect dice have spots drilled approximately 17/1,000in into each face and filled with a paint of the exact same weight as the plastic which has been removed. Buffed and polished so that no recesses remain, they are then ready for the gambling tables of the world.

Now, after 2,000 years, electronic technology has caught up with the oldest game in the world, and electronic dice have been proven to be even more random than their predecessors. The dice described in this article has been designed with minimisation of cost in mind.

The complete circuit uses 74 series integrated circuits only, plus a few discrete components, and is divided into three main areas, i.e. the pulse generator, the pulse counter, and the final decoding electronics.

THE PULSE COUNTER

The Pulse Counter is a simple arrangement utilising two 2-input NAND gates with the output of the second gate connected to the inputs of the first - a straight forward and convenient way of producing a low-cost oscillator. The frequency of oscillation is determined by the capacitor and resistance values selected, the actual frequency being relatively unimportant so long as it is as high as possible in order to avoid any possibility of the user being able to predetermine his 'throw." A figure of 120kHz is suitable. The oscillator output is connected directly to a 7492 counter, which is used in the 'divide by six'' mode, and in this way a full scan of counts one to six occurs 20,000 times each second.

THE DECODER

The BCD output of the 7492 is connected to an arrangement of gates which decode the BCD information into suitable drive signals for the LEDs. The decoding arrangements and the diode drive combination have been derived in such a way that the final illuminated display appears in the same dot configuration as a conventional die. The Boolean equations for the gating system are shown on the circuit diagram.

THE PULSE GENERATOR

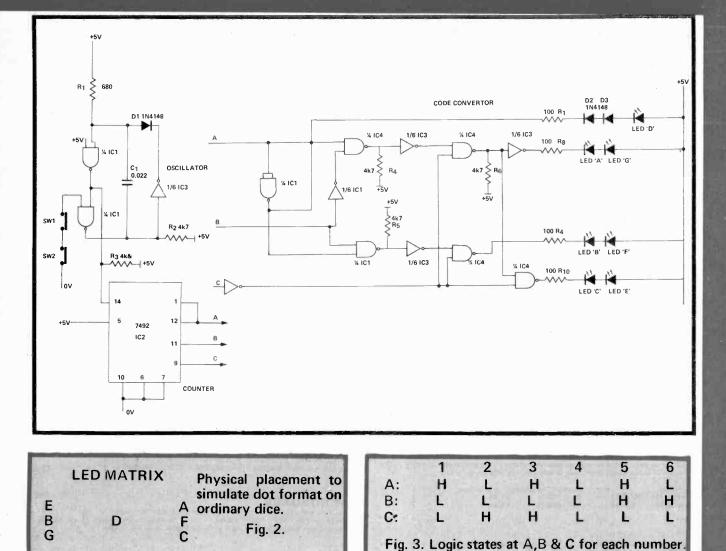
In order to obtain the maximum degree of randomness, it is essential that the dwell time should be equal on each of the six counts. The main factors affecting these requirements are the stability of the oscillator and also the spread of output reactances around the counter. If, for example, the frequency of the oscillator shifts during the one to six count cycle, the length of time spent on some of the counts will be greater, and the

'throw'' will therefore be biased in favour of those numbers. In practice, however, the simple oscillator shown gives adequate stability over the significant period of six pulses," long-term drift being of little importance. A further important point to be borne in mind is the parasitic reactances around the counter area, which could also be instrumental in biasing the "throw," and a good layout of the final unit is therefore essential. A ready-made printed circuit board, complete with assembly and testing instructions, is currently available, and its use is recommended.

The actual degree of randomness of the die is easily obtained by pressing the operating button a number of times, and recording the amount of times each number appears. In order to get a reasonable accurate picture, it is necessary to record at least 3,000 "throws," from which an average should be worked out. The results obtained are excellent, and in tests have shown the random distribution figures of the electronic dice to be in most instances superior to that of the conventional dice. In any group of 10 or 20 "throws" there will always tend to be a predominance of one or two numbers which does tend to be discouraging when first encountered. It is therefore essential to remember that this also happens when using ordinary dice!

CONSTRUCTION

There are a number of ways in which an electronic die can be activated. The method chosen for the particular unit described here is for the unit to display the last throw until play resumes and the operating switch is pressed again. The last "throw" remains clearly visible for all to see, this avoiding any danger of arguments which might arise if the "throw" was only displayed while the operator's finger remained on the button. Two microswitches



are shown on this circuit. One is sufficient, but it is useful to have a number of switches in series so that people at different parts of the table can operate the unit by pushing a local button.

Power for the circuit is provided by four 11/2 volt transistor radio cells, and the randomness of the counter is unimpaired so long as the brightness of the LEDs is reasonable. As soon as the LEDs begin to dim, the batteries should be changed immediately, as deterioration in the random quality of the circuitry may occur. It is important that the battery supply should not be allowed to fall below 4.5V or to exceed 7V. The voltage adversly affects the random operation and the higher voltage is the maximum safe ceiling at which TTL can be run. With LEDs and solid-state electronics used throughout, the reliability of the dice is excellent, and the switches should be first to wear out.

The finished product is very much to the design of the individual

himself. Double or even treble dice, operating from a single switch or, two or three individual buttons, can be made. A buzzer could be added, to be triggered each time a single, double or treble six is "thrown. This is easily done by adding the necessary gating to the outputs of the A, B and C outputs of the 7492 so that the buzzer is activated each time the appropriate combination appears. Some form of sound, even if only the click of the microswitch, is essential in order to prove to other players that the button has actually been pressed, and a further improvement could be a brief audible tone emanating from the dice when activated.

It is important to note that in addition to their obvious leisure uses, electronic dice have other varied applications, particularly where statistical calculations are involved, an excellent example being their use for quality assurance in factories. Here, by their use, a truly random selection of the production run of a factory can be obtained for testing purposes. Perhaps if the Romans had had electronic dice, human nature being as it is, the selection of Christians going to the lions would have been a trifle more random? And Nero would still have fiddled while Rome burned — but chances are he'd have been fiddling with an electronic die!

-	PARTS LIS	т
IC2 IC3	ED 'G' 7400 or 740 7492 7404	
* NB: R2 the 740	microswitch -R6 are not 0 gates - onl collector gate	used with y with

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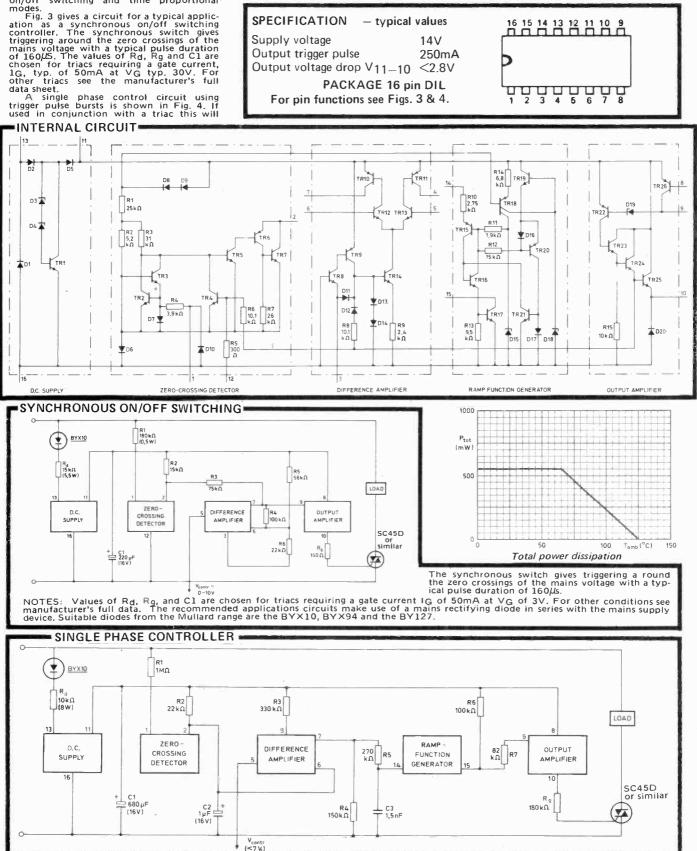
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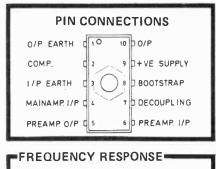


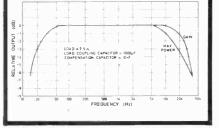
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SL415A & SL414A 3W & 5W AUDIO AMPLIFIERS

The SL415A and SL414A are robust high gain audio power amplifiers each with a separate pre-amplifier. The circuits, which have been optimized to give maximum reliability, have guaranteed power outputs of 5W and 3W respectively under the conditions specified.



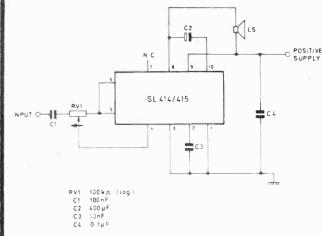


OPERATING NOTES

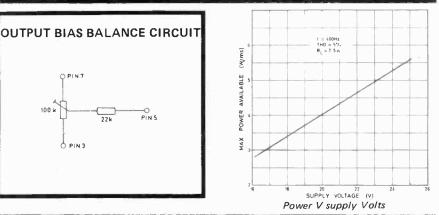
OPERATING NOTES
Each circuit has built in resistance to damage caused by voltage overload or short circuits across the output.
For high inductive loads a high frequency load of 47nF and 22Ω connected in series may be required between pins 10 and 1 to enhance stability. When connecting the circuit shown in Figs. 5,9 and 10 is recommended that:

(a) The capacitor between Pins 9 and 1 should be kept close to the integrated circuit.
(b) The earth path between Pins 3 and 1 should be kept short.
(c) The earth connection from the supply should be connected to Pin 1 not Pin 3.
The circuits are designed to centre the of DC bias point half way between supply and earth. Any variations in this level can be adjusted with the use of the circuit shown in Fig. 4.
The devices should be used with a suitable heat sink of approximately 20cm2 (3 in2). Typical advertised prices are SL-414A £2.00, SL415A £2.75.

MINIMUM COMPONENTS CIRCUIT



	SPECIFICATION
CHARACTERISTIC	
Supply voltage	
SL414A	18V
SL415A	24V
Power into 15Ω load	•
SL414A	2.2W
SL415A	3.8W
Pre. amp. voltage gain	24dB
Main amp. voltage gain	26dB
D.C. input current	400 4
Pre amp.	100nA
Main amp.	100nA 0.2Ω
Main amp. o/p impedance	0.232
Pre amp.	0.1%
Main amp.	0.3%
Noise level	=75dB
NOISE IEVEI	-7505
Ripple rejection	30dB
Input impedance	
Pre amp	20MΩ
Main amp	100MΩ
ABSOLUTE MAXIMUM RA	TINGS
Supply voltage	
SL414A	
SL415A	
Max. peak loud current	
Storage temp.	
Operating temp (with suitab	le heat sink)



CONDITIONS

 $V_{supply} = 18V$ $V_{supply} = 24V$

30W r.m.s. o/p Load = 7.5Ω 5W r.m.s. o/p THD = 5%

Pre amp. o/p = 0.9V r.m.s.

f = 400Hz, Pout = 1W Source impedance = $1M\Omega$,

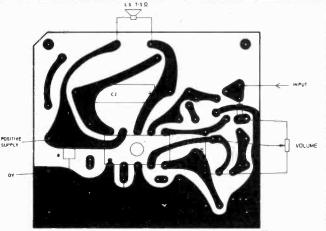
with pin 7 decoupled

-20°C to +80°C -10°C to +70°C

gain = 26 dB

+20V +25V 1.4A

5% THD



(NOT ACTUAL SIZE)

In the 'minimum components' application (which is the simplest possible configuration for the SL414/415) the preamplifier provides a temperature-compensated input bias to the power amplifier (pin 4). The circuit has an input impedance of 100 k Ω . With an input of 250mV r.m.s. and a 7.5 Ω load, typical power outputs of 2.5W (SL414, 18V supply) and 4.0W (SL415, 24V supply) are obtained. The supply rejection is 6dB.

OPIN7

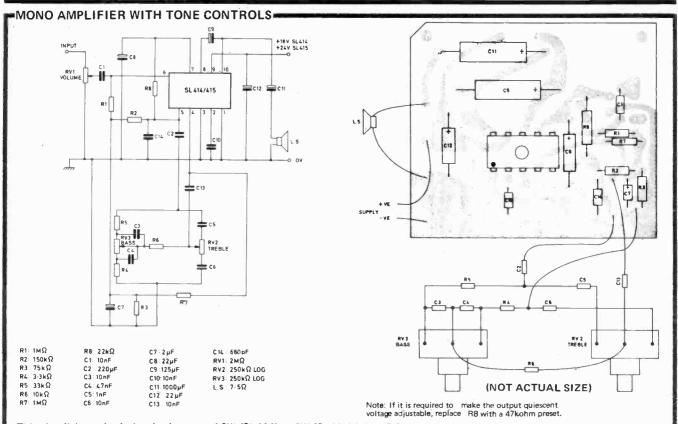
O PIN 3

100 k

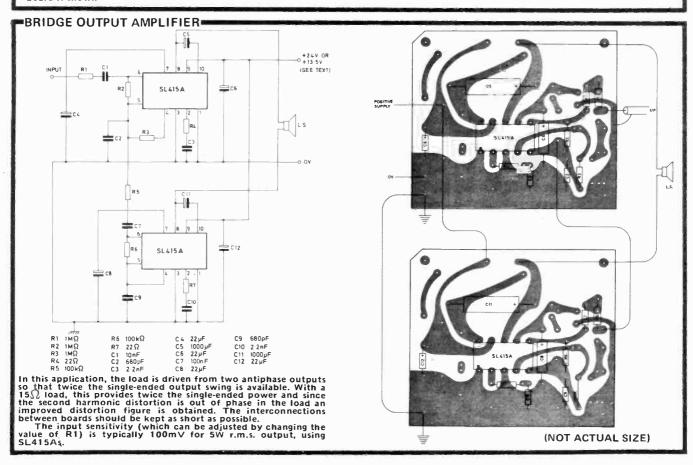
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PLESSEY

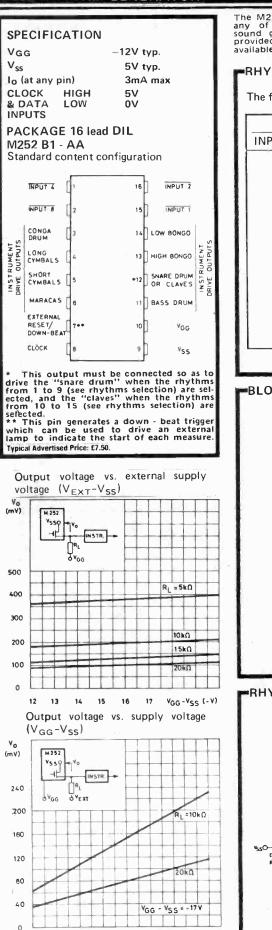


This circuit has a typical output power of 3W (SL414) or 5W (SL415) into a 7.5 load with an input to the preamplifier of 250mV r.m.s. The preamplifier is used at full gain to offset the 20dB midband loss in the tone control network. The tone control is an insertion 'loss' network between the preamplifier and the main amplifier. To drive the main amplifier a maximum swing from the preamplifier of 2.5V r.m.s. is required. As the maximum output swing is normally IV r.m.s. (limited by bottoming) the quiescent output voltage has been raised by inserting the potential divider R2/R3 in the mid-point of the divider via R1 and the feedback by passed at audio frequencies by C7. A component layout using a printed circuit board is shown



M252 RHYTHM GENERATOR

SGS - ATES



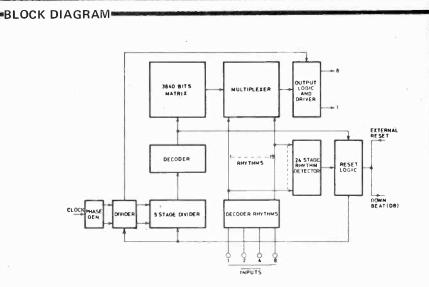
The M252 is a PMOS IC which will supply any of fifteen rhythms to drive eight sound generators. A down-beat output is provided and a version (M252 B1AA) is available with standard music content.

Rhythm is selected by binary coded input, (see table 1) 8 or 9 instrument generators are required as well as a variable clock generator operating at TTL levels.

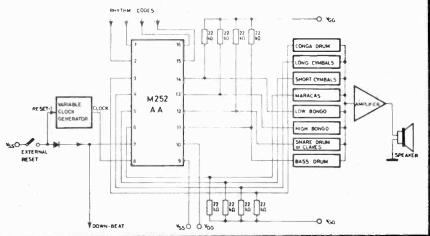
PRHYTHM SELECTION

The following binary code must be generated to select each rhythm (logic positive)

RD	STANDA	CODE						
IT	CONTEN	INPUT 1	INPUT 2	INPUT 4	INPUT 8			
3/4	Waltz	0	1	1	1			
3/4	Jazz Waltz	1	0	1	1			
2/4	Tango	0	0	1	1			
2/4	March	1	- 1	0	1			
4/4	Swing	0	1	0	1			
4/4	Foxtrot	1	0	0	1			
6/8	Slow Rock	0	0	0	1			
4/4	Rock Pop	1	1	1	0			
2/4	Shuffle	0	1	1	0			
4/4	Mambo	Ŷ	0	1	0			
4/4	Beguine	0	0	1	0			
4/4	Cha Cha	1	1	0	0			
4/4	Bajon	0	1	0	0			
4/4	Samba	1	0	0	0			
4/4	Bossa Nova	0	0	0	0			



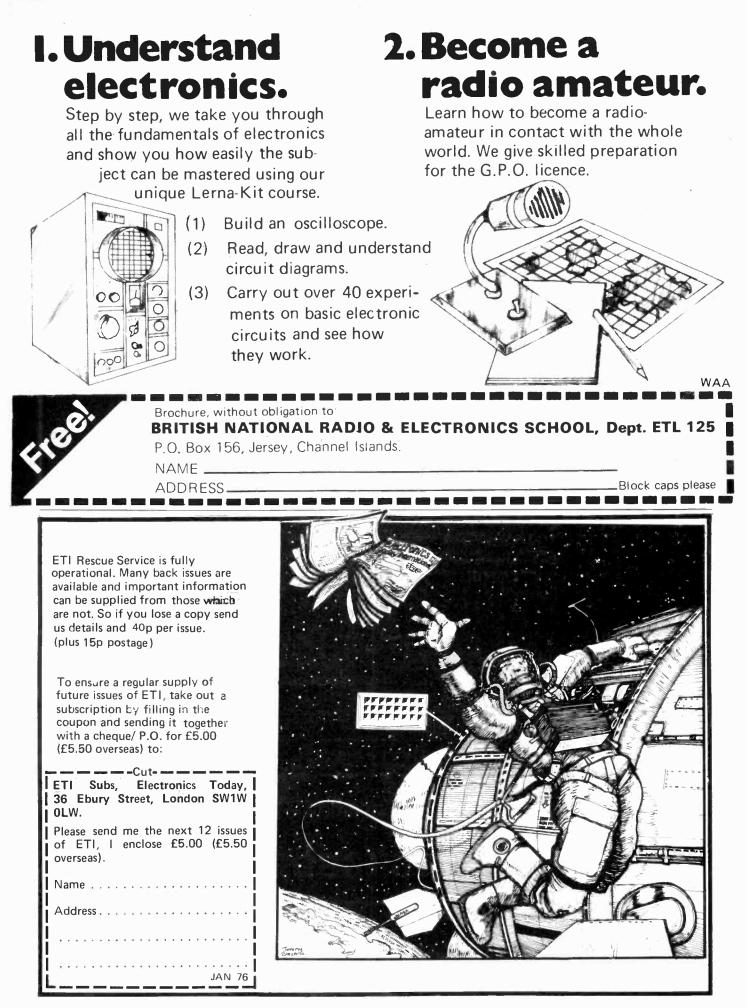
RHYTHM SELECTION (STANDARD CONTENT)



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9 11 13 15

VEXT -VSS (-V)





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HI KIT SYSTEM

MHI-5039 (UNIVERSAL COUNTER) Uses a new counter chip from MOSTEK (MK50395) and will count up or down at speeds of up to 1MHz with a total system speed of 400kHz. Count and compare registers can be loaded from logic ICs or BCD switches. features count inhibit, display latch, display decode. Outputs 6 digit drives. BCO and 7-segment, count = compare, count = zero, etc. Applications include: very fast stop watch, sequence timers, auto-cue for tele-cine, batch counters, repeatable "pill" counters, etc. Interfaces with any six digit MHI display kit £19.50 + VAT

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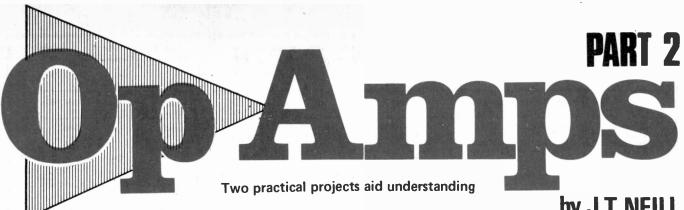


ELECTRONIC MUSICAL INSTRUMENTS Designers and component suppliers to the musical industry

Wite Range 107 Top Project 022 £1.09 Plus Two Add on Occader Amp Decader Amp Unit 423 Nov. 1974 423 91p Frans. 2 Frans.	TITLE	PROJECT No.	ISSUE	BOARO NO.	TOTAL	TITLE	PROJEĊT No.	ISSUE	BOARD NO.	TOTAL	TITLE	PROJECT No.	ISSUE	BOARO NO.	TOTAL
Wide Range 107 Top Project 022 £1.09 Plus Two Add on Decoder Amp Unit 423 Nov. 1974 423 91p Colmeter Unitered Unite	25 watts/chan.					int. 422 Stereo Amp					Music Synthesiser	601	Aug. 1974	Cont:	
LE. Prover Supply 111 Jan. 1973 111 £1.43 Spring Reverberation 424 0ec. 1974 424 £1.52 Thermocupie Meter 113 0ec. 1974 113 £1.57 Util Steros Numble Filter 424 0ec. 1974 424 £1.52 50 501 £2 Outal Seam Adaptor 114 0.1975 116 1.00 Steros Numble Filter 426 Jan. 1975 425 75p 116 601 £2 Volumeter 1177 6Bp Steros Numble Filter 426 742 751 427 £1.96 107 601 £2 601 601 601 <	Wide Range		Top Project			Plus Two Add on	423	Nov. 1974	423	91p				Trans. 2	£1.22
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LAST MONTH the basic theory of operational amplifiers was given. This month we give constructional details of, firstly, a dual power supply suitable for running all the units to be described in this series, followed by the description of a sine wave audio oscillator. The oscillator is the first of a series of small projects designed to give practical experience with op-amps.

THE POWER SUPPLY

The limitation when attempting to reduce the size of any small piece of equipment is, with the present state of the art, the dimensions of the mains operated power supply required to drive it. Thus although an oscillator can be constructed with one IC and a few passive components, the companion ac power supply, by comparison, is extremely bulky. Thus it is fairly pointless to attempt to construct the power supply in such a way as to minimize its total volume. This is not really a disadvantage, however, as the power supply can be used to power other circuits and the diminutive oscillator can, of course, be powered by separate, small, batteries when that is desirable.

Small size may not be a feature of the power supply, but it does have several important characteristics, namely, *automatic* short circuit protection and good voltage stabilisation. A brief specification is given in Table 1, while the full circuit diagram appears in Fig. 1. Consider first that part of the circuit above the dotted line.

Diodes D1 and D2 full-wave rectify the transformer output and charge C1 positively. Capacitor C1 has a sufficiently large value to filter out nearly all the 100 Hz ripple component and provide a smooth dc to the regulator.

The constant potential across the Zener diode D3 maintains the base of Q1 similarly constant. Should the mains voltage vary, or the load current alter, then the output voltage will tend to change too; however, that voltage is fed, via the diode D4, to Q1 emitter,

151	양 : 홍말 방법은 동안 중감 방법은 그는 것은 것은 방법에 잘 많이 모고 유가 위해 가				
	PARTS LIST - POWER SUPPLY				
	D1,2,5,6 Diode IN4001 D3,7 '' BZ x 79 C12 or other 12 V Zener				
	04,8 '' IN914 11 Transistor BC547, BC107 22 '' BC140, BD136 23 '' BC177, BC557 24 '' BD139				
	1,3 Resistor 1.5 k ¼W 2,4 '' 6.8 k ¼W 21,4 Capacitor 1000/JF 25 V electrolytic				
	22,5 '' 22//F 16 V electrolytic 23,6 '' 100//F 16 V electrolytic				
	Transformer 12 V — 0 12 V 100 mA				
	SW1 double-pole double throw				
	Die-cast box 165 x 114 x 51 mm				
	Fl Fuse lamp				
	use holder 20 mm				
12	erminals (screw) 1 red, 1 black, 1 green (earth 1 other) Neon indicator				
	/eroboard, 0.15" pitch 100 x 60 mm				

by J.T. NEILL where it is compared with the Zener

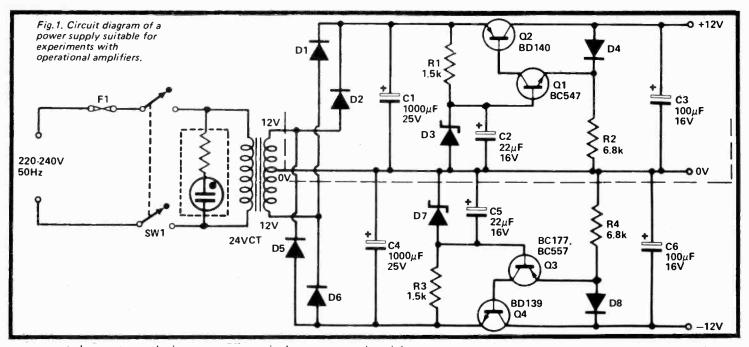
where it is compared with the Zener voltage at Q1 base. Thus, the collector current of Q1, and hence the base current of Q2, will alter, so effectively changing the emitter-collector impedance of Q2 in such a way as to correct for the original variation. The voltage drop across D4, D8 compensates for the drop across Q1 and Q3 thus ensuring the output voltage is the same as the zener voltage.

Such a configuration will give good load regulation but very large variations in mains voltage will not be counteracted as well as is done in some other circuit designs. This is because the Zener diode is fed from an unstabilised supply. Improvement in output voltage stability - by the order of a factor of five or so - can be achieved by modifying the circuit to drive the Zener from the output, rather than the input, of the regulator, but this would not permit the incorporation of short-circuit protection components, in an arrangement now to be described. Short circuit protection of the supply is vital in experimental work, especially on integrated circuits, where the small size and close spacing of the connecting leads printed circuit tracks can so easily result in unwanted shorts which may overload and possibly damage the power supply.

Consider what happens when the output of the voltage regulator is connected directly to the earth line. The germanium diode D4 is no longer forward biased, for its anode,

	TABLE 1
Input	220 – 240 V 50 Hz
Output voltage	1. –12 V and +12 V
or	2. −24 V
or	3. +24 V
Output current	100 mA maximum
Protection	Automatic constant current limiting (210 mA) on short circuit
Regulation	Better than 80 mV variation, no load to full load
Hum and noise	Less than 3 mV

TABLE 2Frequency range120 Hz – 1.2 kHz
1.2 kHz – 12 kHzOutput level1 V rms maximum, continuously
variableOutput impedance70 ohmsMin. load at 1 Vrms1.5 kPower supply needed3 mA at +12 V
2 mA at -12 V



connected now to earth, is not more positive than its cathode; accordingly, D4 can be considered to be absent and the effective circuit arrangement is as in Fig. 2. The base voltage of Q1 is still fixed (by the voltage across the Zener) at 12 V with its emitter taking up a voltage about 0.7 V less. This fixed voltage appears across R2, which means that a fixed current flows through R2 and Q2 into the base of Q1 and hence, the emitter current of Q1 is fixed also. The emitter current of Q2 will be larger than its base current by a factor equal to the current gain of transistor Q2. The emitter current of Q2 is the load current however, so that it can be seen that, under short circuit conditions, a constant current of a magnitude determined by R2 flows into that short circuit.

A suitable value of R2 must be selected to obtain the desired short circuit current. Here, it is chosen so that 210 mA flows in short circuit conditions. It is under these conditions that the greatest power dissipation occurs in Q2 and accordingly it has been ensured that a continuous short circuit will not give rise to overheating of that transistor.

In point of fact, about 3.4 watts are then dissipated in Q2, a value well within the capability of the transistor type employed. It is bolted to, but insulated from, the die-cast box in which the power supply is housed, so that it is thereby provided with a very large heatsink. Accordingly, in normal use all components run with hardly any temperature rise, and even when running into a short circuit, the combination of current limiting and large heatsink ensures that the power supply is not damaged.

What has been discussed so far is a

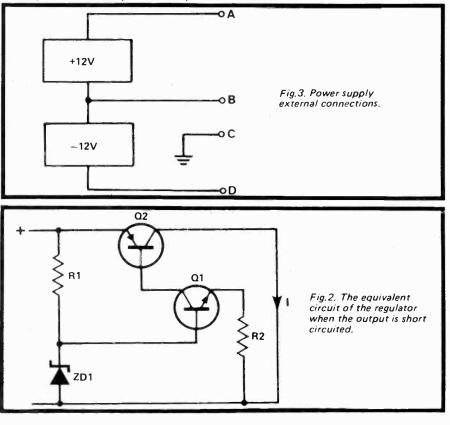
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single power supply, giving an output of 12 V. The actual unit contains two such supplies, as Fig. 1 shows, of similar circuit configuration, but, in the second case, a PNP transistor is used instead of an NPN and vice versa, and with a negative supply voltage, derived from D5 and D6, fed to it. The Zener diode and its electrolytic capacitor, as well as the germanium diode, are all connected with the opposite polarity from before. Therefore, a stabilised and protected -12 volts appears at the output, relative to the centre supply terminal. As quoted in the specification, this

permits the output from the power supply unit to be connected in any one of three configurations: –

- 1. + 12 V and -12 V relative to earth
- 2. + 24 V relative to earth
- 3 24 V relative to earth

This is achieved by connecting the two supplies in series, with the common point brought out as an external connection. Further, this common point is not earthed, but a separate earth terminal provided. The three different modes of operation are then obtained by means of the appropriate external connections – see Fig. 3.



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For use with the type of operational amplifier dealt with here, the first mode i.e. ± 12 V will usually be employed.

The rated output, of 100 mA from either side, will be found to be more than adequate for the intended use, since type 709 and 741 op. amps. draw less than 5 mA each, unloaded. A suggested layout and constructional technique for the dual power supply is given in Fig. 4, but the layout is by no means critical and the constructor may employ any alternative method. Nevertheless, a robust housing is required and the best is probably a die-cast box – any small extra expense incurred, to obtain such a convenient and easily worked case, is well worthwhile.

THE OSCILLATOR

Now for the first constructional project using an operational amplifier. As mentioned earlier, this is a sine wave audio oscillator. The circuit is given in Fig. 5 and its specification in Table 2.

The oscillator makes use of the well known Wien-bridge network to set the frequency of operation. A resistor (in this case RV1a and R1) and a oarallel capacitor (either C1 or C2) are connected to further resistors (RV1b, RV3 and R4) in series with a further capacitor (either C3 or C4). It is a property of the Wien network that the junction of the two RC arms, has, at a single frequency only, a voltage in phase with, but smaller than, that applied to the whole network. Since, in the oscillator, this in-phase voltage is fed to the non-inverting terminal of the op. amp. it constitutes positive feedback, and thus oscillations will occur and be maintained at one specific frequency — a frequency determined by the values of the resistors and capacitors employed in the Wien network.

So much for the frequency of oscillation. What of its amplitude?

Consider for a moment what would happen if, with the oscillator already giving a sine wave output, the output amplitude should increase for some reason. If it continues to do so, eventually the voltage will become so large that it will be limited by the supply rails and a clipped sine wave will result. Conversely, if the amplitude of oscillation should decrease, then oscillations will eventually die away to nothing.

Such variations in amplitude can easily arise due to temperature changes etc., and will in any case occur as the frequency is altered, due to tolerances

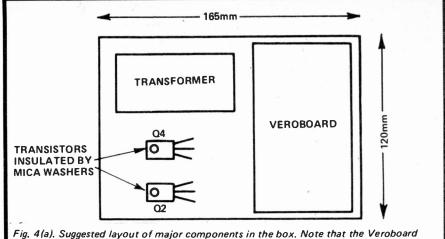
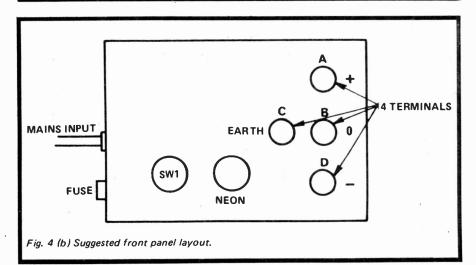


Fig. 4(a). Suggested layout of major components in the box. Note that the Veroboard should be insulated from the box by inserting cardboard between it and the box.



in the capacitor values and tracking errors in the twin-gang potentiometer.

Thus, some means of automatic gain control is essential in order to maintain a constant output amplitude.

It will be recalled that the signal voltage applied to the op. amp. non-inverting input was smaller than the output voltage due to the attenuation in the Wien network. To maintain oscillation the op. amp. must have a gain equal or exceeding this attentuation — which is in fact x3. The desired gain is obtained by selecting the ratio of feedback resistance to input resistance of the inverting input (RV2 + R3)/R2.

If the overall gain, including feedback, exceeds unity the circuit will produce sine wave oscillation at a frequency set by the Wien network.

Stabilisation of the gain is brought about by the action of diodes D1 and D2.

When the instantaneous output voltage is close to zero, neither diode conducts, since even a germanium diode requires 0.4 volts or so forward voltage to bias it on. Consequently, the negative feedback loop is open

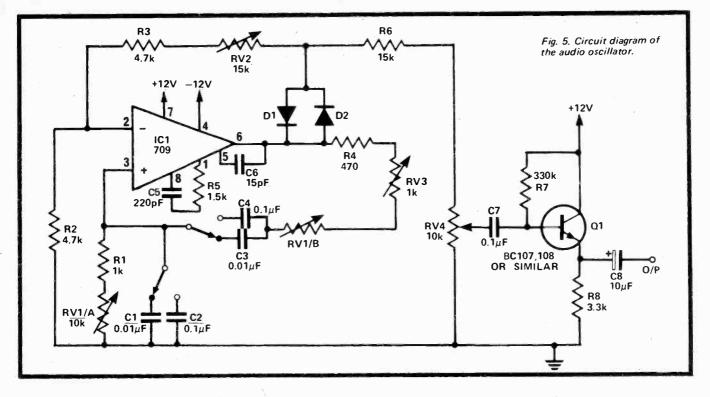
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(giving maximum gain) and, under the action of the positive feedback via the Wien network, oscillations build up rapidly. As soon as their amplitude is sufficient to bias on either D1 or D2 (depending on the polarity of the output voltage swing), then R2, R3 and RV2 provide negative feedback, so limiting oscillations to a convenient level.

Re-inforcement of such oscillations takes place close to each zero crossing when D1 and D2 are open i.e. non-conducting; the setting of RV2 determines the final amplitude.

This method of stabilisation does give rise to a very small amount of crossover distortion, but the effect of this can be minimised by setting VR2 for the largest possible sine wave without clipping. In any event, some distortion is a small price to pay for such a simple, easy-to-get-working sine wave oscillator and, further, it is a *low* level of distortion — some class B audio amplifiers are worse!

Range switching is confined to a choice of two ranges, in the interest of simplicity and cheapness, but more ranges could easily be provided if the constructor is so inclined.



The frequency ranges mentioned in the specification are a little unusual, in that most audio generators provide ranges starting and ending at 1 kHz, 10 kHz and so on. However, in the present case, the selection of easily available components having standard values produced the ranges shown and these were, in fact, found to be convenient in practice.

A simple emitter-follower output stage completes the unit, with a logarithmic potentiometer as a level control, enabling the output to be set from 1 V rms down to 10 mV rms or so.

Suitable compensation components R5, C5 and C6 are required for the type 709 op. amp. A layout found

suitable for the oscillator is given in Fig. 6. Notice that this calls for a box of only $100 \times 75 \times 30$ mm, which, whilst making the oscillator quite small and neat, does not result in cramping of the layout, which is straightforward and easily followed.

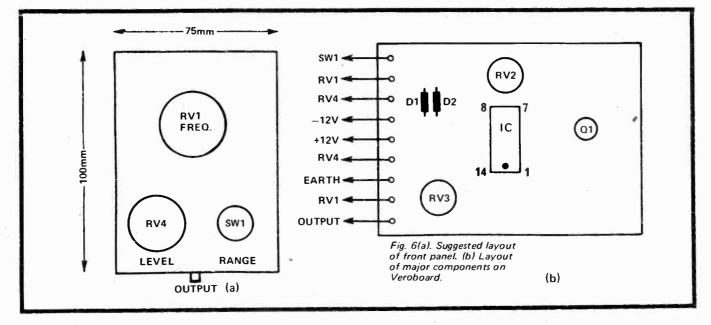
With wiring up completed and thoroughly checked, switch on and, if possible, monitor the output on an oscilloscope. No 'scope? Then a pair of headphones, of reasonably high impedance, can be used instead. Set RV4 about half way, S1 to "low" and RV1 about half way. If there is no output, adjust RV2; clockwise rotation should give increased output. With an ac meter, measure the signal

level at the junction of D1, D2 and

RV2. Adjust RV2 for 3 volts rms. This will ensure the highest output level (thus reducing the effect of crossover distortion) consistent with sine wave operation. (no clipping). This should provide about one volt rms at the output.

It was found on the prototype that changing to higher frequency range gave a slightly reduced output doubtless due to the use of 10% tolerance capacitors in the Wien network. Closer tolerance capacitors are, of course, more expensive but the amplitude difference may be overcome by adding small capacitors to either C1 and C3 or C2 and C4 whichever reduces the amplitude difference.

Variation of output level as RV1 is



Amps

PARTS L	PARTS LIST - AUDIO OSCILLATOR				
IC1 Q1	Integrated Circuit Transistor	709 BC107,108 or similar			
D1,D2 R1	Diode Resistor	OA95 1k 1/8W			
R2,R3 R4		or 1/4W 4.7k 470 1/8W 1.5k ''			
R5 R6 R7		1.5k '' 15k '' 330k '' 3.3k ''			
R8 C1,C3	Capacitor	0.01µF polyester			
C2,C4,C7	.,	0.1µF polyester			
C5 C6 C8		220pF '' 15pF '' 10μF			
RV1	Potentiometer	electrolytic 16V 10k + 10k			
VR2,RV2	.,	ganged 5k pre-set			
VR3,RV3		(horizontal) 1k pre-set			
VR4,RV4		(horizontal) 10k			
S1 Aluminiu Veroboard	d. 0.1" pitch 80 x 6	logarithmic DPDT 75 x 40 mm 52 mm			
8 pin IC h 3.5 mm ja	ick				

rotated, due to tracking errors between RV1 sections, can be minimised by adjustment of RV3. To do this, set RV1 close to its high frequency end. Adjust RV3 to give about the same level of output as with RV1 at mid-travel. If the twin gang potentiometer RV1 is particularly poor in its tracking an alteration to the value of R4 may be called for.

Calibration of the frequency scale is always a problem with any home constructed audio equipment such as this oscillator. Comparison with other audio signals is one method, either by ear, oscilloscope or frequency counter. Ideally comparison with another oscillator will allow the frequency scale to be marked up accurately.

Regarding alternative components: a 741 in place of a 709 will function well, except for some slew rate limiting at the higher frequencies, leading to distortion. If a 741 is used

delete C5, C6 and R5. A type 301 may also be used; in this case C5, C6 and R5 are deleted as before and a 10 pF capacitor is fitted between pins one. and 8. Almost any silicon NPN transistor will be satisfactory for Q1, but some alteration in R7 value may be necessary. Silicon diodes for D1 and D2 give rise to much greater levels of crossover distortion, due to their greater forward voltage drop.

This little oscillator will be found to give a sufficiently pure sine wave to assist in the testing of almost any audio equipment, its restricted frequency range being no great drawback for that work.

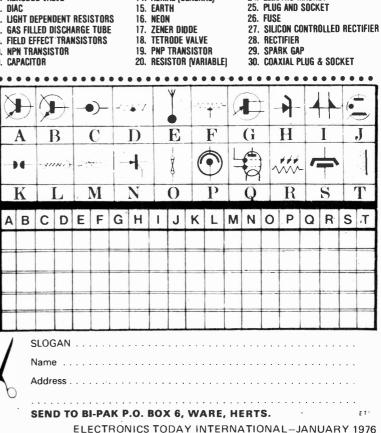
A truly portable oscillator can be made by replacing the dual 12 volts batteries by two small nine volt batteries. If this is done, however, some re-adjustment of RV2 and a reduction in the value of R6 to give a sine wave free from clipping, will be required. to be continued . . .

23. PRESET RESISTOR

24. ELECTRO MAGNET

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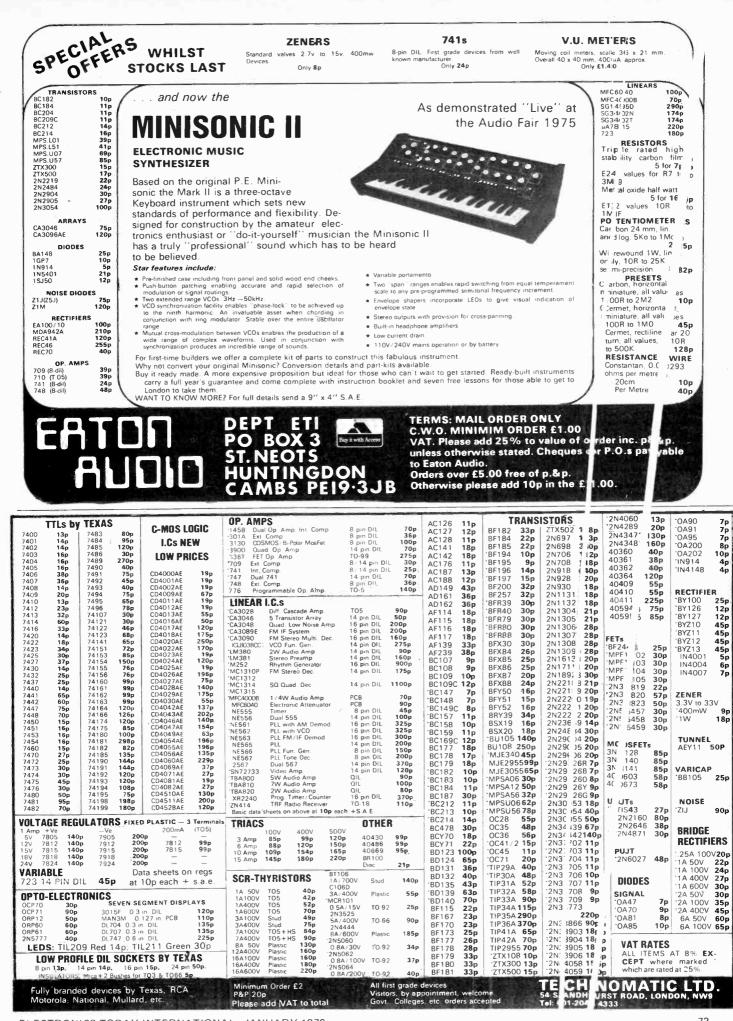


13. THERMO-COUPLE

14. AERIAL (GENERAL)

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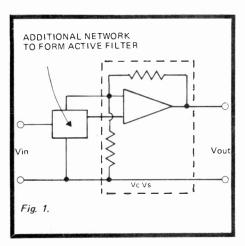
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ELECTRONICS PART 23 -it's easy! Last word on filters

WE CONTINUE our discussion of active filter design following our analysis of single and multiple feedback systems. The most common active filter is the multiple feedback path design shown last month. Other options open to us are to use an op-amp set up as either a controlled source with added elements - see Fig. 1, or as the negative-impedance convertor shown schematically last month. These can offer certain advantages over the voltage-amplifier design but suffer some disadvantages. NIC devices, for instance, do not give the ideal zero output impedance. Stages must be buffered to retain designed performance for example, when they are cascaded to obtain higher orders. One the good side is the small number of passive elements needed. Fig. 2 compares the four alternatives showing that no one type is exclusively the best choice.

At this stage we can only suggest that details of designs can be found in the many text books and application notes now available. Very few people would attempt (or even could) design an active filter from basic theory today. There are now available many well-prepared circuit design guides -recommend ''Handbook the we heartily of Burr-Brown Operational Amplifier Active RC Networks". This contains twelve basic circuits, along with quite manageable design procedures for each, in which desired values are put in formulae to arrive at circuit values for low-pass, band-pass and high-pass requirements.



	Realization Technique					
Property	Infinite– Gain Single– Feedback	Infinite- Gain Multiple- Feedback	Controlled Source	Negative- impedance Converter		
Minimal number of network elements	-	+	+	+		
Ease of adjustment of characteristics	-	0	0	+		
Stability of characteristics	+	+	-	-		
Low output impedance	+	+		-		
Presence of summing input	+	-	-	-		
Relatively high gain available	+	-	+	+		
Low spread of element values	+	-	+	+ `		
High-Q realizations possible	+	-	÷	+		
+ indicates the realization is superior for the indicated property						
0 indicates the realization is average for the indicated property						
- indicates the realiz	ation is inferior	for the indicat	ed property			

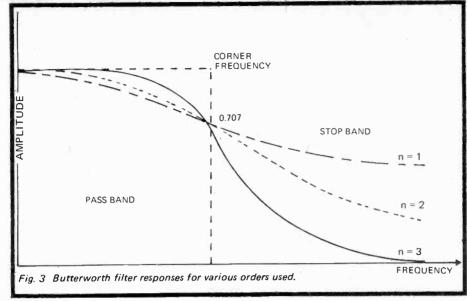
Fig. 2. Comparison table for various kinds of active filter realisations (from Burr-Brown handbook).

FILTER CHARACTERISTIC TERMINOLOGY

filter The ideal edge on a characteristic is usually a sharp 'square'' response with attenuation occuring instantly as the frequency passes through the corner point. It should also have a constant response level at all points in the pass-band regions. As well as the rudimentary RC filter characteristic which falls off at 20 dB/decade from a breakpoint, two other kinds of response are commonly encountered. These are Butterworth and Chebyshev responses. Both derive their names from persons who developed the mathematics involved -(Butterworth designed filters around 1930, Chebyshev developed certain mathematical theory in his study of steam-engine linkages around 1850).

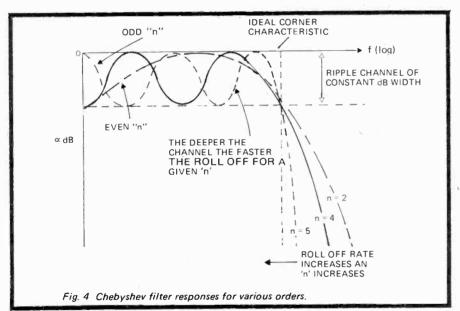
The Butterworth response is said to be maximally flat (that is as flat as possible) in the pass-band region. It has the optimum constancy possible with a given number of available peaking resonances (the complex passive or active filter circuits can be regarded as a group of staggered-tuned resonating sections, each arranged to peak just aside of the others, thereby, providing a broadened response band and a reject region). Fig. 3 shows the Butterworth responses kind of obtainable. Note that each passes through the 3 dB, down half power, point. The order (a mathematical term denoting the number of resonances available) of the filter is denoted 'n' in the chart. A typical roll-off rate is 20 n dB/decade so a fourth order Butterworth response filter (which can

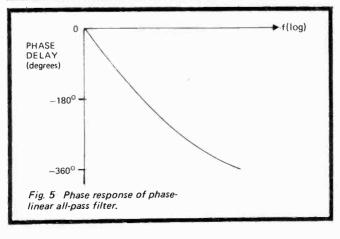
ELECTRONICS -it's easy!



be realised by either passive or active methods) will attenuate at around 80 dB/decade.

Whereas the pass-band response is reasonably constant, the rate of roll-off is not as good as can be obtained if the resonating sections are staggered differently. Other criteria of staggering the resonances can provide higher roll-off rates but only by introducing "ripples" in the pass-band response. When these individual ripples have equal amplitude across the pass-band response Chebyshev polynomials describe the shape, thus giving the name to an alternative response situation. As with Butterworth designs the higher the





R1 R2 INPUT C1 R3 OUTPUT R3 Fig. 6 General configuration of constant time-delay Bessel all-pass filter.

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order the better the roll-off rate as can be seen diagrammatically in Fig. 4. The depth of ripple that can be tolerated also influences the roll-off rate – the smaller the variation that can be allowed the less the roll-off rate. (This can be readily seen by sketching in the required number of ripples of given depth at the appropriate scale).

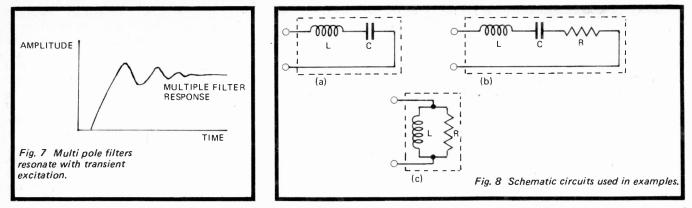
Normally Butterworth or Chebyshev response filters will be of order 1 to 4 but higher orders are possible. These two forms are not the only sophisticated filter responses available: other mathematical criteria could be used to set up workable mathematical for designing equations other networks. These two will, however, meet most demands required and all filter design, as we have seen, is dominated by need to compromise between what is needed and what can be handled mathematically.

PHASE SHIFT AND DELAY FILTERS

These act to provide a phase shift to a signal without selectively attenuating the frequency content. They are sometimes called all-pass filters. The amount of phase shift of practical circuits, however, usually varies with the frequency of the signal even though the amplitude response is invariant. Constant time-delay or linear-phase filters have a reasonably straight (linear) phase response as shown in Fig. 5. The so-called Bessel filter approximates this response using a workable mathematical formulation. Fig. 6 gives the general configuration of such a method realised as an active filter design.

COMPONENTS TO USE

Resistors – In non critical applications the normal 20% tolerance carbon composition resistor may be lf acceptable. tighter filter characteristics are needed then one must resort to more expensive resistors such as 5% or closer tolerance carbon composition. Even better, use



metal-film or wirewound types. It is sometimes permissible to hand choose values from wide tolerance groups in order to produce specific values, but it must not be forgotten that wide tolerance resistors often lack the same degree of time and temperature stability as the more expensive types.

Capacitors – Ceramic disc capacitors can be employed but they are best avoided. Nylon film, polystyrene and Teflon capacitors are much the better to use. When especially long-period filters are needed the capacitance value will be large. In such cases the leakage current due to losses in the dielectric is extremely critical and this rules out, in the majority of cases, using electrolytics.

Op-amp - It is easy to assume all op-amps will provide good active filters but this is not so. The main factor is a low offset current, this especially important beina in long-period filters. As a general rule the more critical the need the better the op-amp should be. When op-amp filters also add gain they should have an open-loop gain at least 50 times the filter gain. Many active-filter design procedures enumerate the requirements of the op-amp.

RESPONSE TO TRANSIENTS

Filters of second order and higher invoke the characteristics of resonating circuits for their operation. In passive filters we can readily identify the inductance and capacitance; in active circuits these may not be so obvious, the mathematical expression showing that resonances do occur.

When a step change in signal is applied to a resonant circuit, the circuit 'rings', that is, the output rises rapidly but then oscillates with decreasing amplitude to the final value as indicated in Fig. 7. The extent to which a resonant circuit rings is decided by the damping provided — the higher the Q of the resonant configuration the greater the ringing effect.

It is not hard to see that higher order filters, therefore, will tend to ring more than the lower order designs when transient signals appear at their input terminals. Transients occur in practice as noise spikes, switching spikes, sudden signal appearance and departure.

THE S-PLANE, POLES AND ZEROS (For the advanced reader) S-Notation

The above study of filters can only act as a guide to filter selection. From there one must turn to the many articles and books available for details. To make good use of such material it is necessary to have a basic understanding of the mathematical methods used. This section is given to assist the more advanced reader. It is possible to get by without this information, provided a suitable configuration and design procedure can be located. Therefore do not be concerned if you are unable to understand this section.

Scanning through even basic. well-organised books on filter (and feedback amplifier) design the terms transfer function, s-plane, poles, zeros and root-locus will be encountered. Sadly, most books omit to provide the background explaining what this is all about. The concepts are not difficult to grasp, any confusion arising almost certainly from the number of synonomous terms used and the fact that the concepts are, perhaps, quite alien to begin with.

We have seen how reactive elements (capacitance and inductance) have apparent resistances of 2π fL for inductance and $1/2\pi$ Fc for capacitance. These terms, however, do not provide information about the phase changes produced with these reactance elements.

Electronic circuit designers use the operator symbol j (mathematicians use i) to denote a phase change of 90° hence, $j2\pi fL$ represents both the reactance value and the phase change. Furthermore $j = \sqrt{-1}$. For capacitive reactance the complete notation is $-j2\pi$ Fc, as the capacitor introduces a 90° phase shift of opposite sign to inductance. Resistance, having no phase shift, nor being frequency dependent is merely R. We can be a little more basic still and use ω instead

of $2\pi f$. ω is the angular frequency being expressed in radians sec⁻¹. (There are 2π radians in one cycle).

When reactance and resistance are mixed we represent the value as a complex number as, for example, R + $j\omega L$. The left-hand part is known as the Real part, the other (that after j) the Imaginary part, the whole forming what is called a complex number.

Where the circuit element is only reactive the complex number representing the impedance reduces to $j\omega L$ or $-j\omega C$ for which the symbol 's' is used instead of j ω . (In some books 'p' is used instead of 's'). A trap can occur here for the -i of $-i\omega C$ indicates a 180° phase shift over j, not a negative quantity in the normal way. To avoid confusion we rewrite $-j\omega C$ as $1/i\omega C$ (which is valid – it comes from multiplying both numerator and denominator $-j\omega C$ by j. Hence we obtain sL and 1/sC as the shorthand inductive and way of writing capacitive reactance in which dependency and phase frequency information are both retained.

Once these terms and concepts are mastered it becomes much more straightforward to write down the transfer function for a frequency dependent network. For example, consider finding the impedance presented by a series, lossless, resonant circuit shown in Fig. 8a.

$$Z = sL + 1/sC = \frac{L(s^2 + 1/LC)}{s}$$

(The individual components of the expression are put on a common denominator, dividing out to get the s^2 terms with unity coefficients).

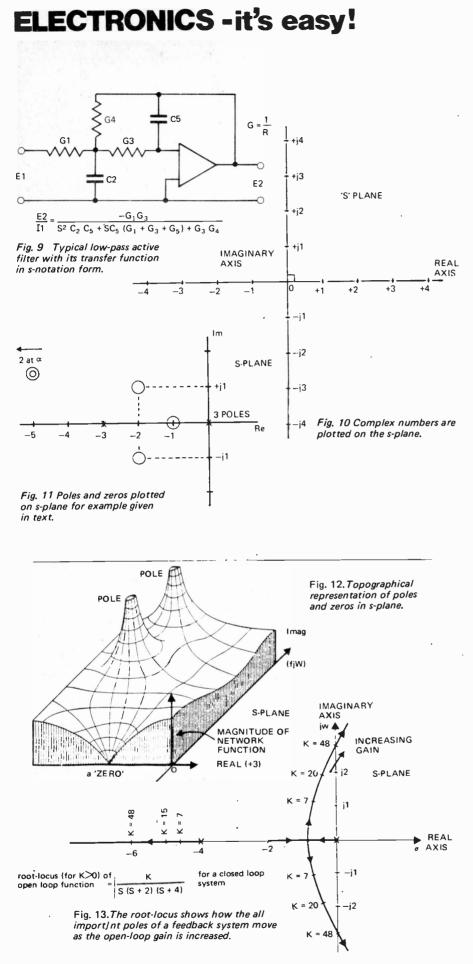
For the series resonant lossy circuit of Fig. 8b.

$$Z = sL + 1/sC + R = \frac{s^2 + R/L \cdot s + 1/LC}{s \cdot 1/L}$$

Again, for the parallel L and R circuit of Fig. 8c.

$$Z = 1/sL + 1/R$$
 from which
 $Z = \frac{R \cdot s}{(s \div R/L)}$

1



It is these forms of expression that are quoted in circuit design books. The

torm of expression is not restricted to two terminal networks - it applies for

all frequency dependent reactive networks. Fig. 9 gives the circuit of a low-pass multiple feedback active filter along with its derived transfer function expressed in 's' notation form.

As these complex numbers possess two parts we must use a two-axis graph to represent them in which the two axes are mutually perpendicular. Thus complex-number quantities need a plane rather than a line to depict a unique number. This plane is known as the s-plane (see Fig. 10). The two axes are usually labelled Re, R or σ for the Real axis and Im, I or j ω for the Imaginary axis, each pair being used respectively.

POLES AND ZEROS

We have seen above how a network of passive elements (active designs also apply) produces a mathematical expression in terms of s notation. As s merely represents j ω and j denotes only phase information we can, whenever s appears, substitute ω (or 2π f) to see how the expression varies in magnitude with varying frequency. Consider the case where a function is given by the numerical example:

$$\frac{(s+1)(s+2+j1)(s+2-j1)}{s^3(s+3)(s+5)}$$

When s = -1, -2 - i1 or -2 + i1, the numerator becomes zero for one of the bracketed terms becomes zero. Hence at each of these frequency values the expression becomes zero. We say it has 'zeros' at these points. Zeros also exist when the singular s goes to infinity in the term denominator. When s = 0 (three times, as it is from $s^3 = s \cdot s \cdot s \cdot s$, -3 or -5, we get the reverse situation for at all of these values of ω the denominator goes to zero making the function rise to infinity. These frequency points are called 'poles'.

Thus the poles and zeros express the peaks and hollows of the function. The position of these can be plotted on the s-plane diagram as shown in Fig. 11. 0 is used for zeros, a cross X for poles. In realisable networks there must be as many poles as zeros — including those at zero and infinity.

Another way to imagine the network characteristic is to draw a topographical representation giving relative height to poles and zeros on the s-plane placed horizontally as shown by the example of Fig. 12. This makes the terms poles and zeros more meaningful in a physical sense.

In the numerical example we avoided, in that case, using a quadratic or higher order term such as $s^2 + 4s + 5$. When these are encountered they must be factorized by finding the roots of the expression – giving the two terms s + (2 + j1) and s + (2 - j1)

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in this case. These are the individual roots, i.e., poles and zeros, of the expression. Note that quadratic elements involving an Imaginary part form mirror image pole or zero pairs called a conjugate pair. If these are lossless (no Real part) they lie on the imaginary axis, if lossy (with Real part) they will be displaced out into the s-plane depending upon the resistive value. Positive values of resistance result in displacement into the left-hand plane, negative resistance gives poles or zeros in the right-hand plane, these halves being denoted LHP and RHP respectively.

Mathematics of complex numbers show that resonant systems with roots lying in the LHP are stable systems, their oscillations die down because to be in the LHP they must contain resistive damping. If the roots lie on the Imaginary axis itself the system is marginally stable – transients will undoubtedly create unstable situations at times even though the system is not absolutely unstable. Note that this situation only arises if the resistive component occurs as negative resistance – oscillators create this condition by the use of an active element.

ROOT LOCUS

When considering the behaviour of feedback systems, such as amplifiers, controllers and active filters, it is highly valuable to plot the changes in position on the s-plane of the closed-loop poles of the system transfer function as the open-loop gain changes. The path traced, by the movement of the poles in this way is called the root-locus. These are often referred to in amplifier and other feedback-mechanism designs and it is, therefore, helpful to at least appreciate what they are. It is, however, not a simple matter to produce them from an original expression; lots of experience is vital.

By way of example the root-locus for a relatively simple transfer function is given in Fig. 13. This tells us that an open-loop gain in excess of 48 places some of its poles in the RHP establishing an unstable situation. The value of the root-locus is that we can "see" the behaviour of the system as the gain is increased and, more importantly, what we should do to the position of the poles most influencing an unstable situation. By altering the transfer function we can place the locus in more favourable situations. This is done by altering original component values where possible or by adding other networks that reduce the effect of the dominant poles those lying close to the RHP.

Next month we look at digital electronics.

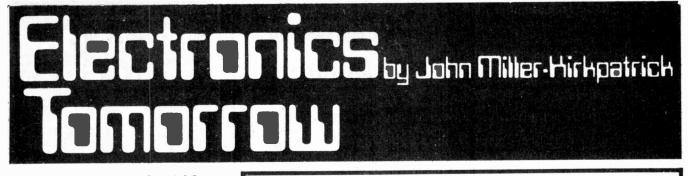
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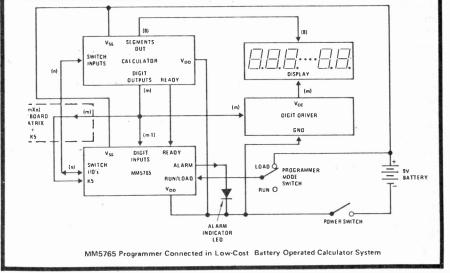
(Mail order only)



MORE MONEY-SAVING IDEAS

Yet again we bring you news of how to get more from a clock chip National less money! for Semiconductor have now released the MM5318 clock chip as a separate chip. You may remember that we mentioned it a couple of months ago as being connected to the TV time display chip. The IC has all of the features of the MM5311 - six digits, seven segment and BCD outputs, 12/24 hour and 50/60 Hz operation. The outputs are multiplexed but controlled. Whereas the MM5311 outputs were under the control of an internal oscillator and multiplexer, the MM5318 outputs are demanded by the data input on three pins. To operate the MM5318 you need a simple oscillator and a BCD divide-by-6 counter, about £1, or less, in cost. (In addition to the displays and drivers you would need with the MM5311). 'How is this a saving?', I hear you ask - the answer is guite simply that the of the recommended price MM5311 is £5.69, but the MM5318 is only £3.36, a staggering difference of £2.33! Thus for about £4.50 you can build a £5.69 clock chip. I can only assume that National have sold such vast quantities of these chips to TV manufacturers that the production costs are very low and that these low costs are being passed on to the low volume market. Large industrial sales can mean that there are no low volume chips available but it could be worth trying to get one if you want to save some money.

To those of you who build computers, data recorders and similar bit processing systems, it is interesting to note that the MM5318 can be strobed by any BCD data highway and deliver its digit data in BCD and seven segment data to any TTL or MOS system. Much to my own personal annoyance I have recently completed the building of a system to feed a pen plotter with a time signal in BCD format for printing, the unit was built in TTL and took many hours and many chips. It could have



been built using a MM5318 and a couple of other chips — less time, fewer chips and less money.

DON'T MISS A THING!

Whilst on the subject of TV clocks and tuners we bring you news of the latest in the 'let's get more ICs in TVs' onslaught. The most elaborate channel-selection device is due on the market from Toshiba early in 1976. No more clock watching so that you don't miss Magic Roundabout, no more boring changing channels when News at Ten starts. With this new chip you settle down with the evening paper spralled open at the TV page and program the TV to change channels at predetermined times, then you can ease back in your armchair to an evening's uninterrupted viewing. The chip displays your selection and lists the time and channel as you choose them, up to 16 programs with the on and off time for each+channel. The clock operates from 4.5MHz (presumably a crystal) and has full power-failure back-up facilities. It would seem that the chip could also be used outside of a television set for programming central-heating, anti-burglar lights and any number of other useful things in the home, do not even bother to start thinking of its applications in the industrial area, and yet this chip has come to life as a chip to make a boring life even more boring.

Programming chips are next year's IN product: microprocessors at under £200, TV programmers, oven programmers, central-heating, etc. The latest is another National chip for programming calculators, or how to put new life into last year's calculator. The MM5765 calculator programmer provides a convenient and inexpensive way of adding a 'Learn mode' to virtually any calculator chip. It memorizes any combination of key entries during the Learn mode, then automatically plays back the programmed sequence as often as required when in the Run mode. Up to 102 characters or operations can be stored in multiprogram sequence blocks. Each program can be executed individually or the operator can make the decision to branch to specific programs.

The MM5765 is coupled to the calculator by paralleling the digit drives and key inputs between the calculator chip and the programmer chip. The calculator encodes a key depression by sensing which digit drive was active when an input was received by one of its 'K' inputs. Most calculators have 6, 8, 10 or 12 digit drives and up to 4 'K' inputs; thus a six-digit calculator with 3 'K' inputs can recognise 18 different key inputs and a 12-digit machine with 4 'K' inputs can have up to 48 keys. The MM5765 can

handle 47 different key positions by having 12 digit inputs and 4 'K' drives. In operation a calculator key is pressed and this will connect one of the digit drives to one of the 'K' inputs, at the same time the digit drive is connected to the digit input of the programmer and to the 'K' input of the programmer. The programmer stores this information as a binary number in six bits (up to decimal 64) as a number 0-46, other non-keyboard entries such as SKIP and HALT give codes in the series 47-63. When the program is put into the RUN mode the programmer will decode this number into a high level output on one of the 'K' lines when the same digit drive is strobed again, in this way the calculator chip is fooled into thinking that a keyboard depression has caused that digit drive to be directed to that 'K' input and will thus execute that function. As we are fooling the calculator chip we can enter any valid key depression or series of depressions and on a scientific machine (such as the NOVUS) the radius of a circle of known area could be ---

00000 000	
HALT	Enter known area
START	start programmer
÷	Divide
3	by Pi
1	
4	
=	Give Result 1
LOG	Log of RES 1
÷	Divide
2	log by 2
=	

ALOG Compute Antilog

HALT with approx result. If you are satisfied with this result you can SKIP to program 2 and use it in a calculation there, otherwise you can continue with program 1 which might now reverse the computation in order to check its accuracy -

START	Cont	PGM	1	with	pre-
	vious				
	result				
Х	Multi	ply resi	ult	by	
=	itself				

- and then by X Pi
- 3
- 1
- 4
- to give comparison with orig area.

If the calculator has memories then it would have been possible to save the original area in a memory and calculate the percentage error in program 1 before continuing with program 2.

The applications of this chip do not end at calculators, many clock

chips use the Digit/'K' method of inputting data and a MM5765 plus a CT7001 could probably make a Lunar Module Command Computer look sick. If the Mostek cooker timer MK50206 is ever fully released it could be combined with the MM5765 to cook a meal or range of meals, all programmed as switch on and off times. You could even use the same combination to set up a selection of TV programmes and on and off times! The price of the MM5765 calculator programmer chip is £9.97, data from National.

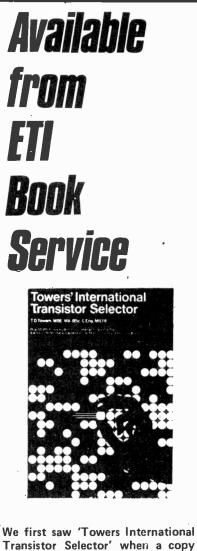
CT7001 et al

The CT7001 series of clock chips and the CT6002 series of watch chips from Caltex were so widespread at the time of the financial crisis of the company that supplies did not start to dry up for some time. Eventually no CT7004s could be found, then 7002s, 6003s, 6002s, and now even the CT7003 and Big Daddy the CT7001 are becoming scarce. This chip caused a great stir some time after its announcement and when its capabilities became known; but just at its peak two things happened. First a lot of faulty chips came onto the market, and secondly Caltex had financial problems. The company finally stopped trading last spring and the future of the 7001 looked grim indeed. The last batch of full-spec devices had just been delivered to the UK and there were no more to come - that batch number 7448 indicated that the 7001s had been made in 1974, it is probable that none were made in 1975 at all.

If you are a TIME/DATE/ ALARM fanatic fear not, because the CT7001 has now become the HCM7001 from Fairchild, who bought the Caltex plants, a few CT7001s are still around at £7.30 (Bywood, Walter Scott and some other retailers) but nobody yet knows the availability or the price of it now that it is the HCM7001. Fairchild also seem to intend to produce the HCM7002, 3 and 4 as well as introducing the HCM 7010 which seems to incorporate most of the family features, further details on the 7010 as soon as Fairchild let me have them.

PERSONAL

Two personal things from me, firstly I would like to buy a cheap teletype terminal to interface to a microprocessor - if you know of one please let me know. Secondly, I would like to wish you all the compliments of the season and an electrifying 1976



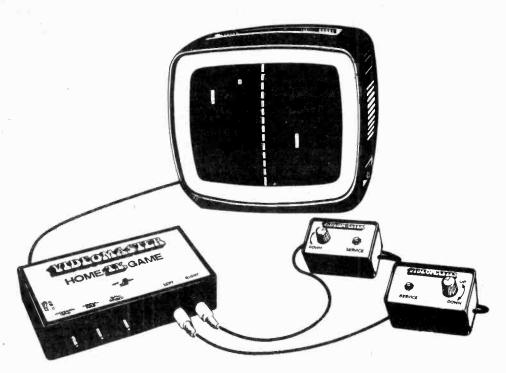
Transistor Selector' when a copy was sent in for review. We were so impressed that we started to supply readers direct. If seems that you share our high opinion as several hundred have been sold to date. This 142-page book gives com-

prehensive details of over 10,000 British, US, European and Japanese transistors including electronic and mechanical specifications, manufacturers and available substitutes.



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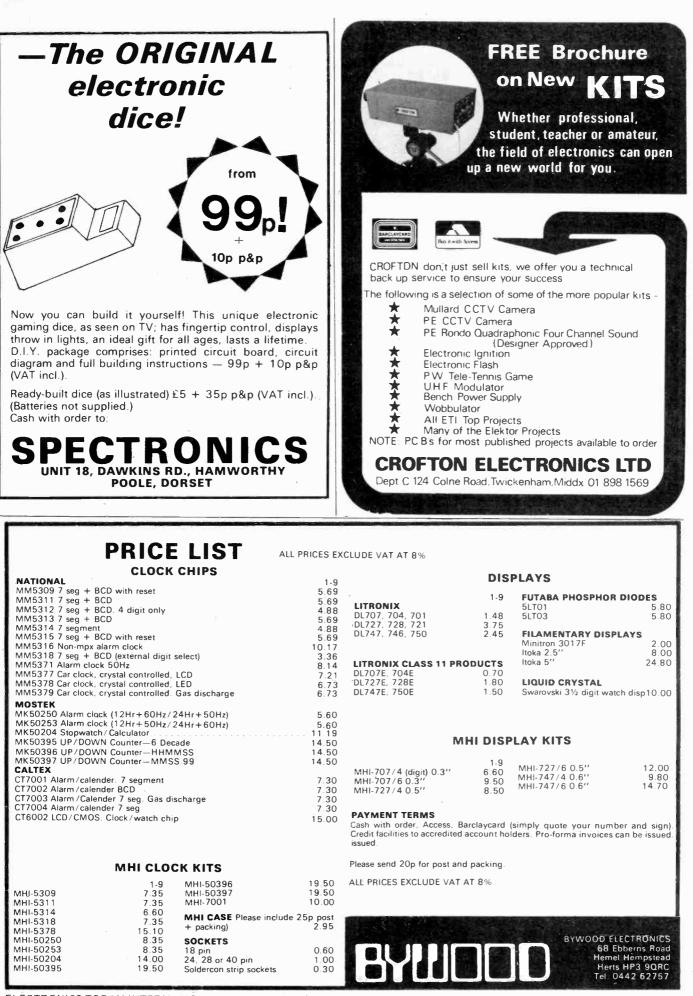
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PROM SELECTOR WALLCHART

Intel have published an attractive wallchart which has been designed to aid the engineer in the selection of programmable read only memories. It lists the significant characteristics of 39 different PROM types manufactured by Intel. These are divided into three groups on the chart: Schottky bipolar, silicon gate EPROMs (ultra-violet erasable) and silicon gate MOS.

For each Intel PROM there is a pin-for-pin compatible mask programmable ROM shown on the chart; therefore, one a design has passed from the development cycle into production, the PROMs can be replaced with ROMs without the need for any circuit or printed board changes.

Also featured on the chart is a comprehensive PROM equivalents guide which lists the important characteristics of PROMs from many manufacturers and gives the Intel equivalent, if one exists. Intel Corporation, Broadfield House, 4 Between Towns Road, Cowley, Oxford OX4 3NB.

RCA TO BUILD METEROLOGICAL SPACECRAFT

RCA has announced receipt of a \$45.6 million NASA contract to design and build eight third generation meteorological spacecraft known as TIROS-N. These are expected to make possible weather forecasts two days in advance with accuracy equal to present day predictions. The first launch is scheduled for 1978.

TIROS-N will weigh about 1,400

pounds in orbit and will stand 17 feet tall. They will be advanced polar orbiting satellites that will carry out not only weather forcasting but also worldwide oceanographic and hydrological services.

The TIROS-N spacecraft will provide day and night cloud cover imagery andwill take atmospheric and sea surfacetemperature and water vapour readings. The device will also measure proton, electron and alpha particle activity around the planet.

ADDENDA

3600 Synthesiser, ETI July 76 The pcb design shown alongside is for the Voltage Controlled Filter.

MOTOROLA SEMICONDUCTOR DATA

Motorola have published a sevenvolume Semiconductor Data Library with a combined thickness of about nine inches giving specifications for all their semiconductor devices. The library also lists the function and the significant characteristics of all EIA registered semiconductor devices.

The product range is divided into

COS-B SATELLITE

The first satellite to be launched by the new European Space Agency was recently successfully put into orbit by a Delta 2913 rocket, from the Western Test Range in California.

This scientific satellite, called COS-B, is a remotely-controlled astronomical observatory which will detect, locate and enable scientists to study extraterrestrial gamma radiation, the most energetic and penetrating form of radiation known.

Gamma-rays come from certain radio-active atoms, from nuclear explosions and from the depths of outer space. They provide a knowledge of the structure of matter, and possibly of the origins of the Universe. Highly energetic gamma radiations from space is detectable even in coal mines after penetrating the earth's rocky crust, but it is found most freely outside the earth's atmosphere in space itself. The gamma radiation which COS-B will detect may be the remnants of exploding stars or quasars, pulsars and other radio and X-ray sources.

The cylindrically shaped COS-B

Continued from page 8

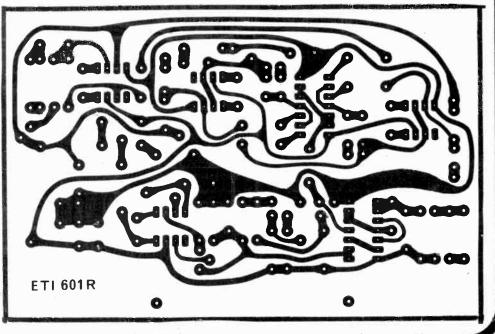
six volumes (1N-2N, 3N-4N, in-house type numbers, MECL, CMOS and linear ICs). The seventh volume functions as an index and an equivalents guide, and it contains outline drawings and an index of application notes.

The Semiconductor Data Library is available from Motorola Distributors for £15. Individual prices include the MECL Data Book for £2, the CMOS book for £1.50 and the Linear Data Book for £2.50.

weighs 278 Kg. including the payload, which is a single experiment assembled from five primary and two subsidiary experiment units supplied by six institutes in four European countries (France, Germany, Italy and Netherlands). COS-B will have an operational life of two years during which time it will study 24 targets for approximately one month each.



After the successful launching and initial performance checks, the Satellite very rapidly commenced its scientific observation programme by looking at the Crab Nebula. Approximately 14 days after launching a genuine Gamma event was observed and with all systems functioning perfectly COS-B is giving every indication of fulfilling its two year mission lifetime successfully.



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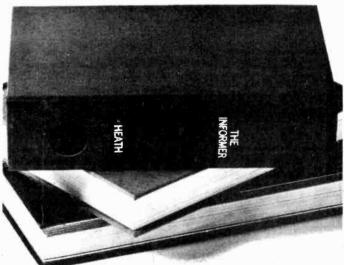
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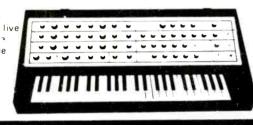
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We stock all the parts for this brilliantly designed synthesiser, including all the PCBs, metalwork and a drilled and printed front panel, giving a superb professional finish. Opinions of authority agree the ETI International Synthesiser is technically superior to most of tocay's models. Complete construction details in our booklet available now, price £1.50, or S.A.E. please for specification.

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