


##  <br> international

## DECEMBER 1975

VOL 4. No. 12.

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THE NOVUS 3500 ELECTRONIC SLIDE RULE - A NEW SCIENTIFIC CALCULATOR

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SEMICONDUCTORS


## PO BOX 6 WARE HERTS




#### Abstract

AL 60 ONLY £3.95 50w. PEAK (25w. R.M.S.) - Max Heat Sink temp $90^{\circ} \mathrm{C}$ - Frequency Response 20 Hz to 100 K Hz - Distortion better than 0.1 at 1 KHz - Supply voltage $15-50$ volts Thermal Feedback Latest Design Improvements Load - 3, 4, 5 or 16 ohms Signal to noise ratio 80 dH - Overall size $63 \mathrm{~mm} \times 105 \mathrm{~mm} \times 13 \mathrm{~mm}$. Especially designed to a strict specification. Only the finest components have been used and the latest solid state circuitry incorporated in this powerful little amplifier which should satisfy the most critical A.F, enthusiast.


## STABILISED POWER MODULE SPM80

SPM80 is especially designed to power 2 of the AL60 Amplifiers, up to 15 watt (r.m.s.) per channel simultaneously. This module embodies the latest components and circuit techniques incorporating complete short circuit protection. With the addition of the Mains Transformer BMT80, the unit will provide outputs of up to 1.5 amps at 35 volts. Size: $63 \mathrm{~mm} \times 105 \mathrm{~mm}$ $\times 30 \mathrm{~mm}$.
These units enable you to build Audio Systems of the highest quality at a hitherto unobtainable price. Also ideal for many other applications including:-Disco Systems. Public Address Intercom Units, etc. Handbook available 10p.

TRANSFORMER BMT80 £2.60
PRICE £3.00


## STEREO PRE-AMPLIFIER TYPE PA1.00

Built to a specification and NOT a price, and yet still the greatest value on the market, the PA100 stereo pre-amplifier has been conceived from the latest circuit techniques. Designed for use with the: AL50 power amplifier system, this quality made unit incorporates no less than eight silicon planar transistors, two of these are specially selected low noise NPN devices for use in the input stages.
Three switched stereo inputs, and rumble and scratch filters are features of the PA100 which also has a STEREO/MONO switch, volume, balance and continuously variable bass and treble controls.
$£ 13.20$

| GUARANTEE | MK 60 AUDIO KIT | TEAK 60 AUDIO KIT |
| :---: | :---: | :---: |
| SATISFACTION OR YOUR MONEY REFUNDED | Comprising: $2 \times$ AL60, $1 \times$ SPM80, $1 \times$ BTM80, $1 \times$ PAI00, 1 front panel, 1 kit of parts to include on-off switch, neon indicator, stereo headphone sockets plus instruction booklets. | Comprising: Teak veneered cabinet size $166^{\prime \prime} \times 111 / h^{\prime \prime} \times 33 /{ }^{\prime \prime}$, other parts include aluminium chassis, heatsink and front panel bracket, plus back panel and appropriate sockets, etc. <br> KIT PRICE: $£ 9.20$ plus 45 p postage. |



## STEREO 30 COMPLETE AUDIO CHASSIS

## $7+7$ WATTS R.M.S.

The Stereo 30 comprises a complete stereo pre-amplifier, power amplifiers and power supply. This with only the addition of a transformer or overwind, will produce a high quality audio unit suitable for use with a wide range of inputs, i.e. high quality ceramic pickup, stereo tuner, stereo tape deck, etc.
Simple to install, capable of producing really first-class results, this unit is supplied with full instructions, black front panel, knobs, mains switch, fuse \& fuse holder and universal mounting bracket, enabling it to be installed in a record plinth, cabinets of your own construction or the cabinet available.
Ideal for the beginner or advanced constructor who
requires $\mathrm{Hi}-\mathrm{Fi}$ performance with a minimum .of
PRICE £15.75
Plus 45p installation difficulty. Can be installed in 30 mins.
postage $\&$ packing
TRANSFORMER $\mathbf{E 2 . 4 5}$ plus 45p
TEAK CASE $£ 3.65$
plus 45p
postage \& packing
postage \& packing

## AL 10/AL 20/AL 30

The AL10, AL20 and AL30 units are similar in their appearance and in their general specification. However, careful selection of the plastic power devices has resulted in a range of output powers from 3 to 10 watts R.M.S.
The versatility of their design makes them ideal for use in record players, tape recorders, stereo amplifiers and cassette and cartridge tape players in the car and at home.

AL10 £2.30. AL20 £2.65, AL30 £2.95


# news digest 

## NEW INOUCTIDN COOKING CONCEPT

Aunew system of induction heating, designed for 'cooltop' electric cookers is currently being demonstrated by RCA. The system, which uses fast-turnoff thyristors operating at 30 kHz , is estimated to halve electricity usage and to offer an efficiency of up to $70 \%$ compared to 35 $45 \%$ using conventional elements.

- The basic concept of the system depends on eddy currents generated in the base of a cooking vessel by the high-frequency current in an induction coil placed beneath the cooker surface. The coil itself does not heat up, so that the working area temperature is only as high as that of the vessel being heated. As a result, heat losses are kept to a minimum.

Unlike conventional electric heating systems, the induction method provides instant power regulation from zero to full power with an 'inertia' even lower than that of the most modern gas cooker.

## CBM SCIENTIFIC

The new CBM SR 7919D features logs, trigs, inverse trigs a memory, . square root, power raising and exponent entry. This calculator costs $£ 18.30$ and is aimed at the 'student scientific' market.


The 8-digit display converts to a 5 -digit mantissa plus 2-digit exponent display. It is battery operated with a mains adaptor as an optional extra at $£ 2.50+$ VAT. The calculator is guaranteed for I year.

## OATA BOOKS

The autumn editions of DATA books are now available. The Transistor DATA book costs $£ 31.60$ (per year) for two issues and lists 19000 types from 128 manufacturers. The Linear IC DATA book is available for $£ 27.60$ (per year) for two issues and lists 7473 devices including 700 new types. The books are obtainable on trial from London Information, Index House, Ascot, Berkshire, SL5 7EU.

## VIEWDATA TRIALS IN JANUARY

A pilot trial of the proposed Viewdata Service will start in January and if the results are satisfactory the service will be made available to the public in 1978. The system will be provided by the Post Office over the public telephone network, and will provide subscribers with data for display in the same format as Oracle and Ceefax. Information is to be provided by organisations independant of the PO, who could be provided with some of the revenue collected by the Post Office. Apart from the VDU, likely to be a colour TV with teletext decoder, the user would require some form of modem and Keyboard.

Compared to broadcast teletext the service will have unlimited capacity. The cost, however, is likely to be much higher - one will have to pay for phone calls and information in addition to the cost of the equipment.

## SUPER-STABLE VO゙LTAGE REFERENCE

A monolithic temperature-stabilised voltage-reference IC, which outperforms standard Zener Diodes by a factor of 20, has been developed by National Semiconductor. The new precision reference, LM199 provides a 6.9 V reference that offers an ultralow temperature coefficient, excellent long term stability, and low noise. Drift is guaranteed to be less than $1 \mathrm{ppm} /{ }^{\circ} \mathrm{C}$.
The LM199 is easy to use. Two leads are connected to the temperature stabiliser, which can be operated at any point between 9 and 40 V . The other two leads are tied to an active Zener. Active circuitry lowers the

Zener impedance to 0.5 and allows the LM199 to operate over a current range of 0.5 to 10 mA with virtually no change in temperature coefficient.
The low dynamic impedance makes the LM199 two orders of magnitude less sensitive to operating currerit than standard 7.5 mA reference diodes. The device is packaged in a four-lead TO46 metal can.

## SOLAR POWER ARRAYS'

The MST 100 solar power module as used on Everest by the BBC Film Crew are available from the Electronic Components Division of Ferranti. With a one-off price of $£ 60$ per module, applications on boats and caravans are also anticipated.

The modules produce a typical output of 1.7 W into a 6 V battery system under the maximum earth surface insolation of air-mass one (approximately $100 \mathrm{~mW} / \mathrm{cm}^{2}$ ) Each consists of 20 semi-circular silicon photo voltaic cells connected in series, mounted in a rugged glass metal sandwich, with a sealed edge, and with the cells embedded in transparent plastic. Ferranti Limited, Electronic Components Division, Gem Mill, Chadderton, Oldham, Lancs.

## FOR REAOERS WHO FANCY A

 BIT ON THE SIDE?

A modification now available on the Solon 65W soldering iron is this adjustable-angle bit. It is claimed to improve accessibility or visibility in awkward applications. GECHenley Ltd., Gravesend, Kent.

## PUSH BUTTON DIALLING IC

The D4037 from General Instrument Microelectronics is a MOS LSI device which converts keyed numbers into pulsed signals identical to those produced by a conventional dialler. The IC has a re-dial facility at the touch of a button, so the dialler can try again if his number was engaged. The device has provisional Post Office approval.

## AMATEUR COMPUTER CLUB

A recent newsletter of the Amateur Computer Club describes a low cost mini computer project. The letter claims the WEENY-BITTER a 256 word 8 bit machine, can be built (without peripherals) for around $£ 50$. Future articles will describe in detail the hardware, programming techniques and upgrading. Enquiries to J.T.C. Aslett, Secretary of the Amateur Computer Club, 7 Doordells, Basildon, Essex.

## CHRISTMAS HOLIDAY LECTURE

'Electronics in Crime Prevention' is the title of this year's Christmas Holiday Lecture to older school children. Geoffrey Philips, Director of Police Scientific Development Branch, will deliver the lecture at the Institution of Electrical Engineers (IEE), Savoy Place, London WC2R OBL on Tuesday 30th December and Wednesday 3lst December 1975 at 2.30 pm .

The lecture will include a number of demonstrations, short films and video tapes and a number of coloured slides.

## TIME ZONE WATCH

The latest watch module from National Semiconductor accurately tracks two time zones, and is designed to make a traveller's digital watch. The MM5880 is a 6 function device which drives four digit LED display. Hours, minutes, seconds and month with-date are controlled by a single push-button.

A second push-button controls the display of time zone. Resetting the second zone time does not affect the time of the first zone.

The MM5880 presents calendar information in the American style (month-date); the MM5860 presents it in European fashion (date-month). Either model can be connected to display 12 hour or 24 -hour time.

## !NTELSAT IV SATELLITE



The first in a series of six new Intelsat IV-A communications satellites was launched on 25th September carrying new technology designed to handle global telecommunications traffic. The first satellite will carry telephone transmissions to and from the United States, Europe and West Africa.

The Intelsat organization is made up of 91 member-nations using the Intelsat global satellites over the Pacific, two over the Indian Ocean and three over the Atlantic. The new satellite will have an average assigned use of 6,000 circuits, or 20 colour television channels. Intelsat IV-A carries a newly-designed antenna that can concentrate signal beams like spotlights into world business centres on both sides of the Atlantic. The new satellite has an overall height of 6.97 meters and a diameter of

2.35 meters. Solar panels, covered with 17,000 solar cells, provide the craft with primary power of 600 watts. The satellite is designed to have an in-orbit life of seven years.


The Science Research Council will be providing a high power Laser for researchers from British Polytechnics and Universities. The Neodymium Glass Laser and associated equipment will initially cost $£ 1$ million. The photo above shows Laser research by Siemens who have succeeded in producing low-loss fibreoptic cable.

## AMI MICROCONTROLLER

AMI Microsystems have introduced a microprogrammable display processor for low-cost processor and control applications. The S9209 contains in a single MOS/LSI chip all the essential elements of a computer's central processor: arithmetic and control system; program and data storage, and data input/output facilities. These elements fit the 9209 for a broad range of specialised calculator and non-calculator fixed program applications, such as in a credit verification terminal or special-purpose industrial timer. Other typical applications include portable data entry devices, low-cost point-of-sale terminals and appliance controllers. For applications requiring increased processing capabilities, several 9209 modules can be connected in tandem, using additional external hardware.

Incorporated in the 9209 is a 6 k micro-instruction ROM organised as $756 \times 8$-bit words; a 256 bit RAM providing four $16 \times 4$-bit data registers, a 4 -bit parallel binary adder; two 4-bit accummulators; 6-bit RAM and 10-bit ROM address registers; input,output and microinstruction decoding and control logic.

The instruction set includes


33 basic instructions for arithmetic processing, data manipulation, testing, data transfer, addressing and I/O operations. Typical instruction cycle time is 15 uS . The S9209 is available in either a 28- or 40-pin DIP, either plastic or ceramic. It has been preprogrammed into several standard special-purpose devices, and samples are available ex stock.AMI, 108A Commercial Road, Swindon, Wiltshire.

## MORE EMI-SCANNERS

A further 15 hospitals in the United Kingdom are to be equipped with the EMI-Scanner brain X -ray system. These latest orders will bring the total of EMIScanner systems in use in UK hospitals to 22 making the National Health Service the largest user outside North America of this advanced British-made brain examination system.

A new, high powered satellite space bus capable of fulfilling a variety of communication missions has been unveiled by General Electric Company of the USA's Space Division at the Telecom 75 World
Telecommunications Exhibition in Geneva. The system is being built for the Japanese Government's experimental broadcast satellite

programme to prove the feasibility of transmitting a high quality colour television service to the entire Japanese terrirtory. This includes the offshore islands of Okinawa and Ogasawara, $1,500 \mathrm{~km}$ and 1000 km southwest and south of Tokyo, as well as the main island chain extending in an arc $2,600 \mathrm{~km}$ long. Down-like transmission frequency will be in the 12 GHz band, with two 100-ATT channels.


The new Extel 8400 range of digital cassette tape recorders interface with most computer equipment, but with prices starting around $£ 900$, not many of us will be buying them to hook up to our calculators!

Continued on page 76



# Spectral amplification is a new trend in Hi-Fi systems. Be one of the first to have a two or three way amplifier by using our simple 

 design...

NO SINGLE loudspeaker can adequately handle the whole range of audio frequencies in high-fidelity reproduction. Thus to obtain the best possible fidelity we must resort to multiple speaker systems where each driver is designed to cover one portion only of the audio spectrum.
This means that some method must be used to divide the audio spectrum, from the amplifier, so that an individual driver only receives the band of frequencies for which it was designed. This is especially important for midrange and tweeter drivers for they are seldom capable of handling frequencies lower than a specified limit without being damaged.

## PASSIVE CROSSOVERS

In simple systems a single capacitor may be used to block low frequencies and pass only highs to a tweeter. But unfortunately such a capacitor only provides 6 dB per octave attenuation. With some tweeters this attenuation is not sufficient to suppress the resonant frequency of the tweeter. The driver could thus be damaged when operated at high power levels. Additionally, the presence of frequencies other than those in the desired passband leads to high levels of intermodulation distortion and a general 'muddiness' of reproduction.
Hence all good multi-way systems use networks which provide at least 12 dB per octave attenuation, in the stop baind, to control the audio band presented to each drive unit. A typical network for a three-way system is given in Fig.1. To keep power losses down in such networks the coils must have dc resistances of less than one ohm. This means that heavy gauge wire must be used, making the coils large and expensive. Additionally the high value of capacitance required would normally call for the use of non-polarized electrolytics, however, there are several disadvantages with these. Firstly, the tolerance on nonpolarized electrolytics is plus or minus $50 \%$ ! This means that a crossover using them could quite easily give a system which had peaks and/or deep holes in the response. Additionally such capacitors have disadvantages such as

limited life, fairly. Jow working voltages and problems due to leakage. Thus all good crossovers use polyester capacitors which, again, are rather expensive.
This all leads to the fact that, for a multi-way high-fidelity system, the crossover can and should be quite expensive. In fact it can cost almost as much as the bass driver!

Many people try to save money by trimming crossover cost - they use lighter wire and electrolytics - and then wonder why an otherwise expensive system does not sound right. The. crossover design is one of the most important features of the whole system - it is better to compromise on a less expensive woofer than to compromise on the crossover.
(Main text continued on page 14)

## ETI 433 ACTIVE CROSSOVER

## SPECIFICATION

| Cutoff Slope(High pass) <br> (Low pass) | $12 \mathrm{~dB} /$ octave <br> $6 \mathrm{~dB} /$ octave |
| :--- | :--- |
| Maximum Output | 2 V rms. |
| Distortion (at 2 V out) | $<0.05 \%$ |
| Noise (Below 2 V) | 86 dB |
| Cutoff Frequency | As required |
| Input Impedance | 47 k |
| Output Impedance (Buffered) | $<10 \mathrm{ohm}$ |
| Minimum Load (Buffered) | 500 Ohm |
| Frequency Response (Sum of all ouputs) |  |
| $\quad 20 \mathrm{~Hz}$ to 20 kHz | $\pm 1 \mathrm{~dB}$ |

Active crossover


## HOW IT WORKS - ETI 433

The input signal is initially amplified by IC1/1. Switch SW1 together with R3 and C2 provide a maximum of 10 dB of boost below 50 Hz at a rate of 6 dB per octave. The frequency at which the boost comes in may be altered by selecting a value of C2 such that its reactance is 220 k at the frequency where the woofer is normally 3 dB down. Thus if the turnover frequency is required to be 100 Hz the value of C 2 should be halved.
If the boost facility is not required R3, C2 and SW1 should be deleted and a link installed between points $\mathbf{B}$ and $C$. The mid frequency gain is set by $\mathrm{R} 2 / \mathrm{R} 1$ to about 13 dB and the input impedance is equal to the value of R 1 , that is, 47 k .
The first high-pass filter consists of IC1/3 where R13, R14, C7 and C8 set the cut-off frequency. The values
of C 7 and $\mathrm{C8}$ required may be found from Table 1. This output is the high range in a two way system, or the mid plus high of a three-way system. This signal, when subtracted from the input signal by IC1/2 gives the bass range output. A second high-pass filter, where C21, C22, R32 and R33 form the frequency determining network, gives the output for the tweeter in a three-way system. This when subtracted from the mid-plus-high signal leaves the mid only as required.
Each of these outputs goes to a level set potentiometer and then is buffered by amplifiers IC2/1 and IC $3 / 1,2,3$. These outputs are now capable of driving loads in excess of 500 ohms. If the crossover is to be used to drive a constant and known load (that is, it is to be used on only one type of amplifier) the buffer
amplifiers may be omitted and the outputs taken directly from the potentiometers.
The full-wave power supply provides plus or minus 13 volts which is regulated down to plus or minus 7.5 volts, by series regulators Q1 and Q2, where zeners ZD 1 and 2 D 2 provide the necessary reference. If the unit is to be powered from the power amplifier C11, 12, and D1 to D4 should be deleted. Resistors R19 and R20 are altered to suit as shown in Table 2. The collector of Q 1 now goes to the positive supply rail of the amplifier and the collector of Q2 to the negative supply rail. If the amplifier supply rail is above plus and minus 20 volts, or if both printed circuit board are being used, (that is it is a buffered three way system) a heatsink must be added to Q1 and Q2.

| PARTS LIST - ETI 433A |  <br> D1.04 Diode in 4001.1 N 40050 si simliar <br> 201,2 Zener Diode 8.2 volt 400 mw |  :These mey be any value between $15 \mathrm{~K}^{\circ}$ and 82k tovar |
| :---: | :---: | :---: |
| Sisior 390 HW |  |  |
|  | O1.O4 Diode $1 \mathrm{~N} 4001,1 \mathrm{~N} 4005 \mathrm{or}$ similar |  |
|  |  |  |
| R13,14,15 \#, |  <br>  | $\mathrm{CLI}_{1,22}$ See Table 1. |
|  |  | IC2 Integrated Circuit PC board ETI 433E |
| *These may be any value between $15 k$ nid ek proviced they are all the same RV 1,2 Potentiometer 10 klln . | The TCA220 is available from Doram Electronics, P.O. Box TR8, Leeds, LS12 2UF. <br> PARTS LIST - ETI 4338 | 3.WAY SYSTEM WITH BUFFERS |
|  |  |  |
|  |  | R40 |
|  |  |  |
|  |  |  |
| C13,14 $\mathrm{C} 15,16$ |  |  |



Fig. 4. Component overlay for complete three-way system. Capacitance values are in microfarads except where otherwise noted.


Response curves of the active filters.

| TABLE 1 |  |
| :---: | :---: |
| CROSS OVER FREQUENCY IN HERTZ | VALUE OF <br> C7,8 or C21, <br> 22 in $\mu \mathrm{F}$ |
| 100 | 0.082 |
| 130 | 0.068 |
| 150 | 0.056 |
| 200 | 0.047 |
| 230 | 0.039 |
| 270 | 0.033 |
| 330 | 0.027 |
| 400 | 0.022 |
| 500 | 0.018 |
| 600 | 0.015 |
| 750 | 0.012 |
| 900 | 0.0082 |
| 1300 | 0.0068 |
| 1500 | 0.0056 |
| 2000 | 0.0047 |
| 2300 | 0.0039 |
| 2700 | 0.0033 |
| 3300 | 0.0027 |
| 4000 | 0.0022 |
| 5000 | 0.0018 |
| 6000 | 0.0015 |
| 7500 | 0.0012 |
| 9000 | 0.001 |

## ACTIVE CROSSOVER



Fig. 5. Printed-circuit layout for the two-way board. Full size $77 \times 90 \mathrm{~mm}$.


Fig. 6. Printed-circuit layout for the add-on three-way board. Full size $77 \times 90 \mathrm{~mm}$.

## ACTIVE APPROACH

Having now established that effective conventional crossovers cost money, we may now wonder if that money could be spent in a better way by using a completely different approach. There is a better way, but until recently it has been much too expensive to be generally used. The
method is to use an electronic crossover, after the preamplifier, followed by separate power amplifiers for each driver. This is feasible because a power amplifier can now be built at a cost which is about the same as that of the passive crossover. Indeed quite a few manufacturers are bringing out systems based on this principle.

Even well-designed crossovers have several serious disadvantages. As we have already said they are expensive, they waste power, they reduce damping factor (in the crossover region damping factor may drop to less than unity) and they only perform correctly into their designed load impedance. Practical drivers exhibit


The basic two-way electronic crossover.


This board provides three-way crossover plus output buffers if required.
their nominal impedance only over a very small portion of their passband, and impedance may well increase to several times the nominal value at the high end of the range. It is possible to compensate for this, to some extent, by using extra networks across the driver (the series RC networks in Fig. 1) - but this adds even more expense. Further, it is very difficult to alter the crossover frequency and also difficult to trim the crossover for best results.
However, if we were to use an electronic crossover incorporating active filters, we overcome most of the problems mentioned in a single stroke. The bulky and expensive inductors and the large and expensive capacitors are eliminated. Damping factor is restored (due to seperate amplifiers being used to drive each speaker directly) and it is quite easy to change or trim the crossover frequency as desired.
Further, as electronic crossovers may have gain, it is quite a simple matter to match the various drivers of the system for sensitivity. This can be only achieved, in passive designs, by attenuating the more sensitive units down to the level of the least sensitive unit. A process which can be quite wasteful of amplifier power.
Of course with active crossovers, as with anything, there are disadvantages. In active filters we generally use operational amplifiers to implement the filters and therefore, bandwidth and noise become considerations. Further, as said before, a separate amplifier is required for each driver or group of drivers - and this can be expensive.
Nevertheless the technique is now quite feasible and is certainly worthwhile. Consequently we have developed a minimum-expense method of building a very fine system based on active filter techniques. This
month we describe a basic two or three way active filter system which may be incorporated into existing amplifiers. To follow next month will be an active filter/amplifier combination based on the 422 amplifier and later still we will be describing how a complete system, including a three-way speaker system, may be built.
For those interested in the design of active filters a full article on this subject will also be published in the near future.

## DESIGN FEATURES

There are several different approaches which may be used in the design of active filters. The first and most commonly used method, is to use separate filters for the bass, mid and high range speakers. This method is capable of compensating for amplitude, if the components are chosen correctly, but not for phase. In fact there has to be a phase change of $180^{\circ}$ between filters to eliminate the hole that would otherwise occur at the crossover point. This is the reason for the tweeter being reversed in phase when a conventional crossover is used in a two-way system.
Another design approach, and the one that we have elected to use, is to use an active high-pass filter to generate the signal for the tweeter, and to subtract this signal from the input signal in a differential amplifier in order to generate the bass output. This substraction process generates the required crossover characteristic with both amplitude and phase taken in to account.
Initially we were worried because the bass output had a slight peak before the cutoff point but the peak is necessary to maintain that response when phase is taken into account. When the output of all channels are
summed the combined response is within plus or minus one quarter of a dB of being flat over the whole range.
With this type of active filter the initial slope can be varied by adjusting the feedback resistor (R13, R32) to give a slow rolloff (Bessel filter) or to give a slight peak and fast cutoff (Chebishev). The sharper the initial cutoff the greater the apparent peak in the bass response.
As several operational amplifiers are required to implement this design we elected to use the TCA 220 triple operational amplifier. This IC, as well as containing three op-amps in the same package, is cheaper than using three separate op-amps of the 741 type or similar. Unlike the 741 type of op-amp, the TCA 220 requires a pull-down resistor on each output and a compensation network. An additional resistor is required to bias each complete IC. The use of the TCA 220 simplifies and cheapens the construction of the filter system considerably.
With active filter crossovers it is a relatively simple matter to alter the gain-versus-frequency characteristic of the filter, within its pass-band, in order to compensate for non-linearities in the associated driver. An example of this kind of compensation is our inclusion of low frequency equalisation for the woofer. Most woofers begin to drop off in the 50 to 100 hertz region. This may be corrected to some extent by adding boost below this turnover frequency. In our design we have provided 6 dB of boost which may be switched in when desired and which is limited to a maximum of 10 dB . The 10 dB limit is necessary to prevent the amplifier being over driven at low frequencies even at fairly low average listening levels.
The turnover frequency may be

## ACTIVE CROSSOVER

selected by means of a simple component change to suit the driver in use. This equalisation technique can effectively extend the low frequency response by another octave, eg, from 50 hertz down to 25 hertz.

## CONSTRUCTION

The configuration of the electronic crossover used will depend very much on the syster. into which it is to be built. The prospective builder should therefore carefully determine his individual requirements before commencing to build a system.
If a fixed load is to be driven (ie, numbers of amplifiers) as would be the normal case, the buffer amplifiers are not required, and the output may be taken directly from the potentiometers.
It must also be decided whether you want a two-way or a three-way system. Rather than use three separate amplifiers to drive the woofer, mid and tweeter drivers separately, it may be better to use a conventional crossover for the $\mathrm{mid} / \mathrm{high}$ crossover
and a two-way electronic crossover for the bass/mid.
Mono or stereo? If a stereo unit is to be built only one power supply is required and the bass-boost switch and the level potentiometers can all be dual units.
If the amplifier has a dual power supply with voltages exceeding $\pm 10$ voits it may be used to power the crossover. This course of action will save one transformer, four power diodes and the filter capacitors.
Mechanical layout is not given as the unit will most probably best be mounted within the amplifier case.
Keep it well clear of the power transformer and mount it using insulated spacers. This is necessary to avoid the possibility of earth loops which will cause a high hum level.
Full component overlays are given for all alternatives but only the circuitry required should be assembled. In a three-way system without buffers one section of IC2 is not used. In this case just leave out the components associated with the
unused section in order to reduce power consumption.
If the unit is being powered from the main amplifier, or a three-way system with buffers is being used, a heatsink is required. The heatsink recommended is a piece of aluminium $60 \times 85 \mathrm{~mm}$ bent into a ' U ' shape and mounted vertically on the end of the board. The transistors should be insulated from the heatsink.
For a stereo system delete the power supply components on one of the boards (up to C15 and C16) and just link the two boards together.

| TABLE 2 |  |
| :---: | :---: |
| MAIN AMPLIFIER | VALUE OF <br> R19,R20 |
| SUPPLY VOLTAGE | 1 k |
| $\pm 10-15 \mathrm{~V}$ | 1.8 k |
| $\pm 15-20 \mathrm{~V}$ | 2.7 k |
| $\pm 20-25 \mathrm{~V}$ | 3.9 k |
| $\pm 25-30 \mathrm{~V}$ | 5.6 k |
| $\pm 30-40 \mathrm{~V}$ | 8.2 k |
| $40-50 \mathrm{~V}$ |  |



## cut the cost of ownership

Dana offer the high-quality 3800 B and 4200 DMMs at quantity prices - and savings only start there. Because they have a one-year specification the costs of calibration, and therefore the total running costs are reduced. What else?

A three-pole active filter and dual-slope integration ensure outstanding noise rejection. As a result you get what is practically unknown in an instrument in this price bracket: freedom from errors caused by all types of noise - not just mains hum.

The 3800 B is a $31 / 2$-digit ( 2,000 counts) DMM, and the 4200 is a $41 / 2$-digit ( 20,000 counts) DMM. Both instruments measure resistance, a.c. volts, d.c. volts and d.c. current.

One-year stăbility is outstanding: a highly stable zener diode voltage reference guarantees the stability of both DMMs to be within their rated accuracies for at least a year after calibration. D.c. accuracy is assured by an extremely high input impedance: 1,000 megohms on the 2 -volt range.

Both models have isolated b.c.d. outputs available, and the 3800B will feed data out at up 60 readings a second. At $£ 145.00$ for the $31 / 2$-digit DMM and $£ 220.00$ for the $41 / 2$-digit DMM one of these has to be the offer you can't refuse.


Others measure by us.

Makers of High-quality Instrúments: Frequency Counters Digital Voltmeters Waveform Generators Communications Test Equipment Microwave Counters
Frequency Synthesizers

## ETI PCB's

| TTLE | PROJECT $\% 0$. | ISSUE | $\begin{array}{\|l} \text { Board } \\ \text { no. } \\ \hline \end{array}$ | total | TITLE | $\begin{gathered} \text { PROJECT } \\ \text { NO. } \end{gathered}$ | ISSUE | $\begin{gathered} \text { BOARO } \\ \text { NO. } \end{gathered}$ | total | TITLE | $\begin{gathered} \text { PROJECT } \\ \text { MO. } \end{gathered}$ | ISSUE | $\begin{array}{\|l} \hline \text { BOARD } \\ \text { NO. } \end{array}$ | total |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Int. Stereo Amp. | Int. 25 | Oct. 1975 | InI. 25 | £4.21 | Discrete S0 Oecoder | 420 E | June. 1974 | 4205 | $£ 1.69$ | Music Spmilhesiser | 601 | Aug. 1974 | Noise |  |
| 25 watis/chan. |  |  |  |  | Int. 422 Ster eo Amp | 422 | Aug. 1974 | 422 | £2.97 |  |  |  | Cont: |  |
| Oual Power Supply | 105 | Apr. 1972 | 014 | ¢1.48 | 50 watis/Chan. |  |  |  |  |  |  |  | 60 IF | ¢1.22 |
| Wide Range Voltmeter | 107 | Top Project No. 1 | 022 | ¢1. 09 | Plus Two Add on Decoder Amp | 423 | Mov. 1974 | 423 | $91 p$ |  |  |  | $\text { Trans. } 2$ |  |
| Voltmeter I.C. Power Supply | 111 | No. 1 Jan. 1973 | 111 | £!.43 | Oecoder Amp | 424 | 0ec. 1974 | 424 | £1. 62 |  |  |  | $6016$ | ¢1.74 |
| Thermocouple Meter | 113 | Oec 1973 | 113 | ¢1.57 | Unit |  |  |  |  |  |  |  | 6014 | £2.36 |
| Oual Beam Adaplor | 114 | 0ct. 1974 | 114 | £1.00 | Stereo Rumble Filler | 426 | Јan. 1975 | 426 | 76p |  |  |  | 6015 | £2.54 |
| Impedance Meter | 116 | June 1975 | 116 | 1.01 | Graphic Equaliser | 427 | Jan. 1975 | 427 | ¢1.96 |  |  |  | Exi. |  |
| Digital | 117 | Oct. 1975 | 117 A | ${ }_{68 p}$ | Colour Organ | 428 | Feb 1975 | 428 | £2.10 |  |  |  | Inpul |  |
| Voltmeter |  |  | 1178 | $68 p$ | Simple Slereo Amp | 429 | Mar. 1975 | 429 | $76 p$ |  |  |  | Mod. |  |
| Simple Freq. counter | 118 | Nov. 1975 | 118 | 68 p | Line Amp | 430 | July 1975 | 430 | $76 p$ |  |  |  | 6011 | £1.06 |
| The Revealer | 213 | Top Prolect No. 1 | 213 | 68p | Fluorescent lamp dimmer | 508 | Nov. 1972 | 011 | 76 |  |  |  | Key Cont. |  |
| Brake Light Warning | 303 | Oct. 1972 | 007 | ${ }_{68 p}$ |  |  |  |  |  |  |  |  | 601 m | 97p |
| Automatic Car | 305 | Aug. 1972 | 019 | 99 \% | Photgraphic Timer | 512 | Aug. 1972 | 023 | $76 \square$ |  |  |  |  |  |
| Theth Alarm International Batiery | 309 | Hov. 1973 | 309 | 98p | Tape Slide Smchroniser | 513 | Top Projecl No. 2 | 026 | $76 p$ |  |  |  |  |  |
| Inernational Batiery | 309 | Hov. 1973 | 309 | 981 | Oigital Slop Watch | 520 | Jan. 1974 | 520A | £2.05 |  |  |  | Sup. 601 M | £3.04 |
| Tacho Timing Light | 311 | Oec. 1974 | 311 | 80 p |  |  |  | 5208 | 50p |  |  |  | Ally. |  |
| Electronic ignition | 312 | May 1975 | 312 | £1.72 | Low Cost laser | 524 | Mar. 1974 | -524 | E1.30 |  |  |  | Board |  |
| CDI/Tacho |  |  |  |  | Push Botton Oimmer | 427 | Feb. 1975 | 527 | 96 p |  |  |  | $601 P$ | f1.54 |
| Car Majarm | 313 | Mar. 1975 | 313 | 67p | Electroaic One Arm | 529 | Sept. 1975 | 529A | £2.32 |  |  |  | 6017 |  |
| Auto Amp | 314 | May 1975 | 314 | 75p | Bandit |  |  | 5298 | ¢2.32 | Hadar Intruder Alarm |  |  | 702 | £1.13 |
| ET Four Input Mixer | 401 | Top Project Mo. 2 | 005A | 67 p | Temp. Controller Pholo Timer | 530 532 | Mar. 1975 Sept. 1975 | 530 532 | . 85 | Int. F.M. Tuner | $75 i$ | Sept. 1975 | 751 | ¢2.75 |
| Super Stereo | 410 | Top Project | 025 | £1.51 | Digilal Oisplay | 533 | Oct. 1975 | 533A | 68 | Light Oinner |  | June 1975 |  | .68p |
|  |  | Mo. 2 |  |  |  |  |  | 5338 | 68 | Print Timer |  | Top Project |  | 68p |
| 100w Guilar Amp | 413 | Feb. 1973 | 413 | ¢1.73 | Music Synthesiser | 601 | Aug 1974 | Oc. |  | Inler Com. |  |  |  | 68p |
| Masier Mixer | 414 | Top Project Mo. 1 | 414 A 4148 | ¢ 1.14 £ 1.52 |  |  |  | 6014 | £2.54 | Intruder Alarm |  | Apr. 1975 |  | 94p |
|  |  |  | 4148 | £1.52 |  |  |  | mixer 6018 | £2.54 | Digital Alarm | Timtronic | Sept., 1973 | 5017 | £1.24 |
| Slage Mixer | 414 | July 1975 | 4140 | £1.89 |  |  |  | Trans |  | Clock |  | Nov. 1975 | AA/BB |  |
| The Over Le. 0 Mixer Pre-Amp International 420 Four Channel Amp | $\begin{aligned} & 417 \\ & 419 \\ & 420 \end{aligned}$ | Nov. 1973 <br> Dec. 1973 <br> Apr. 1974 | 4145 417 | ${ }_{\text {¢ }}^{1.78}$ |  |  |  | 601C | £1.62 | Bicycie Speedometer |  | June 1975 |  | $\begin{gathered} \mathbf{f} 1.68 \\ 68 \mathrm{p} \end{gathered}$ |
|  |  |  | 419 | 918 |  |  | : | 6010 | 90p | At the lime of geing to press we have stocks of all the above boards Allow $7 / 10$ days for delivery by posi, Boards also available for other published designs at 6 p a sq, inch + WAT and $\mathrm{P} \& \mathrm{P}$. large slacks of components also available. |  |  |  |  |
|  |  |  | 4204 | 76p |  |  |  | key |  |  |  |  |  |  |
|  |  |  | 4208 | £1.11 |  |  |  | Board |  |  |  |  |  |  |
|  |  |  | 4200 | £ 1.21 |  |  |  | Cont: |  |  |  |  |  |  |
|  |  |  | 4200 | £1.21 |  |  |  | 601 E | £3. 72 |  |  |  |  |  |

## CROFTON ELECTRONICS LTD.

Dept. C, 124 Colne Road, Twickenham, Middx. 01-898 1569


## ELECTRONIC IGNITION (ETI APRIL 1975)

I built the ET! electronic ignition according to your articles in April and May this year and found that IC2/3 and IC2/4 (Nand Gates of the 7402) packed up after a short while. Can you offer any help?

Also could you tell me if the waveform on the outputs of these two gates should be symmetical.

- H. F. Slough

The failure of the 7402 may be due to the loading (in the 1 state), 1 y transistors Q3 and Q6. This can be cured by adding two diodes in series with output of IC2/3 and IC2/4 as shown in the diagram. These diodes should be 1N4005 or germanium transistors used as diodes (bases and collectors joined). The use of silicon diodes increases slightly fabout 150 mA ) the input current, due to not turnings off the output transistor as hard. Germanium transistors do not alter it. Germanium diodes have too high a voltage drop at the 20 mA required. Adding these diodes reduced the current of the 7402 from 30 mA to 19 mA (our prototype).

The waveform on the output of IC2/3 and IC2/4 should be symmerical except that it is being gated on and off due to the regulation.

## TEXAS REPLIES

May I comment briefly on the 'Electronics Tomorrow' column in your August issue. Your author suggests that it is Texas Instruments' policy to 'sell the ideas, get the orders and then think about designing the product'. Specific reference was made to the TMS 3952 clock IC and passing reference to the TIFAX Decoder system.

The TMS 3952 was, in fact, never released and has still not been. As your author says, "TI have never made a noise about it' so we cannot really be accused of selling the idea too aggressively. With any Cl product it is necessary to make pre-production samples for evaluation and it is inevitable that these sometimes get into the hands of those who are not quite as discreet about them as we ourselves try to be.


Turning to the TIFAX Decoder system, your very comprehensive article (in the July issue) on Teletext referred to our press conference on 6th May in which we stated our positive commitment to designing and producing a set of LSI devices dedicated to Teletext decoding-well in advance of getting any orders! Since then we have adhered to our commitment and we shall shortly begin to sample, to manufacturers, the first part of the system which is a very fast, high capacity, character-generating ROM.

In the final analysis, of course, it is essential to get the orders. The life of any product must depend on this as does the profitability and, indeed, the life of the company which makes the product.

- Richard B. Mann, Market Communications Manager, Texas Instruments Ltd, Manton Lane, Beds.


## CALLING ALL SWIMMMING POOL OWNERS

My Son-in-law wrote to me from the States saying that over there they use automatic, mains operated plants that produce chlorine and all that they add is common salt. The unit has a power pack giving 7.5 V D.C. and all the operator does is to see that if the ammeter drops back in reading, and then he simply adds salt. He could not find out what is used for the electrodes and where such materials can be obtained, and this is what I want to know so that I can experiment to produce an automatic unit for myself.

Possibly some of your readers will have the answers?

- T.P.S. Dixon, Yellowell House, Horley, Banbury, Oxon.

Use ETI TIMING MODULES to build this
DIGITAL STOPWATCH


## CLOCK MODULE ETI 551

The main module is a nine digit crystal-controlled clock with an easily varied counting format -- it can count in "hrs hrs, mins mins secs secs, $1 / 10$ th, $1 / 100$ ths, $1 / 1000$ ths," or in 000,000.000 secs, or with slight modifications, in some other formats such as hours, minutes and fractions of minutes, or in 00.0000000 hrs .

## LATCH MODULE ETI 552

The second module of which any number may be added to one basic clock module, is a nine digit latch which can either contain the same data as the clock module, or can store the number in the latter when a switch is pressed or a control pulse received. This enables one to 'freeze' the time in the clock module without interrupting counting - a stopwatch 'split' facility.

## COMPARATOR MODULE ETI 553

The third module can give out a pulse, set a Flip-Flop or reset the clock module to zero. Any number of these modules may be connected to one basic clock module. One comparator could be used to sound an alarm after a preset time, or several could be connected together to switch a video tape recorder on and off at preset times, or to control some machine or an industrial process. (This will be described next month.

## WHAT IF NINE DIGITS IS TOO MANY?

Although the counter, latches and comparators can all accommodate nine digits, those who only need to 'see' a few digits should not be put off. Because of the elegance of the SR (shift register) counting system used a nine digit counter requires nearly the same number of components as a six digit counter. In fact, on the PCB layouts we have only provided sufficient components to drive 6 -digit displays. Those who require more will have to use a few additional components.

The 6 digits to be displayed can be chosen from any of the 9 digits: laboratory timers usually display minutes down to milliseconds; stop-watches can display hours to tenths of seconds, or tens of minutes to hundredths of seconds, whilst owners of video and other tape recorders could choose tens of hours to seconds. As yet we know no perfectionist who wants tens of hours to milliseconds!

## COUNTING WITH A SHIFT REGISTER

The conventional design for a multi-digit counter used to be a chain of BCD counters, and a large arrangement of switches which sequentially feed the data from each stage of the counter to a single 'multiplexed' BCD output, suitable for driving the displays. This is the system found in most clock ICs, in
some watch ICs and in nearly all circuits for clocks which used TTL.

However, as first realised three years ago, it is more efficient to make multi-digit counters by storing a number in a shift register and circulating' it through a binary adder adding one to a digit or setting it to zero at the required times. This system is found in most timing ICs designed in the last few years (e.g. the Mostek MK5030).

Figure 1 shows a block diagram of an SR counting system. The main advantages of the system are that as the data is handled sequentially (digit by digit) it is already in a format suitable for interfacing to multiplexed displays, and that increasing the size of the counter only requires increasing the shift register capacity and slightly modifying the control circuitry.


Fig. 1. An SR counter.

Figure 2 is identical to Fig 1 but the circuit blocks are filled in with actual components. For simplicity we will first discuss a clock using the "hrs hrs, mins mins, secs secs, $1 / 10$ th, 1/100th, $1 / 1000$ ths" format. This project does not have a separate "How it works" section; rather this is explained as we cover the various design options. The number in the counter is stored in a $9 \times 4$ bit shift register (4006 (1) and (2)). The output of the $S R$ is returned to its input via a 2 digit and carry, 4 bit, binary adder (4008 (1)), and a set of 4 AND gates (4081 (1)). In one pass around the loop any digit may be left unchanged, incremented by one, or reset to zero. All that needs changing is the carry input to the adder and the common control to the 4 AND gates.

A 10 kHz signal goes to the clock input of a 4017 decade counter and the 4017 produces 10 consecutive output pulses on its 10 output pins, Q0 to Q9, such that only one output is 'high' at any one time. One in ten of the 10 kHz pulses which clock the 4017 is blanked by the Q9
output using a two input NOR gate ( $1 / 4$ of 4001 (1)). This signal is used to clock the shift register.

Thus in one millisecond the number in the SR is circulated once, and the 4017 goes through a complete cycle. The clock polarities are such that the SR is clocked (that is, new data is latched into its first stage, and new data appears at its output) at the same instant as a change occurs in the 4017 outputs.

The data present at SR outputs during the time in which QO is high is defined as the value of the least significant digit, called DO, that present while Q 1 is high defines the value of D1, and so on. Q0 to Q8 can be used to drive display "digit drivers", and the SR outputs drive the display segments via a 4511 BCD-to-seven-segment decoderdriver.

## "SET DIGIT TO ZERO"

A digit is recirculated unchanged until there is a 'carry' from the previous (less significant) digit. At this point it is incremented by ' 1 ' by the adder, and the new value recirculated. If however the digit in



Fig. 2. Circuit diagram of the clock. Figs. 3,4,5,6 and 7 show other circuitry on the clock board.
 DIGITAL
STOPWATCH

Main text continues on page 24


198000140014





Fig. 5. Decimal Point Driver.

## ETI TIMING MODULES

## DIGITAL STOPWATCH

question had already reached its maximum value ('5' for the tens mins and tens secs digits, ' 9 ' for all the others), instead of the value being incremented, it should go to zero, and a carry be transmitted to the next digit. This "Set to Zero' signal is derived from various gates as shown. In addition, pushing the Reset button for more than one millisecond sets all digits to zero.

## "CARRY"

Before describing this part of the circuit it is worth looking at the operation of the 4027 J-K Flip-Flop. The $J$ and $K$ inputs are used to set (logical '1') or clear (logical '0') the Q output as follows:

If Q is ' 0 ', and J is ' 1 ', Q will go to '1' a few nanoseconds after the clock input goes from ' 0 ' to ' 1 '. This occurs whether the $K$ input is ' 1 ' or ' 0 '.

If Q is ' 1 ', and K is ' 1 ', Q will go to ' 0 ' just after the clock input goes from ' 0 ' to ' 1 ' (irrespective of the state of the $J$ input.) This a ' 1 ' on the $J$ input will set the $Q$ output on the positive edge of the clock pulse; a ' 1 ' on $K$ will similarly clear it. If $j$ and K are both ' 1 ', Q will toggle between ' 1 ' and ' 0 ', changing at every positive transition of the clock pulse. Q can be independently set or cleared by a '1' on the set or reset inputs.

The Q output of Flip-Flop A ( $1 / 2$ of 4027 (1)) is set to ' 1 ' by pressing the Start button. This ' 1 ' is then used to set the Q output of Flip-Flop B ( $1 / 2$ of 4027 (2)) to ' 1 ' at the start of QO time (the period during which QO of the 4017 is high). This Q output is the 'carry' signal, and when DO (the digit whose value is present at the SR outputs during QO time) is clocked back into the SR at the end of QO, it is incremented by 1 once every loop of the SR, i.e. once every milisecond.

Until DO has reached its maximum value, D1 must not be incremented, and the carry must be 0 , every time D1 is clocked back into the SR (at the end of QO time.) The condition that DO is not at its maximum value is indicated by a ' 1 ' on the MAX output of the "Set to

Zero" circuit, which goes to the K. input of Flip-Flop B and cancels the carry at the beginning of Q1 time. If DO has reached its maximum value, then MAX is low during QO time, the carry is not cancelled and increments D1 at the end of Q1 time.

If both DO and D1 have their maximum values, the carry will propagate to Q 2 time, etc. Thus if the counter is counting, the carry is set during 00 time, and propagates 'down' the number until cancelled. It is also cancelled by Q 8 at the beginning of 09 time so that the counter behaves itself if it overflows.

## 10 kHz SOURCE

The 10 kHz signal is obtained by dividing the output of a 5.12 MHz crystal oscillator by 512 (29) using the first nine stages of a 402014 stage binary counter (see Fig 3.) The disadvantage of using a crystal of this frequency is that a transistor oscillator has to be used rather than a CMOS inverter oscillator (which doesn't have enough gain at this frequency to sustain oscillation). The advantages are that crystals
over 5 MHz are cheaper, inherently more stable, and physically smaller than lower frequency crystals.

## DISPLAY DRIVING

The digit and segment driving circuits are straightforward (Fig. 4). The SR outputs are fed to a 4511 BCD-to-seven-segment decoderdriver, which drives the display segment lines through current-limiting resistors. The maximum recommended continuous output current per segment on the 4511 is 25 mA , and with a 8 V supply (typical output voltage of a 9 V battery) the resistor value required is 150 Ohm .

The digit driver as shown will drive 6 digits. The digits required are selected by wiring jumpers between six of the nine 00 to 08 outputs and the six 4050 hex buffer inputs. For convenience on the PCBs these connections have been laid out in a standard DIL format so that the jumpers can be wired onto a pin-header and the required selection 'plugged in'. QO corresponds to the least significant digit, i.e. milliseconds, and 08 to the most significant, i.e. tens of hours.

The component layouts for the clock, latch and display boards.


Here are three examples:

| 00 | O-O | Do |  |  |
| :---: | :---: | :---: | :---: | :---: |
| 01 | $0-0$ | D1 | (a) is wired to display |  |
| 02 | $0-0$ | D2 | mins | D5 |
| Q3 | $0-\mathrm{O}$ | D3 | secs | D4 |
| 04 | $0 \longrightarrow$ | D4 | secs | D3 |
| 05 | $0-0$ | D5 | 1/10 | D2 |
| 06 | $0 \quad 0$ | N.C | 1/100 | D1 |
| Q7 |  | 08 | 1/1000D0 |  |
| 00 | 00 | Do | (b) is wired to display |  |
| 01 | ) | D1 |  |  |
| 02 | 0 | D2 | hrs | D5 |
| 03 |  | D3 | hrs | D4 |
| 04 |  | D4 | mins | D3 |
| O5 |  | D5 | mins | D2 |
| 06 |  | N.C. | secs | D1 |
| 07 |  | 08 | secs | D0 |
| 00 | 0 | D0 | (c) is wired |  |
| 01 |  | D1 | (c) is to dis | $\begin{aligned} & \text { wired } \\ & \text { play } \end{aligned}$ |
| 02 |  | D2 | hrs | D5 |
| Q3 |  | D3 | mins | D4 |
| 04 |  | D4 | mins | D3 |
| 05 |  | D5 | secs | D2 |
| 06 |  | N.C. | secs | D1 |
| 07 |  | O8 | 1/10 | D0 |

The 4050 buffers are needed to drive the 75492 hex digit driver The latter contains six Darlington Pair NPN transistors with the required resistors and can supply digit currents up to 250 mA per digit.

Decimal points may be lit if required by wiring jumpers from the required 4050 buffer output to the decimal point driving circuit (Fig. 5). Thus decimal points are wanted after hrs and mins on a display (hrs hrs . mins mins . secs secs) for right-hand decimal point digits, the Q 5 and Q 7 time dec pts have to be lit, and jumpers are connected from the outputs of the 4050 buffers corresponding to Q5 and Q7 (D2 and D4) to two of the diodes in the dec pt driving circuit.

## DISPLAYS

The driving circuitry will drive any common cathode LED displays, such as the DL33MMB ( 0.08 in ), DL704 or DL704E (0.3in) and FND500 or DL750 (0.5in and 0.6 in )

## POWER ON CLEAR

A simple circuit is provided which sets the counter to ' 0 ' and puts it into the stop mode when power is turned on (Fig. 6).

## THE LATCH MODULE

The essence of the latch module is that it contains a shift register and display driving circuitry identical to that of the clock module. There is also switching circuitry to enter into the latch SRs the same data as is entered into the clock SRs, or to circulate the data unchanged Finally there is circuitry to time the change between these two states. The data routing is accomplished by a 4019 Quad AND-OR gate which is basically a 4 pole 2 -way switch which routes data from the inputs of the clock $S R$ to the inputs of the latch SR when KA is ' 1 ' and KB is ' $O$ '. It connects the latch SR input to its output when $K A$ is ' $O$ ' and $K B$ is '1'.

The 4027 J-K Flip-Flops time the changeover of the 4019 switch. These Flip-Flops can be wired in two modes. In the 'hold-transparent' mode the latch starts 'transparent' (the data it contains follows the contents of the clock module). When the latch push-button is pressed the latch stays transparent until the beginning of the next word (until the next 00 time occurs) and then the 4019 switches over and the number in the $S R$ is circulated


Fig. 8. Circuit diagram of the latch.

## ETI TIMING MODULES

## DIGITAL STOPWATCH

and displayed: the latch 'holds' the time at which the button was pressed, accurate to within +1 msec . Further pressing of the latch button causes the module to alternate between the 'transparent' and 'hold' states.

Wiring the J-K Flip-Flops the second way makes it operate in the 'hold-hold' mode. In this mode the latch starts off 'holding' its contents. If the latch button is pressed, in the middle of the next 00 time the 4019 is switched to loading new data into the SR. In the middle of the following 00 time the 4019 switches back to recirculating and holding the data. Thus in this mode the display always appears 'frozen', but each time the latch button is pressed the contents of the latch are 'updated' to show the number in the clock module at that moment. This mode is expected to be found most useful in applications where the latch is controlled by an external source. In either mode the "Power On Clear' circuit is connected so that when power is switched on, or when the Master Reset button is pressed, the latch is loaded with zero, and in the first mode it is set to its 'transparent' state. In both modes, two NAND gates ( $1 / 2$ of 401.1 (1)) eliminate any effect of contact. bounce in the push-button switch.

Selection of one mode or the other is governed by wiring up three jumpers or switches. On the PCB there are holes for three horizontal jumpers and three vertical jumpers. If the horizontal ones are connected (indicated by faint dotted lines in Fig. 8) the latch will operate in the hold-hold mode. If the vertical ones are connected (indicated by bold dashed lines in Fig. 8) it will operate in the hold-transparent mode. The display selection and driving circuitry used in the latch modules are identical to those used in the counter module.

Some users may find it best to attach a display to only the latch module, thus saving cost while still obtaining a Stop-watch 'split' action. As mentioned earlier, any number of latch modules may be connected to one clock module. The outputs from the clock module are simply attached to several modules instead of just one.


Fig. 9. How to make connections through the board.

## CONSTRUCTION

The modules are best assembled on double-sided.PCB's as shown, due to the circuit complexity. Connections between copper on the top and bottom of the boards are made using component leads where possible. Otherwise small pieces of tinned copper wire are used (see fig 9 above).

The recommended method of mounting the CMOS IC's is to use Soldercon pin sockets which can be soldered to both sides of the board (and are inexpensive). The ICs may be soldered in but this makes fault-finding very difficult. On the component side of the board, care must be taken only to apply solder to the flat side of the Soldercon Pins.

Connections between modules are made very simple, and easy to check, using flat cable and 24 pin plugs and sockets (using 24-way pin-headers and Soldercon pins in a 24-pin DIL layout). Connections between modules and display PCBs are also made using flat cable and 14-way DIL pin-headers and IC pin sockets.

The PCB's were designed so that one clock module, one latch module and either two DL33 6 digit displays or one DL704 or FND500 8 digit display, with batteries, switches, etc, would all fit into the smallest of the new Vero plastic cases, using a transparent red perspex front window in the space normally occupied.by an aluminium panel. If greater complexity is required, one of the larger boxes in the same range may be used.

## GETTING HOLD OF THE

 COMPONENTSSintel (53 Aston Street, Oxford), who designed these modules can sell the following at reduced prices.
Stopwatch Module Kit (SMK) . . £19.95 (This comprises the PCB and all the parts normally soldered to it: CMOS, SKTS, CRYSTAL, Resistors, Caps, etc. etc...)
Latch Module Kit (LMK) . . . . £11.25
(This comprises all parts plus Vero pillars for support and flat cable for interconnection with SMK)
CHOICE OF DISPLAY MODULES
DL704 Display Kit (DL7K) (.3') . £6.96 (This kit comprises a display PCB and 6 DL704).
DL33 Display Kit (DL3K) (.1"). . £7.10 (This kit comprises of a display PCB and 3 DL33 3-Digit packs)
FND500 Display Kit (FNDK) (.5').
. . . . . . . . . . . . . . . . . . . £11.15 (This kit comprises a display PCB and 6 FND500)
CASE
Verocase . . . . . . . . . . . . . . . £3.17
(This case is big enough to accomodate one SMK and one LMK plus two DL3K or one of DL7K or FNDK, and the case is supplied with a red perspex panel).

ALL PRICES ARE INCLUSIVE OF VAT AND P\&P

## EXTRAS

A stop-watch which gives time to millisecs is great but the average human finger can't do much better than $1 / 20$ th of a second. We expect some constructors will need to actuate their modules using electronic, rather than mechanical, pulses.

## ELECTRONIC START-STOP

Electronic starting and stopping can


Fig. 10. A simple circuit for using the sound of a starting gun to start the Stopwatch.


The insides of the stopwatch. The clock board is at the top, the latch beneath. Here we show one display, but a second display module will plug in directly. A small saving
can be made by soldering the flat cable directly onto the boards rather than using the LSI plugs as shown.
be achieved by feeding positive going pulses to the set and reset inputs of the clock module 4027 A Flip-Flop. These pulses do not have to be 'clean' but it should be noted that the state of this Flip-Flop is not determined if both these inputs are high simultaneously.

Obvious electronic timing systems would be ones where the start time is derived from a signal driving a solenoid operated starting gun, or from sound pulses. Below is a simple circuit of a suggested sound operated start system (see fig 10).

A simplified suggested light operated start or stop is as shown in fig 11.


ETI TIMING MODULES
DIGITAL STOPWATCH


Fig. 12. Controlling the counting with a toggle switch.

COUNTING CONTROLLED BY TOGGLE SWITCH OR CONTINUOUS SIGNALS
If the count enable line (normally driven by the Q output of the 4027 A Flip-Flop, on the clock module) is high the module will count, if it is low it will hold its value. Thus the counting can be controlled by driving this line from an external signal instead of from the flip flop output. For example the counting can be controlled by a toggle switch:


Fig. 13. Two clock modules can be wired up to give a chess clock.

## ENDLESS POSSIBILITIES

Virtually any timing necessity can be satisfied using a system made up from the ETI TIMING MODULES. In our clock we used two displays - but we could have used just one display show the contents of the latch). We could have made a more complicated Stopwatch by adding more and more latches. With ten latches and ten switches you could give individual times to the first ten runners, swimmers, racing cars .....

The possibilities mushroom when you consider the comparator module as part of a system. A clock becomes an alarm clock or timer. If you use the output to reset the clock you have a programmable pulse generator with a period ranging from milliseconds to tens of hours.

## CHESS CLOCK

To build up a system which compares the accumulated time spent in each of two states, such as a chess clock; one could use two clock modules with their count enable lines connected like this:

They should also have their reset lines connected together. When using more than one clock module in one system, such as this chess clock, considerable savings can be made by constructing the crystal oscillator and 4020 divider on only one of the modules, and taking the 10 KHz signal from this to the other boards.

## $\star$ A FANTASTIC BARGAIN OFFER FROM THE UNITED STATES *

# 6 DIGIT DIGITAL CLOCK KIT 

To celebrate the opening our new subsidiary company, SABTRONICS INTERNATIONAL," we are slashing the price of this fine quality digital clock kit which would normally sell for over $£ 12.00$. Rest assured, you will get only first quality, prime tested components. SATISFACTION GUARANTEED OR YOUR MONEY BACK!

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6 -PNP silicon driver transistors
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9-Carbon resistors
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* BRIGHT DISPLAY
$\star$ SLOW SET
* FAST SET
* TIME HOLD

NO ELECTRONICS KNOWLEDGE REQUIRED TO BUILD THIS KIT. All you need provide is a $9-15$ volt / $200-\mathrm{mA}$ transformer and a case of your choice (or leave it uncased and it still looks good)

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Deluxe digital alarm clock.



## IIII

THIS NEW 'SCOPE is likely to replace the 10-102 as Heathkit's most popular scope. The $10-4540$ is long and low and looks really up-to-date with its flashy blue and white front panel and bigger screen. The case is big - 8 inches high, 13 wide and 17 deep - but it is easy to stack equipment on top (the handle is recess-mounted).

The $10-4540$ really is a new scope-our model was one of the first in the country. In fact it was only a week before we had to send this article to the printers when we received the big box from Heathkit! Consequently we have not given the scope much of a trial, but as far as we can see the spec given is accurate

The scope offers a bandwidth of 6 MHz with a sensitivity of $20 \mathrm{mV} / \mathrm{cm}$. The FET front end gives an input impedance of $1 \mathrm{~m} \Omega$ with low capacitance (38pF). The attenuator provides sensitivities between 20 mV and $10 \mathrm{~V} / \mathrm{cm}$ in nine ranges (1-2-5 steps).

A front panel control gives variable attenuation between calibrated points. Up to 400 V dc can be handled. The risetime and overshoot are quite good for a 'scope in this bracket.

Horizontal sensitivity is $0.25 \mathrm{~V} / \mathrm{cm}$ and the front panel socket will accept frequencies up to 100 kHz ; attenuation of $\times 1, \times 10$, and variable is provided

Seven timebase ranges, calibrated in a 1-2-5 sequence between $200 \mathrm{mS} / \mathrm{cm}$ and $2 \mu \mathrm{~S} / \mathrm{cm}$, can be switched into variable control. The input signal is routed to the trigger circuits direct (for DC), via a capacitor (for AC) or through a low pass filter. This filter enables the scope to trigger on the frame pulses of a complex TV waveform for signal tracing. Trigger level control and a socket for external trigger are also provided on the front panel.

TTL ICs are used in the trigger and sweep circuits; other circuitry is all transistor. Four main boards (see photos on the right) carry $80 \%$ of the electronics. These sections are the vertical amplifier, the


10-540 SPECIFICATIONS-

## HORIZONTAL:

Sensitivity: $.25 \mathrm{~V} / \mathrm{cm}$.
Bandwidth: DC to 100 kHz
Impedance: $1 \mathrm{M}!2 / 50 \mathrm{pF}$.
Ext. Horiz. Input: XI and X10 attenuator GENERAL:
CRT: 5DEP31F, $8 \times 10 \mathrm{~cm}$, green, medium persistence phosphor, 5 in round, flat-tace tube Accelerating Potential: Approx. 1.5 kV Graticule: Painted, $8 \times 10 \mathrm{~cm}$
Power Requirements: 110-130 VAC or $220-260 \mathrm{VAC}, 50 / 60 \mathrm{~Hz}, 35$ watts. Dimensions: $\sin H \times 13$ in $W \times 17$ in $D$ PRICE KIT: E99.90 inc VAT and delivery ASSEMBLED: E.174.96 inc. VAT and delivery

Full details available upon request from
Heath (Gloucester) Lid.
Bristol Road.
Gloucester GL2 6EE

The London Heathkit Centre 233 Tottenham Court Road. London W1P GAE
horizontal amplifier, the trigger and sweep circuits, and the power supply and c.r.t. drive. A tuner section wafer switch and tag strip. mounted behind the front panel holds the time base oscillator. The only other components off the PCB's are switches and pots. transformers, one big electrolytic can and the c.r.t.

The power supply section provides stabilised voltages for the sweep and vertical amplifier boards so the instrument remains accurate with changing mains voltages.

## CONSTRUCTION

At the top of the kit package is a 1,55 page manual to help with building up the kit. The manual tells us not to unpack anything except pack 1, the components for the first board. One by one each of the components has to be located and ticked off individual. Heathkit go carefully at one side of the bench.

Then the "Step-by-Step assembly begins. Each component is inserted one by one, and again ticked off individually. Heathkit go to great lengths to be unambiguous

# HEATHKIT $10 \cdot 4540$ SCOPE 

-most parts have four references the part number (e.g. Q103), the device number ( $E L$ 131), the Heathkit number (417-241) and a drawing of what it looks like. This method, however, does lead to confusion. The simple steps are made to appear difficult and much time is spent working out the instructions when a look at a good diagram can say everything in one glance.

Heathkit's method means that building up the kit is a little boring and takes twice as long as it would if the instructions were less detailed.

Building the boards took, including time for ticking the manual, about eight hours (including coffees and distractions) but building the chassis and wiring up the controls and boards took over twice as long again!

The only tools needed are a soldering iron, wire cutters, pliers, and screwdrivers. The quality of the components is very high. The only instrument needed for setting up the scope is a high impedance multimeter, but a low capacity probe and signal generator make things easier. The three boards carrying presets are mounted above the chassis with lots of access, and diagrams in the manual explain clearly all steps of calibration. The two transformers are mounted in a cage which can be swivelled to minimise hum on the trace.

## IF IT DOESN'T WORK

If the project doesn't work first time there's lots of help in the manual. There are several Trouble Shooting Charts and each board has an "X-Ray View" diagram. The circuit diagram is big ( 2 ft across). and each board has a voltage diagram. Heathkit even tell you how to check the transistors and diodes for faults.

The scope costs $£ 99.90$ in kit form.

If you do come to build up an 10-4540 start at the back of the manual and read how it works first. Then you can learn quite a lot by following the circuitry as you build up the scope.



LOGIC PROBE A basic tool for digital servicing.

THE SERVICING of digital equipment is greatly simplified by the use of a logic pulser and logic probe, for these two instruments enable one to follow circuit operation stage by stage.

## THE PROBE

The probe must be capable of detecting pulses as short as 50 nanoseconds (for TTL operation) and
make them visible. It was found that readily available linear ICs were not suitable as they are too slow and required dual supply voltages. Neither could CMOS be used as it also is too slow, for testing TTL gates, and its threshold voltages are not consistent. Further, TTL could not be used as it cannot withstand the voltages used with CMOS logic. This virtually means that the only devices that are suitable are discrete transistors.


The logic probe we built in a solder tube.

## HOW IT WORKS

The probe consists of two independent voltage level detectors which, via pulse stretching monostables, drive light-emitting diodes to give a visual indication of the logic state being monitored. Transistors Q1 and Q4 form the low level or ' 0 ' detector, transistors Q5 and Q6 the high level or '1' detector whilst the remaining components form the pulse stretching monostables and visual indicators.
The high level detector works as follows. If the input level is below about 2.5 volts ( 1.3 volts above the level set on R17 by transistor Q5) transistor Q6 will be cut-off. When the input level rises above 2.5 volts, transistor Q6 will turn on, as will Q7, causing LED 2 to light - indicating a ' 1 '. The transition at the collector of Q7 will, at the same time, be passed to Q8 turning it off. The current which was flowing through Q8 will now flow via R22 in to the base of Q7 holding it on even though Q6 may by now have stopped conducting. After fifty milliseconds the charge on C 2 will leak away via R19, 20 allowing Q8 to conduct. When Q8 conducts it robs the current from the base of Q7 turning it and the LED off. However should the voltage at the tip of the probe still be present Q6 will still be turned on holding on in turn Q7 and the LED.
Resistors R11, 12, 13 and 14 set the operating conditions of Q5 such that the threshold voltage is optimized for either TTL or CMOS. As CMOS logic works on supply voltages ranging from five to fifteen volts, transistor Q5 has been arranged to track the supply so that the correct threshold is maintained at all times.

The low level detector works in exactly the same fashion except that it is inverted in order to detect pulses which approach within 0.45 volts of the negative line (TTL only). Each PNP transistor and each NPN transistor have been replaced with their complements. In this case Q4 sets the thresholds and the circuit operates exactly as stated for the high detector. Note that the diodes have also been reversed.


Fig. 3. Component overlays for the two comparators showing interconnection wiring.

| PARTS LIST - ETI 120 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| R3,18 ${ }^{\text {R4,15,19 }}$ | Resist or | 680 | 4/4, W | 5\% |
| R10, ${ }^{\text {R }}$ | ", | 1 kg | " | " |
| R1,9,12,17 | ", | 2 k 7 | ", | ", |
| R5,14,22 | " | 3 k 3 | " | " |
| R2.16 | ", | 8 k 2 | ". | ", |
| R7, 21 | - | 10 k | ", | " |
| R8,11 | " | 27 100 k | " | " |
| ${ }_{C}^{C 1}{ }_{C}{ }^{2}$ | Capacitor | $\begin{gathered} 0.47 \mu \mathrm{~F} \\ 10.0 \end{gathered}$ | $\begin{aligned} & 25 \mathrm{~V} \\ & 25 \end{aligned}$ |  |
| D1,2 | Diode | IN914 or s | llar |  |
| $\begin{aligned} & \text { Q1, 7, } 8 \\ & \text { Q4, } 3,6 \\ & \text { Q5 } \end{aligned}$ | $\begin{gathered} \text { Transistor } \\ \ddot{\#} \\ ", \end{gathered}$ | $\begin{aligned} & \mathrm{BC} 177 \\ & \mathrm{BC1} 107 \\ & \mathrm{BC} 179 \\ & \text { BC109 } \end{aligned}$ |  |  |
| SW1 | Switch | Two pole, miniature | $0 \text { pos }$ <br> op |  |
| PC boards 2 Probe case LED 1, 2 L 2 Alligator | ff ETI 120 e text) t emitting ps or Ezy-h | TIL209 or | milar |  |

## CHARACTERISTICS

## PULSER - ETI 121

- Will source, or sink, up to 500 mA .
- Operates on supply voltages from 5 to 15.
- Suitable for both TTL and CMOS.
- Power supply drain less than 15 mA under worst case conditions.
- Press for ' 1 ' release for ' 0 '. High impedance at other times (>1 M).
- Will drive capacitive loads up to 1000 pF .
- Protected against accidental reversal of supply leads.
- Duration of pulse 500 nanoseconds.


## PROBE - ETI 120

- Pulses as narrow as 50 nanoseconds will be detected.
- Stretches narrow pulses to 50 milliseconds for ease of detection.
- Operates on supply of 5 to 15 volts.
- Suitable for TTL or CMOS.
- True ' 1 ' and ' 0 ' level detectors. Neither LED is alight if the circuit is faulty or the probe is not making contact.
- Current drawn from the circuit is less than 20 microamps.
- Current drawn from power supply (one LED alight) 12 mA on 5 volts, 35 mA on 15 volts.


Fig. 1. Circuit diagram of the logic probe

## LOGIC PROBE



Fig. 2. Printed circuit board for the logic' probe (2 required). Full size $23 \times 66 \mathrm{~mm}$.


Fig. 4. Linking required between the two boards.

As both high and low logic states must be detected, a discrete transistor voltage-comparator circuit was designed to detect each state separately. These comparators must not load the circuit under test as CMOS is sensitive to current and capacitive loading. In our prototype the current drawn was a maximum of 19.7 microamps for a high, and 10 microamps for a low.
In both comparators the transistors associated with the pulse detector are turned on by an input level that exceeds the comparator threshold.
As transistor turn-on time is much faster than turn-off time, using the transistors in this way ensures the highest possible speed of operation for the particular types of transistors used. Additionally, the delay in turning off assists by lengthening the pulse, thus ensuring more reliable triggering of the monostable on very short pulses.
The input transistors Q1 and Q6 are protected against breakdown, due to excessive base-emitter voltage, by diodes D1 and D2. The diodes are also required to ensure that Q1 and Q6 remain conducting even when the probe tip is taken to the supply voltage.
Transistors Q3 and Q8 are also protected against reverse base-emitter voltages by R4 and R19 respectively.
In operation the probe will light LED 1 if a low level is detected, LED 2 for a high, neither LED if the point being monitored is at ground potential or a poor contact is made with the tip, and both LEDs will light if there is a pulse train present.

A single pulse input will be lengthened, by the monostables, to 50 milliseconds with the pulse polarity being indicated by the LED which is illuminated. Thus even single pulses as short as 50 nanoseconds may readily be detected.

## CONSTRUCTION

We assembled our probe in a case made from a solder tube. This is commonly available from component shops for about 35p (containing Ersin Multicore Solder). Any probe case or tubing with a diameter of 23 mm and a length at least 90 mm (excluding nozzle) will do. The solder tube has a detachable plastic end-cap which supports SW1 and the LEDs. SW1 is used to hold a small name-plate in position as shown in Fig. 6. . Two IEDs are mounted into the end plate, together with SW1, and after soldering leads to the LEDs they should be passed through the holes in the plate, and the plastic end-piece, and secured in position with a drop of epoxy cement. Another hole is drilled in the stopper through which is passed the two supply-voltage leads.

A removeable nozzle has to be made and for this we used a polyester resin filler (Isopon or any of the car body repair fillers is ideal). First saw of the original nozzle and line the inside of the tube with grease or cow gum. This stops the filler making a permanent joint. Then mix some filler and spread it for about 25 mm down the inside of the tube. Roughly mould the nozzle shape around the polythene tubing which comes with the solder and bed this firmly in the end of the tube. After a couple of minutes the nozzle can be whittled
into shape. After hardening remove the nozzle and clean up the inside face (saw off the rough moulding). Remove the polythene tubing and in the hole R15 and the probe tip can be fixed with more filler. Use a darning needle or one of the needles made for - sewing up knitting as the tip. Do not leave more than 15 mm protruding or the needle is likely to break. Finally the nozzle can be filed and sanded to give a neat appearance.

The electronics are built on two printed circuit boards. The two boards are identical and care should be taken to use the correct overlay for each board as different transistors are used and some components are reversed on the two boards. Note particularly diodes D1 and D2 and capacitors C1 and C2. Also note how the two boards are linked together and that the supply rails are reversed. No difficulty should be experienced if the printed-circuit boards and the component overlay as specified are used.

Connect the leads from the stopper assembly to the boards. Position the boards together, copper side to copper side, with a piece of insulating material between them. Make sure that the board assembly will fit into the tube without bending the sides. Cut a piece of cardboard or plastic $75 \times 85 \mathrm{~mm}$, roll it into a tube and fit in the probe body. Now fit the board assembly into the tube - it may be necessary to dress the sides of the boards with a file to obtain a neat fit.
The tip may now be connected and both ends screwed into position. Finally, alligator or, better still, Ezy-hooks clips should be fitted to the supply leads.


Fig. 5. Artwork for the nameplate on the probe.


Fig. 6. How the probe ends are constructed.

# Electronic timekeeping Sintel delivers: designs - kits - components 

Parts for the ETI Stopwatch System:

See this month's article

## STOPWATCH

Complete Kit for Stopwatch without Latch
Conents Verobox 75/410J - Red perspex front panel - Manganese batteries - clips - Transistors - Diodes - Wiring Pins - Screws - Sockets - 14 pin Piñ-Header CMOS - Resistors - Capacitors - Crystal - PCBs - Trimmer - $3 \times$ DL33MMB displays - Full instructions
 (for other display options see below)

## STOPWATCH with one LATCH

Complete Kit for Stopwatch with one Latch ('Split' - display free') facility With two sets of $\mathrm{c} \times \mathrm{OL} 33 \mathrm{MMB}$ displays. etc
Without Displays (see other options below)

## LARGE DISPLAY OPTIONS

The Stopwatches may be used with larger displays - OL704E (0.3") or FND500 (0.5") N.B. When using larger displays we recommend that a segment current boosting circuit be used to obtain higher brightness; details supplied with any parts ordered.

Displays Display PCBs (will hold up to 8 digits)
$\begin{array}{llll}\text { (each):DL } & \text { O4E85p } & \text { For DL704Es } & £ 1.35 \\ \text { FND500 } & £ 1.50 & \text { For FND500s } & £ 1.35\end{array}$

Note that only one of these display PCBs, together with displays, can fit in the 7510 J Verobox supplied with the complete kits.

## FURTHER APPLICATIONS

For those who want to vary or extend the Stopwatch design, components are available from us in various sub-divisions - see box by article for 'PCB Module Kits'. Send a lárge s.a e. for fuller tist. Some possibilities suggested are:

- Multiple Latch Systems
- Multiple Counter Systems
- Alarm Systems
- Man Power Supplies
- Chess Clocks

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 6 digit Alarm Clock Kit with $0.5^{\prime \prime}$ FND500 Displays $\ldots . . . . . . . . . . . \begin{aligned} & \\ & \mathbf{2 6 . 7 0}\end{aligned}$

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## DISPLAYS

FND 500 Very attractive $0.5^{\prime \prime}$ Red Common Cathode LED - only ....... £1.50 DL704E $0.3^{\prime \prime} \mathrm{C}$. Cathode LED 85p MAN3M O. $13^{\prime \prime} \mathrm{C}$ Cathode LED 5LT01 4 digit clock display - 0 " "green phosphor diode ILIXCO $3^{1 / 2}$ digit $0.5^{\prime \prime}$ Liquid Crystal display, with socket
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$£ 9.40$
DISPLAY PCBE - each of the four display PCBS below is $£ 1.35$
For clocks with 4 or 6 displays: PCB for DL704E's; PCB for FND500's
For counters, up to 8 digits. PCB for DL704E's; PCB for FND500's

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MC14511CP £1.95
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| CD4001AE | 0.17 | CD4028AE | 0.74 | CO4052AE 0.77 | CD4082AE | 0.18 |
| CO4002AE | 0.17 | CD4029AE | 0.94 | CD4053AE 0.77 | CD4085BE | 0.59 |
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| CD4016AE | 0.46 | CD4040AE | 0.88 | CD4067BD 9.31 | ME14510CP | 1.26 |
| CD4017AE | 0.83 | CD4041AE | 0.69 | CD4068BE 0.18 | MC14511CP | 1.95 |
| CD4018AE | 0.83 | CD4042AE | 0.69 | CD4069BE 0.18 | MC14516CP | 1.03 |
| CD4019AE | 0.46 | CD4043AE | 0.83 | CD40708E 0.18 | MC14518CP | 1.03 |
| CD4020AE | 0.92 | CD4044AE | 0.77 | CD40718E 0.18 | MC14520CP | . 03 |
| CD4021AE | 0.83 | CD4045AE | 1.15 | CD4Ó72BE 0.18 | MC14532CP | 1.18 |
| CD4022AE | 0.79 | CD4046AE | 1.10 | CO4073BE 0.18 | MC14553CP | 4.07 |
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# Elipliext <br> LOGIC PULSER 

Companion instrument to the logic probe.
ALTHOUGH the logic probe used alone is a very valuable piece of digital test equipment, it is limited by the fact that it can only observe the logic states that occur naturally within the piece of digital equipment under test.
The logic pulser is a further valuable tool that is used in conjunction with the logic probe. It's function is to override the naturally occurring stateat the particular circuit node under test. That is, if the circuit node is normally at the ' 1 ' state, the pulser will drive that node to a ' 0 ' for a very short period when the microswitch is pressed. If the circuit node is normally at a ' 0 ', the probe will drive it to a ' 1 ' for a very short period when the microswitch is released. Thus it puts a short pulse into the circuit node regardless of it's normal state when SW1 is pressed and released.
A fairly powerful pulse is required to override the normal logic state of a circuit node and care must be taken to ensure that the devices either driving, or being driven from that node are not damaged. This is achieved by making the pulse of very short duration. In our probe the pulse width is 500 nanoseconds. Thus although the pulse is of high current the energy released is insufficient to damage normal logic devices.
The probe must be suitable for driving either TTTL or CMOS that is, it must operate from a supply ranging from 5 to 15 volts, it must be capable of operating into loads having a capacitance as high as 1000 picofarads and must supply a current pulse of around half an amp. All these conditions are fulfilled in the ETI 121 Pulser and the prototype has been tested by causing it to generate several hundred thousand half amp pulses without any problems. The probe is quite capable of pulling two (in parallel) high-power TTL 'zeros' to a ' 1 ' level and this is the most severe condition it has to meet.
At the same time as providing high level pulses, the pulser should not draw too much supply current as some CMOS supplies may not have much additional capability. Under worst-case conditions the ETI Pulser drew a maximum of 10 mA .
The probe is capable of overriding a normal logic state but is not capable of overriding a point that is connected to ground or to a supply rail. Thus by pulsing a node and at the same time looking at that point with the logic probe it is possible to tell if that point


A basic tool for digital servicing.

## is shorted to either rail.

The logic pulser combined with the logic probe is thus capable of performing stimulus - and - response testing of both TTL and CMOS logic and of determining the exact nature of a fault at a particular circuit node.

## CONSTRUCTION

Construction is greatly simplified if the printed circuit board of Fig. 2, is used. This should have the components assembled to it in accordance with the component overlay. Note particularly the polarity of C 1 , and the connections of the microswitch such that the normally-closed terminal of the switch is connected to the base of transistor Q1. Also make sure that a red lead is connected to the positive rail of the board, and a black lead to the negative rail, to facilitate later connection.
We used the same probe case for the pulser as for the logic probe. The probe tip again uses a darning needle and the microswitch SW1 is mounted into the plastic filler tip as follows. First check switch to determine what the contact arrangement is. Attach colour coded wires to the switch, to aid later identification. If you use a solder tube as a case you have to saw
off the nozzle and cut a slot for the microswitch. Keep the switch as far forward as possible to give more room for the pcb. Line the tube end with grease so that the filler will not stick. Wire or tape the switch into position and fill the end of the tube with filler. Make sure there is a hole, for fixing the probe tip by inserting the polythene tubing which comes with the solder. Roughly mould a nozzle. After a couple of minutes this can be carved with a knife. Then remove the polythene tubing and insert the needle. Fix this into the correct position using more filler. When the filler is hard the nozzle can be removed for filing and sanding into shape.
Connect the probe tip and microswitch leads to the board and, after insulating the inside of the case with cardboard or plastic as previously described, insert the board into the case. Pass the supply leads through the plastic end piece and then fit both end pieces and secure them in position. Finally attach Ezy-hooks or crocodile clips to the supply leads.
Keep the supply leads as short as is reasonably possible as excessively long leads will degrade the performance of the pulser.


Internal construction of the pulser.


## HOW IT WORKS

The pulser is activated whenever microswitch SW1 is pressed. This switch controls the state of a flip-flop formed by transistors Q1 and Q2: The flip-flop is necessary to prevent contact bounce of the microswitch from having effect.
The output transistors of the probe, Q5 and Q6, which in turn are controlled by Q3 and Q4 are both normally off. However when the microswitch is pressed Q2 rurns off and the rising voltage on its collector is coupled, via C3, to the base of Q4 turning it on. This in turn, turns on Q5 pulling the output to the positive rail. This generates a ' 1 ' pulse if the point under test was at a ' 0 ' level. Resistor R12 provides a current limit of around 500 milliamps. Due to the small value of C3 the pulse output is only about 500 nanoseconds long, short enough so that there is insufficient energy to damage the device under test.
When the switch is released Q2 turns on and the negative-going edge is coupled to Q3 by C2 turning it on. This turns on Q6 causing the output to be pulled to the negative rail. This gives a ' 0 ' pulse which, like the ' 1 ' pulse, is only 500 nanoseconds long.
The output from the probe is taken via the paralleled combination of R13 and C4 where C4 carries the current and R13 discharges C4 between pulses. This network protects the probe against the condition where the probe is inadvertantly connected to a voltage which is above or below the logic supply rails.
Resistor RS isolates the high current pulse from the power supply, capacitor Cl providing the actual current needed.

Fig. 2. Printed circuit board for the pulser. Full size $23 \times 65 \mathrm{~mm}$, or $23 \times 85 \mathrm{~mm}$. If this board is made to $23 \times 85 \mathrm{~mm}$ (same scale as shown herel it will not fit into a solder tube case. In this case the board should be reduced to 65 mm in length (as shown below). To save confusion when ordering the board ask for ET/121-85 or ET/121-65


Fig. 3. Component overlay for the pulser.


Fig 4. The tip of the logic pulser, made from a darning needle, a microswitch, and a small quantity of car body filler.


PARTS LIST



Compries should be sent to: ETI/Heathkit Competition, 36 Ebury Street, London SWiw
OLW to reach us by 30 th December 1975.

## HOW TO ENTER

The four waveforms at the top of the page can be found in the circuit below:

Where would you put the positive and negative probes from the vertical input (y axis) to get these traces? Mark your answers in the boxes on the coupon. Use the letters A, B, C and D in your answers.

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# JANUARYETI 



Have you noticed how your ETI is going up in sizze? There have been 76 pages for two years - until. recently. In November we added 16, in December 8 , and January's issue will be another bumper issue. The reason? ETI's rapidly rising sales and advertising enable us to do this without increasing the price. And ETI readers seem to be enthusiastic and Tech-Tips have been flooding in, so in the January issue there will be a super large:

## TECH-TPS SPECCLL

## SCIENTIFIC CALCLIATORS

Just over a year ago the cheapest scientific calculator cost over $£ 50$. Today there are a vast number available for a fraction of the price - and with far, far more facilities. This is a rapidly moving field, so we've arranged for a thorough survey of the market and give you "instant comparison" tables on the models now available

We also explain the terms being used to describe the calculations that can be made.


## IS THERE LIFE

ON MARS

NASA's Viking landers will soon be on Mars. These remotely controlled probes of superb sophistication will carry out a whole series of tests to find out if there is, or ever has been, life on Mars. In January's issue we explain the mission and tests to be made


## LOGIC TESTER

Until recently the most commonly used family has been TTL. But CMOS is rapidly gaining widespread usage and any tester, to be of value these days, must be able to test both these families. Our unit is able to test both families, and is capable of being used to breadboard and test simple circuits based on single ICs

## CROSSOVER AMPLIFIER

The follow-up to this month's feature on the Active Crossover Unit and how to couple it up to the 50 W amplifier module

## 50W AMPLIFIER MODULE

An updated version of our 50 W unit (r.m.s. of course) which can be used to increase the power (and quality?) of your existing amplifier

At the time of this issue going to press, the features mentioned here are in an advanced state of preparation. However circumstances, including highly topical news, may affect the final contents.


## ETI DATA SHEET

TCA210 AUDIO AMPLIFIER AND PRE-AMPLIFIER

The TCA210 is a monolithic integrated circuit comprising two amplifiers for use in intercoms and other audio systems. The first is a high-gain pre-amplifier with differential input and a class $A$ output stage which can deliver 2.5 mW into an $800 \Omega$ load. The second is a power amplifier with a class B output stage capable of delivering 500 mW into a $25 \Omega$ load. Up to 800 mW can be delivered into a $15 \Omega$ load for short periods. When there is no signal, the turrent consumption is 8 mA Squelch provision incorporated in both amplifiers can be use to ensure maximum battery life.


## SPECIFICATION <br> Supply voltage (VP)

PRE-AMPLIFIER

$$
\begin{aligned}
& 12 \mathrm{~V} \\
& 8 \mathrm{~mA}
\end{aligned}
$$

Open loop voltage gain ( $G V$ )
nput bias current (pins 1 and 115
Unity gain bandwidth (with 6dB/oct compensation) (B)
Noise figure at ( $\mathrm{R}_{\mathrm{S}}=500 \Omega$; $\mathrm{B}=$ 300 to 4000 Hz (F)
Total current (pin 14) 114
Current of current sink 1 C input stage

10,000
$2.5 \mu \mathrm{~A}$
$>10 \mathrm{MHz}$
4 dB

## 4.0 mA 2.5 m



Bias current (pin 2) (12)
Open loop voltage gain (Gv) 13) 1/ (pins 5 and

Unity gain bandwidth (with ot B/oct compensation) (B) pin 10) ( 110 ) Bias current (pin 7) (17)PreanmanceOutput power ( $R_{L} 1=800 \Omega$ ) (Po) 2.5 mW

$$
4 \mathrm{KHz}
$$

$$
4 \mathrm{~mA}
$$

$$
1.5 \mathrm{mV}
$$

$$
500 \Omega
$$



- AMPLIFIER FOR INTERCOM SYSTEMS


The AY51224A is a $P$ channel MOS IC contalning all the logic necessary to make a 4 digit, 12 or 24 hour clock operating from a 50 or 60 Hz input. It has multiplexed

## SPECIFICATION

Clock input frequency Clock Input logic ' 0 '

Clock input logic ' 1 ' Multiplex clock frequency

## Interdigit Blanking

Control inputs logic ' 0 ' Control logic ' 0
Outputs logic ' 0 ' (Vout= 2 V , lout $=4 \mathrm{~mA}$ )
Outputs logic '1' (leakage)
(Vout $=-18 \mathrm{~V}$ )
Power consumption ( $V_{D D}$
$=15$ volts)
Note 1 The clock input pin may be taken positive with respect to $V_{s s}$ provided that the current is limited like a forward biased silicon diod in this condition.
Note 2 The frequency is determined by an external capacitor.
Note 3 At 6.67 KHz multiplex frequency the digit ON time is $450 \mu \mathrm{~S}$ and the OFF time is $150 \mu \mathrm{~S}$.

BCD or 7 -segment outputs and will drive LED, Fluorescent and Gas discharge displays with the minimum of interfacing.
zero blanking in the 12 hour mode The chip needs a single 15 V supply and me is set The IC carrles it's own multiplex oscillator.

## ABSOLUTE MAXIMUM RATINGS

Voltage on any pin
with respect to $V_{s s} \quad+0.3$ to -20 V
Operating temperature range
Storage temperature range
Power dissipation (total) at 700 C ambient 500 mW (per output) 500 mW
50 mW

## ELECTRICAL CHARACTERISTICS

$V_{s s}$
$\operatorname{VGG} \quad=\quad-12$ to -18 V

## PACKAGE 16 Lead DIL




PIN CONNECTIONS

1. Segment $A$ output/2 $2^{\circ}$ output/Set Hours input
2. $V_{55}$
3. Multiplex oscillator
4. $50 / 60 \mathrm{~Hz}$ input
5. VGG
6. Strobe output
7. $M \times 4$ output (Ten Hours)
8. $M \times 3$ output (Unit Hours)
9. $M \times 2$ output (Ten Minutes).
10. Mx 1 output (Unit Minutes)
11. Segment G output/BCD or 7 segment select
12. Segment $F$ output/50 or 60 Hz select
13. Segment E output/12 or 24 hour select
14. Segment $D$ output/2 $2^{3}$ output/ Complement input
15. Segment C output/2 $2^{2}$ output/ Reset input
16. Segment B output/2 $2^{1}$ output/ Set minutes input
Typical Advertised Price £ $\mathbf{3 . 1 0}$ inc. VAT Data supplied by General Instrument.

## PIN FUNCTIONS

Pins 1 and 11 to 16 (Segment output A-G) are multifunction. During multiplex times 1 to 4 they function as data outputs, either 7 segment code or BCD multiplex time 5 (Strobe) they function as inputs

Segment Outpute A-G (Pins 1 and 11 to 16). In 7 segment mode the digits are multiplexed out on to these pins. Normally the outputs are at logic 0 (positive to display). Interdigit blanking for $1 / 4$ the digit time is incorporated for gas discharge displays.

BCD Outpurs $2^{0}-2^{3}$ (Pins $1,16,15,14$ ). In BCD mode the digits are multiplexed on to these pins in BCD code. Normally the outputs are at logic 'O' (positive) i.e. code $0=0000$.

Mutdelar Orteute 1-4 (Pins 10, 9, B, 7). These pins are successively switched to logic ' 0 ' to select the appropriate digit display. A fith multiplex time (Strobe) appropriate digit display. A to enable the control inputs. These outputs have interdigit blanking. The multiplex rate is $1 / 20$ th the multiplex clock frequency.

Strobe Output (Pin 6). This pin is used to enable the control input keyboard, it goes to logic ' 0 ' to enable.

Sot Hours Input (Pin 1). When taken to logic ' 0 ' during strobe time this input causes the hours counter to advance at the rate of 1 hour per second.

Set Minutes Inpur (Pin 16). When taken to logic $\mathbf{U}$ during strobe time this input causes the minutes counter to advance at the rate of 1 min per second and the hours counter to advance at the rate of 1 hour per minute.

Reset Input (Pin 15). When taken to logic '0' during strobe time this input causes the clock to reset to zero.

Complement Input (Pin 14). When left Open the segments and BCD outputs will have normal polarity When connected to Strobe output via a diode the segment and BCD outpuss will be inverted
12/24 hour select (Pin 13). When left open the clock will run in the 12 hour mode, when connected to strobe vil a diode 24 hour operation will result

50/60 Hz Select (Pin 12). When left open a 50 Hz clock will be accepled. When connected to strobe via a diode 60 hz operation will result

BCD/7 Segment Select (Pin 11). When left open 7 segment outputs will be provided, when connected to strobe via a diode BCD outputs will be provided
$50 / 60 \mathrm{~Hz}$ Input (Pin 4). The master clock (50 or 60 $\mathrm{H}_{2}$ ) is input to this pin" Hysterersis is provided on th input so that the input wave form is not criticial

Muttiplex Oscillator (Pin 3). An external capacitor is used to set the multiplex frequency. If required this input can be driven by an external oscillator.
$\mathbf{V s s}$ (Pin 2). Positive supply line nominally $\mathrm{O} V$
VGG (Pin 5). Negative supply line nominally -15 V
Power on Reset. At power ON the chip is reset to zero Counting will not start until either Set Hours or Set Minutes has been pressed

A 20w RMS (MAX) TCI The yanks have managed it with the 303D Power Driver This is an operational transconductance amplifier, intended for use as the driver of (see the 400 W circult below!). The 303D (see the 400 w circuit below!) 4 ohe 3 with can itself arive loads down to Virtually any complementary output palr can be driven then the full supply of $\pm 50$ volts is allowed. In this mode the total amplifier power is limited only by the volt-amp ratings of the output devices.
The device features internal compensation and a wide bandwidth. Crossover bias terminals ( $T$ and $T$ ') are provided but they can be left open if crossover behaviour is not critical. The construction ins.

$R_{L}$ Load Resistance (Ohms)
Graph 1: Maximum Allowable Supply ( $V \pm$ ) versus load for Sinusoidal Output.


Graph 2: THD versus frequency (typical) for 20 and 100 watt amplifiers at full power.


Gain Versus frequency. (20w Amp).

## SPECIFICATION

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when driving a MJ802/
MJ45 42 pair at 100 W
into 8 ?

## ABSOLUTE MAXIMUM RATINGS

Supply Voitage
Output Current
Internal Dissipation
$\pm 50 \vee$ DC
7 Amps peak 20 W in stil

Shortly to be available from Ambit
International, 37 High Street,
Brentwood, Essex
The price will be about $£ 25$.

PACKAGE


## PIN CONNECTIONS

1. ( +1 Input
2. $\mathbf{C}-$ Negative Bias
3. (-) Input
4. B- (Negative Base Drive)
5. $V+$ Supply
6. C+ Positive Bias
7. V-Supply
8. ${ }^{-}+$(Positive Base Drive)
9. T'
10. T

WATT AMPLIFIER


The resitors in series with $C \pm$ terminals provide current limiting at $1=V+/ R 3$ for $B \pm$ outputs. Adjust R2 (200-900 $)$ for current limiting.




At ETI we get enthusiastic about most of the calculators that we see and have made three available on offers in the past. We make no apologies for introducing yet another type - a brand, spanking new model which is due to be announced only after this issue goes to press! The Novus 3500 Electronic Sliderule offers facilities that, even at today's cut-throat prices, makes it a bargain at the suggested selling price of $£ 22.95$ but we have been, able to arrange for ETI readers to make a massive saving from the word go on this fabulous model. Compare the facilities with other models available - it'll convince you of the bargain you'll get!

## MAIN FACILITIES

1 8-digit LED display
2 RPN (Reverse Polish Notation) with 3-level stack.
3 Full memory.
4 Operates with natural and common logs.
5 Full trig facilities operating directly in degrees
$6 y^{x}$ function
7 Direct square root key.
8 Instant reciprocals.
9 x-y key: interchanges stacks.
10 Change-sign key.
$11 \pi$ key: 3.1415926.
12 Usual arithmetic functions.

## OTHER FEATURES

13 Display converts to decimal points after 30 seconds (if no key is pressed) to save battery - contents unaffected.
14 About 15 hours from 9 V alkaline battery: low battery indicator (Battery supplied).
15 Exceptionally clear 44-page instruction manual.
(This specification may mean líttle to readers unfamiliar with current scientific calculators - and remember that a 44 -page manual is necessary to explain all the operations it can handle!)

## AVAILABLLITY

ETI's offers in the past have sometimes been so successful that the initial estimate has proved wildly wrong. 2,000 units are being stocked for this offer initially. If orders exceed this every effort will be made to meet orders promptly but delays may result and it may be necessary to return money if it looks as though over long delivery dates will result. See next month's ETI for the current situation.

For security reasons, stocks of this calculator are not being held at ETI offices for callers.

The 3500 can be used with Novus Mains Adaptor, available from all Novus retailers for $£ 3.95$ (Mains Adaptor not , available through ETI).


# THERE ABE TWO WAYS 

 to Maki ProuiectsBOTH WORK. . .
SOMETIMES.
Here you can see one way. The chap in the picture is trying to get his home made radio to work. He might succeed.

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| Name




OPERATIONAL AMPLIFIERS are small in size, provide very high, stable voltage gains, and are readily available at well under 50p each.

- Unfortunately these immensely practicable devices tend to be described by their manufacturers and countless technical writers in terms that are virtually meaningless to the home constructor.
The purpose of this short series of three articles then, is to show, with the minimum of theory and mathematics, how to extract the essential information from data sheets and how to apply it to practical designs.
The units to be described, each using a single operational amplifier, are a compact sine-wave audio-signal generator, a high-impedance audio amplifier (with various switched frequency-responses available) and a dc amplifier to increase the effective sensitivity of an ordinary 1 mA meter to $10 \mu \mathrm{~A}$ fsd.
Each of these units can be run from batteries or any other suitable source; however, since operational amplifiers usually require dual supplies (which could become expensive if batteries were used for long) the first actual constructional project will be a power supply unit giving $\pm 12 \mathrm{~V}$ fully stabilised and short-circuit protected. This latter feature is rather important, for the operational amplifiers to be used have their lead out wires only 2.5 mm apart which, in experimental setups, will, sooner or later, lead to short circuits of the supply rails by solder blobs, touching wires etc.
Accordingly, a protected power supply, specifically designed for use with op amps and which automatically reverts to correct operation on removal of an unwanted short-circuit is essential.


## WHAT IS AN OPERATIONAL AMPLIFIER?

Originally, the term was used to describe an amplifier suitable for performing mathematical operations in analogue computers. It has since come to include almost any high-gain dc
amplifier capable of having its actual performance, in terms of gain, frequency response and input impedance, determined by external components arranged to provide feedback (usually negative feedback).
An ideal op. amp. has the following characteristics:-
1., Infinite gain
2. Infinite bandwidth
3. Infinite input impedance
4. Zero output impedance
5. Constant phase shift between input and output.
Obviously, such a device is impossible in the real world, but it is possible to manufacture amplifiers that have gains etc. So large, and

output impedances so low, that any departures from the ideal have little effect in practical circuits.
For example, if a working gain of 100 is required and the op. amp. to be used has a gain (without feedback) of 50000 then $50000 \div 100(500)$ is such a large margin that we can say that, for practical purposes, the reserves of gain available are so large that the gain is infinite. Similar reasoning applies to the other parameters whose ideal values were mentioned earlier.

## FEEDBACK

Operational amplifiers are most often arranged to function with negative feedback applied, although there are cases where either no feedback, or indeed positive feedback, is employed. The op. amps. we shall be considering have, in fact, two input terminals, and feedback is considered to be either negative or positive according to which of these inputs it is connected.
The two input terminals are arranged in the following manner. Consider that one input terminal is earthed; then if the application of a positive going signal to the other input results in a positive going output signal, then that latter input terminal is termed the " + ve" input terminal. Conversely, again with one input earthed, if the application of a positive going signal to the other input results in a negative going output signal, then that latter input terminal is termed the "-ve" input terminal. Sometimes the +ve input is called the "non phase inverting" input and the -ve input is called the "phase inverting" input.
By convention, the op. amp. itself is shown as a triangle, with the output being taken from the righthand apex; the two inputs are on the left, one input being the -ve and the other the +ve.
The arrangement of Fig. 1a will result in a phase reversal of the signal, while that of Fig. 1c will not. Accordingly, the first configuration is called an "inverting amplifier" and the second a "non-inverting amplifier".

## TERMINOLOGY

Op. Amps have their own terminology which need expanation. These are frequently used when dealing with op. amps and without explanation could cause confusion. The terms are necessary because these devices do not have the ideal characteristics referred to earlier.
Open loop gain is the voltage gain of the amplifier at low frequencies with no feedback applied, ie with the feedback loop open. The value of 50,000 used as an example earlier is the open loop gain of the amplifier discussed.
Closed loop gain is the voltage gain of the amplifier when negative feedback is applied, ie with the feedback loop completed. It is thus the gain of the whole circuit and is analogous to the stage gain of valve and discrete-transistor circuits. The value of $x 2$ we use below is the closed loop gain in that case.
Input bias current is that current that must be fed into the input terminals in order to make the output voltage zero. The current is necessary since the input transistors of the op. amp require some
base current, however small, in order for them to conduct and so amplify.
Input Offset Voltage is that voltage that must be applied across the input terminals to make the output voltage zero. It is not usually as important as input bias current in the type of application that we shall be considering.
Common Mode Rejection is a measure of how good the amplifier is in rejecting signals applied to both inputs together. Once again, we will not need to pay great attention to this characteristic in our applications.
Frequency Response is usually quoted by stating the frequency at which the voltage gain falls to unity; it is then normally assumed that, as the frequency is reduced, the gain rises at a rate of 20 dB per decade (i.e. the voltage gain rises by a factor of 10 for a ten-fold change in frequency) until it reaches the open loop value. It is then possible, with a knowledge of the open loop gain, to sketch the frequency response, see Fig. 2. Operational amplifiers have a response down to zero frequency, that is, they are dc amplifiers.

Towards the top end of the frequency
range, slewing rate becomes important.
Slewing rate is the fastest rate of change of output voltage that the op. amp. can generate. Provided that the output voltage swing is small, say 1 V peak-to-peak, the slewing rate limitation is unlikely to be a problem, even at a frequency close to the op. amp's maximum. However, if a large output voltage swing is called for, say 20V peak-to-peak at the same frequency, then slewing rate limitation can give rise to distortion; for clearly, at the zero crossing a large-amplitude signal will be changing its voltage as a faster rate than a signal of smaller amplitude.

For example the popular 741 IC may have a bandwidth of 100 kHz to a small signal but the maximum slew rate of 1 volt/microsecond will limit bandwidth to 10 kHz , if an output swing of 20 V peak-to-peak or more is required, or to 40 kHz at four volts peak-to-peak.

Slew rate thus limits the ultimate output swing available at high frequencies and is also a source of high-frequency distortion at high output levels.

The use of negative feedback will not cure the distortion for it is inherent in the op. amp. itself.

Each configuration has its own properties, which we shall now consider.

## INVERTING AMPLIFIERS

Referring to Fig. 1c, consider an input, $V_{i}$ at, say 1 kHz . Irragine that the input resistance of the op. amp. itself is so large compared to the values of $R_{1}$ and $R_{2}$ that it can be said to be infinite. This will be the case if $R_{1}$ is, say, $1 k$, for the input resistance of a typical op. amp. is 1 M . Imagine also that the gain of the op. amp. is very much larger than the final gain of the whole circuit. Once again this will be so, for the gain of a typical op. amp. is 50000 and the overall gain of the whole circuit will be very much less than this extremely high value, as will be shown.
This latter assumption is very important, for it means that the actual level of signals at the -ve input terminal will be so close to nothing that we can consider it to be zero. By Ohm's Law
$I_{1}=\frac{V_{1}}{R_{1}}=I_{2}=\frac{V_{2}}{R_{2}}$
where
$V_{1}$ is voltage across $R_{1}$
$V_{2}$ is voltage across $R_{2}$
But with zero signal at the -ve input.
$V_{1}=V_{i}$ and $V_{2}=V_{0}$
so that the gain, $A$, is

$$
A=\frac{V_{0}}{V_{i}}=\frac{V_{2}}{V_{i}}=\frac{R_{2}}{R_{1}}
$$

which is independent of the actual gain of the op. amp., provided that the latter is very much larger than the value of $A$ - very likely in practice.

It will be instructive at this point to consider the level of signal actually present at the -ve input of the op. amp. With an input signal of 1 volt and a circuit gain of $x 2$, there will be an output of 2 volts. If the op amp gain is 50000 , then the level at the -ve input must be

$$
\frac{2}{50000} \cdot 1=40 \mu \mathrm{~V}
$$

quite close to the zero level assumed. Note that when the amplifier output is fed back to the -ve input, the output voltage adjusts itself to such a value that the actual voltage between the two inputs becomes so close to zero that the difference can be neglected. The greater the gain of the op amp itself, i.e. the better it approximates to the ideal of infinite gain, the less the voltage at the -ve input becomes.
Since the -ve input has such a low level signal present, it is virtually at earth potential, and consequently the input resistance of the whole circuit is equal to $R_{1}$. Such an arrangement as illustrated in Fig. 1a is sometimes called a "virtual earth amplifier".
If a very high value of gain is required, complications can arise if a high input impedance is called for at the same time, for if $R_{2} / R_{1}$ is large, either $R_{2}$ will need to be such a high value that it is impracticable or $R_{1}$ will be too low for the required input impedance.
In that case, the configuration of Fig. 1b can be used. An analysis of this circuit gives, for the voltage gain

$$
A=\frac{R_{2}}{R_{1}} \cdot \frac{\left(R_{3}+R_{4}\right)}{R_{4}}
$$

provided that $R_{2}$ is large compared to $R_{4}$.
Now $R_{1}$ can be kept at a reasonably high value (to raise the input impedance) with $R_{3}$ and $R_{4}$ making up the gain to the required level).

## NON-INVERTING AMPLIFIERS

Now consider the non-inverting amplifier of Fig. 1c. As before, imagine that, due to the high gain of the op. amp., there is virtually zero signal between the two inputs and that the input resistance of the amplifier is very much greater than either $R_{1}$ or $\mathrm{R}_{2}$.
Then $V_{2}=V_{1}$ where $V_{2}$ is the voltage across $R_{2}$

$$
\begin{aligned}
& \text { But } V_{2}=\frac{R_{2}}{\left(R_{1}+R_{2}\right)} \cdot \cdot V_{0} \\
& \therefore V_{0}=\frac{R_{1}+R_{2}}{R_{2}} \cdot V_{2}
\end{aligned}
$$

so that gain A is
$A=\frac{V_{0}}{V_{1}}=\frac{\left(R_{1}+R_{2}\right)}{R_{2}} \cdot \frac{V_{2}}{V_{2}}=\frac{\left(R_{1}+R_{2}\right)}{R_{2}}$
which again is independent of the actual gain of the op. amp. itself.

The input resistance of a non-inverting amplifier is very high, being determined largely by the impedance from the two input terminals to earth. Typically, it is of the order of $200-400 \mathrm{M}$ at low and medium gain levels. It is this extremely high value of resistance that makes the non-inverting amplifier so useful, although, of course, there are disadvantages. For example, it might appear that a non-inverting amplifier would be ideal to accept the output from a high resistance source, but in

## OpAmps

that case the resistance seen by the op amp tve input would be that source resistance, while the resistance seen by the -ve input will be $R_{1}$ and $R_{2}$ in parallel (Fig. 1c). The input bias currents (see later) at each input would then give rise to a voltage difference across the inputs and hence, of course, unwanted voltage offset at the output.
These two circuit configurations, namely, the inverting and the non-inverting, form the basis of all op amp circuitry and are well worth remembering. In a number of uses, the simple resistors used in the examples quoted are replaced by complex impedances of one kind or another in order to modify the frequency response in some way. By such means it is possible to make op -amps respond as frequency selective amplifiers, integrators etc.
Examples of this tailoring of frequency responses will arise in the case of the audio amplifier to be described in part 3.

## FREQUENCY COMPENSATION

Figure 2 shows the frequency response of a type 741 op amp it can be seen that the open loop gain starts to fall at frequencies above about 10 Hz . This is not to say that at higher frequencies useful gain cannot be obtained - it most certainly can. At 100 kHz for example, a closed loop gain of 20 dB is possible. However, the response of the 741 can be a limitation in some applications and then the 709 type amplifier is possibly preferred. The 709 is never used without some form of frequency compensation - it readily oscillates at around 10 MHz if none is employed - but does have the advantage that the values of the components used can be varied to provide various bandwidths, (Fig. 3). In practice, the values of the frequency compensation components are chosen to give just sufficient bandwidth for the application being considered. There is no real objection to employing values to give a greater bandwidth, but instability probems may then arise, and the noise level is liable to be greater.
As a point of historical interest, the 709 came before the 741 (it was itself preceeded by other op. amps of reduced performance) and the need for the provision of external components proved irksome. Advances in technology enabled manufacturers to incorporate capacitors on the integrated circuit chip itself and so

provide an op amp. that was stable without the need for large external components - thus the 741.
At the same time, the designers were able to provide protection at the input terminals, so that should either input have either supply rail connected to it (by a wiring error for example) no damage would be caused. The 709 in such circumstances, would have burned out its input transistors.
Further improvements were incorporated in the 748 op amp which is in some respects between the 709 and the 741, in that it requires one small external capacitor but .provides a greater gain-bandwidth product than the 741.
With so many external connections two supply rails, two inputs, one output and perhaps terminals for frequency compensation components - a special form of packaging was required and in fact thére are two in common use. One, the T099, is similar to the common T05 transistor encapsulation in size of can but has
eight lead-out wires. The other is the dual-in-line (DIL) package and it is recommended that the constructor uses this style, together with the appropriate holders. This will make it possible to check, to some extent, the dc conditions of the circuit when first wired up.' This is done before the op amp itself is inserted thus perhaps preventing catastrophic failure of the device due to a wiring fault. Further, in those cases where a 709 is called for, it is possible to use a 741 for initial testing (although of course full performance in respect of frequency response might not then be obtained). Should an important wiring error have been made, damage is less likely to be caused to a 741 due to the built-in overload protection at its input terminals.
This for the average experimenter is all the theory that need be covered at the moment. Hence next month a start will be made on practical circuits, with details of the power supply and of the compact sine wave audio oscillator.

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# MULTIMETER GUIDE 

## How to choose a meter to suit your needs

THE BUDDING experimenter, after purchasing a basic set of tools, commences building small circuits at the earliest opportunity. Very rapidly he meets the situation where a circuit, as built, does not work. So what now? If all wiring has been done correctly then it must be a faulty component but which one? The simplest way to find out is to use a meter to measure voltages around the circuit.
Thus the first instrument that an electronics experimenter will buy will be some kind of multimeter capable of measuring the common ranges of voltage, current and resistance found in usual circuitry.
Upon investigating what is available the experimenter discovers that multimeters range in price from simple analogue meters at $£ 4$ to sophisticated, highly-accurate digital instruments costing several hundreds of pounds.
The experimenter must ask himself which is the most suitable for his class of work? Is it really necessary to spend several hundred of pounds? Are $£ 4$
multimeters worth having at all?
In this article we examine the factors which must be considered when selecting a multimeter in order to satisfy the conflicting requirements of minimum expense and suitability.
When selecting a multimeter the following factors are of importance:
Input impedance
Accuracy
Resolution
Ruggedness
Number of ranges
Frequency response
Portability.

## INPUT iMPEDANCE

A multimeter must have as high an input impedance as possible if the circuit under test is not to be severely loaded. Loading leads to substantial. errors in the measurement and, if' severe, may even damage components.
The input impedance of analogue meters is usually expressed in ohms per volt. Thus the impedance depends on the voltage range selected. Typical

## THE MOVING-COIL METER



Fig. A.


If an electromagnetic coil is suspended in the field of a permanent magnet, it will be caused to rotate, when energized, by a force proportional to the energizing. current.
In the moving-coil type of meter, as Fig.A shows, the field of the permanent magnet is arranged to pass across a cylinder in which hangs the coil of the meter. A fine spiral tension-spring restrains the
rotation by providing a linearly increasing torque as the coil rotates. Attached to the coil is a pointer that moves across a scale, thus indicating current.

As the number of turns is increased, to improve sensitivity, the designer must use finer wire to keep the mass of the coil small. As a consequence of this requirement, sensitive meters usually have a higher resistance, and are more delicate.

inexpensive meters have impedances of 1000 to 100,000 ohms per volt. Thus when measuring voltage a multimeter is in effect a resistor in parallel with the resistor (across which the voltage is being measured) within the circuit and it reduces the effective value of both to something lower than the value of either. Thus, as a voltmeter is in effect a resistor, connecting it across a circuit will inevitably change the resistance of that circuit, and the meter must shunt current away from the circuit.
This brings us back to the reason for quoting the sensitivity of voltmeters in ohms per volt. Multiplying the sensitivity by the fsd range in use, gives the resistance of the meter circuit that will be shunting the component. Cheaper multimeters will have sensitivities ranging from as low as $1000 \Omega$ /volt to as high as $100 \mathrm{k} \Omega /$ volt To illustrate loading effects, consider the circuit in Fig.1. By Ohms law we know that the voltage between points $A$ and $B$ is 0.75 volts.
Now let us see what happens when we use a 1000 ohms/volt meter on the 1 volt range to measure this voltage. The 1000 ohms of the meter in

parallel with R2 will produce a combined value of 500 ohms. Thus the voltage read by the meter will be 0.5 volts instead of 0.75 volts - an error of 33 per cent!
It is the degree of this shunting effect that is important - in theory it can never be completely avoided, for some
energy must flow into the measuring system from that being measured. In electronic measurements the rule of thumb is that for accuracy, the resistance of a voltmeter should be at least ten times that of the circuit - a hundredfold is better still.
However with the simple moving-coil


Typical of the inexpensive multimeter available this 1000 ohm/volt unit is suitable for general electrical work.

When using any meter with switched ranges, always start off by selecting a meter range much higher than your estimate of the quantity to be measured.
This precaution safeguards the meter should the quantity be much larger than expected.
type of meter a higher input impedance also requires a delicate meter movement which is relatively easily damaged. A good compromise would seem to be a meter having an ohms/volt rating of between 10000 and 50000.
In more expensive meters - those employing electronic amplifiers and those using digital techniques, input impedances are usually at least one megohm and hence loading of the circuit is seldom a problem.

## ACCURACY

The typical cheap multimeter has an accuracy of the order of 3 to $5 \%$ and this is further reduced by parallax reading errors. Better quality analogue instruments have $1 \%$ accuracy and mirror backed scales to reduce parallax reading errors.
Digital multimeters are at least $1 \%$ or better, with $0.2 \%$ being typical. Sophisticated units costing several hundreds of pounds may well have accuracies down to $0.001 \%$. The way accuracy for a digital meter is quoted is far from being as simple as given here, but for our present purposes the simple statement given suffices.
As to what accuracy is needed, it is seldom that an experimenter, even one at fairly advanced level, needs an accuracy better than $1 \%$ and, mostly, even the 3 to $5 \%$ of a simple meter is good enough. So don't get carried away by accuracy, if you can afford $1 \%$ or better - great. But you will not be too badly off if you can't.


## MULTIMETER GUIDE

TAUT BAND SUSPENSION SYSTEM


Better quality analogue multimeters usually employ a taut band suspension system in the meter movement. This system, although more
expensive, has several important advantages over conventional moving coil movements.
The movement still employs the moving coil principle but now the coil is suspended by means of a platinium alloy band. Since, now, no pivots, jewels or hair springs are used errors due to pivot friction and roll of the jewel are completely eliminated Additionally the meter will maintain correct reading regardless of orientation.
A shock absorber is usually fitted to the movement that incorporates dual bumper stops. Thus the movement is rendered insensitive to mechanical shock.
The use of a taut band movement ensures good linearity, freedom from backlash, freedom from effects of vibration and shock and much greater instrument reliability.

## RESOLUTION

Resolution is often more of a limitation than is accuracy for, if the meter movement is small, it is difficult to read accurately. For example, when trying to read 1.5 volts on a 10 volt full scale meter, it may only be possible to say that it is somewhere between 1 and 2 volts. Hence the bigger the movement the better.
In the case of a digital meter the resolution is a function of the number of digits in the display. Thus a three digit display (999) can resolve to one part in 1000 and hence the accuracy must be better than $0.1 \%$ to make full use of the available resolution. Conversely it is little use having more than three digits in the display if the accuracy is only $1 \%$.

## RANGES

Any meter must be able to measure dc volts and current, ac volts and current and resistance to really qualify as a full multimeter. Some instruments also include dB calibration and the facility to measure capacitance.
DC voltage should have ranges from 1 to 2.5 volts full scale to 500 and preferably 1000 volts full scale. AC volts should cover from 2.5 volts full scale to at least 300 volts full scale. The lowest current range should be 1 mA full scale, or better, and the maximum reading should be at least one ampere.
Resistance scales should enable you to read from one ohm to at least one
meter has 1,10 and 100 volt ranges and is quoted as having an accuracy of $3 \%$ of full scale. Now let us suppose we are trying to measure 1.1 volts. We cannot read it on the one volt scale as the meter would read over range. On the ten volt scale we read about one volt but our accuracy on the ten volt range is $3 \%$, that is, $\pm 0.3$ volts. So the best we can say is that the voltage is between 0.7 and 1.3 volts. Hardly satisfactory for working on transistor amplifiers for, with this measurement, we would not be sure whether it was one or two base emitter junctions ( 0.6 to 0.7 volts per junction for silicon).
Had we a meter witu a 3 volt range we would have read around 1.1 volts with an accuracy of $\pm 0.09$ volts and the degree of ambiguity would have been vastly decreased.

## RUGGEDNESS

Drop a $£ 4$ multimeter and you may as well not bother to pick it up. The case will probably shatter and the meter movement will almost certainly be ruined. The more expensive units have poly-carbonate cases which could be bounced off a concrete floor (if you are game enough). The more expensive units will also probably use a taut-band meter movement rather than the simple moving-coil variety. Taut-band movements are virtually impervious to shock.
Some years ago we bounced such a Continued on page 59


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# MULTIMETER GUIDE 

The Sinclair DM2 Multimeter is powered by a single $9 V$ battery which gives up to 60 hours life. It features automatic dual polarity, automatic over-range and overload indication. The carrying handle doubles as a support for bench top use. Cost is around $£ 65$.

## Continued from page 56

taut-band meter off the floor hundreds of times (in order to take photographs) without any damage occuring to the meter whatsoever (a Weston 660 series multimeter).
Ruggedness is very much a function of price. The more you pay the better the case and the movement used. The switches will also be larger, more robust and with silver-plated contacts. So although a £4 meter may appear to offer the same facilities as a more expensive unit it will certainly not last as long.
Steer clear of ultra-miniature meters. These are very fragile as well as being difficult to read. If you ca: afford it buy a meter with a taut-band movement - they are expensive but will be worth the money.

## FREQUENCY RESPONSE

The ac ranges of a multimeter are of little value if the frequency response of the instrument only extends to a few hundred hertz. Such an instrument would only be useful for measuring 50 Hz mains voltages.
If possible obtain a meter that has a frequency response that at least covers the audio spectrum. This is almost indispensable if you are working on audio equipment and do not have a cathode-ray oscilloscope

## PORTABILITY

Most multimeters are portabie as the simple kinds only require a couple of dry cells to power the resistance measurements. Multimeters that have amplifiers built in are sometimes restricted to mains only operation.

For the experimenter a multimeter should definitely be capable of battery


These Danameters are the cheap end of the range from Dana Laboratories, AC accuracy is 1.15\% (Danameter) and $0.6 \%$ (Danameter II); prices are $£ 135$ and $£ 168.50$.
operation. Therefore if a transistorized or digital multimeter is to be purchased make sure that it has rechargeable cells or is capable of running for extended periods on dry cells. Mains only types are fine for the laboratory but not for the hobbyist.

## ANALOGUE OR DIGITAL

An analogue measurement is essentially one that is made continuously. A digital measurement on the other hand is made in a series of discrete steps

The same basic quantities can be measured by both digital and analogue methods. For example, a conventional clock has a pair of hands which traverse a calibrated dial in a continuous sweep, and there is a theoretically infinite number of steps between any two calibrated points on the clock face - measurement is continuous and is therefore an analogue process.
A digital clock on the other hand indicates the time in discrete steps, each of one minute (or one second).

## MULTIMETER GUIDE



## DUAL SLOPE A/D CONVERSION

There are several modes of operation of digital multimeters but by far the most commonly used in cheaper
instruments is the DUAL SLOPE technique. The system, assuming a 3 digit display, works as follows: -


Initially when an unknown voltage is applied to the input a 'start conversion' pulse is generated and simultaneously all the counters are set to zero.

The integrator, which may be a simple operational amplifier design, begins to ramp up with a slope which is proportional to the magnitude of the input voltage. At the same time clock pulses are gated to the counters which commence to count up.

Control logic detects when the count reaches 999 and gates off the input voltage and gates on a reference voltage. The reference voltage is opposite in polarity to the input voltage and the integrator therefore begins to ramp down with a slope proportional to the reference voltage and the counter reverses. The process continues until zero voltage is reached.
At this zero point a comparator closes the clock pulse gate, the counter stops and now holds a count proportional to the input voltage.

Design requirements for the integrator and clock accuracies are much less stringent with this technique than with others because both input ramp and reference ramp use the same circuitry. Hence component inaccuracies tend to cancel out and accuracy becomes dependent mainly on the stability of the reference voltage and, if used, the input attenuator and amplifier. The dual slope method provides good rejection of normal-mode noise.
The dual-slope conversion provides the basic voltage measurement capability additional circuitry being added to measure resistance, dc current and ac voltage and current.


The Tech VOM is a $20 \mathrm{k} \Omega \mathrm{V}$ multimeter. It costs $£ 10.50$ from RCA Components, Sunbury-on-Thames.


This e/ectronic multimeter from AVO features 10 Meg impedance on dc and $316 \mathrm{k} \Omega \mathrm{V}$ on ac. The 39 ranges of this instrument cover all the measurements you are likely to need.


The Fluke 8000A digital multimeter offers $0.1 \%$ accuracy with three and a half digits.

There is no ambiguity of reading. It is either $8: 23$ or $8: 24$ one cannot misread it.
This is one of the great advantages of digital readouts. There are no reading errors due to parallax or scale resolution, and in the case of electronic digital instruments no friction or hysteresis to cause mechanical errors.
Hence even the cheapest of digital multimeters has better than 1\% accuracy, (actually accuracy should be stated the other way - a meter is $99 \%$ accurate, not 1\%). whereas an analogue meter with a mechanical movement of $1 \%$ accuracy is quite
expensive and still subject to further reading errors caused by parallax and scale resolution.

Until recently digital multimeters were priced beyond the reach of the amateur experimenter the cheapest being well over $£ 100$. However now there is a choice, albeit restricted at less than $£ 100$.

Such prices make the digital instrument competitive in price with the best of analogue transistorized multimeters and - they have better accuracy.

All digital multimeters have input
impedances of one megohm or better and hence loading is seldom a problem with such instruments.

Digital instruments are sensitive to noise and a dc voltage with superimposed hum and noise may give incorrect and/or jittery readings on some instruments. Ana!ogue instruments on the other hand tend to reject and average out superimposed noise.

It is doubtful that digital meters will ever completely replace analogue meters. But they will almost certainly replace those at the higher priced end of the analogue range.

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## MHI KIT

MHI (Monolithic Horometric Integration) is a complete module system to enable the building of digital timing circuitry. Two basic modules are required for each system: one clock or counter kit plus one display kit. Each of the six display kits is compatable with any of the clock kits and thus you can decide which display size or how many digits you require quite independently from your choice of clock kit.
Most of the PCBs for the clock kits are 2 in $\times 4 i n$

with the exceptions being only slightly larger, the PCB contains spaces for all of the basic components excluding switches, transformer and display. Each clock kit includes main LSI chip plus socket, segment driver chip, PCB and may also include any other unusual components. The kits exclude resistors, capacitors, transistors and switches which are all easily obtainable types and values. All clock kits will interface to any MHI display kits or to any other common-anode LED displays.

## MHI-5039 (UNIVERSAL COUNTER)

Uses a new counter chip from MOSTEK (MK50395) and will count up or down at speeds of up to 1 MHz with a total system speed of 400 kHz . Count and compare registers can be loaded from logic ICs or BCD switches, features count inhibit, display latch, display decode Outputs: 6 digit drives, BCD and 7 -segment, count $=$ compare count $=$ zero, etc. Applications include: very fast stopwatch, sequence timers, 'auto-cue' for tele-cine, batch counters, repeatable "pill" counters, etc.

Interfaces with any six digit MHI display kit

$$
£ 24.00+\text { VAT }
$$

## MHI-5024 [DIGITAL STOPWATCH KIT]

Based on the MOSTEK MK50204 chip the MHI5024 is a modified calculator chip which will still function as an 8 -digit four-function calculator but has the additional facilities of conversion of hours, minutes and seconds to seconds or vice-versa. The Chip will also count in Hours, Minutes, Seconds and tenths with start/stop/reset facilities. The timing source for the counting is an RC network set to run at 140 KHz .
The Kit includes: MK50204, 28-pin skt., CA3081 segment driver and P.C.B. $£ 14.00+$ VAT
(For H.MM.SS.s use MHI-D $7 \times 7 / 6$, for M.SS s use a four-digit MHI display)

## MHI-5378 (DIGITAL CAR CLOCK KIT)

Uses the new National MM5378 Auto-Clock chip. The Chip has full car/boat clock facilities with a voltage range of $9-20 \mathrm{v}$ with no-loss-of-time down to 5 v . Timing source is a 2.097152 MHz Quartz Crystal which is driven and divided by the chip Facilities include: (i) display on/off switching with ignition leaving the clock running at all times (draws about 5 mA ). (ii) display brightness control. MM5738 kit skt CA3081, 2 MHz Xtal and Trimmers, P.C.B. £ 15.10 + VAT. (Interfaces with MHI four-digit displays kits)

## MHI-5314 (BASIC CLOCK)

Uses National MM5314 chip to give a four or six digit clock with $12 / 24$ hour readout from $50 / 60 \mathrm{~Hz}$ supply. This kit and chip are so simple that no previous electronics experience is really necessary to have an electronic clock working within a couple of hours. £6.60 + VAT.

## MHI-5025 (ALARM CLOCK)

For a digital bedroom clock with accurate alarm time, snooze facility and display brightness control. Six digit output in $50 \mathrm{~Hz}, 24$-hour format. Alarm tone oscillator is on-chip and will drive small loudspeaker with single transistor interface. Very simple to assemble. $£ 9.35$ + VAT.
MHI-5023 as MHI-5025 but with 12 / 24 hour option. $£ 9.35$ + VAT.

The DL707 display is a standard 0.3 in LED display readable from distances of 10 feet or so. Four or six digits plus a PCB
MHI-D707/4£6.60 + VAT
MHI-D707/6£9.50 + VAT
NEW MHI-D727 0.5" DIGITAL DISPLAY KITS
The DL727 is a new double-digit display from Litronix presented in an 18 -pin pack. Four or six digits are provided with P.C.B. The MHI display kits connect directly to the outputs of any of the MHI clock kits.

Four digits - MHI-D727/4£8.50 + VAT
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The DL747 "JUMBO" display is the largest single package LED display available with a digit height of 0.6 in . Can be read from distances over 25 feet. Four or six digits plus PCB.
MHI-D747/4£9.80 + VAT
MHI-D747/6£14.70 + VAT

## DISPLAYS

| DL701, DL704, DL70? | £1.48 |
| :---: | :---: |
| DL721, DL727 (per pair) | £3.75 |
| DL749, DL750, DL747 | £2.45 |
| 5-LT-01, 5-LT-03* | $£ 5.80$ |
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## MHI-7001 (ALARM/DATE/TIMER)

A six digit clock with optional display of date. Has switched alarm output and a switched timer (clock/radio, "sleep") output. Apart from being a very unusual clock this kit can be used for remote switching of tape recorders, etc. We advise the use of a six digit readout with this kit. $£ 10.00$ + VAT


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# ELECTRONICS PART 22 -it's easy! Mara daot fliess 




Fig.2. Ladder network.

AS WE saw in the previous section resistor-capacitor filters can only provide roll-offs of 6 dB /octave . 20 $\mathrm{dB} / \mathrm{decade}$. On the other hand combinations of inductors and capacitors can provide much steeper rolls-offs and a response at the turn-over point which can be tailored to a desired shape.
The variety of LC component combinations that can be employed is great indeed and, to the uninitiated, the design of such filters can seem to be very confusing. However, circuit analysts have established design procedures which enable a filter having any practical characteristic to be designed in a logical, formalized manner. The method is based on the use of cascaded basic sections.

## TWO-TERMINAL PAIR NETWORK CONCEPTS

As we have seen at various times in the course so far, filters can be circuits having just two terminals - a resonant circuit for example, or they can have two input and two output terminals the so-called two-terminal pair networks. (The RC filter is of the two-terminal pair kind). The two different types are illustrated as system blocks in Fig. 1. Note that it is conventional to show input on the left and output on the right.
As said before many possible circuit configurations exist for filters, and the designer has to make a compromise between using a simple arrangement of many components that can be easily handled mathematically, or, a few components in a more complex network that cannot be treated by general formulae. Here we will examine the approach based on grouping numbers of simple and similar networks, to obtain the desired
response, by the methods originally proposed by Zobel in 1923.
The simplest type of network is the LADDER, as illustrated in Fig. 2, the defining feature being that it has a common line. When the lower line also includes impedances <resistor elements are used to represent what are usually reactances) the network is called a LATTICE; these are much harder to design and are less commonly used. Let us examine how a ladder network is broken down into even more basic structures.
By convention the series elements of a ladder are labelled $Z_{1}$, and the shunt elements as $Z_{2}$. These elements will be either capacitors or inductors and, it is assumed that the filter is driven from, and drives into, pure resistances.
Within the ladder arrangement, shown in Fig. 2, can be seen three basic building structures - called the $L$ section (inverted L to be absolutely correct), the T section and the $\pi$ section. The three are shown separated in Fig. 3.
In Fig. 4 we see how standard $T$ or $\pi$ sections can be connected to provide the same effective ladder network. Conversely a ladder network may be subdivided into standard T or $\pi$ networks by breaking the values up as shown.
The interesting and quite vital point
is that the T or $\pi$ stages have the same input and output impedance. That is they are symmetrical. The L section, however, is unsymmetrical in that input and output terminal pairs are not interchangeable. Two $L$ sections in series will produce a $T$ or a $\pi$ section. When two identical T or $\pi$ sections are cascaded they are matched into the same impedance - maximum energy is transmitted and no reflections occur. Each terminal sees an image of itself, this property giving the name image parameter design to this filter design method.

## CONSTANT-K FILTERS

Even though a quite simple configuration has been used there can still be a wide range of combinations each with complicated mathematical solutions.
By introducing another assumption we can make some headway toward realising a wide range of characteristics with a reasonable degree of mathematical simplicity. This assumption is that $Z_{1} \cdot Z_{2}=R_{0}{ }^{2}$ where $R_{0}$ is a true resistance called the characteristic resistance. (This may seem strange but the multiplication of capacitive reactance with inductive reactance yields just that). Hence $Z_{1}$ and $Z_{2}$ must be a combination of capacitor and inductor giving us


## ELECTRONICS -it's easy!



Fig.4. Building up the ladder network from basic sections.
equivalent stages with L and C proportions as shown in Fig. 5. The rule holds true for an $L$ section provided we treat full shunt or series reactance as 2 L or 2 C .
The name constant-K arose from the original terminology where Zobel, in 1923, used $K$ instead of our now accepted $R_{0}$. Filters designed to this rule are hence called constant- $K$ filters.
Regardless of whether the stage is designed to be high pass or low pass the cut off frequency will be the same, that is, at the resonance point of the LC values of the standard equivalent $L$ section.

> That is cut-off frequency $\frac{1}{2 \pi \sqrt{L C}}$ $$
f_{C}=
$$

For example in the $T$ section of Fig. 6 the equivalent $L$ section networks have $L$ of 1 mH and a C of 0.5 microfard.

That is cut-off frequency

$$
\begin{aligned}
f_{c} & =-\frac{1}{2 \pi \sqrt{ } 10^{-3} \times 0.5 \times 10^{-6}} \\
& =7.1 \mathrm{kHz}
\end{aligned}
$$

Also from $Z_{1} \cdot Z_{2}=R_{0}{ }^{2}$
characteristic resistance $R_{0}=\sqrt{ } Z_{1} Z_{2}$
However the capacitive reactance must be written as a reciprocal and in Fig. 6 this is $Z_{2}$. Hence:-

$$
\begin{aligned}
R_{0} & =\sqrt{ } \frac{Z_{1}}{Z_{2}} \\
& =\sqrt{ } \frac{10^{-3}}{0.5 \times 10^{-6}}=45 \mathrm{ohms}
\end{aligned}
$$

Thus we see that the source and load impedances used with this network must be 45 ohms, if maximum power is to be transferred, and the network is a low-pass stage having a cut-off
frequency of 7.1 kHz .
If $L$ and $C$ were reversed the filter would have identical $R_{o}$ and $f_{c}$ but it would now be a high-pass stage.
An important feature of image-parameter design is that image-matched stages can be cascaded without altering the cut-off -frequencies or the characteristic resistances. Each additional stage improves the roll-off, thereby giving a powerfully reliable way to obtain the desired rapidity of attenuation without having to re-design the whole system as extra stages are added.
It can be shown that the attenuation, a, in the stop band, expressed in decibels, is a $d B=9.7 n \propto$ where $n$ is the number of standard $-T$ (or standard $-\pi$ ) sections cascaded, and $\propto$ is 2 Cosh $^{-1} \mathrm{f} / \mathrm{f}_{\mathrm{c}}$. Cosh ${ }^{-1}$ means the cosh function (a hyperbolic trigonometric expression) whose ratio is $\mathrm{f} / \mathrm{f}_{\mathrm{c}}$. For those readers who are not familiar with this function Fig. 7 gives the relationship between values of $\propto$ and frequency ratios, normally encountered. Note that either $f / f_{c}$ or $f c / f$ is used depending on
whichever gives a value greater than one.
The following example shows how a constant-K filter is designed to given response requirements.
The basic design formulae are:

$$
L=\frac{R_{o}}{2 \pi f_{c}} C=\frac{1}{2 \pi f_{c} R_{o}} n=\frac{a d B}{8.7 \propto}
$$

The values given at the start will be $R_{0}, f_{c}, \propto$ and $a d B$. We need to establish, in the synthesis situation, the values of $\mathrm{L}, \mathrm{C}$ and n . The necessary configuration is established by logical deduction of the appropriate placement of components in the sections.
Example: Design an high-pass filter having a cut-off frequency of 10 MHz and a signal attenuation of 100 dB at 5 MHz . The characteristic resistance is to be 50 ohms in order to match the existing system into which the filter is to be fitted.
$L=\frac{R_{o}}{2 \pi f_{c}}=\frac{50}{2 \pi 1010^{6}}=0.769 \mu .4$
$C=\frac{1}{2 \pi f_{c} R_{o}}=\frac{1}{2 \pi 1010^{6} \cdot 50}=318 p F$
To determine $\propto$

$$
\frac{f_{c}=10.10^{6}}{f}=2
$$

From the chart $\propto=2.64$.
Number of stages required, $\mathrm{n}=\frac{\mathrm{adB}}{8.7 \alpha}$

$$
=\frac{100}{8.7 \times 2.64}=4.35
$$

We cannot however have 0.35 of a stage and therefore must use five stages to obtain at least 100 dB attenuation at 5 MHz .
The formulae for $\mathrm{L}, \mathrm{C}$ are for the basic section so we have half values accordingly, giving us the circuit of Fig. 8. We could just as correctly divide the system into a $\pi$ rather than a $T$ configuration. Design of a low pass stage proceeds in just the same way.


Fig. 6. A T-section filter and its equivalent $L$-sections.


The design of band-pass and band-stop stages is more complicated going beyond the scope of this course. Suffice to say that the components in the arms now become series or parallel resonant combinations. The basic L-section for a constant-K band-pass is shown in Fig. 9a and the basic L-section for a band-stop in Fig. 9b. Readers who wish to pursue these can obtain guidance from the reading list.

## M-DERIVED SECTIONS

As can be expected the simplifying assumptions made in the constant-K design, to obtain a reasonably straight forward mathematical procedure, also create practical disadvantages. The first defect is that the image impedance does not remain constant and varies in such a way that noticeable reflections occur near the cut-off points. The second defect is that the roll-off is slow just near the cut-off point: it is adequate further away from that point.
Zobel's concept to overcome this involved additional cascaded stages that, in effect, flatten out the passband response and sharpen up the cut-off point attenuation. These extra stages are called $M$-derived sections: one is usually added on each end of the ladder designed by the constant-K method.
We can only give an example circuit to illustrate this - Fig. 10. Although the formulae for arriving at the values are simple they must be applied with great care, the user having adequate experience in order to know the
correct procedures. Again we must leave it to the reader to take this up in more specialized texts. The design of a full M-derived system requires extensive effort and training and is much more the task of a professional circuit designer than the reader for which this course is designed. The most extensive application of M -derived filters has been in communications engineering telephones, telegraphy and multiplexed radio links. Voluminous books have been compiled that list tables giving values for chosen designs. Special computer program's have also been developed to provide automatic constant-K and M-derived section filter designs.

## ACTIVE FILTERS

## The basic active RC building blocks

Passive filter designs had reached their present sophistication as much as 50 years ago and in the absence of anything markedly better they continued to be the most used design until the mid 1950's. Amplification was added. to make up for the attenuation that usually is experienced
with passive designs.
With the introduction of reliable and less power-thirsty solid-state amplifiers in the late 1950's came the so-called active-RC filters. These combine an operational amplifier with passive RC components thereby producing filtering action more efficiently than the more obvious passive network followed by an active stage. One very valuable feature is that the effective value of, say, a capacitor can be multiplied up many times on its actual value thereby saving space and enabling designers to build circuits needing large effective values. It is also possible by active filter design to avoid the need for inductors in filter circuits. Inductors are best left out, if possible, for they are usually bulky, expensive and very lossy - they are nowhere as "ideal" as capacitors. They also are non-linear in operation and carı be saturated by excessive current.
The basis of an active RC network is more often than not a reasonable quality operational amplifier set up to provide one of the following four basic circuit concepts.

1. The high gain ( 60 dB or more)


Fig. 10. Example of constant $-K$ filter with $M$-derived end sections.

## ELECTRONICS -it's easy!



Fig. 11. Realising an INIC with an op-amp.
voltage amplifier with close to infinite input impedance and almost zero output impedance - in short, the normal mode of an op-amp as we have discussed previously.
2. The low gain ( 20 dB or less) voltage amplifier, also referred to as a voltage-controlled voltage source or just VCVS.
3. The negative-emittance or negative impedance converter NIC. This is a most interesting system block for it enables positive value capacitance or resistance (that obtained with normal capacitors and resistors) connected at its input to appear as negative value capacitance or resistance at its output. It enables circuit designers to physically build circuits requiring non-physical negative capacitors and resistors. (INIC indicates an ideal current-inversion NIC, and VNIC indicates an ideal voltage - inversion NIC). A typical realisation is shown in Fig. 11.
4. The Gyrator. This is another intriguing unit for the output appears


Fig. 12. The gyrator.


Fig. 13. Single-loop feedback active fi/ter schematic.
as the reciprocal of any impedance connected to its input. Thus a capacitor at its input appears as an inductor at the output. The gyrator, therefore, eliminates the need to use physical inductors and what is more, can provide more "ideal" inductors than real units. It can be realised using op-amps as shown in Fig. 12.
With these four basic possibilities available the circuit designer is rarely


Fig. 14. The multiple-feedback-path active filter.
restricted by having synthesised a circuit needing non-physical components.

## ChOOSING AN ACTIVE <br> FILTER DESIGN

Given the above four svstem blorks it is possible to produce an incredible variety of active filters. As with advanced passive designs, few people have enough training to be expert active-filter designers. Here we can only give a guide that provides the necessary awareness of what to look for, along with words of caution as to what it is reasonable to expect from an actual active-filter design.
The voltage amplifier can be used in its simplest conceptual way with a single-loop feed-back path (SFP) as shown in Fig. 13 - remember how we have already seen that an op-amp integrator acts as a low-pass filter and how a notch-rejection filter, introduced into the feedback path, produces a notch-acceptance response instead.
Alternatively, we can make use of multiple feedback paths (MFD) as depicted in a general sense in Fig. 14, the design using minimum component count. These, somewhat surprisingly, use fewer passive elements than single-loop circuits. For this reason this form of active filter is the configuration most often used.
Our discussion about active filters will be continued next month....


# Electranics โตm 

## ONE CHIP OR TWO?

Microprocessor chips that is. Is it better to build a microprocessor with only one chip or do you really need two? is what semiconductor manufacturers seem to be saying at present. Rockwell International have recently announced a two chip MP system designed for equipment manufacturers requiring a complete microcomputer capability for a total component cost of about $E 17$ in quantities of 1000. The first chip in the high speed PPs-4/2 system has a clock, CPU and 12 I/O lines; the second chip contains a 2 K by 8 bit ROM, a 128 by 4 bit RAM and 16 1/O lines. The prices of the chip pair in low volume quantities is only about $£ 40$ but before you rush out to order one you should know that the ROM masking charge is about £600, with a MP system with a separate ROM you can use a PROM and mask it for yourself at a lot less. A typical layout of the two chips and the additional chips required to make them do anything is shown in the photograph. As you can see the system can easily interface with a 64 key keyboard, a 16 character alphanumeric display, a printer, cassette, modem and TTL sub-systems.

For those of you want to build a microprocessor now (I would advise waiting for a few more months) there are some new kits available from Cramer. The kits are available with Intel 8080 chips, Texas chips and others. We hope to carry more information about these kits in the near future when we can find a good cheap printing Teletype to interface with the kits - any ideas?

## THE CASE FOR A BETTER BOX

Having built your microprocessor or clock or power supply or whatever your latest project is do you put it in an old tobacco tin or something similar or do you attempt to make it look good both inside and outside? Vero have just published a small booklet that may help if you are as fumble fingered as I am. It is entitled 'Vero products for the home constructor and costs 10 p . It
introduces you to Veroboard and the tools and accessories that go with it including a rather nice range of cases. How many of you still cut your copper strips with a knife or a drill bit - have you never heard of a face cutter or pin insertion tool? Did you know that Vero make breadboards for use with DIL ICs, cable clips, PCB standoffs, plastic cases, metal cases and 19 in rack modular cases. All this information and more is yours for only 10 p and a $7 \mathrm{in} \times 9 \mathrm{in}$ SAE from Vero. I have used most of the Vero cases at one time or another and I can well recommend them as being well designed with screw mounting holes, PCB slots, etc all well placed. There have already been several projects (Frequency counter, stopwatch) using the new plastic cases with aluminium front and back panels, and these projects would not have looked quite so professional in any other case.

Vero's other latest case for the home constructor is intended for very large and complex projects either for home or lab use or for the small volume special equipment builder. The 1 gin card frame/case is basically a standard European 19 in rack frame with the optional extra of a surrounding case. The cased frame will take any of a range of plug-in boards or mobile carriers. The finished product allows for easy access to any module simply by releasing a catch and pulling the module out, a simple plug and socket system on each module allows for accurate alignment when the module is slid back into the case. As with most Vero products the 19 in case system is expensive, but if you are spending a lot of time and money on a special project then the additional expense of such a case might well seem insignificant.

## ARE YOU BEING LED ASTRAY?

Digital watches are here with a vengeance this Christmas with about 20 different types available with virtually any number of brand names on them. There are several points to be wary of before rushing out to buy a digital watch and I
hope that I might be able to help you to get value for money.

First decide whether you want LED or LCD display. With LED the display can only be seen after pressing a button whereas with LCD the display can be read in any good ambient lighting conditions. Some of the very latest LCD watches have a button that you press to backlight the LCD for viewing in darkness and with LCD technology now giving a much longer lifetime for the display I would join the experts in forecasting the demise of the LED watch within a couple of years. So for my first tip buy a back-lit LCD watch in preference to LED. It might cost you - $£ 10$ more but it is more convenient than either LED or analogue watches.

Secondly decide what functions you require on the watch. Most advertisers are using this rather stupid "Fantastic Two Function Watch' approach - what are the two (or three or four or five or six) functions?
TWO FUNCTIONS - Hours and Minutes (1 button push).
THREE FUNCTIONS - Hours and Minutes (1), seconds (2).
FOUR FUNCTIONS - Hours and Minutes (1). Seconds (2), Day number (3).
FIVE FUNCTIONS (a) - Hours and Minutes (1), Seconds (2), Day and Month number (3).
or (b) - Hours and Minutes
(1), Seconds (2), Alpha Day (MO,

TU, WE) and Day number (3).
SIX FUNCTIONS (a) - Five (a) plus Alternative Time Zone.
or (b) - Five (b) plus

Month and Year number.
A lot of date watches now have 28/30/31 day calendars automatically but only if they also have month number as a feature. This is not an extra function but should always be available with the 5(a) type of watch. Watches with no month number feature usually have a 31. day calendar for each month, but this can be set back to the 1 st without affecting the setting of the Hours, Minutes and Seconds counters.

Back-lit LCD watches may be
available with an alarm feature sometime in the middle of next year. The alarm will vibrate on the back of your wrist rather than emitting a loud noise. No further details yet but 'watch this space.'

Prices appear to reflect the number of functions and the case style. For a stainless steel case work on the basis of $£ 10$ plus $£ 5$ for each function. Thus a two function watch should cost about $£ 20$ and a five function about $£ 35$ (plus VAT at $8 \%)$. For plastic cases subtract about $£ 2$, and for gold (plated) add $£ 1$ per micron of gold ( $£ 5-£ 10$ ).

Do not buy a watch with a gold thickness of 10 microns or more because this then becomes an item of jewellery and attracts $25 \%$ VAT. Beware of watch kits. Most of them are not designed for the amateur and require very fine instruments for construction and good equipment for adjusting the crystal accuracy. The rumour is that Uncle Clive has overcome most of his problems with his ITT watch chip and will be announcing his plastic watch within the very near future. On the basis of past products it will probably be available as a kit as well as a finished watch. Clive's kits are very good and are designed for the amateur but it might lead to a bulky watch - if it does come out as a kit I shall attempt to get hold of one for review in Electronics Tomorrow.

A final warning on watches - 18 months ago a simple LCD two function watch was selling for over $£ 200,12$ months ago it was about $£ 100$, six months ago about $£ 50$, now it would be about $£ 25$. Watch prices may halve again in the next year or so but they will not go much further. You may be able to buy a two function LED watch for Christmas 1976 for less than $£ 10$ but don't rely on it.

## REFERENCES

1 Rockwell International, D-6374 Steinbach/Taunus, Industries trasse 8, Germany.
2 Vero Electronics Lid, Chandlers Ford, East/eigh, Hants SO5 3ZR.
3 Cramer Electronics, 16 Uxbridge
Road, Ealing, W5 $2 B P$.

## ANY GOOD AT CEEFAX?

If you have already built a Ceefax decoder ihen ETI would like to hear from you. We are in an advanced state with a CEEFAX receiver and wurd like to compare notes with other constructors. We don't think it would be right is start a Ceefax series until we have got all the problems sorted out. Contact ETI on 7308282.

## THIS IS AN EI GAL



A couple of months ago we got a few T-shirts for the staff and a few friends; so many people have asked if they can get one that were making them generally available.

Made in cotton (yellow with black printing), these are available in three, sizes, suitable for both sexes.

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## SIMPLE OSCILLATOR

The negative resistance region of a reverse-biased silicon transistor can be used in a relaxation oscillator circuit. Its advantage is that a surplus transistor is used instead of a UJT (which is more expensive) and it does provide a minimum of components. The frequency is governed by the time constant RC, the power supply voltage and the size of the negative-resistance region. The latter also governs the signal amplitude, so various transistors (from a surplus batch) should be tried for best results. The output is a sawtooth waveform with a mean dc level around 8 V . Replacing the resistor with an 80 ohm earpiece makes an effective buzzer.



UNIVERSAL WIPER DELAY


Fig. 1. Wiper Display Uni?
Having recently experienced some difficulty in trying to fit a thyristor type wiper delay unit to the car, the trouble was eventually found to be a result of the design of the car's wiper
circuit and also by noise spikes which spuriously trigger the thyristor. The following circuit should overcome these problems in both negative and positive earth vehicles.

IC1 is connected in the astable mode, driving RLA. C3, D1 and D2 prevent spikes from the relay coil and the wiper motor from triggering IC1. VR2 is adjusted to give the minimum delay time required. VR1 is the main delay control and provides a range of from about 1 second to 20 seconds. SW1 is an override switch to hold RLA

permanently on (for normal wiper operation).

The relay should have a resistance of at least $150 \Omega$ and have heavy duty contacts. A set of change-over contacts, as shown in Fig 1, are necessary if the circuit is to be used on a char whose wipers are wired as on the Anglia or Cortina (inspection of the car's wiring diagram will confirm this).

The suppression circuit shown in Fig 2 was found to be necessary for the protection of IC1.

Tech-Tips is an ideas forum and is not aimed at the beginner. We regret we cannot answer queries on these items.

ETI is prepared to consider circuits or ideas submitted by readers for this page. All items used will be paid for. Drawings should be as clear as possible and the text should preferably be typed. Circuits must not be subject to copyright. Items for consideration should be sent to the Editor, Electronics Today International, 36 Ebury Street, London SWIW OLW.

## STABILISED POWER SUPPLY

The operation of the circuit is quite simple and straightforward, as regulated power supplies can be considered merely as special kinds of feedback amplifier. Here, the output signal is sampled by R1 and R2, and compared with a reference voltage supplied by D2. The resultant correction signal is fed back via the 741 to the series pass element Q1. Note that the stability of the circuit is improved by supplying the reference source R3-D2 from the stabilised output as opposed to from the unstabilised input as is usual. In order that the circuit operates when turned on, a leakage resistance R 4 is put in parallel with the series pass element. This ensures that the feedback loop starts to operate. No regulation is lost as a consequence of R4, because it is the overall output that is sampled by R1-R2, and so the effect of the ripple current flowing through R4 is corrected by the feedback.

The output may be made variable

## BATTERY TESTER

This device tests the condition of dry cells. The circuit consists of a simple oscillator whose output frequency is relatively independent of supply voltage, but varies greatly with changes in supply impedance. Thus, with the

component values shown, a fresh battery or cell will give a note of about 500 Hz , whereas an exhausted cell will give a note above 1 kHz . The device has been tested with battery voltages between 1.5 V and 14 V , using a 2N2923 as Q1, and an OC81D as Q2. The unit is undamaged by reversed supply potentials.


This device gives a two-tone alarm from a digital clock. It may be used with any CMOS alarm clock chip having an active high alarm output and 1 Hz (optional) output. It was built to work with a CT7001 chip and requires no interface components.

The 555 operates in normal astable mode when the alarm goes high (ie point (a) approaches VSS). Pin 5 is the normal control voltage input and swings from almost VSS to VDD via the 27 K resistor at a 1 Hz rate. This causes the audio output to switch
supplied from the unregulated input, with consequent slight loss of stability.

The amount of power the circuit can deliver is limited chiefly by the current rating of Q 1 and the rated output of the unregulated supply.
by replacing R1-R2 with a potentiometer, but in its present form, the circuit cannot be made to regulate below the zener voltage of D2. If continuous variation is required, the reference source R3-D2 must be


## tech-tips

DIGITAL DIE


This device is based on a multivibrator (ICI) which has a frequency of about 1 kHz . Oscillation continues as long as the input to pin 12 is high; as soon as the input is taken low or connected to earth it stops the cycle. This is used as
a stop switch which causes the LED to indicate the random digit.

A diode AND gate, made up of D1 D2, D3 and R7 is used to reset the counter (IC2) to zero so that only $0-6$ are counted. To stop the 0, pin 5
of IC3 is connected to the negative supply. A $4.5 v$ or $5 v$ supply may be used; capacitor C 1 reduces the noise on VCC line when TTL outputs switch logic states.

## PRECISION VOLTAGE DIVIDER

This circuit has the advantage over the simple 'two resistor' voltage divider in that the voltage ratio $\mathrm{V}: \mathrm{V}^{\prime}$ does not depend on the current taken from it, The ratio of resistances $R: R^{\prime}$ sets the voltage ratio. The OP AMP detects any change in this ratio via $\mathrm{R}_{\mathrm{f}}$ and provides correction. The actual voltages used will be limited by the upper and lower operating voltages of the OP AMP. The circuit shown was designed to provide 15 V for operational amplifiers from a single supply.

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\section*{PULSAR CLOCKS}

We would like to apologise to those readers who have had to wait for the
delivery of their Pulsar clocks.
This offer was so enormously popular that stocks were exhausted very quickly. For the first two months, we were able to get new supplies very fast but new deliveries stopped for several weeks.
We hope that by the time that this. issue appears that the back-log will largely have been cleared, but we are entirely in the hands of our suppliers.

\section*{BALANCED MIXER}

This single-balanced mixer has low distortion and conversion loss and is designed for applications requiring large quantities, such as television tuners, こATV converters, FM stereo, mobile radio and instrumentation. Its \(2 n d\) order

distortion intercept is +32 dBm ; its 3 rd order intercept is +8 dBm . Conversion loss is 6.5 dB and isolation (LO to RF/IF) is 45 dB at \(200 \mathrm{MHz} ; 25 \mathrm{~dB}\) at 900 MHz .

The Hewlett-Packard 5082-9200 printed circuit balanced mixer covers the range dc to 1200 MHz (RF/IF) and 100 to 1200 MHz (LO). It uses a monolithic Schottky diode pair and a printed circuit transformer. Celdis Ltd., 37/39 Loverock Road, Reading Berkshire.

\section*{ERRATA}

International 25, October, p28. R51 and R52 should be 330 ohms. International 25, November, 155 \& 56 In Fig.2, the emitter and collector lables of the BD266 are reversed. The correct way to mount the BCX35 and BCX31 (05,06,011,Q12) is shown below.


\title{
ETI HELPING HAND COMPETITION
}


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This is our open competition to find solutions for problems facing the deaf.

This closing date is March 31st 1976. ETI and the Royal National Institute for the Deaf (RNID) are co-operating fully in the organisation of this competition.

Three problems are shown above. We invite individual readers, clubs, schools, universities, companies, in fact anybody, to develop a practical
solution. The rules are as basic as possible and impose virtually no restriction apart from insisting that any Patent Royalties are waived if the idea is produced.

The prizes, three in all, will each be a silver trophy specially designed for ETI. At the close of the competition the magazine will hand over \(£ 250\) to the RNID to help with development costs. There is a \(£ 1.00\) entry fee (payable to

\section*{THE PROBLEMS}
1. A sick person is being looked after by a deaf person. The deaf person has no useful hearing and requires to know whether the sick person is all right and above all needs to know if the sick person is in a state of distress anywhere in the sick room
2 A hard of hearing person is attending a College of Further Education and has considerable difficulty in understanding what the lecturer says due to his distance from the lecturer and to the background noise in the room. A device is required to enable him to make the best possible use of his hearing.

3 Many deaf people have great difficulty in using the telephone and in fact many of them cannot use the telephone at all. The development of a writing tablet which would allow them to write a message on a small pad and for this to be communicated over the telephone line to a pad at the other end would have many great advantages. In addition the communication should be two way so that the person can receive a message or an indication that the message has been received.

RNID) and this will be added to the £250

Background information has been prepared to help readers and say what is alreayd known. This is available from ETI on receipt of a large self-addressed envelope. Enquiries should be sent to:

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