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$25270 x t o r d$ Street Landon WRR 1FF Telephone 014341781 '2 Telex EB11896

| Rick Maybury | R(a) Merris $\mathrm{E}^{\text {a }} \mathrm{Sc}$ | Editor |
| :---: | :---: | :---: |
|  | y len Graham B Sc Henry Budgett | Editarial Assistants |
|  | Drego $M$ Rinson | Aot Dirseter |
|  | Pete Howells | Produttion Editer |
|  | Paul Edwards Tony Straras | Technical llustraters |
|  | Ray Marsion | Projoct Editar |
|  | Jahm Fixaterald | Project Engineer |
|  | Steve Ramsahadeo | Project Development |
|  | thargaret Hewitt | Adminisiration |
|  | Aan Caritan \&Managen Kıro Hemlin | Rieader Services |
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## digest..

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[^2]
# Stevenson Electronic Components 

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# POWER AMP SURVEY 

The Americans would describe it as a 'crowded marketplace'. Power amplifiers appear almost daily and the resulting choice can easily lead to confusion. Ron Harris attempls an overview,


UFGRADING HIFI is a cosily business using commer cisl unts as betior can somehow read dearer arce oyer the threshold mito a hifi emporium Once conracted however the improving bug is no respoctor of price and pocket

Quite commoaly the malady can be caught wia the congs of new loudspeakers which are crying out for more wetts to drive them The amplifier fust has 10 gol

## The Modular Comnection

Ons method of gaining the extra power - if you re quite content wath lacilities etc - 15 to replace ourput stages of your present equipment with two power amplifier modules There are certainly enough on the markot to choose from

This will sertanly be chapar and most of these modules outperform simularly priced commercial units so performance need not sufler Since you need not nocessarily hava to pay for a PSU and case you don! need it must be cheaper Vary often too the exising case can be ualrsed to house the new boards with atondant saving in that most onerous of tasks metalwork

Judfird by the contrituing popularty of the audio projects which appear within ihase pages do it-yoursell hi ficonunues to abound even though building up from serateh is ofien no cheaper than buying commercial units Modular construction - with mosi designs being Dre tested - can make this rask easier and more certain

With kit consiruction however there is obviously more to go wrong and this tents to mean the resuts are more dependent (at times') upon the constructor than the supplying company whe have been told by several reputable kit supplars that the greaiest single reason for non functioning units is poer soldering'

## Board Decision

With the large number of avalable kits for power amplifiers in mind we deeided to exclucde them from our deliberations and concentrate on modules alone This was defined as a unit in which the amplifier is suppliad completely preassembled in oither words as a PCB which can then be utilused

Undoubtedly there are some modules we have missed oat in our scan across the adzens - and if you know of amy we have mitsed please let us knows that as fow inusuices as possible are perpetrated

[^3]Uum woll have ser up the anplliter alieaty and hopefully trated a few io specification all thet shauid remair for then purchaser to do is to conneet up a PSU some unput and output sockets and a case Muse shou to hen flew forth - suntably amplifed
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## Choosing

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new underpowrred predecessor remember ilial io obvem a barely perciptuble ancrease in sound volume ( 3 IE) you wif need io DOLLELE prower oulpu1 to mack nequblours out of hed to it they coupd sire through your 1812 iendengus befor- wal pexta 10 W is
noigning to add sigulicin1 umph to yoir prorthoros ouplisation and be ternule with ondimee oun irell ithan wo underpozer and retiret if laler the rovient ledine deppurds upon the volume of the room wou intend to whay

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securely in the cavernous bosom of the GPO and should they evar be disgorged Audieptile will be more thar pieased to follow ip and complete the project

Anyway oniy slightly daunted we shall proceed with what we have and consider the two amplifiers which cird amve (and the ane on Ioanl)

Our source for the listening resis mas to be a solny EL 7 Elcasel machune which gives reelto, reel quality of reproduction wathout all the time consuming drawbacks of that medrum When vou re tiying to compare several pleces of gquipment such luxuriaus convenifence is not to be scornad lightly

I could never understand why Elcaset has not done better tor iself The Sony machines in partsular ofler a standart at reproduction tat above that which ony cessette mochins achueves

The reference amplifigr was a Lecson AP3 II

## AL-120 RI-PAK

This unit amves thrse quarters wiapped in a black heatsink with connection boing made to pads at one end whrch protrude beyond the edqe of sald heatsink The output par ( 2 N3055s) are bolted ro the back of the hearsink and are hard wired into the circuit

The quality of constructon vas ganereal y high and in use the AL 120 s pave us no mouble at ad They drave the requised speakers (Celestion/KEF) with no apparent distrass and gave a sound technical account of them selver

## Grimson GEE0B

There s nat really a lot to say about Crimson Elactrik that hos ret been said alteady Ther producis are weil constructed well thought out ant well thought of the Cebor is me axception

Crimsan supplied us their unt completely assembled writhin the supert metal work shown in the photograph which melutles a PSL and stablases board to run one of their pre amp motiules

The metalwork is black and in style looks not unitike a Quad 405 power àmplifier unit

## iLP hYso

Since these ara completely encapsulated we cen offer no real comment on constructional finish A mere five pins protude from the mopralwork along which travels all communteation between the HYSO and the world

## Three in A Testibed

Once introduced to then proper PS $\mathrm{L}_{5}$ all th ree amplither's tunctioned well and gave no real problems at alt The ILP gove a poorer hum verlormance than the others regardless of how we thed to ware it so tha probien must lie within the black box

Of the three the Crimson geve what musi be regarded as the best ovetall performance its sound is very cigan and th possesses good athack However tha BI PAK A2 120 was not for behard and loses out mamily due to a slight lack of iransparency when directly comparged 10 The CE6OB it has a warmer sound overall too and one that many poople mey weld preler

Alas the ILP HY50 did nos produce reproductaon of the same quality as the other two The rest modulea are about three years ofd though - our neve review samples not having turned up in zime - so things may well have improved here We hope to give a listen to some more


日I AAK* AL- 120 nodula, ramored from in a heassink The
 haptguilk


The Magmum Audia range Thair power amp is shown in The wenkre forrgapund Nole that this in fact a dual ung moorparatang two amplitier cifeuiza.

The ILP H*50 Thas is an thtopend latad uris, and onaly tare pins ara racq ulfed for connet. tien purposias

recent samples at soon as possible to contirm or deny this but as at is the imporstith same of a had grity sound which was immedialeity ds'ingulshud in compsr 180חオ

## Conclusions

Well there it is Nol de complete as might haw boen bui very interesting twe hopel norettcless As for the compar sons twe never gDt if the manufacturers agree we II follow those up in the nexi fow issues in Audiophole


Laft: the Sinny EL. 7 Etontel unst whier prourd the source for the listering 1世sidi. Somehow tho maching hat tov* ratolued the attertror it dostinet fat ite par4ow maticic.

Belowe: fentund you of anything? Looking like esquastad 4t 5 is the Crimaon unit all bexed and set to go.

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# VIDEOGRAPH 

Turn your colour television into a dual trace oscilloscope with this UHF colour modulator and video display generator.

THE FURCHASE of even the simplest oscillosrope is prubably umpustited tor mast amateur electronies constructors Other amateurs feel rightly ol wrongly that their money is better spent on propects which ather members of the family can appreaciate'

Which ever category you triong to or Even if you ase in the scope leegus already Videograph will be found to be a lascinating and useful puece of egripment which will give many hours of plasure

## Princeple Df Operation



The videograph makes บse of the fact irhat the telewision screen is scanned trom top to bothom evacy 20 mS This is used as the effectiva
oscilloscope timetbase trace modulation baing obtained by varying the timing between start of


Sinewave geonrenen with Videbgreph
oach line and a fixed-length binght up pulse

Two complete cricuits are required ro produce a twin trace and these are colour coded blue and orange respectively Thesecirculls are uggerad by a comman syne pulse generater and further componemts generate an eight-stage background colour change trieggered by peak signats There is also an internal freme-locked square mave gencrator wheh serves as a test waveform for irfection into amplitiars and tape recorders

Contols are provided for inverting ane channel treezing the tackground colour and switching a filter to give a relatively smopth music display

Complete kits can be obtaned from Uhtlliam Stuart Systems itd who hole the FCE COpywright They olso produte a ready drilled cabinut The heavy gauge anodised facia plate s gerean printed to improuta trinish and the PCBs are galk scraened to ald construction

## Construction

Two pronted enctur board assembles tre invoivad one consisting of a UHF Colour Modulator and the other the -

Fig 1. UHF Colour Modulator crreuit diagram
HOW IT WORKS -MODULATOR

ensures that no earth loop can
exist between the TV and the hi-fi some equipment

## Setting up

The modulator tuming capacitor is set
to $30 \%$ of maximum. Generator anticlockwise The GAIN controls should be at minimum and the
 pue A1 4 loq vo yolims pue alqe3 woн bulyoreas 'pubis poobe channel 21 upwards The picture
ue ani 6 ot (ouAs әu!7) 6AH $15 n 1 p y$ isnlpe pue punos Bulunt $A \perp$ ayt zeut papiand Alliqeis eq MOU p|nous əintard aut foexa s ere red then adjust RV8 iframe pulse width) for best C3PACITOR 5 IOn pulyation $\begin{array}{ll}\text { C6 } & 222 \text { p thmine } \\ \text { C7 } & 2 \text { cenme } \\ \text { C8 } & 8 \rho 2 \text { ceramic }\end{array}$

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Fig. 2 Colour modulator componemt overiay.

## PARTS LIST-MODULATOR


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legends so that componenis can be Nore that each board hes a separate
The ICs should be mserted last of all and $1 C 7$ on the generator board should be left out initially instead shown This gives a fixed green background and results in easier setting up and tuning. IC can be Inserted later on to give the
dutomatic colour change
पुวe 아 palveuwoo gגe spjeoq atl other by short lengths of wire Field, + ve. Video B.R, G, $X$ and Sync All the controls can be board mounted and the only other wires needed are for connection to the aerial and DiN sockets. and 9 volt
power
The aerial socket can be connected directly to the modulator via two closed loops. one on the
board and one on the socket The
loops are simply bent to couple
poytow sicl dayio цaea ylin Ajasojo

A
galb ol gat pue zAy isnipy and orange vertical stripes these
PROIECT: Videograph







Fig. 6 Videograph generator component overlay


Circuit boildse completed and
installed in the Videograph chassiv


No, it's not something from outer space!

## BUYLINES

A complete kit of parts is available for this project from William Susart Systems Lid. Dower House, Herangate. Brentwood, Essex CM13 3SO. The PCBs reman their copyright and will be available only from them. Alt components are available separately, and the PCBs are normally supplied as a minikit' along with ICl-3 and ready wound coils. See advert elsewhere in this issue for prices.
pots are turned clockwise Position both siripes centrally, theri separate them using the LOCATE controls At this stage the line sync (RV9) should be fine-adjusted to give perfect colour registration on the stripes

IC7 may now be inserted (and the link Temoved!! to give the background colour change function the sequence being black, white. cyan yellow green mauve, blue, red.

## PARTS LIST－GENERATOR

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# Gm REVISITED 

## Nothing to do with American car manufacturers Gm is in fact a throwback from the days of valves. now finding a new lease of life with up-todate semiconductor devices. K. T. Wilson explains ....

WANY A LONG YEAR ago when Jrdnsistors weme an leem which hadn t been dreamt of by science fiction writers we all used valves and we all know the magic derters $G m 6 \mathrm{~m}$ slood for a quantiv callea mutial conductance and it measured an impostant featare of the volve from which we could work out how much woltage gan we could get out of a given bome well the years have passed anc values ale dead for matry purposes but Gm lives and is back woikinit for Lis

It s odd that Gom should have gome our of esh on for so long becauge the dea of Gm is fwen more uselu in trangistor anmplifier circuits than it ever was in valve carcuils Sill the ideca seerns 10 be conting back in a hug way solet 5 trake a look at II

Mutual conduclanee af any elactronic dabice means


 hase anden entter Fig 1 Thi squaldgle above the letirrs
 talking about not the sleady blas wodeges and currents
 tramsistor as a gemerator of ssgral currenis the ampunt of segnal current being Gm Vin Now a current gernerator means a device which will deliver its currant into any loar tighor low tow valve or sembicanductor is ipally lake this but for most of the usan way make nt transistors tha Ited of a current generator is nol idr from the murk

## Current Generators

If a t'ansistor were a Derfen curtent gemerator it woutd have an imfimite testblarice at its output That means |ust thet a sigral voltage applied between the collartor and


Fq7 1 Mज्ञाज GOM ductames, Ic Vbu far in tromabilar




 realstor of around 40 K temween collactor and emitter

Now, the usofulness of all this is that it allows us to draw an qquivalont circuil for a iransestor An equlvalent cuctur is a circuit mode of sample components which anhaves in just the same bort ot way as sume dewice athich is in reality buiuh more complicated A simple equwalent garcuit ior a irarisistor is therefore as showit in Fig 2 If consuges of a current generator which generates a signel current Grmble and a cesrstor of atoont 40 k in paralkel This simpio circuit accounts for the size of the stgnal current at (he output (the collectar) and the autput ras stance between collector and emitter

How does this help us? Quate a lat if we Ierriember all the time thel equivalent erreuits are about signal currents not about bias sutrents As far as sagnal cumenis bre concertied the positive supply line of an amplifier is just as eartred as the earth line Why? Hecause in the power supply theres a smoothing capactor of seweral thousand microfalads connected between the +ve and -ve lines As dat as DC is conceinet this tapanitar is an insulator but for AC signals the capacitor is fust a share circuit shorting the the tine to the -ve leve Whan we connect a load re sintor between the collector 1erminal oi a transistor and -he pusitrve line theri as far as sigrats are concer red the Iodd resistor is conmec!ed betwoen collector and emater D ew bus mto the equivelen! curcut and the rosult is $\mathbf{F}$ ig 2 Back in the old days of valses dnostaig a cormer this') we found the sum of these ive resistors in parallel which was
and then the sateage signal out was fust the cusrent akial rime, this resiatance IOtim s Lew shil rulcs OK' s. 4 n
$\frac{\text { Gns Rce } R L}{\text { dee }+A L}$


Fig 3 For $A C$ aginele, in Igaed ramithor edrimetad betwan
 colitictor and amittien

$$
\begin{aligned}
& G M=\frac{g}{k F} \text { le } \quad \text { E CHARGE CARPIEDEY ANELECTRDN } \\
& \text { * = BDLTZMANMS COHSTANT } \\
& \text { F = TEMPERATURE IN KELVIN SCALE } \\
& \text { IE H STEMDV (BIAS) COLLECTEA CUAREMT }
\end{aligned}
$$

## Simple Silicon

One of the things that makes Ife simpler in these days of slicon transistors is that the quanticy fice the output resistance of the transistsor is quite a largo value comperad to most of the losed resstors we use An oulout resistance (the wsual symbot nowadays is h) of 40 h is quite a but larger than the 3 kJ or so we use as a load so that most of the signal eurrant from the frangister is through this resistor in the equivalent ciscult That simelities the output wolage to GmR. so that the gain of a) trangistor amplitige is jusi $G m R$

If its as easy as thel why don twe wee it in text books? The reasons ere husioncal - we thidn t stan with © I LoH ransistors arid a transibtor unl ne a valve doesn 1 have a conetant value of Gm If we plot a raraph of collecior current against base voliage as in fig is the result s not tha nice straght I ne we get when we plot such e grapth for a walur, or the not too crooked line vir gapl when ar bilom the grash for an FET but a vey curve ch hit inider a This incicater that the value of $C$ an is not conceary but a value vatirh chondem as hr curtent throwith int tralustor challzes This counted with the 'ather low eutput rasciance of the early foanar um


## Ebers Moll

A few years back though the Ethers Moll equation was noticed You ve never heard of i1> You re hoi alone very tew tept books mention it and some mention is whthout explaining it Vory bribfly it san equation which links the colloctor current with the vbe walue for a transistor in ather woreds it E the aquation for finding Grm Now tho full equation is a fearsome looking thing full of matherrstical symbols you may never have seen before It repays rlose attention though becausc most of tha symbols are ol quantities that are presty well constant and ondy two of them vary wfry much dre of there is tie steady bias current ic and the ather is temperalute As it happens temperature lor the pur paser of the Ebers Moll aquation is menswred in the Kelun scale which barts at the abspoule aroo of tim perature around - $2 / 3 \mathrm{C}$ Roor temperotu'e is heretore around 293 K tho degrees surit) in the Kelsin


Fig 4 Trartisten sircisil with load menster (kL) Gim cen be raplacad by 401 c
scale and a fen degrees above or below doesn 1 make retich difference to the gquation

Thet leoves ic as the one thing that reallyatfects Gim and the relationshap works out at approximately

$$
\mathrm{Gm}-401 \mathrm{c}
$$

Put in words that means we can 1 ke a $G \mathrm{~m}$ value of 40 limos the steady bias collector current in miliamps For a bias current of 1 mb the Gm value of a transistor is 40 ma a Too good to be true? Looks it but it really does apply to any silicon transibtor apart from a few track types

Thisbrings back ithe Gmudea ins bug way and wecan torget a tot of the ald formutae we once used in calcu lating the design of transistor amplifiers The fact that Cm is not constant bul waries wilh the bias current is odedy mough a help rather than a hirdrance

## Gain

Going back to owr aquivalent circuit and ignoang the large output resistance of the trangistor we ran now write 40 It in place of 0 m (fig 4) Thas makes the gan of a transistor wath toad resstor RL become 40 Ic RL Butic on this equation is the steady bias eollector current and so kFL must be the sleady $D C$ voltage across AL the load resistor This makes caiculaning the gatn of transistor amplifiers with resistive loads a bit gasier than talling off alog Pick o volue of voliage across the load resistor muluply toy 40 and that S vour value of gann

Fot example we very ofter design voltage amplitigra so that about half ot the supply voltege is dropped across the load resistor For a $9 \forall$ sumply that $s 45 \mathrm{~V}$ Co this and you can expect a yolvoge gain of $40 \times 45=180$ times Dont believe it it works all inght and testo on a single wansistor amplifier contirm it as a rule of thumb you don $t$ of courss expect to get a gain of exactiv 180
 on load resistors apart from anything else bul you re nevar far but that swhat a rula of thermits is tor

When you couble a ennale transistor amplatier to arouther stage of eourse that \& another stery You may have sel the yall of the first stage to 180 times but nist atl of its ouput signad enda up usefully at the input of the

next stage Reason? The next stage has a rather low imput resistance and feeding signal from the collector of one transistor into the base of another even if they are directly connected is rather like feeding stgnal through a voltage divider There are, in fact two ways of calculating how much of the signal is passed on One simple way is to imagine a voltage divider (Fig 5) in which the load resistance of the first stage forms the upper resistor and the inpur resistance hie of the second stage the quantity $h_{1,}$ (on $k$ ohms) is equal to $h_{\text {is }}$ Gm where $h_{\text {fo }}$ is the current gain of the transistor a quantity which does vary between one transistor and another For a transistor with $h_{f s}=100 . \mathrm{Gm}$ set to $40\left(1 \mathrm{~mA}\right.$ collector current) $\mathrm{h}_{\theta}$ is $100 / 40=2 \mathrm{k} 5$ If we feed this from a transistor with a $4 k 7$ load resistor the amount of signal reaching the second transistor is

$$
\frac{25}{25+47}=35
$$

of the signal at the output of the first This brings the gain of the first transistor stage down to $180 \times 35=63$ which is the sort of value we usually measure for one stage of a mult-stage amplifier

With all thes going for it GM is coming back. folks As Sam Goldwy is supposed to have sard. 'simplicete and add lightness Let s hope we ve added a bit of lightness today

ET



[^4]CHAFCROFT ELECTRONICS LIMITED

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# CUICK ELMINATOR 

Part two of the Click Eliminator article, presented here, is in fact a redesign of the project leading to better performance and lower cost.

In the January issue of ETI we presented a design for a click eliminator unit However between that issue and the time for the February ETI - in which we were to complete the project we found several disturbing inconsistencres which would have rendered the design s repeatability doubttul--to put it mildiy

These problems mamly concerned the area around Q 1 , IC9 and IC10 The biasing arrangement for 01, and its function within the cireuit means that the adjustments are very very critical indeed. Our prototype operated satisfactorily, especially in its breadboarded form, but was too dependent upon too many varables for us to be happy with the project

## Taking Aim

The aim then as now was to present a design for a umit which would remove the clicks and scratches from damaged LPs without impairing the music material contaned therem

Operatıon was to be indicated by an LED, and threshold of operation was to be variable to make the Eliminator flexible in use

However as we sald development work has continued since initial publication, and while we felt that there was nothing wrong with the alms of the propect, our method of realising them left something to be desired

## Change Of Track

Accordingly we are presenting here an aiternative design, and
recommend our readers to construct this in ineu of the design shown in Part One of the article A comparison between both crecuits will show this version to be greatly simplified, and using components which will make construction cheaper

For example the 570 has been replaced with a 4016 , which is closed to the signal for a short period of time to blank the click signal


Fig 1. Basic block diagram for Clrck Eliminator Mk 2


Fig 2. Carcuit diagram for the audıo pre-amplifier and delay line sections of the Eliminator unit.
Note that only one channel is shown, but both are identical.

## HOW IT WORKS















ELECTRONICS TODAY INTERNATIONAL - APRIL 1979

Fig 3. Circuit of the click detector section of the Mk 2 Click Eliminator. The LED flashes

## HOW IT WORKS


to indicate operation.


Fig 4 (a). Above: the waveform of the Click





As the block diagrams of $F_{i g} .1$ will show, the basic reman unchanged The micoming audio is delayed by a TDA 1022 . long enough for the circu 10 do which shuts off the transruission gate
4016) as the click arrives 8

The waveforms shown in Fig 8 give an indication of the timing of the blank penod is made to straddle the click signal Circuits and Components Figures 2-6 show the schematic lor the Click Elimmator Figure 2 is the dudio mput and delay line circuit Figure 5 shows the clrck detection components Inputs $A$ and $B$ come from points $A$ and $B$ rnarked on the left and right auctio mputs

Circuits 5 and 7 are the output blanking (and bypass) and system clock respectively The latter is eferred in the audso circuit simply ds $Q$ and $\bar{Q}$

## Construction

The unit is assembled onto a sirgle PCB and so construction is really board carefully, remembering to fit rosistors and capacitors tirst, and ICs last Sockets are best used for these devices especially the high cost items This will facilitate checking

I hee eassest place to make a mistake is in fiting the polarised components - electrolytics, diodes best to buld up the PSU first and check this before connecting to the rest of the circuif


Fig 5. Chick blanking carcuit. Note that

## HOW IT WORKS



Next assemble and check the audio circuitry Make sure a signal is present at the level control RV2a and RV2b Normally 1 CB gates will be open and so an audio output should be present at the phono sockets if all is well

If no output is present check the audio through to RV2, and If a signal is present here, the fault probably lies with IC6 and Q1 Disconnecting the base of 01 will restore output if this is the case

## Over the Threshold

In use the unit is connected between the output of a record player puck-up

and the input of a steree amplifier Volume control RV2 should be adjusted so that no perceptible difterence occurs in audio sound levels when the bypass switch is switched in and out Pre-sets RV1 and RV101 should be adjusted for mininium distortion on the Right and Left channels respectively Threshold control RV3 shoula be adjusted in use so that LED 1 just operates in the presence of a click'

It should be noted that the relative amplitude of a ciick is proportional to the velocity of the record track past the prok-up head and decroases as the head moves towards the centre of the disc the threshold control may
consequently need occestona! readjustment as the record progresses through its play

There is no equalisation crecuitry within our design, and so it cannot be used in place of the preamp in your system, it must be used in front of it instead

When playing damaged LP's simply advance the Threshold control RV3 from its minimum setting until the chick is removed This is the correct setting

LED 1 wili indicate the unit operation and if it flashes on musical peaks chances are you have the threshold control set too high and are removing some of the signal as wetl


Fig 7 Component overlay for the Click Elimmator unit. Note that All the components bar the potentiometers mount on this PCB. The operation LED is also best front panel mounted.

Being composed mainty of standard components, the Eliminator shoutd pose most component shops na problems The Lf 356 is avallabla from Wattord in case of dificulty.

PARTS LIST



## HOW IT WORKS



Fig 9. Some typical waverorms which ilfustrate the timing of the circuitry within the general block of the cfick Efiminator
Blanking puise width is fixed.

Close wp of the socket wiring for the Cfick Eliminator. Neep these as close to the board's as possible, and use screened leads if this is not possible, earthing only one end of the screen.



# $7 \mathbb{R}$ <br> The exciting new <br> <br> TRITON <br> <br> TRITON Personal Computer 

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 than fot is Amp inkide This has rocultad in a yonge of dual and quad us. ACTp packapec Teve have brought wut a rango of Etter
 ther are different in ibat ther, have FFT ITpuls giving them a her bigh ingut impedance
Chant I shatscemparada e ferfomarope for several standard op Amp type The pefameters chosen are the most vilporldat orys when colectung Op Amps

## Antio Amplifiers.

Scialal manufuturery prualuce monolithic thedium red a er


 ruis ari hagh of if inmutfirient hide wikimg it prosided The re dre rxas quite a wher range of clewaces somp nt zh hich arl shown in Chart 2

## Multipliers

 ativ more have been added to tho list garth inepumed the the neads of telephone compansion zu stemis Thi wosciom 5 produce a beter hginat ao nowe ratin ot pt the line Another and wert cummon huse
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## Dscillators

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CHART 1 OP AMP - ABRIDGED PERFORMANCE $s-\operatorname{single} \quad D$ - Ood 0 - 1 Owo

| $\begin{aligned} & \text { Opamp } \\ & \text { cyle } \end{aligned}$ | trput ofther whage miv | Irout Dtas cusfont ni | Tipe ol мры st ucture | Bard midith MHz | $\begin{aligned} & \text { Siew } \\ & \text { nig } \\ & 4 \text { NS } \end{aligned}$ | $\begin{aligned} & \text { Valtage } \\ & \text { gain } \end{aligned}$ $\text { Danco } \mathrm{CB}$ | $\begin{aligned} & \text { Moxamuin } \\ & \text { suppl, } \\ & \text { oplagy } \\ & \text { of } \end{aligned}$ | $\begin{aligned} & \text { CMAF } \\ & d B \end{aligned}$ | CT, | Commenis |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 709 | 2 | 300 | NPN | 1 | 025 | 90 | $\pm 18$ | 90 | S | Werits frecwency compansarion |
| 347 | 2 | 70 | NPN | 1 | 025 | 100 | $\pm 18$ | 90 | \$ | Interraf frequency compensation |
| 301 | 2 | 70 | NPN | 10 | 05 | 100 | $\pm 18$ | 90 | 5 | Needs treguency compensation |
| 741 | 2 | 80 | NPN | 1 | 05 | 106 | $\pm 18$ | 90 | 5 | Intermat frepuracy compenseitas |
| 748 | 1 | 120 | MPN | 10 | 05 | 103 | $\pm 22$ | 90 | 5 | A decompontated 741 |
| 308 | 2 | 15 | NPN | 3 | 05 | 170 | $\pm 1$ 㤩 | 100 | S | Low fnpply curvest ofunt Ansma Handt frapuiatey companpafron Very dow offlifrwntiai inpul voitege range |
| 318 | 4 | 150 | NPN | 15 | 50 05 | 106 106 | $\pm 20$ | 100 | S | Very taw differeminef mpons voitage range Sipmetimes meads fregubicy compensation |
| 747 | 2 | 30 | NPN | 1 | 05 | 106 | $\pm 18$ | 90 | D | intarazal firquench ciompremsation |
| 1458 | 1 | 80 | NPN | $\dagger$ | 08 | 103 | $\pm 10$ | 90 | D | insarmer frequencr companisation |
| 4135 | 05 | 40 | PNP | 3 | 10 | 110 | $=18$ | 100 | D | Lownorse |
| $\begin{aligned} & 3900 \\ & 3401 \end{aligned}$ | Cursent inputs | 30 | Current Supks | 25 | $\begin{aligned} & 06 \\ & 20 \end{aligned}$ | 70 | $\pm 18$ | - | 0 | Current balancrig ampitifier |
| 324 | 2 | 45 | FNP | 1 | 05 | 100 | $+30$ | 70 | 0 |  |
| 3403 | 2 | 150 | PNP | 1 | 12 | 100 | $+36$ | 90 | 0 |  |
| 348 | 1 | 30 | NPN | 1 | 05 | 103 | $\pm 18$ | 90 | 0 | $\left\{\begin{array}{l} \text { Low powey } Z \text { dma dram par ic } \\ \text { Class } A B \text { output } \end{array}\right.$ |



## TELEPHONE CALL TIMER Submitted by Mr A. M. Tucker ol Dorchester.

TO CARRY OUT its funcion which is to display tme cest of individual calls, and also to ksep a tunnmeg total of all metrered calls the circuit must add the ameunt of the unit charge (at present 3 py to pach register when the call commences, and subsequentlyat the end of sach charge period Thas period well vary for peak slandard and cheap tumes and with distance Provision should be made for altering the settings of the counting circuits if there is a change in the Post Office charges
$V$ Vatous circults were considered, and this was considered in be as cheap to make as any for the teciltites provided es although there is a largenumber of iCs the bulk ore low priced

The two sets of figures sue erculated in a single shifi regifiel, the digits baing interlaced, ie the least signficanl figure in one reg stor is followed by tho loast significant figurg in the ather register, and then by the rext figure in the first regislet, and so on:

In order to be cble to adjust the unit charge, and the pervods awailable per unit the outpula of the dividers are connectad to sockets imo which leads from the inputs of the resetting gites ate plugged. These sockels pius "parking places' for sparegates. can be made from 1 C sockets or soldercon pins in plastic sugports Te prevent damage to the pins of sockets when curting into sec tions push into a piece of rigid foam plostic. The wander leads are usi langhts of connect ing wire Solid care is suitable if stranded wire is used. tim the end and check that it is thin eneugh to insert in to the socket

In the interosds of economy small hov consumption displays have been used If larcer clisikuys are required at witl probably be mecessery to add segment duvers the
drivers should dien be suppied from the uncegulated sade of the supply, and S1 made a double-pole switth

The 9 volt standby batery is essential, as ather wise the tatal cost register would be cleared in the event of a mains tandite In arder to reduce consumption duritg Idle time the counters IC1 fad IC2 and their associated gales. the oscillator IC21 and the display buffers and driver IC.2.-IC26 are switched of by S1 It is unwise to try to include other ICs as somc inputs may lue high. In any case with the oscallator ofi power consumption is very low in the remaning circurts

It may simplity the miring of a 4001 and a 4011 are surbstituted for the 4069 One NOR gate can he used instead of IC 200 and IC22a and a choice of ICs is avaliabie for the other invertera

I he metar can be adapled for battery powver only by incluxing a 4518 to divide the 10 kHz oscillator requancy down to 100 Hz and doubling the division in ICt by shifting each flying lead one place to the night Setung the ascillator fregueccy exactly can be carried Dut either by comparing the 100 Hz output with 50 Hz frem the ma ns on an oscilloscope. or by varving the seming unti the charges are dicremented at 10 socond intervals for long distance calls at peak tetes

Decoupling capactors for pulses in the supply ines may be required while CMOS is less exacing than TTL in thas respect 10 n non inductive capacitors should be trited across the supply pins of ICs at the end af supply lines and across each of the more complex ics

A flashing LED is provided as an indication (and reminderl') that the toming curcults तre operanng

## HOW IT WORKS

 swathed ow, and SWu and SWh nel When the person repites. 5 W 2 is sloved This
 atart ornthlify $304 x$ meins putser it ith
 pulse widet elears ihe thede call netiviet the difith betine selected by icflo and x相

Ar the trutimman of the puive. 0 gose
 IC thre pow bow foms of umul nete bs 1C7, Which te mabied by ine high 9 mutput
 anm the LSA of che regaifis are preseet al 4 , wrid $Q_{+}$af Cl , uritl the sutput coe. nesied to !Cze geen Heb. When ICAb



1.St three cycise of the athilh mpleters arp zactand to towement the reguters by 3p





A puloe stroicher $\{$ RS, $\mathrm{Cx}, \mathrm{D})\}$ inchuted is whur K.1 meis
Wher the fomeng polme redekes if iod.
 untrithartit the two nety of fequres ere
 and IC13 and yot kululnied Mreauth the adjer a(Clis fite dipls and veketred for

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#### Abstract

VClant Kan yceic $\ddagger$ a mote and the ther ctocking. putue rmquirte by the thith rogavers is prowided by KCll Whes ihe caH is enmpieted. swi in wiarthed la oft, and the reweth in FCI and Cibobly wapping the couent The com of the calt remark to ibe iegnere unic swhio etownil ior the wasd calt Al the snd of a    the fhp-fope ar eurrocrity $=$ in mikialts, and thes ficl is not searem tia the middier of a Whate pertod When mo more cesth are expected to be made for a whilk $\$ W$ ! th opmed drapp na   maisitalifutio


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The case is semstity nugged and is proiled an tha back with a Woild Time Zanas map, a th inf a chaek really, especially as that time is relistive to Jaman'

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AMEUSHI is a somce game par excellence I repaesen 18 a spaces ship (yours! that is about to be aracked by a ficet of sulcide crat! The craty can alrack you on one of four randomiv selecteo cuadrants the alleck s come one of a tome Aी tandorsly selected intervals that vary betwean nought and tove seconifs Your ship bas a limited store of armmunltion and veu cen defend the vesseh unth one of tou FIRE butuons You hava to hil the cortec: one pt thase buttons to slup the attack if you hir more than one butmon at a fimme you lree up ammuntion at am excessive rate

The geme contunas unil all the attacking cratt are destroyed or umbl you are watoed out You canter whed out by berrag too stow in thatimg a FIRE button by hiting the wrong FIRE butan ar by runining out of
ammiminen throughincorrect operainon of the FiRE buttons You can chose to face an alteck by enther ten |a DEK or a humbred a CENT) 5uleide cratt ammuntion storage is eutomatically selected to sur The type of garne chosen A DEK game typical ly takes less than one mrnute to pray A CENT gome takes several


## Sound And Light

The garme is toaded with sudio and visual efiects On the sound side there are indiwudal norses toreplesarn an attack or the operating of FIPE weaprons and to ind cute the viraning of losimg of a gatme The level of \$mo ATTACK sound varies with the quadrant of attack ottacks frem the loward quadiant are silent those trom port or starboart are al
(Fhoto Bf courtesy of 20 oth Eentury Fow?
half volume and those from aft are al fut volume

The visual effects are also quite inf pressum the attacks are shown by an array of LED 5 arranged in the torm of a cross with atme of warying lengiths The upoer arm represents the Porward altack guadrant and comprises five oramge LED s The lower arm represenis the alt attack guadratt and comprises seven green LED s 1 he port and starboard arms each comprise sis yellow LED s At the centra of the cross is a red $L E D$. representing your own shisp

The game is also prowided with an ammunition level indticalor in the Iorm of a three colour cotumn of ten LEDs and with a two digit attack counter with sewen-segment LCE reedents Thore are individual LED s to indicate the GAME WQN and GaduE LOST stases

## Science Project

Ambush！is a CMOS based design of considerable technical interest and should make an excrilent educational project for schools and colleges it uses seventeen IC s plus a couple of transistors The IC types range from simple NAND and NOR gates to complete decade counter－decoder chips，and include flip－flops．date latches， 12 －stage ripple counters． and multiplexers．

## Playing The Game

Game Start．The game starts as soon as power is applied to its circuits A game can be restarted by pressing the RESET switch．

## Attacks：

（1）The game can be set for play aganst oither ten（a DEK）or a hundred（ $a$ CENT）attecks
（2）Attacks come at random intervals varable between nought and approximately five seconds

13）The quadrant of each attack is randomly selected，except for the first attack of the game，which always

EII

comes from the aft quadrant
（4）The speed of attack can be pre－set by the player to suit skill levels A＇respectable＇attack speed is equal to about 50 mS per LED division on the quadrant attack indicator
（5）At＇respectable＇attack speeds． the player has approximately 250 $m S$ of attack warning on the forward quadrant， 300 mS on the port and
starboard quadrants and 350 ms on the aft quadrant
（6）Attacks on the aft quadrant are accompanied by a full volume staccato sound．Port and starboard attacks are at reduced volume，and those from the forward quadrant are silent
（7）The accumulated number of attacks is registered on a 2 －digit display throughout the game

## HOW IT WORKS

SIMALFIED BLOKK DIACRAMOF THE ANBLSH GAME

The heart of the urik is trie Display Matrix Driver and Legiec bioch．which in matity take the ferm at a wily secude commer with ien decooded thutputs Out－ paty 7 to 7 of the coundior ate fed to the 16D display matrix and outputs foasare selectlvety fed wa a maxflplexer to the CAME LOST Indicator bock ind re the CLOCK DHSABLE 保和 che 4617 ．The infout of thas 4012 is deriver fratn zetock jemeraior vis a gate．which ith tum is controltud by a simgle START－STOP （Reset－sel）bustabte．

The urecatity sfaumge of tite athere sir blooles is cairly simpole finitially，the broable tis in the star modt，the grate 倍 chased the Hety in in the RESET siate，and
 some teralomy determitred time a START pulse is fed to the vistable the give apens clack puluses start to fewen the 4017，and LED s are serquafotalls switehed oll tax quese of the arnis of the display malrix．If the gate remaints apen，othe of
 16 asetrualy gnes tigh and apertites the GAMF，LOST－fndicator and disabeles the cluck inyrat ine of the 9012
Altematively，the bistate casm be set to the stop moute bofure the game terman－ Dy byeraing the approariate flRE柆地h fathin cas the bistable glenses the tlock fate and the 4017 renetis fothe ctro
 when anther rationm SifART pulse ts fex
＊o the mput of the bistable Nove that output a a the dol is fed to the ATEACK COUNTER so that the counter acivanices 6y one côunt each kirne the clock generam


Fig 1a
tor gate opers．The game ends shorty aiter the atiack counter reaches its fuil（at 10 ar 100 s attete，at which point the GAME WON indicuto circuits tame into opera llan．
The SIMRT sighaf to the tastable is derlved fatm the randoni delay generator， which is integial wht the FIRE Swith
 one of the four flete gitches is selec． tively couplied to the STOP side of the


## PROJECT:Ambush


\{right\} This board carmes LED display matrix drivers. multiplexers and logic, plus audio and power connections.

## Defence

(a) The player has four FIRE buttons for defence The buttons are marked $F$ (forward) $P$ (port) $S$ (starboard) and $A(a f t)$ To stop an attack the plaver musi press the FIRE button appropriste to the prevalling attack quadrant before the attacking vessel reaches its targey (the red LECD at the centre of the display) A correct firing is accompanied by a rasping sound
(left) ICs 2 and
6-11 mounted on an Ambush PCB
 button is pressed.
(b) The ship has sufficient ammunition to fight off attacks only if each FIRE duration is limited to about 100 mS or less Thus there is sufficient ammunition for about one second of continuous fire in the DEK game, and ten seconds of fire in the CENT game The ammunition state is shown on a register throughour the game
(c). When the correct FIRE button is pressed, the rate of ammunition usage is directly proportional to the total number of FIRE buttons that are pressed at that time Thus, if a.l the fire buttons are pressed at once the ammunition supply will exhaust in 025 seconds in the DEK game or 2.5 seconds in the CENT game The audio trequency of the FIRE sound is proportional to the rate of ammunition usage. When the ammunition store is exhausted the player has no defence. and loses the game after the next attack

Game Lost. The player loses the game by having his starship hit by an attacking suicide craft When the game is lost the red LED at the centre of the attack quadrant indicator turns off, and simultaneously a loud droning noise is generated and a red GAME LOST LED flastes on the control panel.

Game Won. The player wins the game by defeating all attacks At GAME WON a green LED illummates on the control panel and a coarse beating or throbbing sound is generated
> bistmble visa a mistiplexer, and a simulated fire sqund is gerierated if the uperator activates the corract switeh. the mequency of the' fire' pound is fetermined by the FIRE RATE, SEIECTOR circult and is proportien to to the towal number of FIRE switches pressed at any given mo. ment

The ourpur of the fire sound generxcor is used to dtive the emanumition register. whech counis axd gives a vishat readdout of the that number of cycles gernesated. Fhe smund is atsis wsed to generate a tazhed randam 'select' code for the fout muliplesers that are wsed w the gatme. These muliplexers ate used for TIRE
ewitch selection, For LEO DAspley Matix lime and fine length selection and to determine the audio fevels of the ATHAGK soxands.
The ATTACK. FIRE. WIN and LOSE sound tignals are ake red 10 a simple two transister audio armplifier which *ives a 40 ohm output speaker.


## HOW IT WORKS

RANDOM DELAY and＂TRE＂SOUND GENERATOR phus PIRE RATE SELECIOR and FIRE SWITCH MUT TI PLEXER
THS IS probably the most complex ＇block＇in the entire game，because most of ins individual sections are interdepett dent．Fig． 2 showethe gircuit dagram of this mmjor＂block

THE THEE SOUND GENEEATOR
Lei＇s deal frrst with the＇FIRE＇SOUND GENERATOR．KC2 is ORe half of a 4052 dual 4 －channel multiplexer．This connecto a melected one of its four fmputs to its output，depending on the＇$s$－$b^{\prime}$ borary code signat that is fed to its＇sefert＇（pins？ and 10 ）Eerminats．Thus．when the appro． prinke one of the four FIRE swithes is pressed，a logic 1 signal appears at vutph！ pan－3 of the multiplexer．That signal is debounced＇by Ro－CG amd R7．And is passed to the signal inpuit of the INHTEIF GATE formed by IC3M and IC3A．

If paisces uignais only when iss GATE input is at logicole pinai is the＂ $\mathrm{S}^{x}$ terminal of this particular gate，and is thed to groulifd via RS but can be difven high by the outpucs of the LOSE and OUT OF AMMOdecectors．The gate thus passes on the FIAE wiftch signa onty when the
facme is mot lest and tisinmmunition store is not exhausted．
The outpula of the tahibut gate is uned to activate a gatea FIRE sount oscillayg destgned around ICSks and IC3ra The main 1 iming eompornents of this uscintator are C2 and 212 ko R15．These khing resigtors are eonnected via IEI， which is a 4018 guad biaternd wwich． which hat each of its form intextal surisches＇activated by one of the cour FRE switchas：these interasl switches atit normally open，unt close when thear approprizuse Fire switch is closed

Thus，the compiete getion of the＂FIRE＊ sound generator is zuch that sound is produced oniz when the＇correct＇FIRE watch is pressed．and anly when the game ts yot last ot the ammumition exhausted The frequency of the sound is
 switches presmed snd varies from about Boc Hz for ouse swilch，to abrut 320 liz for fout switches

The pln 4 output of the＇FIRE anclilator is low in the normal quiescent stake，and ite signats are passed to the mput of an audio amplifier for sound effects．and also to the inputs of the ammanition realstet and the Rendam Delay generazof．Ant inverted onsput（noimmally hich） 1 首 150 taken frons the pin output of the osctitater and is fed to the wIN LOMIC circultyy．Note that the gate input signai of the cscillator is atso fed to the STOP
side of the tristable ind to the RESET pin of the display matrix driver，so thet IC 12 is reset each time the correct FIRE swith 15 pressed．

## THE RANDOM DELAY GENERATOR

The hear of the random delay gethers－ Lar is ICA，a 4017 decade counter with tert decoded out purs frumbered 0 toe 9 ）：the 9 ： putput of the counter is cougled to the START side of the bistible will a normally－ON inhibit gate．The elock input to the coarter is derived from alow （athout 2 Hz ）oscillator（ $\mathrm{CC} 5 / 1$ and （C572） and frems the＂FIRE＂oncillator outpui wia an OR gate formed by Di－D2 and k3．

Whenever the correct FIRE butcon is presged duming an attack elogic－1 signal is fed to the＇G（oin 13）terminal of che unhlibit gate，whech turns oft and blocks the tignts from the 4017 counter． Simukunecusly．frast clock sitmats are ted into the counter from the＇IIRE＇sound generator Consequently，when the FIRE swith is released and the inhibit gate returnis to the ON stite the counter is an tuknown or random number of steps from the＇9＇count（which is the ane that provides the ST，ART sigmat to the bist－ able），Clest signals are then fed to the counter from the slow ossetiater oaly unid，after a delay that is inilndtely vari－ able fromi zero to about five secionds，the counter reachest the＇g state and feeds a START command to the bistable．

## HOW IT WORKS

THE BISEAHLEE CLOCK OENERATDR， －ATEACK＇SOUND MULTIPLEXER AND＇GAME LOST＇INDIGATORS
 made Erom a pait of WOR gates（1C9／1 and IC9P2 Its＇SIART＇imput wh derived from the random detny generator via CA，and STOP inputs are abtained from the ＂FIRE togic or the＇GAME LOST＂delector circuity via the D6－D7－R30 sude OR gate，The pin－1 toutput of the bistable is mormally high，but gdes low in the SIART mode．and is led ta one input of the ICIOE NOR 刻裉；when provides the elock input sigaz！to SC 12 （the display matrix cownter－के fiver）．Thisother mptat of

variable－bpeed CLOCK GENERATOR （Fitiofl and（Giol2）of from the wix OETECTOR cincuitry via the D4－DS．算景 firsde $O R$ gate
Thus input pin－s of the NOR gate is notmally high，and tis output is fockend Thw，to it is umable to pass ctock signast When a＇START＇kignat is fed to the bistable from the random delay geners－ rew，input pin－t of the getw in driven low， and it cloas pass elock signats．The gate is twithed of again when a＂stop＇sgrat is fed to the bistabte from the＇FIRE＇logic fircuitry，Ninte that the gate gets locked
 to pith－5 input from the＇W／N＇detector

signal ts fed to the＂sTop＂side of the baxtable via D6
The IC10／I and IClofa clock gemeratos defcrmines the speed of any attack，and its firequency tr vatimble via Rtil，The clock signal appegring ar the pin－ 11 out－ Mat of the tello， 3 Nor gate provides the basic＂ATFACK sound of the gltac．The amplitude of this sound is det ermined by multiptexer 1 C 2 k 2 and resistors R 31 and R32．Attacks from the aft quadrant are it foll wolume those from port or starboard are at reduced volume，and thase from the forward quadrant spe silemt．

The＇GAME LOST＂undicators tase four NAND and ome NOR gates：theit basic theyt signats are omalned from pin－11 of C12，which ic nommally lox but groes hyh under the game lost candituon 1 CO ／s Wirwd as a wimplo inverter，and drives the




## BUYLINES

The case we used for the Ambush project is available from Boss Industries. Fulf details next month. Since panel layous is not critical. inventive ETI readers may be able to come up with their own hardware designs. All the ICs are common types. available from most component maif order firms.

If you think you ase likely 10 spend every waking hour zapping the starfleet, it's worthwhile investing in a mains adaptor, available from your local Tranny shop.

## PROJECT:Ambush

## HOW IT WORKS

FHE ATTACK COUNTER AND GAME won detector and indicakars

THE '1' OUTPUT of 1 Cl 12 (the displiay matrix dover) brelly goes high at the start of each attack. This 'i signal pro. vades the clock signal to the ICl4.ICI5 ATTACK COUNTER These ewo IC's are 4026 detade counters with decoded euaputs, suitable for directly dirting cammen cathode 7 -segment LED displays at low power levels. The two counters are cascaded, to give 60 to 90 Indications:
leadiug zercy suppressson is not used in the comenter
The 'GAME WON' detector ts designed argund KCI6, a to13 duat D bip-flop, and IC1OA, a NOR gate. ICIB 1 is Conmected as a bistable divider stage, and bis ctorked va one ar otherr of the atteck counter outputs The action is suem that Its $\bar{Q}$ output is nomatly high, but switeches low at the start of tire 10 ch attick in a DEK Bane or the 100 th attack in a CENT game. The Q output is ted to one of the unputy of the ICIOF4 NOR gate, which has its other

Fig 7 (left) Circuit diagram of dis. play drivers, miltiplexers and logic with audio and power connections
inpu: provided fow the normally-high output of the ICS 'FIRE sound generator. The output of the NOR gate is fed to the SET (pin-8) terrminimi of ICI6;2, which :s wired as an R-S flip-flop. Both bustables sre reset al the start of each gatme.

The action of the complete GAME WON" desector is such that 'FIRE' signals禁e fed to one input of the NOR gate each sime a ${ }^{\text {FIARARE, signal is generated but are }}$ quable te reach IC16f2 untıl IC16/1 changes sate after the syast of the loth ( m a DEK game) on looth (in a CENT game) attack, th whith point the $Q$ output of $1 C 16 / 2$ 等 goes low and drives green ${ }^{2}$ WIN LED 37 'DN' vial $[C 9 / 4$, and the $Q$ output goes high and activates the 'WIN' sound generaror

The 'WIN' sound geremetor is designed around ICI7, and consists of two virtually identica* medium-frequency gated astable multyibrators. which are operated in parallel and have theis outpats fed to the *udio amplifier wia the D26-Dz7-R46 dinde OR gate. Because of trievitable slight differences in timing companent values. these two astables oscilizte at slighty differeme frequenctes, and produce coarse *beating' or 'throbbing' sound when they are aitivated by the 'WIN' detectgr.
win detectgr.

Fig. 8 (below) Attack counter and 'GAME
 cators.


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# 3080 CIRCUITS 

## The 3080 is not a run of the mill op amp. These ten circuits from Tim Orr show you why.

The CA3080 is known as an operational transconductance amplitier. (OTA) This is a type of op amp the gain of which can be varied by use of a control current, (lasc) The device has a differential input a controlinput known
as the Amplifier bias input and a current output it differs in many respects from conventional opamps and it is these differences that can be used 10 realize many useful cricult blocks


## Vollage Controlled Amplifier

SIGNAL nganging ingung ov

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CONTROL
 ov


The CA3080 can be used as a gan controling device. The input signal is attenuated by R1, R2 such that a 20 mV pp signal is applied to the input terminals If this voltage is much larger, then significant distortion will occur at the oupput. In fact, this distortion is put to good use in the triangle-to-sinewave converter. The gain of the circuit is controlled by the magnitude of the current Ianc. This current flows thto the CA3080 at pin 5 , which is held at one diode voltage drop above the -Vcc rall. If you conmect pin 5 to 0 V , then this dsode wull get zapped, (and so will the IC)! The maxamum value of IABC permitted is 1 mA and the device is "linear' over 4 decades of this current. That is, the gann of the CA3080 is 'Inearly' proportional to the magnitude of the IABC current over a range of 0.1 ua to 1 mA . Thus, by controlling labC, we can control the sagnal level at the output. The output is a current output which has to be 'dumped' into a resistive load (R5) to produce a voltage output. The output impedances seen at IC 1 pin 6 is $10 \mathrm{k}(\$ 5)$, but this is 'unloaded' by the voltage follower (IC2) to produce a low outpat impedance. The circuit around IC3 is a precision voltage-to-current converter and this can be used to generate lasc. When Vin (control) is positive, it linearly controis the gain of the circuit when it is negative, labc is zero and so the gain is zero.

This type of circuit is known by several names. It is a voltage controlled amplifier, (VCA), or an amplitude modulator, or a two
quadrant multiplier
Ore problem that oceurs with the CA 3080 is that of the 'input offset voltage'. Thas is a small voltage offset between its input terminals. When theis is no signal input and the control input is varied a voltage similar to the control input will appear at the output. By adjusting RV1 it is possible to null out most of this control break through


OUTPUT


## Triangle To Sinewave Converter

By overloading the input of a CA3080 it is possible to produce a 'sinusoidal' transfer function. That is, if a triangle waveform of the correct magnitude is applied to the CA3080 input, the output will be distorted in such a way as to produce a sinewave approximation. In the circuir shown, RV1 is adjusted so that the output waveform resembles a sinewave. I tested this circuit using an automatuc distortion analyser and found the sinewave distortion to be only $1.8 \%$, mostly third harmonic distortion, which, for such a simple arrangement, ssems very reasonable indeed. This could be used to produce a sinewave output from a triangle/square wave oscillator.

## Schmitt Trigger

Most Schmitt trigger circuits prove to be very complicated when it comes to calculating the hysterysis levelis. However, by using the CA 3080 these calculations are rendered trivial plus there is the added bonus of fast operation The hysterysis levels are calculated from the sumple equation.

$$
\text { VHYST }=+(\operatorname{lABC} \times \mathbf{R} \mathbf{2})
$$

The output squarewave level is in fact equal in magnitude to the hysterysis leveis. The circurt operation is as follows.

Imagine the output voltage is high. The output voltage will then be equal to ( $R 2 \times \mid A B C$ ) which we will call $+V$ hyst. If $V$ ins becomes more positive than + VHYs I , the output wilf start to move in an negative direction, which wilt increase the voltage between the input terminals which wilt further accelerate the speed of the output movement This is known as regenerative teedback and is responsible for the schmitt trigger action. The output snaps into a negative state, at a voltage equal to - (R2 x ABC) which is designated as -VhYst. Only when VIN becomes more negative than - VHyst will the output change back to the + VhYST state

The Schmitt trigger is a very usefi! building block for detecting two descrete voltage levels and finds many uses in circuit designs.

V OUT



Iabc CONTROLS HYSTERYSIS LEVELS AND OUTPUTLEVEL


## Voltage Controlled Oscillator

By using two CA3080's and some op amps it is possible to make an oscillator, the frequency of which is voltage controllable. This unit finds many applications in the field of electronic music production and test equipment. The circuit has been given a logarithmic control law. that is, the frequency of operation doubles for every volt increase in the control voltage. This makes it ideal for musical applications where linear control voltages need to be converted into musical intervals (which are togarithmically spaced) and also for audio testing where frequencies are generally measured as logarithmic functions.

IC2 is an integrator The IABC current that drives this IC is used to either charge of discharge C $\uparrow$. This produces triangular waveforms which are buffersd by IC3, which then dirives the Schmutt trigger 1C4. The hysterysis levels for this device are fixed at +1.5 V , being determined by R6, R7.

The output of the schmitt is fedf back in such a way as to contral the direction of motion of the integrator's output. If the Schmitt output is high, then the integrator will ramp upwards and vice versa. Imagine that the integrator is samping upwards When the integrators output reaches the positive hysterysis level, the Schmitt will flip into its low stato, and the integrator will start to ramp downwards. When it reaches the low hysterysis level the Schmitt will flip back into its high state. Thus the integrator ramps up and down in between the two hysterysis levels, The speed at which it does this, and hence the oscillating frequency is determined by the value of labc into IC2. The larges the current. the faster the capacitos is charged and discharged. Two outputs are produced. a triangle wave (buffered) from tc 3 and a squarewave (unbuffered) from IC4. If the squarewave output is loaded then the oscillation frequency will change.

The log law generator is composed of $01,2,3$ and IC:. Transistors 01 and 02 should be matched so that therr base emitter voltages (Vbo) are the same for the same emitter current, ( 50 uA). Matching these devioas to within 5 mV is satesfactory, although ummatched pairs could be used. When matching transistors take care not to touch them with your fingers. This will heat them up and produce erroneous measurements. Transistor 02 is used to produce a reference voltage of about -OV $\overline{6}$ which is connacted to ICt pin 3 . This op amp and


03 is used to keep 01 enntier at this same voltage of -OV6. The input contral voltage is attenuated by $\mathrm{R} 1, \mathrm{R} 2$ such that a +1 $V$ increase at the input produces a change of only +18 mV at the base of $\mathbf{Q 1}$. However the emitter of Q 1 is fixed at -OV6, so the current through $\mathbf{Q 1}$ doubles. (It is a property of transistors that the colfector current doubles for every 18 mV increase an Vbe)

The emitter current of Q1 flows through Q3 and into IC2 thus controlling the oscillator frequence. It is possible to get a control range of over 1000 to $t$ using this cifcutit. With the values shown, operation from 10 Hz to 10 kHz is acheved. Reducing C1 to 1 n wil increase the maximum frequency to 100 kHz , although the waveform quality may be somewhat degraded

Changing C1 to luf (non-polarized) will give a minimum frequency of 0.1 Hz .


## Fast Comparator

The high slew rate of the CA3089 makes it an excellent fast voltage compasator. When pin 2, IC1 is more positive than Vref the output of IC1 goes negative and vice versa. Vref carr be moved around so that the point at which the output changes can be varied As long as the input sinewave levelis quite large ( 1 V say) then the output can be made to move at very fast rates indeed. However, care must be taken to avoid overloading the inputs. If the differential imput voltage exceeds 5 V , then the input stage bresics down and may cause en undesired output to occur.

One use of a fast comparator is in a tone burst generator. This device produces bursts of sinewaves, the burst starting and finishing on axis crossings of the sinusoid. The comparator is used to detect these axts crossings and to produce a square wave output which then drives a binary divider (IC3). The divider produces a "divide by suxteen" output which is high for eight sinewave cycles and then low for the next eight. This signal is then used to gate ON and OFF the simewave. The gate mechanism is a pair of transistors which short the sinewave to ground when the divider output is high and lot it pass whan the divider output is low. The resulting output is a tonehurst. However, if the comparator is not very fast, then there will be a delay in generating the gate and so the tone burst will mot start or finish on axis crossings. Using the circust shown, operation up to 20 $\mathbf{k H z}$ us obtainable.



## Slew Limiter

The current output of a CA3080 can be used to produce a controlled slew limiter. By connecting the output current to a capacitor, the output voltage cannot move faster than a rate given by
stew rate $=\quad \frac{\operatorname{IABC}}{C 1}$ Volts per sec.
C1
Note that fabc determines the slew rate and as labc is a variable then so is the slew rate. The output volkage is buffered by a voltage follower, IC2. This is a MOSFEF op amp which has a very hugh input impedance, which is necessary to minimise the loading on C1.

When an imput signal is applied to IC1 the output tries to move towards this voltage but its speed is limited by the slew rate. Thus the output produces a linear ramp whinch stops when it reaches the input signal level.


## Sample And Hold

The slew limiter can be modified so that it becomea a sample and hold unit. In this circuit lasc is either hard ON (sample) or completely OFF (hold). In the sample mode, the output voltage quickly adjusts itself so that it equals the input voltage. This
enables a short sample period to be used In the HOLD mode, IABC is zero and so the voltage on C1 should remain fixed. The circuit is in fact an analogue memory. It is used in music synthesisers (to remember the pitch). in analogue to digital converters and manv other circuits.


RING MODULLATICN

## 4 Quadrant Multiplier

The CA3080 is a two quadrant muluplier but, with the addition of a few extra bits of electronics, it can be made into a four quadrant carcuit. A two quadrant multiplier has two mputs, one, can accept bipolar signals (the invarting or non inverting input) and one can only accept a unipolar signal. (the taac current). However, a four quadrant manitiplier can accept bipolar signats on both of its inputs which enables it to perform frequancy doubling and ring modatation.

The circuit is fairly similar to that of the two quadrant multiplier described earlier except for two differences. 1C3 is used to genarate laac in such a way that the $Y$ inpuit can go both posfove and negetive. thus the $Y$ input is bipotar. when $Y$ is at 0 V
and there is a signal on the $X$ input the desired output $\{X \times Y$ should ba zero. This is achueved by adjusting RV1 so that the signal valCt (this is inverted) is exactiv cancelled our by that via R3. Now, when $Y$ is increased posittruely, a non-inverted value of $X$ is produced at the output and, when $Y$ is increased negatively, inverted value of $X$ is produced. When $Y$ is zero, so is the an inverted value of $X$ is groduced. $\operatorname{output}$. This is known sometimes as ring modulation. if a speech signal is connected to the $X$ input and a variable frequency oscillator to the $Y$ input the resulting sound is that of a 'dalek'. Also, if a simewave is connected to both the $X$ and $Y$ inputs, the $X Y$ product is a sinewave of twree the frequency. This is known as a frequency doubler. but it wifl only work with simewaves.


## Single Pole Filter

A singlepale lowpass filter can be constructed using a CA3080 as a current controlled resistor. The filter 15 , in fact, just a simple RC tow pass section where the $\mathbf{R}$, which is controllable, is constructad out of IC1, R4, R5 Varying labc changes the amount of current drive to C 1 . This would normally make the circuit a slew limiter, but because the signal level that IC 1 (pint 2
and 3) handes is so small, the CA30B0 works in its linear mode. This enables in to look like a variable resistor When this resstor is varied. the break frequoncy of the filter also vartes. By applying some positive feedbach around the fitter (R6, C2) in is possible to produce a peaky filter response The peak acruathy increases with frequency making the circuit useful as atin Wah Wah unit


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ELECTRONICS TODAYINTEANATIONAL - APRIL 197 O


## microfile

## Gary Evans looks at PLT add-ons, a Simon that's not simple and has news on superboard II.

WHIH THE PLETHOFA of nen smal Computer Eysunis appearing on the market its inace to see some of the old warhorses tegmnimes to meet this unsiaughi hy suppuring the user with a broad base of herdware Surely one of ithen oldest wornors ats flowary prose it s imbenthland une s.firt has to date been poorly suppertest by is mar ufateree is The PET

A number of companice havc stepped mu the void coused by lack of commadore peripherals everything from ह $\$ 232$ interfaces to PET comparata floppy dives are avalable but net from Commodore The larat r-ve it it PET User a Club nevesletter incticates that thac siti a' CH क about to change

The maet exciting of thr ff T add oris from Commedon is thear 2040 Dual Drive Flapny Disk Derats are sketcto at |rasem1 buil 1 louthine ine sper ot ine sodo as presented an the newaletter

The drive will allow 360 K byter of date to be shored on Two standaid 5 Ann Dak rives ishugar $\$ 2390$ f This is accomplished witheut raserting to double lracking or double density This is echieved we re not toid exactly houst) by the ase of two MPUs - 6504 and 6502 and tifteen memory I is within the 2040

Formationg is by the drwe itself and ony min thpopy disk may be used 35 tracks inth a constant density recording on each trark prounde 171520 bytes for user storage per disk sude

The 2040 requires only one connection to the PET an mterl'ace cord conmecting the unis to PET 5 IEEE port

Just what we se been wallingtor - kul you li have to watr until Moy and part with $E 79920$ to the pleasure of fitung this box at ricks next to to $1 \mathrm{PE}^{\top}$

Good news that we dan : have to wait tor is a price reduction in the PET madel Tont 8 The 8 K machme thet until now has been the anly. PET Compuler is down in price to 259400

The BK machine sctejo ad by a 4 K machme a +496 OO and two modes teaturing 16 K and 32 K of memory The memorv used if theje larger systems is dynamie a departure from the stall AAM used in the 8 K and 4 K vergions The 16 K ana 32 K machines will also feature a full typewfiter strie <evboard in place of the calculator keyboard that was ore of the micst persistant criticisms of the 8K 2001 B norde to mase room for the lurger koyboard the int egral casse ite deck heas been omitted and a seperate deck will hawe to de ditaines in alder 10 pecord programs

The AKPET is due in Febrian in a ma auter ersons $\therefore$ ill be here in Moy

The last andition 10 Commactore 5 rad sare is the 2023 printer This will replace the il fater - 20 pirter armounced bat not seen - and has 'o quote a significantly belter qualiy, and m. $n$ non ritpad The 2053 is dive in Apoil

Well there we are then a farge of ve' soeced PET perupherals Let s hope that Commodore ranage to meet the piomsod delivery dates as in the past th s ib the area in which Conmodore have been distincty acking in pertor mance
 from Compu-itirik is sualabio now and pluge in to o PET ther has haon fited with on mimumum of 15 K additnon at mamary


## Toying With mPUs

Al last the witl has lound its way inta the loy market Chrisimas saw a number of electronic games Invicla's Nastermind beng one of the most popular and the new yoar is seeng many more games addad to the shops shelres

The current rage in Amcrica is a game callad §imon Presented with four buttons of difterent cotpurs. the player has to remember the sequence in which the machine Cells them The sequence staris off with just two colours but rapidly extencts this und the player must press the tour buttons in a sequence that as it extends wil evemualiy defeat the user

Not very easy to explain but its all the ragy in the US and will be over here soon - you it be able to see it for yourselt then

## Super Ohio

I am assured that tho long awated Ohn Scientifics Supertouard II will be avalable "oft the shelf wish in the next 45 days Needless to say 1 am trying very hard to get hold of ane of thase boards and will report on its pertor mance soon

ETT

## Back numbers

Not all hack isenes of EFFare available. Indeed more are not than are! The toble below showt which copigs can he obtained from our officas Each copy costs 60p inc $p$ \& ${ }^{2} p$ and please mark your envelopss "Back lssues".

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WHAT'S IN THE APRIL ISSUE . . . . . .


## COMPUTER SURVEY

The number of small systams on the market has increasad graatly over the past year and the chorce of a machme to serif your applicetion.
The Appil issug of Computiag Today surveys some of the more poputiar small computers and presemts in a clear, concise, foshion the capsbifluies and faculities afferad by the differant products.


## NASCOM ADD ONS

The NASCOM I computer has been one of the most successsfut of the DIY computer kits on the market recently NASCOM introduced a number of extras that aflow the dasic machme's potential to be considerably enhanced.
We take a loak at the axpansion basrd and RAM card as weff as the TINY BASVC Nascom are now offerng.

## EXPANDA PET

The commedore PET has been with us for aver a year show but petiphorals for the compurter have beert slow to appar. Ona of the essential devices in many appllgatrons is a tloppy disk to provide a system of mass storago that is faster in oparetion than the tapa system of the standard mschune. Naxt month we reviaw the Compu'Think disis drive and distmon operating system that will plug straight into your PET.


## AMBUSH GAME

The Apfal issue of our segter magaztrie, ETI, carries a projact cafled Ambust). Ambersh is an
ekerting space war gente Computing Yoday will carry a program that will aldow those af you who don't debble in efectromics to play Ambush on your compurar

## CONSUMER SHOW

The recant Wirter Consumar Efectronic Show in Las Veges saw the merraduction of many naw MPU based products inchuding a chess challenger that talks.
Gerafd Chevin was there for Corrputing Todsy and hrs repart appears in the Aprit distuo.


Plas all the regular features, news, softspot, hardines and next month, a naw regwlar letters page.

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# WIND METER 

Here is the project all you amateur meteorologists have been waiting for. When this meter gets the wind up you'll know how fast and where it's coming from.

TRADITIONALLY THE FOUR promary elements are fire earth, water and air At ETI we ve designed projects concerned with the first three (temperature meters, solt moisture indicators rain alarms) but not much for the last The major property of the asr apart from the fact that it is mecessary to support. life is the movement of the arr wind Light wands generally aren't of terribly much significance except to meteorologists but stronger winds can be useful as a source of power for traditional miling for electricity generation or as a means of propulsion for sailing yachts Stronger winds such as hurricanes, can be destructive, causing damage to life or property.

So for all the private pilots yachtsmen amateur meteorologists and general weather watchers who read ETI here is a device which will tell you the wind s speed and direction, with a remote indication of both quantities Our design is we'd like to think both stylish and unusual but there are simpler methods of mechanical construcwon which you can follow if you wish

## The Head

The drawings along with the photos will give the general clesign that we used The actual dimensions have to be left to the individual constructor as components such as the ball races and light bultos may vary in size

Whise we used a single head for both speed and direction, it may be simpler to use separate heads

The discs we used were 15 mm thick clear plastic with a plece of photographic film glued onto it it may be easier to make it out of thim alumimum and cut out the slots For the speed disc simply drilling holes will suffice

The most important part of the design, apart from ensuring that the discs rotate with a minimum of friction, is the shielding of the light and preventing light scatter striking a

transistor which should be dark As can be seen from the photos and diagram the bulbs and transistors are embedded in aluminium blocks with small holes providing a passage for the light beam

The wiring of the head is shown in fig 3 Note that the base lead is not used and can be cut off close to the body Insulate the joints onto the transistors to ensure that they do not short on the aluminum blocks The bulbs may touch the block with their outer connection but this is the 0 volt line and does no harm In fact it provides some electrical shielding for the leads The bulbs we used were 12 V but they were bright enough on 6V giving a much longer life

## Design Features

When we started destgn on this project it was to have a digital
readout of wind direction with a resolution of either one or two degrees This would also make it useful in a salling boat to tell the wind direction relative to the heading

Difficulties however soon became apparent The first of these was the sensor head The only accurate method is a digital head, probably optical Two methods could have been used, one using a disc with a single optical track of 360 stots and an updown counter and the second using eight or nine tracks in a grey code The first is smpler in head design but the second is less prone to error The problem and the reason for rejecting both is thet with such resolution, the reading would move around so much when the wind is gusty to be unreadable What is needed is an averaging circuit which unfortunately becomes


Fig. 1. Complete circuit diagram of the ETI Wind Meter
difficult when the wind as changing from just west of north to just east of north ie 355 to 005 How do you average these (use a microprocessor?)

As this was intended to be a simple project we relaxed our original sperfication, deleting the use in a boat (we may get back to this problem A four track Grey scale allows the wind to be given to within $11^{\circ}$ of its true heading, without the complexity of a nine track one, and the use of LEDs to give direction solves the problem of averaging as the variations can be seen and averaged by the brain

## Construction

The electronics is relatively simple provided the PCB described is used Due to a height limitation C1 should be mounted on the rear of the board The LEDs should be mounted about 7 mm from the board with care being taken not to damage them as the leads have to be bent out slightly The regulator also has to lie down to give clearance.

We mounted the unit behind an aluminum front panel with the LEDs protruding through holes if this is to be done it is preferable not to solder the LEDs untll after alignoment with
the front panel
The head is more difficult as some mechanical ability is necessary to ensure good results The requirements are basically simple A disc is to be allowed to rotate either continuously with the wind or aligning it to the wind with a bulbon one side and phototrarisistors on the other

The method used by us is shown in fig 4 with the aluminium blocks providing the shielding necessary to give accurate results As the urit will be exposed to the weather it must be made waterproot otherwise the bali races will corrode The races used

## HOW IT WORKS

## Wind Direction

Wind dfrectrom is indicaled by a series of 14 equally spated LiED）s argunde a carcle．These represent the buan pornts on the compass． These are controlied by IC2 and IC 4 which are mi uarn controlled by the direction fentar head
Itte sensur head，which is described in fig 3 ，transigus of $a$ dise which has tow ppacat fracks and four bulles and pholo－ fonsiztors．The photegtransistors sense enther aclear dive（logieal＂ 1 ＂）or a mack disc fogical at 3 3nd thus contral 1 I2 2 and 1CA．The code used is spectal in that enty． one bit is changed af eathl location ellmin－ atme gross errors which occur whe the biniry code if the heads are art perfectly ahigned．An example of chas is wiong from
 is not dorné simultáneotsh almosil anve focation can be speaifind With the gety code the samte chatige is from 0100 to 11,00 Hert there can be no ambsy wity ass anly one： bAt is changed Remember these bits irem meit weighsed similarly to bnary and in lookup kbie must be used to decide what mumbur： （decimal）a particular code is．
The decader，lea，多 an ridht woupat andogue demulifteater with the ensmonor tine jained to the t 3 V line，When a parti－ catar 3 bit code is pressented to tis controt Ingots one of the elohi qutpots will be goined to the＋BV lise The founh oulput fram the serisor head zontmols TC 4 whiwh gives two inverted，outpuls th orvice ether bank of LEDs．कThe complete four bit code therefore rpecifies a parvicular［ ED bo be Dit By piacing the LEDS comerectly armand the circle the efrey code ts decrated

## Wind Speed

This is a simple frequancy counier mes sunng pulses from the sensor head．The bead cousigis of a dise whth engtit hales whichbrealisa ：bight beam teits assomiated phototrancstar．The enstplat tet this photo－ transinger is squated up by a schmith telgge formed by ICEG and icscl．
The coatheing fo dore by IC Ma and ICat（s dtal devate fatmier）with ICt and IC． prowidln娄 the stare and LELD detyers necessary to dryw the seven segment drs－ play Tline base 75 provided hy If，which gives is mas wide negative pulse about evers one spond whe say ahout as it is adjustable hy RY＇as indisidual beness watt Hive different responses and callorathon with te necessary．
This negative pulse opens the store us allow the fumber nowhed by the counters to be displayed while simultaniernus誓 stop－ puag any fukter counting by dicabling the cchmite iffiger．On the convivetion of the ？ms pulse Lesa，and Tosb senerate a sios Wide puake whuch resels the connter lC＇s ta recammence the＊equence

## Power Supply

This＇is sinspl）a fut wave rectifyed aupply With ICI giving a regulated +6 V outhut Thun resatiation w apeded toensurt that the unie basex（143）remains accurater


Above and Below Constructional details of the sensor head


TO TRANSFORMER


Fig. 2. Component overlay for the Wind Meter

## PARTS LIST



will normally have to be washed out to give low enough friction with a light spray of WD40 or similar to give some protection.

While our housing is a little ornate, it did work but the more usual halt ping pong balls may be more suitable.

## Calibration

Wind Speed
The easiest method for wind speed calibration is to provide the unit with a DC supply ivia the common and one of the AC inputs) and to take a drive in the car with the unit supported above the vehicle Providing there is no wind the potentiometer should be adjusted until the reading corresponds to the speedo

Direction alignment is simply a matter of aligning the vertical rod so that it gives the correct results.

ET

## BUYLINES

The metalwork for thes project we must leave to our readers, as this wiet be fabricated lo sut undividual requarments The displays can be any type no's really. fuist observe polanty Simalaly with the LEDs The Miptodarlufetens can bes supplied by Merthalls

Discs used in the sens or head - 1.5 mm thick, clear plastic with photograptic film glued an



## What to look for in the May issue: On sale April 6th

#  armoured Ivision <br>  

 weund a racetrack at 40 mph ? Well, much as tedd like to baisd a Chetan as a praject it would never go into one issue and an we offer you a fully perahortions; six channel radio ronat fod system for models inetiad.The desien offers joystiek contrel (er swfiehed pasition) and special actension has been patd to metalwork and setthag ap procedure. A kit of motalwark fand ready wound coill!) wili be mvallable, and nitigment requires nought butia simple volitmeles.

We're contident the syatem will be the standard by which others wral judged! Dont miss it:

How It Works - AM/FM
The second in our oceastomal series by Gordon king. This time he turns his attention co radto, and Eoesun and out of the ins and ouks in great easly explained, detail. Misses of eircuits to illustrate The points, and a ctust for anyone remotely incerested th the "ield.


Its toodbyr to the tadritul IF strip gat wr know Lt. SAW will soon be tound in TVV receivers, replacing the wrual array of coll and capacitors. You can ekpect to see and hear a lot mare about them in the tutur't, be
 is ment month't ETL P.S. SAW -- Surface Acousik Wave!

-雷
OK, so you've seen them before. Ours have a novel method of display decoding, switchable odds to allow adapietton for wargaming, ece. Sisgle board construetion makes tite easter and overall we think its a nice orte!

See what you thunk next month.


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# GUITAR EFFECTS UNIT 

Our guitar effects unit isn't just a fuzz box. Use it to give you a new sound to play with.

LIKE US, YOU probably thought that one guitar offects und was much the same as any other Afier fuzz and Whan- Wah, whas do you do? wel, we thum we have come up with a new one which we heve christened

## strtires

Whith this und you can sefoct erther a convertional fuxe effect or ou new struzzetfect a depith control allows you to alter the sustian rate of the effect If the neightours start bangung the wall, you can instantly cut out the crunchy effects with a bypass switch

## Make-up

Construction should not pose any problems If 5 even easier il you use ouf PCE Make sure the elecirolytic capactiors see put in the correct woy round As almeys don I plug in the ICs unt y you have crecked the circur: thoroughily:
Happy fuzzing and slruzzing p

## BUYLINES

The oniy comgonime that may be dilheutt 10 tind it the LF358 PET op-amp $W_{\text {atford }}$ Electranica ctin supply thatic

## Smashing sound

Now you are wondering what struzz sounds like, aren't you Well il 3 a dismortion of fure The hindamental frequertcy of the inpui is full wawg rectified but the numerous harmonics are nor The result sounds rather like an ant que piano frially succumbing 10 the ravages of woodworms, and cokeapsing if vou play the gulat iwe dent if you will, no doubt, find meny mpre musical utes for thats effect than we could

Suitching between 'uzz and siruzz white pleying produces an interesting sound You rty ght hke to use a footswitch for this purpose


Fig.1. (above) PCB component overiay

## (Above right) Completed PCB

Fig.2. (Below) Circuit diagram






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## Gentle Clock Alarm

I. Hill-Smith

RINGI RINGI BUZZ This is DLT CLANG! PIP PIP PIPI

There are gentler ways to wake up. This circuit provides an alarm which builds up from being inaudible to loud over about one minute. As a result you are always woken by the minimum volume required to wake you, a far more comfortable experience than the usual trauma. The three multivibrators in cascade provide a signal like the sound of a warbler telephone. As $C$ slowly charges through $R$ a larger fraction of the signal is amplified by the op amp producing a louder output

## Calculator Radio Alarm

## T Corringham

This very simole circuit used with a Sinclair Cambridge Programmable calculator enables a transistor radio to be turned on after a predetermmed time. (within the range of a few seconds to five months)

None of the components are critical, but the SCR should have a suf-

ficientiy high voltage and current rating for the radio used

If a transistor radio is used the SCR ts connected in series with the battery but if a cassette recorder/player is used it can be connecled to the remote socket

The LDR is placed above the left hand three digits of the display RV1 is adjusted so that the circuit is triggered by 888 berng displayed, but not by the background light only.

Using the program given, the time

In minutes of the required delay is put in and /RUN/ pressed to start the timing period

To stop the program prematurely /-C CE/ is pressed.

The calculator should be used with a mans adaptor.

The timing is accurate to within five minutes in eight hours

If a buzzer or similar alarm is used the same circuit can be used to give an audible indication of the termination of long programs.

| Arbeti E\$1 3n4 <br> NASCOM 1 DISTRIBUTOR |
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## Semiconductor Guides. <br> 

## Keyboard/display sound converter



8
K. G. Redd

This curcult can be used in several modes It can provide quantized feedback (a distinct improvement over the normal single bleep) from the key actions made on a calculatortype keyboard: It can be used to give a sound translation of a digital display or completely replace the display when sound would be a better communication medium.

The keyboard or display information (a maximum of 16 bits with one 16-line 74150 multiplexer) is translated into a series of 16 high or low frequency tone pulses, corresponding to the high or low logic state of the 16 bits

The circuit illustrated was used in conjunction, with a digital multımeter, requiring three 4 -bit words for the digits and three additional bits for over-range negative and decimal point Thus 15 lines only were reauired, the 16 th being used for resetting

The 15 bits are latched on to the inputs of the 74150 multiplexer Presentation of the erable pulse results in a logic 1 appearing at the output of gate B, allowing clock pulses to pass via gates $A$ and $H$ to the 7493 counter Gates B E D and C torm a tatch which remains set' until all 15 bits have been sampled As each bit is sampled the inverse state appears at the multiplexer output, opening gate
$J$ or $K$ and thus operating one of the two reed relays. As a count of 1111 appears from the counter the output of $F$ drops low resetting the latch and counter The operation of elther relay results in a tone appearing at the loudspeaker for earpiece), the tone frequencies being set $(12 \mathrm{kHz}$ maximum) by the 1 megohm pots The tone pulse length is governed by the clock rate

## Digital Pulse Compressor

NC Hald
Whilst constructing a digital frequency meter the author found it necessa ry to be able to accurately trim the width of a gate pulse. The circuit shown uses only two ICs and can reduce the width of a pulse applied at its input by up to a few milliseconds. The table shows the reduction achioved by using different values of C?



## a digitizer adds another dimension

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## Darlington Drivers for a few pence

C. J Ramey

This circuit offers a very efficient way of driving a pair of transistors in Darlington configuration from CMOS The carcult in Fig 1 shows how two loads of up to 1 A may be driven from a single 14007 chip with no external resistors Using a 2 N 3055 in place of the BFY5 1 will enable loads of up to 3 A to be driven at voltages itmited only by the Vceo of the transistors (Vcc)

Fig 2 shows the internal circuit of one section of the 14007. A high on
oin 6 switches the lower CMOS tran sistor on, holding 02 off and sinking the leakage current of Q1. A low on pin 6 drives Q 1 and switches the lower CMOS transistor off and the upper CMOS transistor on

The result is fast switch off at low cost and efficient switch on

A bonus is the inverter between pins 10 and 12 Note Vce should be 5-6V to prevent excessive current beng drawn from the CMOS chip


## Precision Rectifying with the LM3900

## A. Winsor

The LM3900 is different from most op-amps in that it is current differencing and operates from a single supply rall. which mean that the inputs bias at one base-emitter voltage above ground Hence standard technques are not applicable as the diode would always be forward-biased Two feedback paths are therefore provided:A3 for DC stability and R4 for the $A C$ signal after C2 and R5 have filtered out the DC bias When R2 $=2 \times$ R3 point $A$ will be at Vcc/2, allowing the diode to be reversed at will For large positive input returned to ground Input impedance equals R? and voltage gain equals - R4 / R1 since R4 is

made very much smaller than R3. C1 and C3 are DC blocking copacitors and determine the low frequency tolloff. Component values quoted are those used on the prototype and may be altered to suit individual require-
ments
This circuit has obvious potential. especially in portable equipment where the 4 amps in one package and single supply ral yield a more compact, more convenient unit

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# PCB FOIL PATTERNS 

GATHERED HERE are all the PCBs for this month's profects from now on the boards will be grouped together like this in order to facilitate their use by those readers wishing to produce therr own PCBS from these patterns

All are shown toil side up, and full size Companies wishing to produce these for sale as ready made PCBs should note that where the board carries a copyright
symbol, the designer retains that copyright to himself so his company, and that particular board may not be produced on a commercial basis

These pages form the basis of our ETIPRINT sheers which are etch resistant transters of the foll patterns designed to simplify one-off PCB production See the ad on page 49 for further detals


Below teft Wind Speed Indicator PCB
Below right Click Eliminator Mk 2 board
Right Struzz effects unit
All are shown full size and will form the basis of ETIPRINT sheet 023 which will be available shortly



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    ELECTRONIC BROKERS LTD 4953 Pancras Road London NWI $20 B$ Tel－01837 7789 Telex 2gng94
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