







Beats Crossroads . n27





FEATURES

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Circuits from you to you

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The new space game with sounds Get the wind up this unit

We think we ve found a new one for you

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CALL FOR ANALYSIS?



Howless Packards new HP 2009 is a multiplicities on based in strumment for checking multiplexed telephone equipment. The scope-size unit replaces two large nocks of test guar and automatically. displaces its results an ensure rather than days.

Over forty different measurements from gain to intelligible crivistalik and local abrine car be assembled into a test source defined by the user

The results are displayed in tabular form on the instructents own CRT. The in formation can be fed to a computer or prozer through an mtignal (FEE 488 (HP-IB) digital interface.

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tagrafice the territological The analyser is produced in two models = the 3779A for the 3779B Europe and the 3779B for Bell system users Further details from Hewlett Winbardh King Streft Lane Winbardh Kong Streft Lane Winbardh Kong Streft Lane

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The grosss Do not however expect to see this ghost eliminator available just yet. It is still many months from the full production GERALD CHEVIN

AND AL50

An enterprising American TV station has finally decided to write a software package allowing American teletext to link up with British Viewdata

In what is briteved to be the first US attempt to interface the two systems: statum KSL TV (balt Lake City) hopes to use the combination of the two systems to store and edit in coming, US international wave copy in its General Automation 16 440 coupsiyr

COME UP AND SEE ME

A new model of the far illur pocket bleeper will be keeping, athletes on their towa a the Moscow Olympics

Multiture's new RE[3] reserveruses a combination of single digit numerical dispass with a choice of eight audible codes to copy ex-mule information than any othic being range rest is even the market. The necessar also has a

The receiver also hits a memory in a meeting for in stance where bleeper sound would be intrusive call refer mation can be stored and recilied after the meeting.

Ten remote control units shill be used in Moscow to send i ut

GLOW BAR



The new RGB 1000 from Entroms is a red 10 element long 20 pm DiL package for dividual addressable anode and enthode and intensity colour coding for displas uniformity are featured At 20 mA toxid winnows internats for displayind element an 5 and 0 s medrespectively. Suggested applications include solid strate nuctors and positional indicators batals frees I trong line 23 Churchgate Hitchin. Heres S(c) DN.

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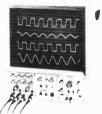
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ELECTRONICS TODAY INTERNATIONAL - APRIL 1979

BIG SCREEN SCOPE



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news

Choose have introduced what cost, large screen (17 incm) oscilloscope in Britain de signated the BWD 1722 The high sensitivity four

chopped presentation. All in pots are AC or DC coupled with independent guin and shift

MIGHTY MINI-SWITCH

5

controls. Trigger output is taken from channel one

Continuously variable sen-sitivity, from 35 mV 10 5 V per inch a provided. Auto manual inch is provided. Auto-manual, line and external triggering with a horizontal sensitivity of 100 mV to 50 V per litch are provided. The BWD 1722 selfs at £1350 from Chmaire Ltd Instru ments, Apsley House, Apsley Road New Malden Surrey

Diptran's new series of menat ure push buttons are built to last. The Series 12000 Minfbutton is designed for use in appli-cations where severe provining

The switch is designed for a life of one million detent opera tions. It meets the shock vabration, moisture-resistance. thermal shock, salt spray, explosion - proofing and sand and dust recurrements of MU-STD-202, a stringent specifica-tion Eight or ten standard dial positions are available. Series 12000 is available from Digitran UK, Melbourn, Royston, Herts.

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news digest.....

INFRARED FYES



NORBAIN has exensured the miroducion of two reacesline tice object tensors: Option types OPB76s and OPB799 are ceffective transducers incorporating a gallium orsensity inf rured emitting diode and a pla are altrone phototransistion (OPB708) or photodarlington (OPB709).

With a reflective surface of magnetic tape 0.15 inches from the read head, typical values of



photo current area to may (OPRIMI) near the may (OPRION). An aluminum foot as the end of of 1 max and 140 max respect the source of curst to the read lead maximum crosstalls curtice sources (OPRION) and 250 as a (OPRION) further details from Norbaum Optoal lectronics Division Norbaum Hoste Ark wright Road, Reading Berk. 1HREE FUNCTION TOOL

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DOING TIME?

ARE you one of the select few whose calculator is doing via months in Parkhurst? Have you been ordering digital watches from the Lord Chancellor? What FTI reader in his right mind would do that?

It seems that Moustanderc's ald phone number was similar to that of the Lord Chancellor's Phono Office. Hence the confusion

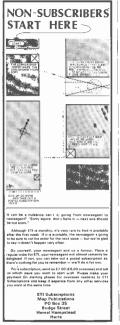
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POWER AMP SURVEY

The Americans would describe it as a 'crowded marketplace'. Power amplifiers appear almost daily and the resulting choice can easily lead to confusion. Ron Harris attempts an overview.



UPGRADING HIF is a costly business using commer cal units as better can somehow read dearer once over the threshold into a hif is importuni. Once contracted however, the improving, bug is no respector of prices and pocket.

Quite commonly the malady can be caught via the cones of new loudspeakers which are crying out for more watts to drive them. The amplifier just here to go!

The Modular Connection

One method of gaining the extra power – if you requite content with facilities etc. – is to replace output stages of your present equipment with two power amplifier modules. There are certainly enough on the market to choose from

This will extrainly be cheaper and most of these modules outperform smaltry proced commercial units so performance need not suffer. Since you need not need in must be cheaper Vary often too the existing case can be unitsed to house the new boards with artendant saving in that most onerous of tasks metallow?

Judging by the continuing popularity of the audio projects which appear within these pages do in-yourself his feantimes to abound even though building up from scretch is often no cheaper than buying commercial units Modular construction — with most designs being pre tested — can make this task easier and more certain

With kit construction however, there is obviously more to go wrang and this tends to mean the results are more dependent (at times) upon the constructor than the supplying company. We have been told by several reputable kit suppliers that the greatest single reason for non functioning units is poor soldering!

Board Decision

With the large number of available kits for power amplifiers in mind we decided to exclude them form our deliberations and concentrate on modules alone. This was defined as a unit in which the amplifier is supplied completely pre-assembled in other words as a PCB which can then be utilised.

Undoubtedly there are some modules we have missed out in our scan across the adverts — and if you know of any we have missed please let us know so that as few injustices as possible are propertiated!

Advantage Points

Using thrse inits is very straightforward. The manufac some input and output sockets and a case. Music should urers will have set up the amplifier already then flow forth - suitably amplified

with technique taking leadspeaker PSU and board amplification at 50 Hz alone i.e. hum. Use a spider

Connect all the earth tags on the input phone sorkets together and take out a single lead to the PCBs only this will alleviate any loop problems which may other When laving out the case, keep this to isformer as for

FLECTRONICS TODAY

Choosing

If you is trend your new varis to replace on aging or obtain a barely perceptible increase in sound volume (3 dB) you will need to DOUBLE nower putnut

through your 1812 renderings before. That extra 10 W is to mck neighbours out of brd - if they could sire? not going to add significant, umphilito your overtures.

It is hetter to choose too high a power output for your application and be gentle with volume control. then to underpower and regret it later. The conset rating fepends upon the voluane of the ream you intend to play

Allow 25 W for the first 1000 cu ft and add 10 W per for normal listening levels with a decent reserve If you use transmission line designs, add 15 W to methonency of this loading method



CESOS modifies mounted in their case along with PSU and preamp stabiliser board Inset a CEGOS in detail The Crimson Efektink amphilies system. Shown here are two

Table A Motion

ranging in power output from about three waits to well

All the companies produce their own power supplies in that not crowigh reserve is allowed for in the PStI design is for driving a single module. drains, the supply thus distorting the accord channel by is generously rated at least 50% above the

Wot Happened?

from each rohue. Must manufacturers seemed unable to respond within the time required approx two works common to all mages - 60W segmed reasonable and auld up a unit from each suppliers modules. This would have fold us much about the sound quality reliability

Press Dn

The scheme is not however deal and buned In fairness to Magnum Audio they came upon the whome late and were very quick indeed sonding us information and a samile of their excellent instruction vet - it is at least pussible that our samples are reposing

C1 5/7 8/2 \					12	X	COMPARISON TABLE	SISC	Z	IAI	BLE			
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NTERNATIONAL -	BI-PAK AL30A AL30A AL80 AL80 AL120 AL250	10W 25W 35W 50W	<pre>> < 28 (88) > < (88) > < (88) > < (88) > < < (88) > < < (88) > < < < < < (88) > < <</pre>	0 25% (5W) 0 1% (25W) 0 1% (25W) 0 05% (50W) 0 05% (50W)		60Hz 20kHz~ 3dB 20Hz 30kHz~ 2dB 20Hz 30kHz~ 2dB 26Hz 30kHz~ 1dB 25Hz 20kHz~ 1dB		1111	75 mV 280 mV 500 mV 450 mV		VES VES VES	15V 30 50V 40 50V 50 80V	74×63×28 103×64×15 103×64×15 192×89×49	E 4.20 E 5.11 E 7.2 E 1.2 E 1.8 E 1.8 E 1.8 E 1.8 E 1.8
APRIL 1979	CRIMSON CE608 CE1004 CE1708 CE1708 CE1708	55W 81W 92W 160W	(88) (48) (48) (88) (88)	All models 0.01% full 0.00.35% 10W		All models 2011, 20kHz* %dB	All models 110dB unweighted	All models 40 1 (50Hz)	All models 775 mV	All models 20 uS	All models YES	36 0 36 V 36 0 36 V 61 0 61 V 61 0 61 V 61 0 61 V	All models 80×120×25	L16 30 L19 22 L23 22 L31 90
	NLP HY30 HY50 HY120 HY120 HY400	15W 25W 60W 120W 240W	15W (8R) 25W (8R) 60W (8R) 120W (8R) 240W (4R)	D 1%(15W) D 04% (25W) D 04% (25W) D 04% (20W) D 05% (120W) D 1% (240W)	10Hz 15kHz ⁺ 10Hz 45kHz ⁺ 10Hz 45kHz ⁺ 10Hz 45kHz ⁺ 10Hz 45kHz ⁺	3kHz* 3dB 5kHz* 3dB 5kHz* 3dB 5kHz* 3dB 5kHz* 3dB 5kHz* 3dB	75dB 75dB 96dB 94dB		All models 500 mV	11111	All models YES	18-0-18V 25 0 25V 35 0 35V 45 0 45V 45 0 45V	PCB mounted 105×50×20 114×50×85 114×100×85 114×100×85	E7 05 19 20 120 53 1230 73 141 70
_	KINGSLEY ET1100	1001	V [4R	100W [4RI 0 1%(100W)		5Hz 50kHz+0dB -3dB	100dB	20	500 mV	T	YES	40.0.40V	I	£18 35
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21	STERLING SOUND SS103 SS105 SS105 SS105 SS110 SS120 SS120 SS120 SS120 SS120 SS120 SS120 SS120 SS120 SS1100	3W 5W 10W 220W 225W 40W 64W	24 24 24 24 24 24 24 24 24 24 24 24 24 2	0.3%(1W) 0.3%(5W) 0.3%(5W) 0.3%(10W) 0.05%(20W) 0.1%(30W) 0.1%(30W)		805 +24 805 +24	70dB 70dB 70dB	11111111	100 mV 80 mV 80 mV 140 mV 350 mV 500 mV		YES NO NO VES YES	20V 14V 24V 50V 50V 70V 70V	82×50×25 82×50×25 82×50×25 82×50×25 125×80×25 125×80×25 125×80×25 125×80×25	E 285 E 395 E 465 E 465

securely in the cavernous bosom of the GPO and should they ever be disgorged. Audiophile will be more than pleased to follow up and complete the project.

Anyway Only slightly daunted we shall proceed with what we have and consider the two amplifiers which did arrive (and the one on loan)

Our source for the listening tests was to be a Sony EL7 Eleaset machine which gives reel-to-real guility of reproduction without all the time consuming drawbacks of that medium. When you re trying to compare several pieces of equipment such luxurious convenience is not to be scormed lightly.

I could never understand why Elcaset has not done better for itself. The Sony machines in particular offer a standard of reproduction far above that which any cassette machine achieves.

The reference amplifier was a Lecson AP3 II

AL-120 BI-PAK

This unit arrives three quarters wrapped in a black heatsnik with connection being made to pads at one end which portrude beyond the edge of said heatsink. The output pair (2N3055s) are botted to the back of the heatsink and are hard wired into the circuit.

The quality of construction was generally high and in use the AL120s gave us no roouble at all. They drove the required speakers (Celestion / KEF) with no apparent distress and gave a sound technical account of them selves.

Grimson GE608

There is no really a lot to say about Crimson Electrik that has not been said already. Their products are well constructed well thought out and well thought of/ The CE608 is no exception.

Crimson supplied us their unit completely assembled within the superbimatalwork shown in the photograph which includes a PSU and stabiliser board to run one of their pre-amp modules.

The metalwork is black and in style looks not unlike a Quad 405 power amplifier unit

ILP HYSO

Since these are completely encapsulated we can offer no real comment on constructional finish. A mere five pins protrude from the metalwork, along which travels all communication between the HYSO and the world.

Three In A Testbed

Once introduced to their proper PSUs all three amplifiers functioned well and gave no real problems at all. The LP gave a poorer hum performance than the others regardless of how we tried to wrre it so the problem must be writhin the black, box

Of the three the Crimison gave what must be regarded as the best overall performance. Its sound is very claan and it possesses good attack. However the BLPAK, A2120 was not far behand, and loses out mainly due to a sight lack of ir mansparency when directly compared to the CE608. It has a warmer sound overall too, and one that many people may well prefer.

Alas the LP HYSO did not produce reproduction of the same quality as the other two. The test modules are about three years old though — our new ferview samplies not having turned up in time — so things may well have improved here. We hope to give a listen to some more



BI PAKs AL-120 module, removed from its heatsank. The output pair at centrally on the inverse of the black heatsask



The Magnum Audio range Their power amp is shown in the centre foreground. Note that this in fact a dust unit incorporating two amplifier circuits.

The ILP HY50 This is an encepsulated unit, and only five pins are req uired for connection purposes



recent samples as soon as possible to confirm or deny this but as it is the impression is one of a hard gritty sound which was immediately disfinguished in comparisons.

Conclusions

Well there is is Not as complete as might hour been but very interesting two hope: nonetholoss. As for the comparisons we never got if the manufactures agree we lit follow those up in the next few issues in Addiophile

FLECTRONICS TODAY INTERNATIONAL / PRIL 1979

FEATURE:Power Amps



Left: the Sony EL-7 Eleaset unit which proved the searce for the listening tests. Somehow the machine has never received the attention it deserves for its performance.

Below: remind you of anything? Looking like a squashed 405 its the Crimson unit all boxed and set to go.



-Suppliers

Magnum Audie Ltd 13 Hazelbury Crescent Luton Beds LU1 1DF

BI PAK Semiconductors Dept ETI PO Box 8 Ware Herts

Crimson Elektrik 1A Stamford Street Leicester LE1 6NL

Stirling Sound 37 Venguard Way Shoeburyness Essex

ILP Electronics Ltd Graham Bell House Roper Close Canterbury Kent CT2 JEP

Kingsley TV 40/42 Shields Road Newcastle upon Tyne N56 1DR



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PROJECT

VIDEOGRAPH

Turn your colour television into a dual trace oscilloscope with this UHF colour modulator and video display generator.

THE PURCHASE of even the simplest ascillosrope is probably unjustified for most amateur electronics constructors. Other amateurs feel rightly or wrongly that their money is better spent on projects which other members of the family can soncecase?

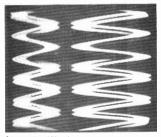
Which ever category you belong to or even if you are in the scope league already. Videograph will be found to be a fascinating and useful pace of equipment which will give many hours of pleasure

Principle Of Operation

The Videograph makes use of the fact trhat the television screen is scanned from top to bottom every 20 mS. This is used as the effective.



oscilloscope timebase trace modulation being obtained by varying the timing between start of



Sinewave generation with Videograph

ELECTRONICS TODAY INTERNATIONAL - APRIL 19 9

each line and a fixed-length bright up, pulse

Two complete crucits are required to produce a two trace and these an ecolour coded blue and orange respectively. These circuits are traggered by a common sync pulse generate and further components generate an angle-stage background colour change tradingtone background signals. There is also an internal signals. There is also an internal withis serves as a sets waveform for impection into amphifers and tipe recorders.

Controls are provided for inverting one channel freezing the background colour and switching a filter to give a relatively smooth music display.

Complete kits can be obtained from William Stuart Systems Ltd who hold the PCB copywright. They also produce a ready dnilled cabinet. The heavy gauge anodised facia plate is screen printed to improve finish and the PCBs are sitk screened to aid construction.

Construction

Two printed circuit board assemblies are involved one consisting of a UHP Colour Modulator and the other the

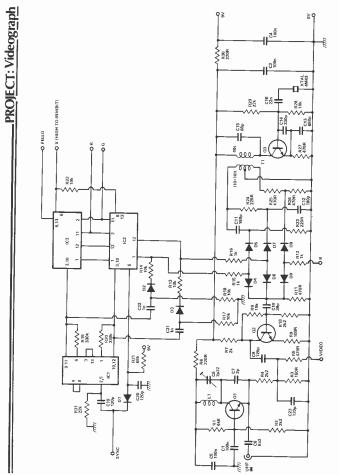
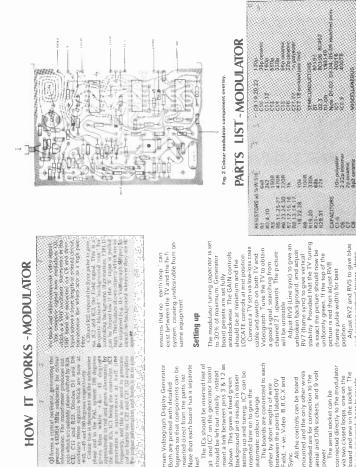


Fig 1. UHF Colour Modulator circuit diagram

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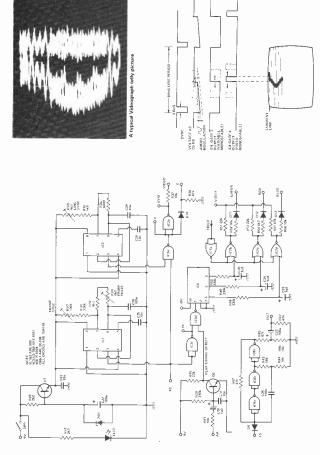
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-PROJECT: Videograph



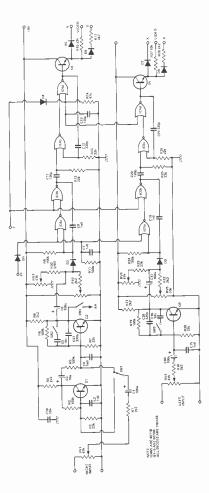
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Fig. 5 Generating graphics with the Videograph.

Fig. 3 Videograph generator circuit diagram

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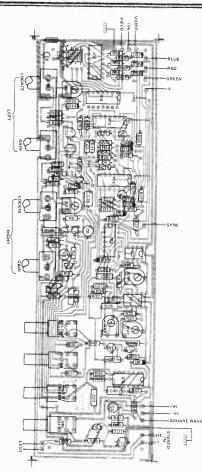


Fig. 6 Videograph generator component overlay



Circuit boards completed and installed in the Videograph chassis



No, it's not something from outer space!

BUYLINES

A complete kit of parts is available for this project from William Stuart Systems Ltd. Dower House, Herongate. B rentwood, Essex CM13 3SD. The PCBs remain their copyright rad will be available only from them. All components are available separately, and the PCBs are normally supplied as a "minkit" along with IC13 and ready wound coils. See advort elsewhere in this issue for proces.

pots are turned clockwise. Position both stripes centrally, then separate them using the LOCATE controls. At this stage the line sync (RV9) should be fine-adjusted to give perfect colour registration on the stripes.

IC7 may now be inserted (and the link removed!) to give the background colour change function the sequence being black, white, cyan, yellow green, mauve, blue, red.

PROJECT: Videograph

PARTS LIST - GENERATOR

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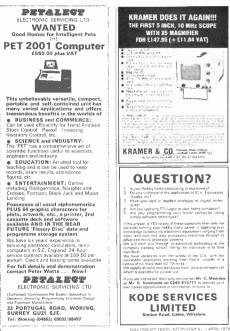


Above and below: Videograph 5 two dollour innon





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Gm REVISITED

Nothing to do with American car manufacturers Gm is in fact a throwback from the days of valves, now finding a new lease of life with up-torate semiconductor devices. K. T. Wilson explains

MANY A LONG YEAR ago when transitors were an item which had to been dream to by science licton writers we all used valves and we all known the major letters tim. Tim stood for a quantity called mutual conductance and it measured an important feature of the valve from which we could work out how much weare have passed and valves are deal for many valves have passed and valves are deal for many projectes. but Gen heres and is back variation to us

It is odd that Gm should have gone our of "ashion for so long because the idea of Gm is even more usefu in transistor amplifier circuits than it ever was in valve orouits. Still the idea seems to be conting back in a big way so let take a look at it.

Using Gm therefore allows us c : circles m a value or transitor as a generator of signal currents. The amount of signal ourrent being Gm Vin. Now a current generator means a device which will deliver its current into any laad high or low. No valve or semiconductor is really like this but for most of the uses we make of transistors the idea of a current generator is not 16 if form the mark.

Current Generators

If a transistor were a perfect current generator, it would have an infinite resistance at its output. That means just that a signal voltage applied between the collector and the signal voltage applied between the collector applied between the collector and the signal voltage applied between the collector applied



Fig. 1 Mutual.com ductance, Ic Vbs for a transietor

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Ohre again it's not built correct but not fair from the trick whit it collector signal current does flow but not it's nick about sign sch as would flow 1 there were a revision of around 40k between collector and emitter

Now the updations of all this is that it allows us to drawn equivalent cruck first instructs. An equivalent of even equations of the same sort of ways some device which is an reality much more completated. A simple equality for the same sort of ways is some device update it could be a familitate in therefore as shown in Fig. 2. It consists of a current generation, which also the device the shown of the same shown and the same shown are also the same shown and the same shown are also the same shown are al

These does that help us? Duras a fait if we enreresses at the train that equivalent protricults are about all the train that equivalent protricults are about that the train that equivalent protricults are about the second second second that the Why? applies of the second second second second second that the second second second second second based on the second second second second second that the second second second second second that the second second second second second that the second second second second second the second second second second second second second the second second second second second second second the second second second second second second second second the second second second second second second second second the second second second second second second second second the second secon

RCP RL RCP + RL

and then the soltage signal out was just the current signal times this resistance (Ohm s Law still rules. OK?) ovino.

Gm Acc BL Rce+BL

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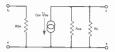


Fig. 3 For AC signals, a load resistor connected between collector and positive supply behaves as if connected between collector and amitter.

Gm = & charge carried by an electron & e = Charge carried by an electron & e = DLTZMARN & CONSTANT T = TEMPERATURE IN KELVIN SCALE ic = STEADY (BIAS) COLLECTER CURRENT

Simple Silicon

One of the things that makes life simpler in these days of science managers is that the quartity Roe the output resistance of the transition is quite a large value compared to many of the load resistors we use A mount quite a bit larger than the 3d or to wr use at a load so that most of the signal current from the transition is through this resistor in the equivalent circuit. That a fransistor the larger shares the gain of a fransistor amplifier as gais Qmm.

If it is all only as that why, don't we see all in text book. The restories we hadron 4— we within 1 star wurth 11.00 in transistom, and a transistom, unlike a value of collection current agains there value graves and the dot start any start agains there value graves in a gravitic of unlike current agains there value graves and graves and the start agains there values and the start agains there values and the start of the start of the start of the dot start any start is start of the start of the start of the current start of the start of the start of the start of the interval start and the start of the start of the start of the collection current start of the start of the start of the interval start of the start of the start of the start of the interval start of the start

Ebers Moll

A few years back, though, the Ebers Moll equation was noticed. You ve never heard of it? You re not alone very few text books mention it and some mention it without explaining it. Very briefly, it s an equation which links the collector current with the Vbe value for a transistor. In other words, it is the equation for finding full of mathematical symbols you may never have seen before it repays close attention, though, because most of the symbols are of quantities that are pretty well constant and only two of them vary very much. One of thom is the steady bias current. Ic and the other is temperature. As it happens, temperature, for the our noses of the Ebers Moll equation is measured in the Kelvin scale which starts at the absolute zero of turn perature around - 273 L. Room temperature is therefore around 293K (no degrees sign) in the Kelvin

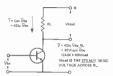


Fig. 4. Transistor circuit with load reaster (RL). Gm cm ba replaced by 401c

scale and a few degrees above or below doesn t make much difference to the aguation

That leaves Ic as the one thing that really affects Gm and the relationship works out at approximately

Gm=401c (lc in mA)

Put in words that means we can take a Gm value of 40 times the steady bias collector current in milliamps. For a beas current of 1 mA the Gm value of a transistor is 40 mA A Too good to be true?Looks it but it reality does apply to any silicon transistor apart from a faw freak types.

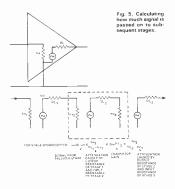
This brings back the Gm idea in a big way, and we can forget a lot of the old formulae we once used in calculating the dangen of transistor amplifues. The fact that Gm is not constant but varies with the bias current is addly enough a help rather than a hindrance.

Gain

Gauge back to our equivalent orcuit and ignoring the large output restance of the transistor we rain now write 401 cm place of Gm (fig. 8). This makes the gain of in this equation is the trady back collector current and to local must be the steady back collector current and so local must be the steady back collector current and four restor. The makes calculating the gain of transisfor implicity with resistive boars as bet sater than falling multiply by 40 and their syour value of gain.

For example, we very other disign voltage amplitures to sharabour Hard of the supply voltage is dropped across the lead resistor. For a 9. V supply that s 4.5. V D this times. Don't believe it if works at lingth and tests on a single transistor amplifier continuit as a rule of thumb vold on to docume with supply and again of earch 18.0 on the case, we substance there are at 1. obtained to the providence of the super super super supply of the super supply of the super s

When you couple a single transistor amplifier to another stage of course that s another story. You may have set the gain of the first stage to 180 times but not sli of its oupput signal ends up usefully at the input of the



next stage. Reason? The next stage has a rather low imput resistance and feeding signal from the collector of one transitor into the base of another even if they are directly connected, is rather like feeding signal from the collector of lating how much of the signal is passed on One simple way is to imagine a voltage divider (Fig. 5) in which the load resistance of the first stage forms the upper resistor and the input resistance hie of the second stage. The quantity A, (on k ohms) is equal to h_x (Gm where h_x) a value one transitor and another For a transitor with h_{e} = 100. Gm set to 40 (1 mA collector current h_is 100 / 40 = 25. If we feed this from a transitor in the second transitor in the single resistor the amount of signal reaching the second resistor is the second resistor in the amount of signal reaching the second resistor is a signal to a signal a second reasistor in the amount of signal reaching the second reasistor is the second reasistor in the amount of signal reaching the second reasistor is a signal signal formation and the second reasistor is a second reasistor in the amount of signal reaching the second reasistor is a signal sis signal signal signal signal

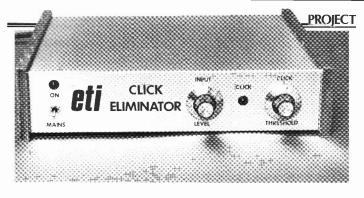
$$\frac{25}{25+47} = 35$$

of the signal at the output of the first. This brings the gain of the first transistor stage down to $180 \times 35 = 63$ which is the sort of value we usually measure for one stage of a multi-stage amplifier.

With all this going for it Gm is coming back, folks As Sam Goldwyn is supposed to have said, 'simplicate and add lightness' Let's hope we ve added a bit of lightness today



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CLICK ELIMINATOR

Part two of the Click Eliminator article, presented here, is in fact a redesign of the project leading to better performance and lower cost.

In the January issue of ETI we presented a design for a click eliminator unit. However, between that issue and the time for the february ETI — in which we were to complete the project we found several disturbing inconsistencies which would have rendered the design s repeatability doubtful—to put it midlly

These problems mainly concerned the area around Q1, IC9 and IC10 The biasing arrangement for Q1, and its function within the circuit means that the adjustments are very very operated satisfactorily, especially in its breabbarded form, but was too dependent upon too many variables for us to be happy with the project

Taking Aim

The aim then as now was to present a design for a unit which would remove the clicks and scratches from damaged LPs without impairing the music material contained therein

Operation was to be indicated by an LED, and threshold of operation was to be variable to make the Eliminator flexible in use However as we said development work has continued since initial publication, and while we felt that there was nothing wrong with the aims of the project, our method of realising them left something to be desired

Change Of Track

Accordingly we are presenting here an alternative design, and recommend our readers to construct this in lieu of the design shown in Part One of the article. A comparison between both circuits will show this version to be greatly simplified, and using components which will make construction cheaper.

For example the 570 has been replaced with a 4016, which is closed to the signal for a short period of time to blank the click signal

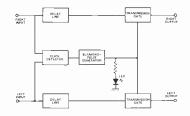


Fig 1. Basic block diagram for Click Eliminator Mk 2

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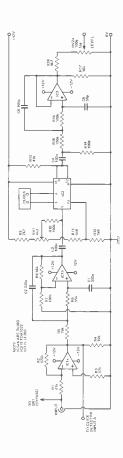
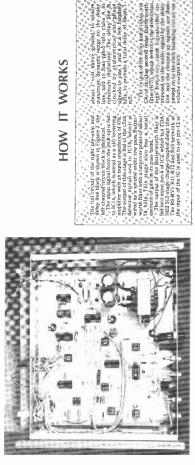


Fig 2. Circuit diagram for the audio pre-amplifier and delay line sections of the Eltiminator unit. Note that only one channel is shown, but both are identical.



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dominantly in phase recorded signals, so that the output of the IC consists of an chcks." This signal is passed to threshold The "threshold" level of IC5 can be so that the output of the IC is just held arrives, the output of ICS switches to negative saturation, to produce a large negative-going phise. This pulse is used to trigger, monostable multi-vibrator. IC6. each input. The action of this IC is such audio signal with greatly emphasised loop volfage comparator, with its output high throughout the passage of a "clean" signals, but tends to cancel the predetector IC5, which is whed as an open adjusted via panel-mounted control RV3 that "t' amplifies, the anti, phase "chol record. Then, each fune that a normally at positive saturation. Fig 3. Circuit of the click detector section of the Mk 2 Click Eliminator. The LED flashes 92 TO PINS 56,12,813 OF IC8 Ş¢ 432 HOW IT WORKS ē3 829 47k 010 tector block, which incorporates a "elick " a threshold detector, and a blanking pulse generater, is shown in "chck" or scratch has a number of ponents. Also, it appears to a stereo pick-up head as a set of recorded antinormal, recorded signals, tend to be in and decay times, and its output us conse-quently, rich in high-frequency comtal movement of the stylus. The ETI Click Eliminator uses these unique phase cha-The full circuit diagram of the click de unique characteristics. It has fast attack phase signals, since it causes purely verphase and cause predominantly horizon C9 1001 jical displacement of the stylus. 1428 LEDI 1 6125 10k 82% dentifier. Figure V2L to indicate operation. waveform, which includes some ringing. Fig 3(b). Below. the combined waveform ig 4 (a). Above: the waveform of the Click climinator blanking pulse stradding the click showing the blank period inserted into the 223 R22 100k 624 IS 741 ICS IS LF396 ICS IS LF396 ICS IS NE595 O1 IS BC100 LED1 IS TIL 220 NPUT & O O 17 7 7 **ADTF** give an indication of the timing of the blank period is made to straddle the the click and generate a pulse which circuit, and the manner in which the long enough for the circuit to detect the Chek Eliminator Figure 2 is the Figures 2-6 show the schematic for irom points A and B marked on the The unit is assembled onto a single 00 igure 5 shows the click detection components Inputs A and B come As the block diagrams of Fig. 1 audio input and delay line circuit audio is delayed by a TDA 1022. The waveforms shown in Fig. and blanking pulse generation shuts off the transmission date **Circuits and Components** will show, the basic remains 4016) as the click' arrives unchanged The incoming eft and right audio inputs respectively click signal

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Construction

resistors and capacitors first, and ICs and servicing should this be needed ast. Sockets are best used for these quite straighforward Assemble the poard carefully, remembering to fit items. This will facilitate checking PCB and so construction is realive devices especially the high cost The easiest place to make a

Cs etc so check these carefully. It is components - electrolytics, diodes check this before connecting to the pest to build up the PSU first and mistake is in fitting the polarised est of the circuit

and drives output transistor QI to satu-ration for the duration of the 5 mS pulse. The output of Q1 appears as a blanking whise, and is fed to the click blanking

circuit of Fig 4.

which has a period of about 5, miS, and which drives "click indicator" LED-L-on

racteristics to provide its primary means In the pressive, the amplified pick-ab two, channel, pre-amplifiers' (ICLa, Fig. 2. and are passed to one or other of the two minals of ICs in Fig 3 IC4 is a differencial simplifier or "sub-

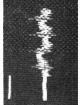
or click slenufication.

signals are laken from the outputs of the

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blanking (and bypass) and system Circuits 5 and 7 are the output clock respectively. The latter is D and Q



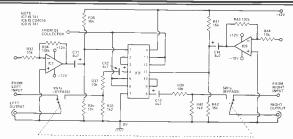


Fig 5. Click blanking circuit. Note that SW1 is the bypass switch.

HOW IT WORKS

switches of ICS with negligible loss or gain, but in the presence of a "click" the type scruesconnected witches or (CS open LimS before the series) of the click and remain open for sound 5 mS, thus replacing the click with an imperceptible "black". 'blank." .

Nute in the erecuit that the inputs of 10 -0 are biased at half-supply volts to enable

the IC to pass signals with a minimum of distortion when operated from a single ended power supply. The 1016 IC suffers adde power supply The sup it, success from a certain amount of control-signal breakthrough, by using a times ten ampufee before the input and a divide, by-ten attenuator nice the output of the (C, this breakthrough, is genoued to in-significant levels mative to those of the sic audio ugnal. 14 . ÷

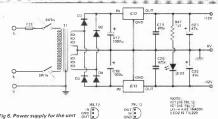


Fig 6. Power supply for the unit

and the input of a stereo amplifier Volume control RV2 should be adjusted so that no perceptible difference occurs in audio sound levels when the bypass switch is switched in and out. Pre-sets RV1 and RV101 should be adjusted for minimum distortion on the Right and Left channels respectively Threshold control RV3 should be adjusted in use so that LED 1 just operates in the presence of a click

It should be noted that the relative amplitude of a click is proportional to the velocity of the record track past the pick-up head and decreases as the head moves towards the centre of the disc, the threshold control may

consequently need occasional readjustment as the record progresses through its play

There is no equalisation circuitry within our design, and so it cannot be used in place of the preamp in your system, it must be used in front of it instead

When playing damaged LP's simply advance the Threshold control RV3 from its minimum setting until the click is removed This is the correct setting

LED 1 will indicate the unit operation and if it flashes on musical peaks chances are you have the threshold control set too high and are removing some of the signal as well

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The curcuit of the click blanking block is shown in Figure 5. Circuit operation is fairly straightforward. The output of rach channel is taken from its volume control (Fig 2) and is fed through a times ten crting amplifier (IC7 or IC9), and is then passed to one half of IC8, a 4016 quad bilateral switch. In each channel, two-of the Internal "switches" of the 4016 are wired in series, and are normally held on by the high control signal from the col-lector of OI (Fig 4), but turn off for 5 mS when a blanking pulse arrives from the click detector circulit. The output of each click detector circuit. The output of each channel is then passed on to the outsideworld via a divide-by-ten (approx) attepuator network Thus, during "clean" parts of the record the output signal from the delay line is, passed through the click blanking circuit of Fig 5 via the two series connected on -The power supply is a straightforward design based on a pair of three-terminal IC regulators, which provide plus or minus twelve volt outputs: LED 2 is a panel-mounted component; which indi-

Next assemble and check the audio circuitry. Make sure a signal is present at the level control RV2a and RV2b Normally IC8 gates will be open and so an audio output should be present at the phono sockets if all is well

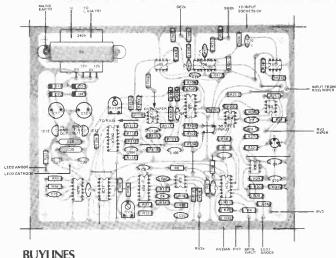
cates the power on state. ->

If no output is present, check the audio through to RV2, and if a signal is present here, the fault probably lies with IC6 and Q1. Disconnecting the base of Q1 will restore output if this is the case.

Over the Threshold

In use, the unit is connected between the output of a record player pick-up

PROJECT: Click Eliminator



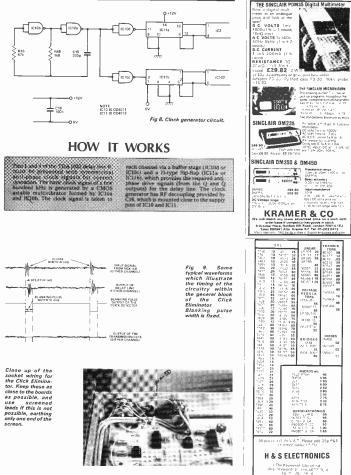
Being composed mainly of standard' components, the Eliminator should pose most component shops no problems. The LF 356 is available from Watford in case of difficulty.

Fig 7 Component overlay for the Click Eliminator unit. Note that all the components bar the potentiometers mount on this PCB. The operation LED is also best front panel mounted.

PARTS LIST

RESISTORS (all % R1, 3, 13		POTENTIOMETERS		SEMICONDUCT	ORS TLOB3
29.45	47k	RV1	4k7 preset	ICt	
A2	470k	RV2	100k log twin gang	IC2	TDA1022
R4, 17, 23, 25,		RV3	5k Lin	IC3 5	LF 356
33, 37 39, 44	10k		and the second second	IC4 7 9	741
R5, 6, 30, 31		1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1		+C6	555
35.36.41.42	15k (*)		The second s	ICB	4016
R7. 10. 14.		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		1010	4013
15, 16, 20.	Contraction of the second s	2 C. G. W. L. W. C. L.	a the second	IC11	4013
22, 23, 34.		Contraction of the second	A	IC12	7BL12
43	100k	CAPACITORS	Contraction of the second s	IC13	79,12
RO	56k	C1, 2, 15	330p polystyrene	01	BC 109
R9	2k7	C3, 4, 9,	and the second sec	D1-D4	1N 4001
	6k8	10,16	100n polyester	LED1, 2	TH. 220
A11		C5	56p ceramic	LCG1. 2	HULLO
R12. 27. 28	1k	C6	100p ceramic	and the second second	AT A REPORT OF STREET,
R18, 24	447	C7	10n polyester	CARL ARE	19 K
R21, 26, 32	22k	C8	2n2 polyaster	1000	
R3B, 40	182	C11-14	4u7 25V electrolytic	MISCELLANEOL	2
R46	188	C17, 18	1000u 25V electrolytic		ransformer (100mA), fusi
R47	1k5		470n polyester		case to suit, DPDT main
Resistors 101-118 to R1-18	tor RH channel identical	C19.20 C21.22	47u 25V electrolytic	switch control kn	

PROJECT: Click Eliminator



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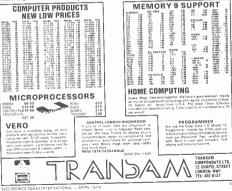
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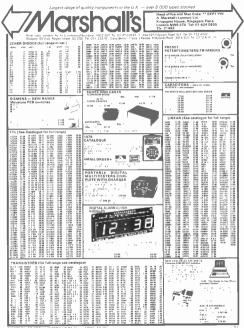


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data sheet

IC SURVEY

THERE ARE VERY many K sasariable on the market today and new devices seen to appear dash (probable buarts). This barrage of is chosen seen to appear sain (probane hours.) This barrage of is chosings can be rather daunting particularly to the new conser-to electronics. This following article crisis to unitargle some of the confusion by surveying IC technology in four groups of devices. On After auto amplifiers multipliers and explications

Operational Amplifiers (Op Amns)

There on many different types of OP Atop and they are manufac-tured to several different companys. Most of these companyes produce standard Op Amp devices but they put their own part

In pownt years, the word has been to decalors IC , with more than on- Up Amp inside. This has resulted in a range of dual and guad Up. Amp inside. This has resulted in a range of dual and quad Op. Arring packingles. Texas naive provinging ous a range or ourse Op. Amps. These are goin fore pin compatible in sith standard types but those are different in that they have FET inputs, giving them a vers high intell immedance

Chart I shows comparative performance for several standard () Amo types. The parameters chosen are the most important ones when selecting On Amos

Audio Amplifiers

Several manufacturers produce monolithic medium power amplifiers for audo use. This makes the design of small andor CHART 1 OR AMO

amphilter vections relatively ensy. There are some pitfalls to watch amplifier sections renarces easy. Increases and pressure owners out for 1C amplifices can easily destrict themselves if the power rails are high or if insufficient heat staking is provided. There are now quite a wide range of devices some of which are shown in

Multipliers

The paper of multiplier IC shas are er from sure large four recently a few more have been added to the list partly inspired by the needs of telephone companyian is stems. The sessions produce a better signal to noise ratio over the line. Another and very common noise reducer is special multiplier) is the Dolby B club. This unfortun ately is only obtainable under lacense

Bscillators

Uscillators Diff an mini-occilator K is that can pervade mass forms with periode of several hours to tens of many seconds. For high inclumons work there is the SNTA-DIA at SNTM- and the LITT's 200 MHz. There are TLL devices thes are not linear and are include for NNTA-There is relatively the laboration address and known linear VLO. Teterism, also make a wale range of VLO modules. The VNMU's not the SNTM are the SNTM. both pseudo random oscillators, that is, they oscillate but the way form is so complex that the resultant output just sounds also ADDIDOLD DEDEGDMANOS

_		CHAR		UPAN	1P -	ABRIDG	ED PER	FORMAN	ICE S	= Single	D = Divid = Quad
Op typ	s amp se	input offser vohage miv	Input Oiss Generati GA	Type of input shruttare	Band width MHz	Slew Mile V NS	Voltage gan gan d8	Maxernum supply voltage V	CM/RP dB	ūr _y	Comments
71	09	2	300	NPN	1	0.25	90	± 1.8	90	\$	Needs frequency compensation
31	87	2	70	NPN	1	0.25	100	±18	90	S	Internal frequency compensation
31	01	2	70	NPN	10	05	100	±18	90	5	Needs frequency compensation
74	41	2	80	NPN	1	05	106	± 18	90	s	Internal frequency compensation
74	\$8	1	120	NPN	10	05	103	±22	90	s	A decomponsated 741
36	18	2	15	NPN	3	05	110	218	100	5	Low supply current dram 0.3mA Needs frequency companisation Very low differential input voltage rense
3		4	150	NPN	15	50	106	±20	100	S	Very low differential input voltage range Sometimes needs frequency compensation
74	47	2	80	NPN	1	0.5	106	± 18	90	D	Internal frequency compensation
	\$58	1	80	NPN	1	0.8	103	±18	90	D	Internal frequency compensation
	138	05	40	PNP	З	10	110	±18	100	D	Low noise
	900 401	Current inputs	30	Current	25	0 5	70	±18	-	Q.	Current balancing amplifier
	24	2	45	PNP	1	0.5	100	+ 30	70	Q	Ground sensing mputs Output voltage can go to ground Low power 0 8mA drain per IC
34	403	2	150	PNP	1	1 2	100	+36	90	۵	Ground sensing inputs Cless AB output Output voltage can go to ground Low power 3mA drain por IC
3	48	1	30	NPN	1	05	103	±18	90	۵	Low power 2 4mA down per IC Cless AB output

NEWS

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Part Number FAIRCHILD				,XAR Toledvor	382209	LIN VED Jow cost LIN VED LIN VED	SprDit 14 perDit 16 perDit	0 D1Hz to 1MHz 1000 1 sweep range 10Hz to 100kHz	and Puter and
nA 739 nA 706	 D Wi 	noise storeo p itt audio ampl olizga	ther bow	EXAR EXAR	XR2206C XR2205C	LIN ICO + AM + FSK LIN ICO + AM	16 p 4 04	2000 1 sweep isros D D1Hz to 1 MHg 2 1 sweep up to 40MHz	100 mes -
MOTOROLA MC 1306		asti audio an 2V operateo		EXAR EXAR	X822070 X822090	LCD UN VCD	sá ger Dit. Biy e Dit.	1000 1 sweep range 0 01Hz to 1 MHz 1000 1 sweep	· ·
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sA739	1	300	NPN	10	86	±18	90 0	Feirchild device on Low noise autho an Needs frequency c	palatier
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CA3140	8	0.010	MOSFET	4 5 9	9 100	+36	90 S	Ground sonsing inp Very high input vinc	redenca
CA3150	6	0.005	MOSFET NPN	4 1		+15 ±22	90 S	Ground sensing inp Vary high / imput in Vary fast op amp	pedence
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	15	0.4	JFET	3 1:	3 83	±18	70 S	op amps, with	7d 1
TLD81								and wide	
TLD81 TLO82	15	0.4	JFET	3 1		± 18 ± 18	70 C	bandwidth (TEXAS)	7458



TELEPHONE CALL TIMER Submitted by Mr A. M. Tucker of Dorchester.

TO CARRY OUT its function which is to display the cost of individual calls, and also to keep a running total of all metered calls the circuit must add the amount of the unit charge (at present 3p) to each register when the call commences, and subsequently at the end of each charge period. This period will vary for peak standard and chean times and with distance. Provision should be made for altering the settings of the counting circuits if there is a change in the Post Office charges

Various circuits were considered, and this was considered to be as cheap to make as any for the facilities provided as although there is a large number of ICs, the

The two sets of figures are circulated in a single shift register, the digits being interlaced, in the least significant figure in one register is followed by the least significant figure in the other register, and then by the next figure in the first register, and so on

In order to be able to adjust the unit charge, and the periods available per unit, the outputs of the dividers are connected to sockets into which leads from the inputs of the resetting gates are plugged. These sockets, now parking places" for spare gates, can be made from IC sockets or soldercon pins in plastic supports. To prevent damage to the pins of sockets when cutting into sections push into a piece of rigid foam plastic. The wander leads are just lenghts of connecting wire. Solid core in suitable if stranded wire is used, tin the end and check that it is thin enough to insert into the socket

In the interests of economy, small low consumption displays have been used. If larger displays are required at will probably be necessary to add segment drivers. The

drivers should then be supplied from the unregulated side of the supply, and S1 made a double-pole switch

The 9-volt standby battery is essential, as otherwise the total cost register would be cleared in the event of a mains failure. In order to reduce consumption during cates, the oscillator IC21 and the display buffers and driver IC23-IC26 are switched off by S1. It is unwise to try to include other ICs as some inputs may be high. In any case with the oscillator off, power consumption is very low in the remaining circuits

It may simplify the wiring of a 4001 and a 4011 are substituted for the 4069. One NOR gate can be used instead of IC20a and IC22a and a choice of ICs is available for the other inverters

The meter can be adapted for battery power only by including a 4518 to divide the 10 kHz oscillator frequency down to 100 Hz and doubling the division in IC1 hy shifting each flying lead one place to the right Setting the oscillator frequency exactly can be carried out either by comparing the 100 Hz output with 50 Hz from the mains on an oscilloscope, or by varying the setting until the charges are incremented at 10 second intervals for long distance calls as peak rates

Decoupling capacitors for pulses in the supply lines may be required While CMOS is less exacting than TTL fitted across the supply pins of ICs at the end of supply lines and across each of the more complex ICs.

A flashing LED is provided as an indication (and reminder) that the timing circuits are operating

HOW IT WORKS

TO commence timing a call, SWI is switched on, and SW4 and SW5 art. When the person repites, SW2 is closed. This removes the reset from IC1 and IC2, which removes the ensuring software makes at the start containing software makes at the same time IClis is trigged, producing a ImB pulse which clears the single call regulater like digits being selected by IC21b and

At the terministion of the pulse, Q goes how and triggers ICBb. The Q deliput of this IC thes goes low for 7m5 or until reset by IC these green low for 7mS or until reset by IC7, which is mabled by the high Q output of IC6b and is clocked through IC80b each of ICEs and in clocked through ICEEs each time the LSB of the registers are present at 0, who 0, of C11, until the output con-nected to IC22e goes high, when ICEs resets not infibilite IC7. The output from IC7 is feel through ICB to

the 'carry to' of the adder (HC14) driving the

LSB. Three cycles of the shaft registers are required to increment the registers by 3p. SW6 and SW3 set the time available for

one solt. For present Prat Office rates 1C1 Is preset to divide by 250, giving an output pulse every 35, JC2a divides by test, three or tweise, (Cilb by three or twee

A pulse stretcher (R3, C3, D5) is included to ensure IC1 meets

to resour ICI result. When the traming pulse reaches (C204, ICIB) is retraggered, clocking, up another out change. The two sets of figures are stored in low S stage shift report ICI3 and ICI3 and are circultural through the addit (ICI 40, The digits, an selected for addit (ICI 40, The digits, an selected for display by the dreider

Clocking of these ICs and IC16 to rffeeted by the ISL oscillator IC21s, b. The exact frequency of this is not insportant. but must he related to the length of the monoscables

KORs and HORs.

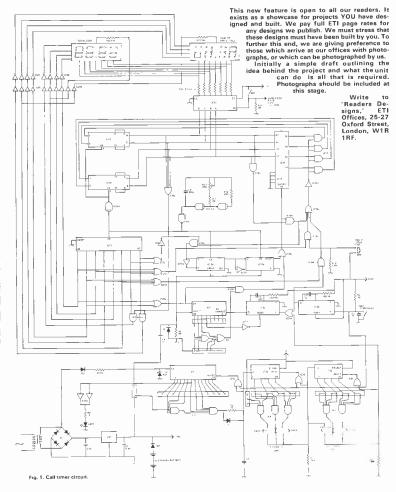
Milic is a buller and the law clocking traine required by the shift restators in myticed by IC21d

provided by RCHd. When the call is conspicted, SW2 is swached to off, and the resets or RCI and RC3 po high, stopping the count. The cost of the call remains on the registrer until SW2like cloved for the next call. At the end of a quarter, the "total cost" register can be quarter, the 'total cost' register can be rested by pressing SW3. C4. D4. R6 provide a power on reset which ensures that the flip-flops are correctly set mitially, and that EC1 is not started in the middle of a charge period.

on an ever calls are expected to be made for a while 5W2 is opened, drooping Current Consumption to a very loss forupy at that a battery batkup can be used against mains failure

PARTS LIST

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AMBUSH! is bound to rate as the most fascinating, exciting, and addictive space game of the year. It gives visual and sound effects of a space battle, and is loaded with realism. Impress your friends (and enemies) by building this unique and fascinating

AMBUSHI is a space game par excellence. It represents a space ship by a fleet of suicide craft. The craft can attack you on one of four randomly selected quadrants. The attacks come one at a time at randomly selected intervals that vary vessel with one of four FIRE buttons those buttons to stop the attack if tione, you use up ammunition at an

game.

AMBUSH

The game continues until all the attacking craft are destroyed or until FIRE button by hitting the wrong

ammunition through incorrect operation of the EIRE buttons. You can chose to face an attack by either ten (a DEK) nr a bundred (a CENT) suicide craft, ammunition storage is typo of game chosen. A DEK game typically takes less then one minute to play. A CENT game takes several

Sound And Light

The name is loaded with audio and visual effects. On the sound side represent an attack or the operating winning or losing of a game. The level of the ATTACK sound varies with the guadrant of attack attacks

(Photo be retained and 20th Century Fax)

half volume, and those from aft are at

The visual effects are also quite impressure. The attacks are shown by an array of LED's arranged in the lengths. The upper arm represents comprises five orange LED s. The quadrant and comprises seven green LED s The port and starboard arms the centre of the cross is a red LED.

The game is also provided with an ammunition level indicator in the

Science Project

Ambush' is a CMOS based design of considerable technical interests and should make an excellent educational project for schools and colleges. It uses sevenieen IC's plus a couple of transistors. The IC types range from simple NAND and NOR gates to complete decade counter-decoder chips, and include flip-flops, data latches, 12-stage ripple counters, and multiplexers.

Playing The Game

Game Start. The game starts as soon as power is applied to its circuits. A game can be restarted by pressing the RESET switch.

Attacks:

 The game can be set for play against either ten (a DEK) or a hundred (a CENT) attacks

(2) Attacks come at random intervals, variable between nought and approximately five seconds

(3) The quadrant of each attack is randomly selected, except for the first attack of the game, which always

HOW IT WORKS

SIMPLIFIED BLOCK DIAGRAM OF THE AMBUSH GAME

The heart of the unit is the Druptoy Marks Driver and Logic block, which in reality takes the form of a 4017 decide counter with the decoded autors. Outputs 16 of a fibe counter are feed to the Display area with, and outputs 16 to are selectively feed via a multiplexer to the selectively feed via a multiplexer to the OLOCK DISABLE prior of the 4017. The input of the 4017 is derived from a clock CLOCK DISABLE prior of the 4017. The selectively declared and a pate, which in turn is controlled by a simple START-STOP (Reset set) bushable.

The maximum concerns of the above six backs is fairly simple, initially, the initiality is an its BTOP mode, the gate is lossed the sim the STOP mode. The gate is some standard to the simple size of the some standard size of the size of the gate operation of the size of the size of the gate operation of the size of the size of the maintain of the size remains open, one of the selectively bears of the size is the GANF LDN the size of the size is the GANF LDN the size of the size is the GANF LDN the size of the size is the conk input of the size of the size is the conk input into of the size of the size is the conk input into of the size of the size is the conk input into of the size of the size is the conk input into of the size of the size is the conk input into of the size of the size is the conk input into the size of the size is the conk input into the size of the size is the size is the conk input into the size of the size is the size is the conk input into the size of the size is the size is the conk input into the size of the size is the size is the size is the conk input into the size of the size is the size i

Alterniadverly, the bistable can be set to the STOP mode before the game terminacts by operating the oppropriate FIRE aware in in this case the bistable closes the clock gate and the 4017 revels to the zero state. A new sequence of operations starts when monther random START palse is fed



comes from the aft guadrant

(4) The speed of attack can be pre-set by the player to suit skill levels A 'respectable' attack speed is equal to about 50 mS per LED division on the quadrant attack indicator

(5) At respectable attack speeds, the player has approximately 250 mS of attack warning on the forward guadrant, 300 mS on the port and starboard quadrants and 350 mS on the aft quadrant

(6) Attacks on the aft quadrant are accompanied by a full volume staccato sound. Port and starboard attacks are at reduced volume, and those from the forward quadrant are silent

(7) The accumulated number of attacks is registered on a 2-digit display throughout the game

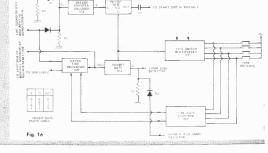
to the input of the histable. Note that output 1 of the 4017 is fed to the ATTACK COUNTER so that the counter advances by one count each time the clock genera-

> Figs. 1a and b. Simplified block diagram

of the Ambush game

tor gate opens. The game ends shortly after the attack counter reaches its full (at 10 or 100) state, at which point the GAME WON indicator circuits come into operation.

The START signal to the bistable is derived from the randoni delay generator, which is integral with the FIRE switch fircuitry. In each attack, the appropriate one of the four FIRE switches is selectively coupled to the STOP side of the



PROJECT: Ambush



(right) This board carries LED display matrix drivers, multiplexers and logic, plus audio and power connections.

Defence

(a) The player has four FIRE buttons for delence. The buttons are marked F (forward). P (port) S (starboard), and A (aft) To stop an attack the player must press the FIRE button appropriate to the provaling attack quadrant before the attacking vessel reaches is target (the red LED at the centre of the display). A correct firing is accompanied by a tasping sound

bistable via a multiplexer, and a simulated fore' sound is generated if the operator activates the correct switch, the frequency of the first mound is determined by the FIRE RATE SELECTOR circuit, and is proportional to the total number of FIRE switches pressed at any given moment. No sound is produced in the wrong button is pressed.

(b) The ship has sufficient ammuniton to fight of fattacks only if each FIRE duration is limited to about 100 mS or less Thus, there is sufficient ammunition for about one second of continuous fire in the DEK game, and ten seconds of fire in the CENT game The ammunition state is shown on a register throughout the game.

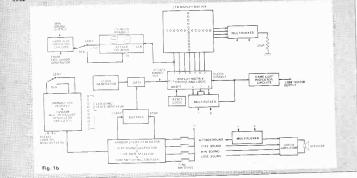
The output of the fire sound generator is used to drive the annumition register, which counts and gives a visual readout of the total number of cycles generated. The sound is also used to generate a latched random 'select' code for the four multiplexers that are used in the game. These multiplexers are used for FIRE. (c). When the correct FIRE button is prossed, the rate of ammunition usage is directly proportional to the total number of FIRE buttons that are pressed at that time. Thus, if all the fire buttons are pressed at once the ammunition supply will exhaust in 0.25 seconds in the DEK game. The audio frequency of the FIRE sound is proportional to the rate of ammunition usage. When the player has no defence, and loses the player has no defence, and loses the game after the next attack.

Game Lost. The player loses the game by having his starship hit by an attacking suicide craft. When the game is lost the red LED at the centre of the attack quadrant indicator turns off, and simultaneously a loud drowing noise is generated and a red GAME LOST LED liashas on the control panel.

Game Won. The player wins the game by defeating all attacks At GAME WON a green LED illuminates on the control panel, and a coarse beating or throbbing sound is generated

witch selection, for LED Display Matrix line and line length selection and to determine the audio fevels of the ATEACK sounds. The ATEACK, FIRE, WIN and LOSE

The ATTACK, FIRE, WAN and LUSE sound signals are all fed to a simple two-transistor audio amplifier which drives a 40 ohm output speaker.



FLECTRONICS TODAY INTERNATIONAL - APRIL 1979

HOW IT WORKS

RANDOM DELAY and 'FIRE' SOUND GENERATOR, plus 'FIRE' RATE SELECTOR and FIRE SWITCH MULTI PLEXER

THIS IS probably the most cumplex 'block' in the entire game, because most of its individual sections are interdependent. Fig. 2 shows the stircuit diagram of this major 'block'

THE FIRE' SOUND GENERATOR

Lets deal there with the "ERE" SOUND (GENERATOR IC2 is one half of a 4022 dual 4-channel multiplexer. This connects a selected one of its four inputs to its output, depending on the 'a — b' brany code signal hand is fod to its select (prins 9 and 10) terminals. Thus, when the appropresends a logic 1 signal appears at output pres-3 of the multiplexer. This signal is passed to the signal unput of the INHIBIT April 50 the INC 51 and IC2/46.

The passes signals only when its GATE input is at logic-0, pin-1 is the "O" terminal of this particular gate, and is ited to groffid via RS but can be driven high by the outputs of the LOSE and OUT OF AMMOdetectors. The gate this passes on the FIRE switch signals only when the game is not lost and the ammunition store is not exhausted.

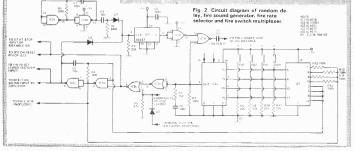
Thus, the complete action of the "FIRE". Sound generator is such that a sound is produced only when the 'correct' FIRE switch is pressed, and only when the game is not lost or the ammunition exhausted. The frequency of the sound is propertional to the total number of FIRE which is presed and varies from about 800 Bits for one switch, to about 320 Hz for four switches.

The sint-doutput of the TERF oscillators is low in the normal quiexeent state, and static amplifier for sound effects, and also to the limputs of the ammunition register and the Random Delay generator. An and the Random Delay generator, an and the Random Delay generator. An and the Random Delay generator, and taken from the pin 3 output of the oscillator and is fed to the With LOGIC circuitry. Note that the gate input signal of the oscillators has be fed to the STOP side of the bistable and to the RESET pin of the display matrix driver, so that IC12 is reset each time the correct FIRE switch is pressed.

THE RANDOM DELAY GENERATOR

The heart of the random delay generation to the K-andom delay generation is C4, a 4017 decade counter with the indecoded outputs (numbered 0 co 9) it the 9° output of the counter is coupled to the START side of the britsble via a normally-ON inhibit gate. The clock input to the counter is derived from a slow (shout 2 Hz) occiliator (IC571 and IC572) and from the TRE oscillator output via an OR gate formed by D1-D2 and R3.

Whenever the correct FIRE button is presedduring an tatick a logical - lignal is fed to the 'G' (ton 13) terminal of the mathematical and blocks in the terminal of the standard blocks Simultaneously, fast clock signals are fed switch is released and the inhibit gate mitures to the CNs state the counter is an enviro the counter of the state of the sound generator. Consequently, when the FIRE switch is released and the inhibit gate mitures to the CNs state the counter is provides the START signal to the bistble). Clock signals are then fed to the counter from the slow ordificate only unit, after a delay that is findingly varicounter reaches the START signal and feed to a CART command to the bistable.



HOW IT WORKS

THE BISTABLE, CLOCK GENERATOR, ATTACK SOUND MULTIELEXER. AND 'GAME LOST' INDICATORS IFE BISTABLE is a simple RS type IFE BISTABLE is a simple RS type ICS/21 is START' input is derived from the random deby generator via C4, and STOP' inputs are obtained from the "FIRE logic outgo of the Stable is present the simple result of the Stable gate. The pin-1 output of the Stable is "START' mode, and is fold on emploit of the ICM2 NG bats with prevides the matrix counter-driven. The other rapus of the NOR gate is, Schalmed from the

variable-speed CLOCK GENERATOR (ICI0/1 and ICI0/2) or from the WIN DETECTOR circuitry via the D4-D5-R28 diode OR gate

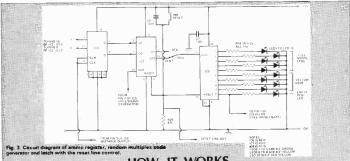
ducide UK gate multiple of the NOR gate is multiple to the start is control to focked linew so it is strable to pass clock signals. When a START signal is do to the bissable from the random delay generation of the gate is do to the gate is the to the signals. The gate is the to the histole from the "REE" logic circuitry. Note that the gate gets locked into the of fastal is doing? Josef 1000 to the gate is a longic lignal is fed to the gate is a longic lignal is det to the gate is a longic lignal is det to the gate is a longic lignal function.

signal is fed to the 'STOP' side of the bistable via D6,

The ICI0/I and ICI0/2 clock generator determines the speed of any attack, and lock signal appearing at the pin-1 output of the ICI0/3 NOR gate provides the amplitude of this sound is determined by multiplexer IC27 and resistors R31 and R32. Altacks from the aft quadrate are at east are attacked of union of the sound is determined or a strendocd volume, and those from the forward quadrata are silent.

The 'GAME LOST' indicators use four NAND and one NOR gates: their basic input signals are obtained from pin-1 (CI2, which is normally low but goes h under the game lost condition (CO/1 wired as a simple inverter, and drives the

PROJECT: Ambush



HOW IT WORKS

THE AMMO REGISTER, RANDOM MULTIPLEX CODE GENERATOR AND LATCH AND RESET LINE CONTROL THIS BLOCK is relatively simple in its This pLOCK is relatively added in theory of operation. IC? is a 4040 l2-stage ripple counter, and takes its clock input from the output of the 'FIRE' sound generator. IC8 is a 4013 dual 0 flip-flop, which is wired as a dual data fatch with its clock signal taken from the output of the clock signal taken from the output of the bistable and its data taken from the Q1(+ 2) and Q2 (+4) outputs of IC7. Thus, whenever a PHE batton is pressed and then released IC7 sets randomly deter-mined states on the data inputs of IC8 the next time that the output of the bistable goes light das an stack begins on receipt of the searble START command) these states are latched into the 4013 and are

ed on to the games multiplexers as a 2-bit binary code.

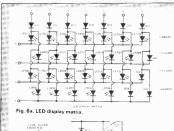
IC6 is yet another 4017 decade counter with ten decoded outputs. It has its outputs fed to a vertical line of ten LED's. which act as the ammunition register which act as the ammunition register The O'output of the 6017 goes to the top (FULL level) of the lifte, and the '9' output goes to the bottom (EMPTY level) of the ine. The '9 output also goes to the inhibit gate controlling the 'FIRE' oscillator. preventing the oscillator from working under the ammo exhausted condition. At under the 'ammo exhausted' condition. At the start of each game the counter is reset to zero, so that the line of LED's indicate the FULL state. The elock input of the counter is taken from one of the outputs of the IC7 ripple counter via SW2a. When SW2 is set for a

DEK (ien attack) game the Q7 (+ 128) output is fed to the clock input of IC6, giving a clock signal of about 6,2 Hz when a single FIRE button is operated, and thus a single FRE: button is operated, and thus cousing the register to empty in a bobut 1.5 seconds. When SW2 is set for a CENT (bundred static) parts the CU() (- 1024), the second to the set of the second state register of about 0.8 Hz from a single FRE button, and cousing the register to empty in about 1.12 seconds. Thus, to win a DEK game the veryage FRE duration must be limited below 130 mS in sech attacks, and in the CENT game it must be limited below 112 mS

The games main reset line is activated autoinstically at switch on vie C8 The Ine can be operated manually at any time via RESET button PB5.

wired as a low-speed gated astable, which drives a red 'GAME LOST' LED, Both red LED at the centre of the games main splay matrix. This LED is normally on. NUM NIN 13 OF IC1 stables are normally off, with their out-puts low. Under the 'GAME LOST' con-dition both astables operate, the LOSE' sound is generaled and the 'LOSE' but goes off when the same is inst. IC11/1 and IC11 2 are wired as a medium-speed gated astable, which pro-vides the 'GAME LOST' sound output via D9 and R34, and ICIE/3 and ICIE/4 are TED flashes on and off. H 100.81 100.81 01.22 01.22 01.22 STOP INPUT FROM PIN 3 OF OF ICE MULTIPLEX CODI, LATCH I AD12 HEDI Fig. 4 (left) Display matrix counter/ driver, target LED and 'LOSE' indica-NORMALLY-ON TARGET LED AT LENTRE OF DIAPLA'S MATRIX tor 5 (right) Bistable, clock gen. Fig 5 (rig sound multiplexer and GAME LOST' indicators.

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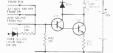


Fig 6c Audio amplifier

LED DISPLAY MARTIX DRIVERS, MULTIPLEXERS, AND LOGIC, PLUS AUDIO AMPLIFIER AND POWER SUP-PLY CONNECTIONS

THE MAIN PART of the LED display matrix is made up of four inner of LED's, arranged in the form of a cross. The upper (forward) line is far to LED's interful the other two lines are each site LED's long. The individual LED's interful issues with the other two lines are each to conster with the other two lines are each to conster with the other two lines are each to the the start diodes DD is to DD's are used to eliminate sneek paths in the materx and ensure that only a single selected LED

HOW IT WORKS

turns on at any one time. Figure 6b shows the positions of the LED's in the actual display. Note that LED 11, at the centre of the display, is normally on and represents the players own vessel.

Prior to the start of acch article (C1 to in the RESET state, so all (EDS) in the matrix (except IED, 11) are off. As soon as in attackstarts, Jold V selects after of length in i'm the display matrix, and (EDV connects the or i output of (C1 and one pin-13 clock disable terminal the selected line turn on sequenciable and nun towards the centre of the cross if a RESET signal field to pin-15 of IC12 from the "FIRE" logic arcuity before the "n+1".

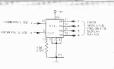


Fig. 6b. Line selection



Fig. 6d. Panel LED display.

the attack is not defeated, pin-13 of IC12 is driven high as the counter reaches the n+1' state, and all further clock signals are inhibited and all GAME LOST indicators are activated.

All sound effects signals that are generated in the game use digital in form, and are fed via gate diodes and amplitude-fetermining resistors to the simple Q1-Q2 audio amplifier stage, which is unbiased. The amplifer directly drives a 408 speaker, which has transfernt limiting provided by D14

The game is powered by a 12 V battery supply, and typically consumes 50 mA to 150 mA of current, depending on the state of play, Readers can. If they wish, power the game via a simple mains adaptor.

BUYLINES

The case we used for the Ambush project is available from Boss Industries. Full details next month. Since panel layout is not critical, inventive ETI readers may be able to come up with their own hardware designs. All the ICs are common types, available from most component mail order firms.

If you think you are likely to spend every waking hour zapping the starfleet, it's worthwhile investing in a mains adaptor, available from your local Tranny shop.

	PAR	TS LIST	*****
81	6M8	SEMICONDUCTOR	P.11.11.11.11.1
R2	390k	HC1	4016
R3, 8, 9, 10 11, 31, 40, 48	22k	IC2, 13	4052
84	10M	HC3, 9 10	4001
R5, 26, 28, 29, 30, 39	47k	IC4. 6 12	4017
R6. 16-25, 36, 37, 47	1.k	IC5. 17. 11	4011
R7, 12, 13, 14, 15	1.00k	1C7	4040
R27	330k	IC8 16	4026
R32	648	IC14, 15	4013
R33	680k		
R34, 41, 42, 46	10k	NOTE All CMOS devices are B	Series
R35	2M2	Q1	8C109
R38	2708	02	BFY50
R43	33R	D14	1N4001
R44, 45	1M5	All other diodes are	1N4148
849-62	470R	LED 1-37 are standard 0.2in dia	a.
	11111	LED 7 segment displays at cathode 0 3 in	e common
POTENTIOMETER			
RV1	1MD	MISCELLANEOUS	1.000
A REAL PROPERTY AND A REAL PROPERTY.		LS1 2in 40R	and the second second
and the property of the second second		5 off SPST push buttons	
CAPACITORS		1 off SPST latching push button	
	100n	1 off DPDT min, toggle 8 off HP11	A 10 10 1 10 10
C1. 5, 6, 7, 8, 11 14, 15	100		
C2, 3, 4, 10, 12, 13 C9		2 off 4 section battery holders	
	150#	case to suit	

HOW IT WORKS

TO BISTABLE INPLI VIA DE

THE ATTACK COUNTER AND GAME won detector and indicators

THE 11 OUTPUT of IC12 (the display matrix drive) benefy goes high at the start of each attack. This 1' signal provides the clock signal to the IC14/IC15 ATTACK COUNTER. These two IC5 are 4026 decade counters with decoded ourputs suitable for directly diving common cathed 7 -segment I-ED displays at tow power levels. The two counters are caseded: the size 90 to 39 Indications:

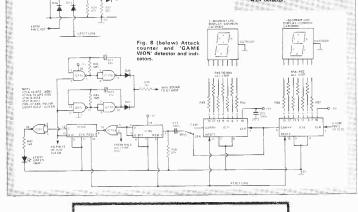
IN THE LOT OF ICTA ATTACK COUNTER leading zero suppression is not used in the counter.

The CAME WOY detector if designed around ICIs 4403 dual billip-fop, and ICI04, a NOR gate. ICI0 1 is connected as bitshed billioner stage, and is clocked via one er other of the strate country output is momently high, bits writeshes low at the stars of the 10th stratek in a DEK same or the IODA strake in a DEK. The Courtput is fed to one of the angults of the ICI074 NOB gate, which has is other

Fig. 7 (left) Circuit diagram of display drivers, multiplexers and logic with audio and power connections input provided from the normally-high output of the ICS 'FIRE' sound generator. The output of the NOR gate is fed to the SET (g_{1n} S) terminal of IC16/2, which is wired as an R-S dip-flop. Both bistables are reset at the start of each game

When is an N-S lip-hou, Both batables are repeat life bata of each given as ME ware of the star of each given as ME ware of the star of each given as the star of each given as the star of the star

The "WIN" cound gueration is designed around ICU. and consists of two wirtually identical medium-frequency gated asiable multivators which are operated in parallel and have their outputs led to the sudia ampliture via site 266.2027.846 diode OR gate. Because of inevitable sudia transfer via site 266.2027.846 diode OR gate. Because of inevitable dipt of different frequencies, and produce a coarse "Desting" or Throbbing sound when they are activated by the WIN' detector.



Next month we conclude the project with full constructional details and component overlays. In addition we'll show you the act of inspired heroism which led to the saving of the starship Eatyeigh and the designing of this project I for those who to get started the Parts List and circuit diagrams given here are complete.

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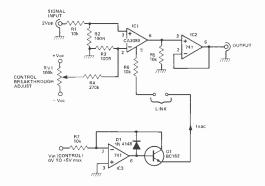


3080 CIRCUITS

The 3080 is not a run of the mill op amp. These ten circuits from Tim Orr show you why.

The CA3080 is known as an operational transconductance amplifier, (OTA) This is a type of op amp, the gain of which can be varied by use of a control current, (IABC) The device has a differential input a control input known

as the Amplifier bias input and a current output. It differs in many respects from conventional op amps and it is these differences that can be used to realize many useful circuit blocks

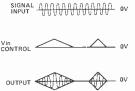


+Vcc I ABC 5 CA3080 4 Vcc

Voltage Controlled Amplifier

The CA3080 can be used as a gain controlling device. The input signal is attenuated by R1, R2 such that a 20 mVpp signal is ed to the input terminals if this voltage is much larger, then apple significant distortion will occur at the output. In fact, this distortion is put to good use in the triangle-to-sinewave converter. The gain of the circuit is controlled by the magnitude of the current lass. This current flows into the CA3080 at pin 5, which is held at one diode voltage drop above the -Vcc rail. If you connect pin 5 to 0 V, then this diode will get zapped, (and so will the IC)! The maximum value of IABC permitted is 1 mA and the device is 'linear' over 4 decades of this current. That is, the gain of the CA3080 is 'linearly' proportional to the magnitude of the IABC current over a range of 0.1uA to 1 mA. Thus, by controlling IABC, we can control the signal level at the output. The output is a current output which has to be 'dumped' into a resistive load (R5) to produce a voltage output. The output impedance seen at IC1 pin 6 is 10k (R5), but this is 'unloaded' by the voltage follower (IC2) to produce a low output impedance. The circuit around IC3 is a precision voltage-to-current converter and this can be used to generate IA®C. When Vin (control) is positive, it linearly controls the gain of the circuit When it is negative, IABC is zero and so the gain is zero.

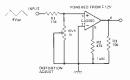
This type of circuit is known by several names. It is a voltage controlled amplifier, (VCA), or an amplitude modulator, or a two



quadrant multiplier

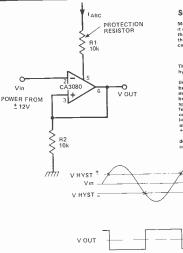
One problem that occurs with the CA3080 is that of the 'input offset voltage'. This is a small voltage offset between its input terminals. When there is no signal input and the control input is varied a voltage similar to the control input will appear at the output. By adjusting RV1 it is possible to null out most of this control breakthrough

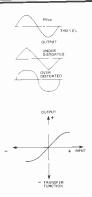
FEATURE



Triangle To Sinewave Converter

By overhoading the input of a CA3080 tris possible to produce a "smaxbdid" transfer function. That is, if a transfer water of the correct magnitude is applied to the CA3080 input, the output will be distorted in such a way as to produce a sinewave approximation. In the circuit shown, RVI is adjusted so that the output waterform resembles a sinewave. I tested this circuit using an automatic distorted water water the site of which, for such a simple arrangement, seems very reasonable indeed. This could be used to produce a sinewave output from a triangle signare wave oscillator.





Schmitt Trigger

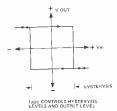
Most Schmitt trigger circuits prove to be very complicated when it comes to calculating the hysterysis levels. However, by using the CA 3080 these calculations are rendered trivial plus there is the added bonus of fast operation. The hysterysis levels are calculated from the simple equation.

VHYST = + (IABC × R2)

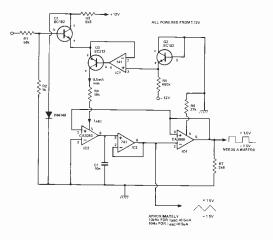
The output squarewave level is in fact equal in magnitude to the hysterysis levels. The circuit operation is as follows. Imagine the output voltage is high. The output voltage will

Imagine the output voltage is high. The output voltage will then be equal to $\{R_2 \times | x_i < v|$ which we will call $V \vee v = T_i$. If V becomes more positive than + V + v + T_i he output will write move an angeous the direction which will interface a straight between the input terminals with which will interface accelerate the speed of the output onormable for the schmit trigger accion. The output on angeous the schmit trigger accion. The output can page in the angeous voltage equal to - (R2 × lact) which is designated as - V + v = T_i v + v = T_i + v + T_i + v = T_i + v + T_i + T_i + v + T_i + T

The Schmitt trigger is a very useful building block for detecting two descrete voltage levels and finds many uses in circuit designs.



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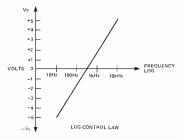
Voltage Controlled Oscillator

By using two CA3080° and some op amps it is possible to make an oscillator, the frequency of which is voltage controllable. This usit finds many applications in the field of electronic music production and test equipment. The circuit holes. This makes logarithmic control law, that is, the frequency of operation of the second second second second second second second is an electronic second second second second second indeal for musical applicable is the control logarithined to be converted into musical intravist (which are logarithneed to be converted into musical intravist (which are logarithically spaced) and also for audio testing where frequencies are generally measured as logarithmic functions. IC2 as in integrator The lake current that drives this IC is used

IC2 is an integrator The lack current that drives this IC is used to either charge or discharge C1. This produces triangular waveforms which are buffered by IC3, which then drives the Schmitt trigger IC4. The hysterysis levels for this device are fixed at ~ 1.5V, being determined by R6, R7.

The output of the schmitt is fed back in such a way as to control the direction of motion of the integrator's output. I the Schmitt output is high, then the integrator will ramp upwards and When the integrators output, it makes the program upwards level, the Schmitt will flip into its low tates, and the integrator will start to ramp downwards. When it craches the low hysterysis level the Schmitt will flip hack into its high starts. Thus levels, the Schmitt will flip into its low tates, and the integrator will start to ramp downwards. When it craches the low hysterysis level the Schmitt will flip back into its high starts. Thus levels, the generated at which it does this, and hance the oscillatory frequency is determined by the value of latc into IC2. The larger the current, the faster the capacitor is charged and discharged and a squareward (undurf thin high way buffered) from IC3 and a squareward (undurf thin high way buffered) from IC3 and a squareward (undurf thin high way buffered) in the start and a squareward (undurf thin high way buffered) from IC3 and a squareward (undurf thin high way buffered) from IC3 and a squareward (undurf thin high way buffered) from IC3 and a squareward (undurf thin high way will change.

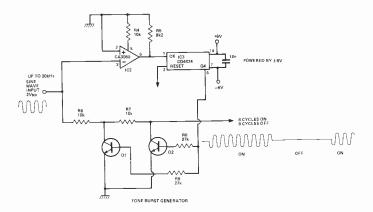
The log law generator is composed of Q1, 2, 3 and IC1. Transistors Q1 and Q2 should be matched so that ther base emitter voltages (Vba) are the same for the same emitter current, IS04, M1 and C1, should be vices to writin 5 mV is matching transitors taken and roduce areas of the same matching transitors taken and produce areas. Integrating the thet mum paid produce areas. Mout -00% which is connected to IC1 pin 3. This op map and sout -00% which is connected to IC1 pin 3.



Q3 is used to keep Q1 emitter at this same voltage of -Q46. The input control voltage as attenuated by R1, R2 such that a + 1 Vincrease at the input produces a change of only + 18 mV at the base of Q1. However, the emitter of Q1 is traced at -Q46, so the current through Q1 doubles. (It is a property of transistors Webe). When

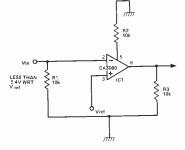
The emitter current of Q1 flows through Q3 and into IC2 thus controlling the oscillator frequency. It is possible to get a control range of over 1000 to 1 using this circuit. With the values shown, operation from 10 Hz to 10 kHz is achieved. Reducing C1 to 1 n will increase the maximum frequency to 100 kHz.

although the waveform quality may be somewhat degraded Changing C1 to 1uf (non-polarized) will give a minimum frequency of 0.1 Hz.



Fast Comparator

The high elew rate of the CA3080 makes it an excellent fast voltage comparator. When pin J. C1 is more positive than Verthe output C1 cases negative and vice versa. Verl can be the output C1 cases negative and vice versa. Verl can be an be varied As long as the input snewave levels quite large(1 V say) then the output can be made to move at very fast rates indeed. However, care must be taken to avoid overloading the inputs. If the differential input voltage exceeded 5 V. then the inputs tage breaks down and may cause an undesred output to occur.

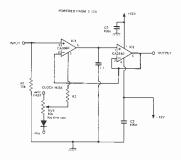






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Slew Limiter

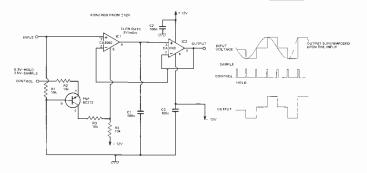
The current output of a CA3080 can be used to produce a controlled slew limiter. By connecting the output current to a capacitor, the output voltage cannot move faster than a rate given by

slew rate = $\frac{IABC}{C1}$ Volts per sec.

Note that IABC determines the slew rate and as IABC is a variable then so is the slew rate. The output voltage is buffered by a voltage follower, IC2. This is a MOSFET op amp which has a very high input impedance, which is necessary to minimise the loading on C1.

When an input signal is applied to IC1 the output tries to move towards this voltage but its speed is limited by the slew rate. Thus the output produces a linear ramp which stops when it reaches the input signal level.

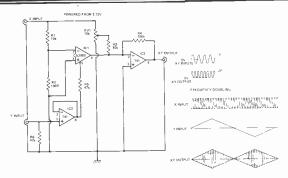
R2	C1	FASTEST SLEW RATE
150k	100n	1.5V/mSec
150k	10n	15V/mSec
150k	1u0	0.15V/mSec
1M5	1u0	15V/Sec



Sample And Hold

The slew limiter can be modified so that it becomes a sample and hold unit. In this circuit laac is either hard ON (sample) or completely OFF (hold). In the sample mode, the output voltage quickly adjusts itself so that it equals the input voltage. This

enables a short sample period to be used. In the HOLD mode, lasc is zero and so the voltage on C1 should remain fixed. The circuit is in fact an analogue memory. It is used in music synthesisers (to remember the pitch), in analogue to digital converters and many other circuits.

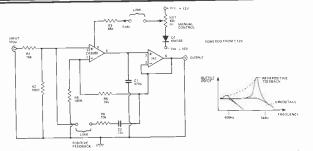


RING MODULATION

4 Quadrant Multiplier

The 6.A3080 is a two quadrant multiplier but, with the addition of a few orta bios of electronics, it can be made into a four quadrant circuit. A two quadrant multiplier has two inputs, one ecan accept bioplar signals, (the inverting or non inverting input) and one can only accept a unipolar signal, (the late current). Avever, a four quadrant multiplier can accept bioplar signals on both of its inputs which enables it to perform frequency doublance and industance.

The circuit is fairly similar to that of the two quadrant multiplier described earlier except for two differences. IC3 is used to generate lasc in such a way that the Y input can go both positive and negative, thus the Y input is bipolar, when Y is at 0 V and there is a signal on the X input the desired output (X × Y) and the zero. This is achieved by signisting RVI so that the signal to a IC1 (this is inverted) is exactly cancelled outby that via 8.3. Now, when Y is increased positively, a non-inverted value of X is produced at the output and, when Y is increased negatively, an inverted value of X is produced. When Y is zero, so is the output. This is known sometimes as ring modulation, if a speech signal is connected to the X input numd shut fat frequency oscillator to the Y input thecreal to both the X and Y disk. Also yrodicit is a survay of the to the K and Y This is known as a frequency doubler, but it will only work with spreweves.

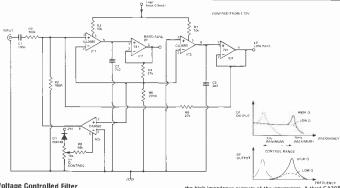


Single Pole Filter

A singlepole towpass filter can be constructed using a CA3000 as a constructed restored for the filter is in fact, just a simple constructed out of IC1, R4, R5 Varying lasc changes the amount of current drive to C1. This would normally make the crucit a slew limiter, but bocause the signal level that IC1 (pins 2 and 3) handles is so small, the CA3080 works in its linear mode. This enables it to look like a variable resistor When this reserve is varied, the break 'frequency of the filter also varies. By applying some positive feedback around the filter (R6, C2) it is possible to produce a pasky filter response The peak actuably increases with frequency making the circuit useful as a gurar Wah Wah unit

- 5

FEATURE: 3080 Circuits



Voltage Controlled Filter

A standard dual integrator filter can be constructed using a few CA3080's. By varying last the resonant frequency can be swept over a 1000 to 1 range. IC1, 3 are two current controlled integrators. IC2, 4 are voltage followers which serve to buffer the high impedance outputs of the integrators. A third CA3080 (IC5) is used to control the Q factor of the filter. Q factors as high as 50 can be obtained. The resonant frequency of the filter is linearly proportional to IASC and hence this unit is very useful in electronic music production. There are two outputs produced, a low pass and a band pass response ETI

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microfile.

Gary Evans looks at PLT add-ons, a Simon that's not simple and has news on superboard II.

WITH THE PLETHOPA of new small computer systems appearing on the market its succ to see some of the old wathorses beginning to meet this analogit by supporting the user with a broad base of hardware. Surely one of the oldest warnors usel flow or yn prosts this amonthand one which has to date been poorly supported by its manufacturer is the PET.

A number of companies have stepped into the void cused by lack of commodore peripherals, evolution from RS232 interfaces to PET compatible floppy drives are available burnon from Commodore. The facet incrue if the PET User's Club revoletter indicates that mis sit, at on s about to change.

The most exciting of the PET add ons from Commedioris their 2040 Dual Drive Floppy Disk. Details are sketchy at present but III outline the spec of the 2040 as presented in the newsletter.

The drive well allow 360K bytes of data to be stored on two standards 5 kin Dak drives (5hugar 5k390). This is accomplished without resorting to double tracking or double tracking to double dracking the same standard by the use of two MPUs = 6504 and 6502 and frien memory (Cs within the 2040).

Formatting is by the drive itself and any mini floppy disk, may be used. 35 tracks with a constant density recording on each track provide 17152D bytes for user storage per disk side.

The 2040 requires only one connection to the PET an interface cord connecting the unit to PET's IEEE port

Just what we ve been waitingfor — but you li have to wait until May and part with £799.20 for the pleasure of fitting this box of mcks next to yo in PE". Good news that we don't have to wait for is a proce reduction in the PET metel ?001% The BK machine that unul now has been the only PET computer is down in proce to E594.00 The BK machine is 'c' 1e jo, ed' by a 4K machine at

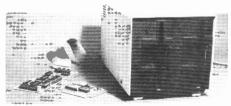
The BK machine size (is point by a 4K machine at 0450 d0 and two mides is fauture). This was a 0450 d0 and two mides is fauture. To the state of th

The 4K PET is due in February - h & h- arcen lers ons will be here in May

The task addition to Commodore's hadvare is the 2023 printer. This will replace the it fates LC20 printer announced but not seen — and has to quote a significantly better quality and minimum. The nin thead The 2023 replacement and the second s

Well there we are then a range of user speeced PET perspherals. Let a hope that Commodore manage to meet the promised delivery detes as in the past in a sinkle area in which Commodore have been distinctly acking in performence.

If you can a want for Commodere a floppy disk unit, this product from Computtionk is available now and plugs into a PET that has been fitted with a minimum of 15K additional memory.



ELECTRON US TODAY INTERNATIONAL - APRIL 1975

NEWS

Toying With MPUs

At last the MPU has found its way into the toy market Christmas saw a number of electronic games Invecta's Mastermind being one of the most popular and the new year is seeing many more games added to the shop's shelves.

The current rage in America is a game called Simon Presented with four buttons of different colours, the player has to remember the sequence in which the machine calls, them The sequence stars off with just two colours but rapidly executs this with the player must prese the four buttons in a sequence that as it extends will eventually defeat the user.

Not very easy to explain, but its all the rage in the US and will be over here soon — you'll be able to see it for yourself then

Super Ohio

I am assured that the long awaited Ohio Scientific's Superboard II will be available "off the shell" within the next 45 days Needless to say 1 am trying very hard to get hold of one of these boards and will report on its performance soon

Back numbers

Not all back issues of ETt are evailable. Indeed more are not than are! The table below shows which copies can be obtained from our offices. Each copy costs 60p inc p&p and please mark your envelopes "Back issues".

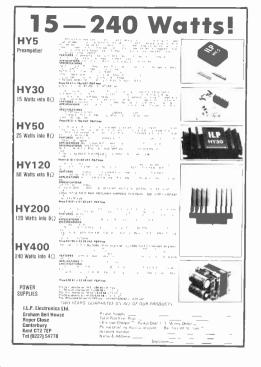
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ELECTRONICS TODAY INTERNALIONAL - APRIL 1979





WHAT'S IN THE APRIL ISSUE



COMPUTER SURVEY

The number of small systems on the market has increased greatly over the past year and the choice of a machine to suit your application.

The April issue of Computing Today surveys some of the more popular small computers and presents in a clear, concise, fashian the capabilities and facilities offered by the different products.



CONSUMER SHOW

The recant Winter Consumer Electronic Show in Las Vegas saw the introduction of many new MPU based products including a chess challenger that talks.

Gerald Chevin was there for Computing Today and his report appears in the April issue.

ELECTRONICS TODAY INTERNATIONAL - APRIL 1979

NASCOM ADD ONS

offering.

The NASCOM 1 computer has been one of the most successful of the DY computer kts on the market recently NASCOM introduced a number of extras that allow the basic machine's potential to be considerably enhanced. We take a look at the expansion board and RAM card as well as her TNN RASIC Nascom are now

EXPANDA PET

The commodore PET has been with us for over a year now but peripherals for the computer have been slow to appear. One of the essential devices in many applications is a floppy disk to provide a system of mass storage that is faster in operation than the tape system of the standed machine.

Next month we review the Compu/Think disk drive and diskmon operating system that will plug straight into your PET.



AMBUSH GAME

The April issue of our sister magazine, ETI, carries a project called Ambush. Ambush is an exciting space war game Computing Today will carry a program that will allow those of you who don't dabble in electronics to play Ambush on your computer



Plus all the regular features, news, softspot, hardlines and next month, a new regular letters page.

83

APRIL FOOL

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FLECTRONICS TODAY INTERNATIONAL - APRIL 1979

WIND METER

Here is the project all you amateur meteorologists have been waiting for. When this meter gets the wind up you'll know how fast and where it's coming from.

TRADITIONALLY THE FOUR primary elements are fire earth. water and air At ETI we ve designed projects concerned with the first three (temperature meters, soil moisture indicators rain alarms) but not much for the last. The major property of the air apart from the fact that it is necessary to support life is the movement of the air wind Light winds generally aren't of terribly much significance except to meteorologists but stronger winds can be useful as a source of power for traditional milling for electricity generation or as a means of propulsion for sailing yachts Stronger winds such as hurricanes, can be destructive, causing damage to life or property.

So for all the private plots yeachismen a mateur meteorologists and general weather watchers who read ETI here is a device which will tell you the wind is speed and direction, with a remote indication of both quantities. Our design is we'd like to think both stylish and unusual but here are simpler methods of mechanical construction which you can follow if you wish

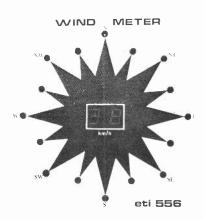
The Head

The drawings along with the photos will give the general design that we used. The actual dimensions have to be left to the individual constructor as components such as the ball races and light bulbs may vary in size.

While we used a single head for both speed and direction, it may be simpler to use separate heads

The discs we used were 1 5mm thick clear plastic with a piece of photographic film glued onto it. It may be easier to make it out of thin alumnium and cut out the slots. For the speed disc simply drilling holes will suffice.

The most important part of the design, apart from ensuring that the discs rotate with a minimum of friction, is the shielding of the light and preventing light scatter striking a



transistor which should be dark. As can be seen from the photos and diagram the bulbs and transistors are embedded in aluminium blocks with small holes providing a passage for the light beam.

The wring of the head is shown in fig 3 Note that the base lead is not used and can be cut off close to the body. Insulate the joints onto the transistors to ensure that they do not short on the aluminium blocks. The bulbs may touch the block with their outer connection but this is the 0 volt line and does no harm. In fact it provides some electrical shielding for the leads. The bulbs we used were 12V but they were bright enough on 6V giving a much longer life.

Design Features

When we started design on this project it was to have a digital readout of wind direction with a resolution of either one or two degrees. This would also make it useful in a sailing boat to tell the wind direction relative to the heading

Difficulties however soon became apparent. The first of these was the sensor head. The only accurate method is a digital head, probably optical. Two methods could have been used, one using a disc with a single optical track of 360 slots and an updown counter and the second using eight or nine tracks in a grey code. The first is simpler in head design but the second is less prone to error. The problem and the reason for rejecting both is that with such resolution, the reading would move around so much when the wind is gusty to be unreadable. What is needed is an averaging circuit which unfortunately becomes

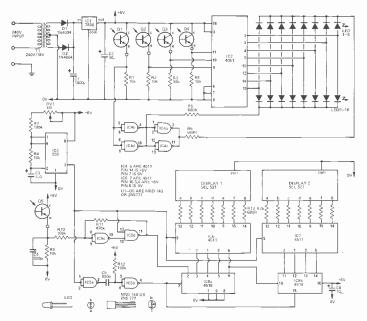


Fig. 1. Complete circuit diagram of the ETI Wind Meter

difficult when the wind is changing from just west of north to just east of north i e 355 to 005. How do you average these (use a microprocessor?)

As this was intended to be a simple project we relaxed our original sperification, deleting the use in a boat (we may get back to this problem A four track Grey' scale allows the wind to be given to within 11° of its true heading, without the complexity of a nine track one, and the use of LEDs to give direction solves the problem of averaign as the vanations can be seen and averaged by the brain

Construction

The electronics is relatively simple provided the PCB described is used Due to a height limitation C1 should be mounted on the rear of the board The LEDs should be mounted about 7mm from the board with care being taken not to damage them as the leads have to be bent out slightly The regulator also has to lie down to give clearance.

We mounted the unit behind an aluminium front panel with the LEDs protruding through holes if this is to be done it is preferable not to solder the LEDs until after alignment with the front panel

The head is more difficult as some mechanical ability is necessary to ensure good results. The requirements are basically simple. A disc is to be allowed to rotate either continuously with the wind or aligning it to the wind with a bulb on one side and phototransistors on the other.

PROJECT: Wind Meter

HOW IT WORKS

Wind Direction

Wind direction is indicated by a series of 18 equally spaced LEDs around a circle. These represent the main points on the compass. These are controlled by IC2 and IC4 which are in turn controlled by the direction sensor bead

alor make be used to be the what humbor The decoder (3.2) is an elebh output analogue demulipleare with the common line joined to the +3V line. When a partieular 3bit code as presented to its control imputs one of the eight outputs will be longed to the +6V line. The fourth output lines the sensor head controls ICU which gives two inverted, outputs in drive either herefore spectres, a particular till 2D to be wherefore spectres, a particular till 2D to be War By planing the LDb correctly around the origin the grow code a decoded.

Wind Speed

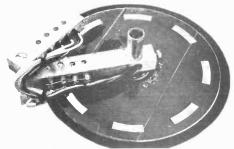
This is a simple frequency counter measuring pulses from the sensor head. The head consists of a disc with eight holes which breaks a light beam to its associated photoransistor. The output of this phototransistor is squared up by a schmitt trigger. formed by ICSe, and ICSe.

The bounding is force by LCSs and LCBb (a divid decade counter) with LCS and LC2 providing the store and LED drivers necessary to drive the seven segment display. Time base is provided by LC3 which gives J 2 mS wide negative pulse about every one second We say about as it is adjustable by RV1 as individual heads will have different responses and calibration will be secessary.

This negative pulse opens the store to allow the number reached by the counters to be displayed while simultaneously storping any further counting by disabling the schmitt trigger. On the comoletion of the mSp pulse LOSa, and LOSb generate a Shuwide pulse which resets the counter LOS to recommence the sequence.

Power Supply

This is simply a full wave rectified supply with IC1 giving a regulated $\pm 5V$ output. This regulation is acceded to ensure that the time base (IC3) remains accurate.



Above and Below Constructional details of the sensor head



The finished unit in use

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TO TRANSFORMER

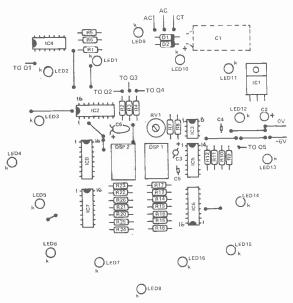
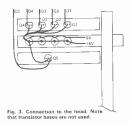


Fig. 2. Component overlay for the Wind Meter

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ELECTRONICS TODAY INTERNATIONAL - APRIL 1979

Discs used in the sensor head - 1.5 mm thick, clear plastic with photographic film alued on

PROJECT: Wind Meter

will normally have to be washed out to give low enough friction with a light spray of WD40 or similar to give some protection

While our housing is a little ornate, it did work but the more usual half ping pong balls may be more suitable.

Calibration

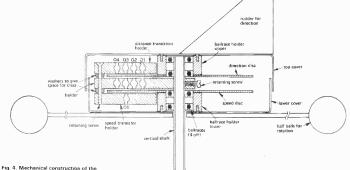
Wind Speed

The easiest method for wind speed calibration is to provide the unit with a DC supply (via the common and one of the AC inputs) and to take a drive in the car with the unit supported above the vehicle Providing there is no wind the potentiometer should be adjusted until the reading corresponds to the sneedo

Direction alignment is simply a matter of aligning the vertical rod so that it gives the correct results. BUYLINES

ED

The metalwork for this project we must leave to our readers, as this will be fabricated to suit individual requirements The displays can be any type no's really, just observe polarity Similarly with the LEDs. The inptodarlingtons can be supplied by Marshalls



sensor head

ELECTRONICS TODAY INTERNATIONAL - APRIL 1979

What to look for in the May issue: On sale April 6th

START YOUR OWN ARMOURED DIVISION

Ever lancied driving your own tank across the battlefields? Or taking a Porsche around a meetrack at 40mph? Well, much as we'd like to build a Cheman as a project it would never go into one issue and so we offer you a fully proportional six channel radio control system for models instead.

The design offers joystek control (or switched position) and special attention has been paid to metalwork and setting up procedure. A kit of metalwork (and ready wound colls) will be available, and alignment requires nought but a simple voltmeter.

We're confident this system will be the standard by which others are judged!

How It Works - AM/FM

The second in our occasional series by Gordon King. This time he turns his attention to radio, and goes in and out of the ins and outs in great, easily explained, detail. Masses of encuits to illustrate the points, and a must for anyone remotely interested in the field.



Its goodbye to the faithful IF strip as we know it. SAW will soon to found in TV receivers, replacing the usual array of colls and capacitors. You can expect to see and hears and more about them in the future, be one jump ahead and read the expose in wort month's ETL P.S. SAW-Surface Aroustic Ware!



OK, so you've seen them before. Ours have a novel method of display decoding, switchable odds to allow adaptation for wargaming, etc. Single board construction makes life easier and overall we think its a nice one!

See what you think next month.

W BLC



HEADPHONE AMPLIFIER

A project to warm the ears and please the rest of the universe. Based on a high quality Class A design, this unit provides hield drive for one or more pairs of dynamic headphones, allowing you to wallow within an undisturbed sound field, and leaves everyone around free to do their own thing without having to listen to yours.



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EXP 350 specifically designed for the C's Wtr 270 content prints including two 20 pc nº bus bars the EXP 350 accepts a visize DIF with 0.3 spacing

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EXPERIMENTOR 300 S7 29

The honorways deal Breadboard accepts 6 x 14 DIP or 5 x 16 DIP has 550 con ac*

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a project requires up to 5 x 14 DIP chos and needs up to six bus bars. Which to buy? Easy from the table below select an

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GUITAR EFFECTS UNIT

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6	Add S	2.8.8.2 a 10.0
	5- struzz	CONTRACTOR OF STREET, ST
guitar af	Гаста шла	22.4.2

Our guitar effects unit isn't just a fuzz box. Use it to give you a new sound to play with.

LIKE US, YOU probably thought that one guitar effects unit was much the same as any other. After fuzz and Wen-Wah, what do you do? Well, we think we have come up with a new one, which we have christened **struzz**.

With this unit you can select either a conventional fuzz effect or our new struzz effect. A depth control allows you to alter the sustain rate of the effect. If the neighbours start banging the voll, you can instantly cut out the crunchy effects with a bypass switch.

Make-up

Construction should not pose any problems. It is even easier if you use our PCB Make sure the electrolytic capacitors are put in the correct way round. As always don't plug in the ICs until you have checked the circuit thoroughly.

Happy fuzzing and struzzing

BUYLINES

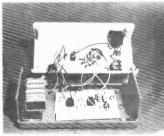
The only component that may be difficult to find is the LF356 FET op-amp. Watford Electronics can supply this IC.

turned water of the otherstal



Now you are wondering what struzt sounds hive, aren' you Well it is a distortion of fuzz. The lundamental frequency of the input is full wave rectired but the numercus harmonics an enot. The easil sounds rather like an entorus brand sounds rather like an entorus brand sounds rather like an entorus brand sounds rather like an entorus load wordewen, and colleagues of woodewen, and colleagues of woodewen, and colleagues of woodewen, and fixed on tyou will, no doubt, find many more musical uses for this effect than we exaid

Switching between fuzz and struzz while playing produces an interesting sound. You might like to use a footswitch for this purpose





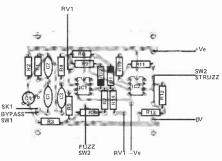
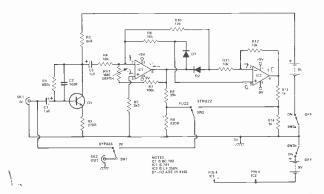


Fig.1. (above) PCB component overlay

(Above right) Completed PCB

Fig.2. (Below) Circuit diagram



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PROIECT:Struzz



HOW IT WORKS

THE SIGNAL from the gustar pick up is ice to common-contex amplifier QI via blocking capacitor Cl, QI has a voltage gains of about reversty-five, and brings the gains of about reventy-reve, and brings the guider signal up to a reasonable level for driving the fuzz and stancz circuitzy. The upper frequency emposas of QI is real revised by C2, in the interest of circuit stability.

Operational amplifiers IC1 and IC2 are Operative a amplifiers (CI and (C) are wind together as a presented of tall wave rectifier, with its true output signal appearing a type 16 of PET operamp (C). A very heavity (toped version of the toput (C) collectors agains) appears at pin 8 of (CI), and has b reak-toping amplitude of about 12 voits. PU) stables the amali-sipal-voitage gain of CI to be varied from 16 to havita 14 (M), and controls the depth and sustain' characteristics of the

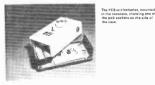
gain of unity, The fuzz curput of the stat is taken from the output of ICI wa patential

devider RS-IDE, and is a perfectly convex tional heavily clipped, haze upped, with variable depth and sostean. The strats output, on the other hand, is way unamusi and is taken from the output of IC2 struct mede the original guitar signal in tal tone (which passes drough aeto cross-over points in each crois) has its frequency doubled, but the overtrous (which randulate the fundamental and do not past through zero cross-over soints) do add have their frequencies sitered. The struzz output signal glito has amplitude distortion imported to it, due to the fullwave register action

Thus, the fuzz output signal has very bravy amplitude distortion and the truzz corput has both amplitude and frequency desortion. The sound effects unsi can be vestched in and out via bypase switch SWI, and slicoid be interposed between the guitar and the main

the case

The PCB and batteries, mounted



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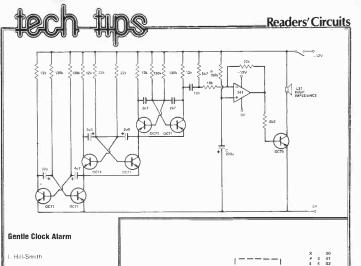
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L Hill-Smith

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There are gentler ways to wake up. This circuit provides an alarm which builds up from being inaudible to loud over about one minute. As a result you are always woken by the minimum volume required to wake you, a far more comfortable experience than the usual trauma. The three multivibrators in cascade provide a signal like the sound of a warbler telephone. As C slowly charges through R a larger fraction of the signal is amplified by the op amp producing a louder outnut

Calculator Badio Alarm

T Corringham

This very simple circuit, used with a Sinclair Cambridge Programmable calculator enables a transistor radio to be turned on after a predetermined time, (within the range of a few seconds to five months)

None of the components are critical, but the SCR should have a sufficiently high voltage and current in minutes of the required delay is put rating for the radio used

PM¹

50

(DE

SCR

1040

If a transistor radio is used the SCR is connected in series with the battery. but if a cassette recorder/player is used it can be connected to the remote socket

The LDR is placed above the left hand three digits of the display RV1 is adjusted so that the circuit is triggered by 888 being displayed, but not by the background light only.

Using the program given, the time Tach-Tips is an ideas forum and is not aimed at the beginner. We regret we cannot answer

in and /RUN/ pressed to start the timing period

01 02

06

08

12

5 5 03 04

÷ A 13

şın O 14

ŝ 8 18

8 Ř 20

rton0 22

A 8 05

È 09

3 10

0 15 9 9 16

> 8 18

To stop the program prematurely + / c CE/ is pressed.

The calculator should be used with a mains adaptor.

The timing is accurate to within five minutes in eight hours

If a buzzer or similar alarm is used the same circuit can be used to give an audible indication of the termination of long programs.

ELECTRONICS TODAY

ETI is prepared to consider circuits or ideas submiriad by readers for this page. Hed will be paid for. Drawings should be as clear as possible and the text should p a or ideas submittee and the test moving Id be as clear as possible and the test moving st to copyright. Hams for consideration shock winternational, 25-27 Oxford St., London W be p at not be



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Readers' Circuits

WORD 4

NORD 2



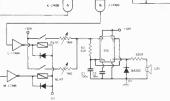


K. G. Reid

This circuit can be used in several modes: It can provide quantized feedback (a distinct improvement over the normal single bielep) from the key actions made on a calculatortype keyboard. It can be used to give a 'sound translation of a digital display when sound would be a better communcation medium.

The keyboard or display information (a maximum of 16 bits with one 16-line 74150 multiplexer) is translated into a series of 16 high or low frequency tone pulses, corresponding to the high' or low' logic state of the 16 bits

The circuit illustrated was used in conjunction with a digital multimeter, requiring three 4-bit words for the digits and three additional bits for over-range, negative and decimal point Thus 15 lines only were requred, the 16th being used for resetting.



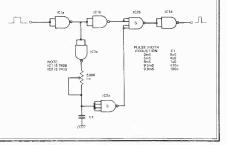
The 15 bits are latched on to the inputs of the 74150 multicever Presentation of the enable pulse results in a logic 1 appearing at the output of gate B, allowing clock pulses to pass via gates A and H to the 7493 counter. Gates B \pm D and C form a latch which remains set' until all 15 bits have been sampled A seach bit is sampled the inverse state appears at the multiplever output opening date

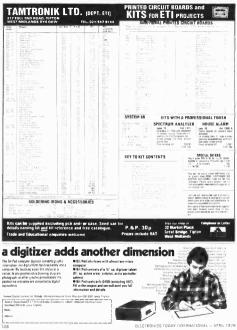
Jor K and thus operating one of the two reder relays. As a count of 1111 appears from the counter: the output of F drops low, resetting the latch and counter. The operation of either relay results in a tone appearing at the loudspeaker (or earpicec), the tone frequencies being set (1.2 kHz maximum) by the 1 megohm pots The tone pulse length is governed by the clock rate.

Digital Pulse Compressor

N C Hall

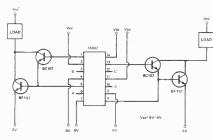
Whilst constructing a digital frequency mere the author found it necessary to be able to accurately trim the width of a gate pulse. The circuit shown uses only two ICs and can reduce the width of a pulse applied at its input by up to a few milliseconds. The table shows the reduction achieved by using different values of C1





Readers' Circuits





Darlington Drivers for a few pence

C. J. Ramey

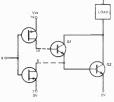
This circuit offers a very efficient way of driving a pair of transistors in Darlington configuration from CMOS. The circuit in Fig 1 shows how two loads of up to 1A may be driven from a single 14007 chip with no external resistors. Using a 2N3055 in place of the BFY51 will enable loads of up to 3A to be driven at voltages limited only by the Vceo of the transistors (Vcc)

Fig 2 shows the internal circuit of one section of the 14007. A high on

pin 6 switches the lower CMOS transistor on, holding Q2 off and sinking the leakage current of Q1. A low on pin 6 drives Q1 and switches the lower CMOS transistor off and the upper CMOS transistor on

The result is fast switch off at low cost and efficient switch on

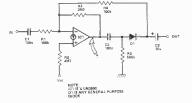
A bonus is the inverter between pins 10 and 12 Note: Vcc should be 5-6V to prevent excessive current being drawn from the CMOS chip.



Precision Rectifying with the LM3900

A. Winsor

The LM3900 is different from most op-amps in that it is current differencing and operates from a single supply rail, which mean that the inputs bias at one base-emitter voltage above ground. Hence standard techniques are not applicable as the diode would always be forward-blased. Two feedback paths are therefore provided:-R3 for DC stability and R4 for the AC signal after C2 and R5 have filtered out the DC bias. When R2 = 2 × R3 made very much smaller than R3. C1 point A will be at Vcc/2, allowing the diode to be reversed at will. For large positive input returned to ground Input impedance equals R1, and vol- those used on the prototype and may tage gain equals - R4/R1 since R4 is be altered to suit individual require-



and C3 are DC blocking capacitors and determine the low frequency rolloff. Component values quoted are

ments

This circuit has obvious potential. especially in portable equipment where the 4 amps in one package and single supply rail yield a more compact, more convenient unit

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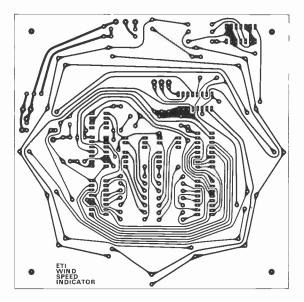


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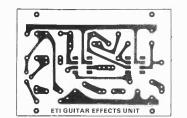
GATHERED HERE are all the PCBs for this month's projects From now on the boards will be grouped together like this in order to facilitate their use by those readers wishing to produce their own PCBs from these patterns

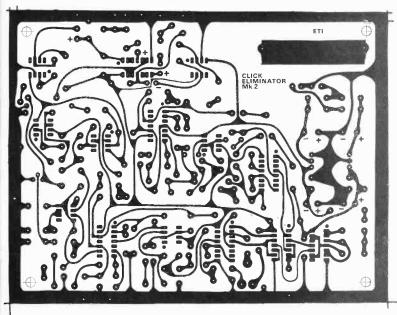
All are shown foil side up, and full size Companies wishing to produce these for sale as ready made PCBs should note that where the board carries a copyright symbol, the designer retains that copyright to himself so his company, and that particular board may *not* be produced on a commercial basis

These pages form the basis of our ETIPRINT sheers which are etch resistant transfers of the foil patients designed to simplify one-off PCB production. See the ad on page 49 for further details.



Below left: Wind Speed Indicator PCB Below right: Click Eliminator Mk 2 board Right: Struzz effacts unit All are shown full size and will form the basis of ETIPRINT sheet 023 which will be available shortly





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