

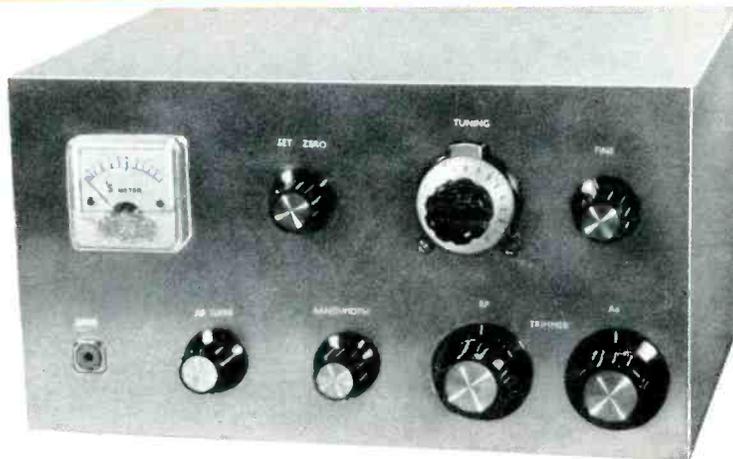
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50	50		500	6	
100	18		100	25	
125	10		100	6	
150	12		6	3	
150	25		8	6	
8	50		25	6.4	
12	20		250	18	
10	20		250	30	
8.2	20	400	16		
50	25	400	40		
2.5	64	8	500		
25	25	100	200		
		100-100	150		

CONDENSERS

MFD	Volt	
0.005	500	} 2p each
0.001	1,250	
3.3PF	500	
500 PF	500	
1000 PF	500	
2,200PF	500	
3,300PF	500	
0.1	350	
0.1	500	
0.25	150	
0.03	350	} 3p each
0.13	350	
0.056		
0.061		
0.066		
0.069		
0.075	350V	
0.08		
0.1	1,500	
0.25	350	

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Amp	Volt		Amp	Volt	
1/2	1,600	BYX10 .. 30p	2	30	LT120 .. 30p
1	140	OSH01-200 .. 30p	0.6	6-110	EC433
1.4	42	BY164 .. 35p	Encapsulated with built-in heat sink .. 15p		
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IN4004	400 volt	6p
IN4005	600 volt	7p
IN4006	800 volt	7p
IN4007	1,000 volt	8p

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	Amp	Volt	
LT102	2	30	10p
BYX38-600	2.5	600	25p
BYX38-300	2.5	300	20p
BYX38-900	2.5	900	28p
BYX38-1200	2.5	1,200	30p
BYX49-600	2.5	600	25p
BYX49-300	2.5	300	20p
BYX49-900	2.5	900	28p
BYX48-300	6	300	27p
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BYX48-900	6	900	40p
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OA47/81	4p	GET111	20p
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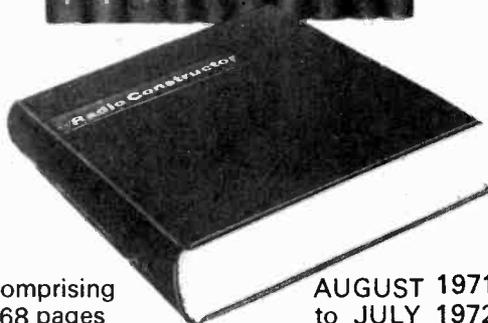
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AC 132	15p	AF 117	26p	BC 154	33p	BD 155	88p	BF 195	13p	OC 19	38p	2G 344	20p	2N 2217	24p	2N 3394	15p	2N 4289	18p
AC 134	15p	AF 118	38p	BC 157	20p	BD 175	66p	BF 196	15p	OC 20	69p	2G 345	17p	2N 2219	22p	2N 3395	18p	2N 4290	18p
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AC 176	22p	AS 28	27p	BC 179	21p	BD 199	99p	BF 277	30p	OC 72	22p	2N 524	44p	2N 2905	23p	2N 3707	12p	25 321	61p
AC 177	26p	AS 29	27p	BC 180	26p	BD 200	88p	BF 278	30p	OC 74	15p	2N 527	44p	2N 2905A	23p	2N 3708	7p	25 322	46p
AC 178	31p	AS 30	27p	BC 181	26p	BD 205	88p	BF 279	30p	OC 75	16p	2N 598	46p	2N 2906	16p	2N 3709	10p	25 322A	46p
AC 179	31p	AS 31	27p	BC 182	11p	BD 206	88p	BF 280	26p	OC 76	16p	2N 599	49p	2N 2906A	20p	2N 3710	10p	25 323	46p
AC 180	18p	AS 32	27p	BC 182L	11p	BD 206	88p	BF 281	26p	OC 77	27p	2N 599	14p	2N 2907	24p	2N 3711	10p	25 324	77p
AC 180K	22p	AS 34	27p	BC 183	11p	BD 208	88p	BF 282	26p	OC 81	16p	2N 697	16p	2N 2907A	24p	2N 3819	31p	25 325	77p
AC 181	18p	AS 35	27p	BC 183L	11p	BD 208	88p	BF 283	26p	OC 81D	16p	2N 697	16p	2N 2923	15p	2N 3820	55p	25 326	77p
AC 181K	22p	AS 36	27p	BC 184	13p	BD 210	88p	BF 284	26p	OC 82	16p	2N 699	38p	2N 2924	15p	2N 3821	38p	25 327	77p
AC 187	24p	AS 37	27p	BC 184L	13p	BD 211	88p	BF 285	18p	OC 82D	16p	2N 706	9p	2N 2925	15p	2N 3822	31p	25 701	46p
AC 187K	22p	AS 38	27p	BC 185	31p	BD 212	88p	BF 286	18p	OC 83	22p	2N 706A	10p	2N 2926(G)	14p	2N 3903	31p	40361	46p
AC 188	24p	AS 21	44p	BC 187	31p	BD 219	77p	BF 287	16p	OC 139	22p	2N 717	33p	2N 2926(O)	11p	2N 3905	33p	40362	49p
AC 188K	22p	BC 107	10p	BC 207	10p	BD 121	49p	BF 288	16p	OC 140	22p	2N 717	38p	2N 2926(O)	11p	2N 3905	33p	40362	49p
ACY 17	27p	BC 108	10p	BC 208	12p	BD 123	55p	BF 289	16p	OC 169	27p	2N 718	26p	2N 2926(O)	11p	2N 3905	33p	40362	49p
ACY 18	22p	BC 109	11p	BC 209	13p	BD 125	49p	BF 290	16p	OC 170	27p	2N 718A	55p	AA 119	9p	BY 130	17p	OA 47	7p
ACY 19	22p	BC 113	11p	BC 212L	12p	BD 127	55p	BF 291	16p	OC 171	27p	2N 726	31p	AA 120	9p	BY 133	23p	OA 70	7p
ACY 20	22p	BC 114	16p	BC 213L	12p	BD 152	60p	BF 292	16p	OC 200	27p	2N 727	31p	AA 129	9p	BY 164	55p	OA 79	7p
ACY 21	22p	BC 115	16p	BC 214	12p	BD 153	60p	BF 293	16p	OC 201	31p	2N 743	22p	AA 130	10p	BY 164	55p	OA 81	7p
ACY 22	17p	BC 116	16p	BC 225	27p	BD 154	60p	BF 294	16p	OC 202	31p	2N 744	22p	AA 130	10p	BY 164	55p	OA 81	7p
ACY 27	19p	BC 117	16p	BC 226	36p	BD 155	77p	BF 295	16p	OC 203	31p	2N 914	15p	AA 130	10p	BY 164	55p	OA 81	7p
ACY 28	21p	BC 118	11p	BC 30	26p	BD 156	53p	BF 296	16p	OC 203	31p	2N 914	15p	AA 130	10p	BY 164	55p	OA 81	7p
ACY 29	30p	BC 119	33p	BC 31	31p	BD 157	60p	BF 297	16p	OC 204	27p	2N 918	33p	BA 116	23p	BY 12	33p	OA 91	6p
ACY 29	31p	BC 120	88p	BC 32	33p	BD 158	60p	BF 298	16p	OC 205	38p	2N 929	33p	BA 126	24p	BY 12	33p	OA 95	7p
ACY 31	31p	BC 125	26p	BC 33	26p	BD 159	66p	BF 299	16p	OC 309	44p	2N 930	23p	BA 148	15p	BY 12	33p	OA 95	7p
ACY 34	23p	BC 126	20p	BC 34	27p	BD 160	44p	BF 300	20p	P 365A	47p	2N 1131	23p	BA 154	15p	BY 17	38p	OA 200	7p
ACY 35	23p	BC 132	13p	BC 37	15p	BD 162	44p	BF 301	20p	P 397	47p	2N 1132	24p	BA 155	15p	BY 18	38p	SD 10	5p
ACY 36	31p	BC 134	20p	BC 37	15p	BD 163	44p	BF 302	20p	C 111E	44p	2N 1302	15p	BA 156	14p	BY 19	31p	SD 19	7p
ACY 40	18p	BC 135	13p	BC 37	15p	BD 164	44p	BF 303	20p	OC 111E	44p	2N 1303	15p	BY 100	16p	CG 62	11p	IN 34	7p
ACY 41	19p	BC 136	16p	BC 37	15p	BD 164	44p	BF 304	20p	OC 111E	44p	2N 1304	18p	BY 101	18p	(Eq) OA 91	51p	IN 34A	7p
ACY 44	48p	BC 137	16p	BC 37	15p	BD 164	44p	BF 305	20p	OC 111E	44p	2N 1305	18p	BY 105	18p	CG 651 (Eq)	11p	IN 914	6p
AD 130	42p	BC 139	44p	BC 37	15p	BD 164	44p	BF 306	20p	OC 111E	44p	2N 1306	23p	BY 114	13p	OA 70-OA79	64p	IN 916	6p
AD 140	53p	BC 140	33p	BD 121	66p	BF 176	38p	BF 307	20p	OC 111E	44p	2N 1307	23p	BY 126	15p	OA 55L	23p	IN 418	6p
AD 142	53p	BC 141	33p	BD 123	71p	BF 177	38p	BF 308	20p	OC 111E	44p	2N 1308	25p	BY 127	16p	OA 55L	23p	IN 501	11p
AD 143	42p	BC 142	33p	BD 124	66p	BF 178	33p	BF 309	20p	OC 111E	44p	2N 1309	25p	BY 128	16p	OA 10	38p	IN 501	11p

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NEW COMPONENT PAK BARGAINS

Pack No.	Qty.	Description	Price
C 1	250	Resistors mixed values approx. count by weight	0.55
C 2	200	Capacitors mixed values approx. count by weight	0.55
C 3	50	Precision Resistors 1% mixed values	0.55
C 4	75	1/4 W Resistors mixed preferred values	0.55
C 5	5	Pieces assorted Ferrite Rods	0.55
C 6	2	Tuning Gangs, MW/LW VHF	0.55
C 7	1	Pack Wire 50 meters assorted colours	0.55
C 8	10	Reed Switches	0.55
C 9	3	Micro Switches	0.55
C 10	15	Assorted Pots & Pre-Sets	0.55
C 11	5	Jack Sockets 3—3.5m 2 — Standard Switch Types	0.55
C 12	40	Paper Condensers preferred types mixed values	0.55
C 13	20	Electrolytics Trans. types	0.55
C 14	1	Pack assorted Hardware — Nuts/Bolts, Gromets, etc.	0.55
C 15	4	Mains Switches, 2 Amp Plus	0.55
C 16	20	Assorted Tag Strips & Panels	0.55
C 17	10	Assorted Control Knobs	0.55
C 18	4	Rotary Wave Change Switches	0.55
C 19	3	Relays 6 — 24V Operating	0.55
C 20	4	Sheets Copper Laminate approx. 10" x 7"	0.55

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PIV	1A	2A	3A	4A	5A	6A	7A	8A	10A
100	0.25	0.27	0.38	0.38	0.51	0.55	0.81	0.58	1.26
200	0.27	0.38	0.51	0.51	0.55	0.81	0.89	1.51	
400	0.38	0.40	0.54	0.54	0.62	0.67	0.82	1.78	
600	0.47	0.51	0.61	0.61	0.73	0.82	1.02	1.92	
800	0.58	0.62	0.75	0.75	0.91	1.06	1.37		
1000	0.69	0.77	0.88	0.88	0.99	1.23	1.65	3.10	

SILICON RECTIFIERS TESTED

PIV	300mA	750mA	1A	1.5A	2A	10A	20A
	(D07)	(S016)	(S016)	(S016)	(S016)	(S016)	(S016)
£	0.04	0.05	0.05	0.07	0.15	0.23	0.66
100	0.04	0.06	0.05	0.14	0.17	0.25	0.82
200	0.05	0.10	0.08	0.15	0.22	0.26	1.10
400	0.06	0.14	0.07	0.22	0.30	0.0	1.37
600	0.07	0.17	0.11	0.25	0.37	0.49	2.04
800	0.11	0.18	0.12	0.27	0.40	0.60	2.20
1000	0.12	0.27	0.15	0.33	0.50	0.69	2.75
1200		0.36		0.42	0.62	0.82	

TRIACS

VBOM	2A	6A	10A
	TO5	TO66	TO48
£p	3p	5p	4p
100	0.33	0.55	0.77
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T3	8 1D1216 OC81D
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T6	8 2K344B OC44
T7	8 2K345A OC45
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TO-3 case. O.P. Switching & Amplifier Applications. Brand new Coded R 2400 VCB0 280V/CEO 100V/IC 6A/30 Watts. HFE type 20/FT 5MHz. OUR PRICE EACH: 1-24 25-99 100 up 52p 44p 41p

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U5	60 200mA Sub-Min. Silicon Diodes	55p
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UIC05	12 x 7405	55p	UIC53	12 x 7453	55p
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UIC10	12 x 7410	55p	UIC70	8 x 7470	55p
UIC13	8 x 7413	55p	UIC72	8 x 7472	55p
UIC20	12 x 7420	55p	UIC73	8 x 7473	55p
UIC30	12 x 7430	55p	UIC74	8 x 7474	55p
UIC40	12 x 7440	55p	UIC75	8 x 7475	55p
UIC08	8 x 7408	55p	UIC56	8 x 7456	55p
UIC11	8 x 7411	55p	UIC58	8 x 7458	55p
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UIC17	5 x 7417	55p			
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UIC19	5 x 7419	55p			
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UIC22	5 x 7422	55p			
UIC23	5 x 7423	55p			
UIC24	5 x 7424	55p			
UIC25	5 x 7425	55p			
UIC26	5 x 7426	55p			
UIC27	5 x 7427	55p			
UIC28	5 x 7428	55p			
UIC29	5 x 7429	55p			
UIC31	5 x 7431	55p			
UIC32	5 x 7432	55p			
UIC33	5 x 7433	55p			
UIC34	5 x 7434	55p			
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SN7417	0.58	0.51	0.49	SN74122	0.51	0.51	0.41
SN7418	0.55	0.51	0.49	SN74123	0.48	0.47	0.46
SN7419	0.55	0.51	0.49	SN74124	0.75	0.70	0.61
SN7420	0.49	0.46	0.44	SN74125	0.85	0.83	0.75
SN7421	0.49	0.46	0.44	SN74126	0.30	0.29	0.27
				SN74127	0.30	0.29	0.27
SN7422	0.77	0.71	0.66	SN74128	0.32	0.31	0.31
SN7423	0.16	0.15	0.11	SN74129	0.32	0.31	0.31
SN7424	0.91	0.46	0.44	SN74130	0.32	0.31	0.31
SN7425	0.86	0.82	0.77	SN74131	0.32	0.31	0.31
SN7426	0.70	0.68	0.66	SN74132	0.32	0.31	0.31
SN7427	0.70	0.68	0.66	SN74133	0.32	0.31	0.31
SN7428	0.70	0.68	0.66	SN74134	0.32	0.31	0.31
SN7429	0.70	0.68	0.66				
SN7430	0.16	0.15	0.11	SN74135	0.54	0.54	0.43
SN7431	0.16	0.15	0.11	SN74136	0.54	0.54	0.43
SN7432	0.16	0.15	0.11	SN74137	0.26	0.26	0.26
SN7433	0.16	0.15	0.11	SN74138	0.26	0.26	0.26
SN7434	0.16	0.15	0.11	SN74139	0.26	0.26	0.26
				SN74140	0.26	0.26	0.26
SN7435	0.73	0.70	0.64	SN74141	0.26	0.26	0.26
SN7436	0.73	0.70	0.64	SN74142	0.26	0.26	0.26
SN7437	0.73	0.70	0.64	SN74143	0.26	0.26	0.26
SN7438	0.73	0.70	0.64	SN74144	0.26	0.26	0.26
SN7439	0.73	0.70	0.64	SN74145	0.26	0.26	0.26
SN7440	0.73	0.70	0.64	SN74146	0.26	0.26	0.26
SN7441	0.73	0.70	0.64	SN74147	0.26	0.26	0.26
				SN74148	0.26	0.26	0.26
SN7442	0.73	0.70	0.64	SN74149	0.26	0.26	0.26
SN7443	0.73	0.70	0.64	SN74150	0.26	0.26	0.26
SN7444	0.73	0.70	0.64	SN74151	0.26	0.26	0.26
SN7445	0.73	0.70	0.64	SN74152	0.26	0.26	0.26
SN7446	0.73	0.70	0.64	SN74153	0.26	0.26	0.26
SN7447	0.73	0.70	0.64	SN74154	0.26	0.26	0.26
SN7448	0.73	0.70	0.64	SN74155	0.26	0.26	0.26
SN7449	0.73	0.70	0.64	SN74156	0.26	0.26	0.26
SN7450	0.73	0.70	0.64	SN74157	0.26	0.26	0.26
SN7451	0.73	0.70	0.64	SN74158	0.26	0.26	0.26
SN7452	0.73	0.70	0.64	SN74159	0.26	0.26	0.26
SN7453	0.73	0.70	0.64	SN74160	0.26	0.26	0.26
SN7454	0.73	0.70	0.64	SN74161	0.26	0.26	0.26
SN7455	0.73	0.70	0.64	SN74162	0.26	0.26	0.26
SN7456	0.73	0.70	0.64	SN74163	0.26	0.26	0.26
SN7457	0.73	0.70	0.64	SN74164	0.26	0.26	0.26
SN7458	0.73	0.70	0.64	SN74165	0.26	0.26	0.26
SN7459	0.73	0.70	0.64	SN74166	0.26	0.26	0.26
SN7460	0.73	0.70	0.64	SN74167	0.26	0.26	0.26
SN7461	0.73	0.70	0.64	SN74168	0.26	0.26	0.26
SN7462	0.73	0.70	0.64	SN74169	0.26	0.26	0.26
SN7463	0.73	0.70	0.64	SN74170	0.26	0.26	0.26
SN7464	0.73	0.70	0.64	SN74171	0.26	0.26	0.26
SN7465	0.73	0.70	0.64	SN74172	0.26	0.26	0.26
SN7466	0.73	0.70	0.64	SN74173	0.26	0.26	0.26
SN7467	0.73	0.70	0.64	SN74174	0.26	0.26	0.26
SN7468	0.73	0.70	0.64	SN74175	0.26	0.26	0.26
SN7469	0.73	0.70	0.64	SN74176	0.26	0.26	0.26
SN7470	0.73	0.70	0.64	SN74177	0.26	0.26	0.26
SN7471	0.73	0.70	0.64	SN74178	0.26	0.26	0.26
SN7472	0.73	0.70	0.64	SN74179	0.26	0.26	0.26
SN7473	0.73	0.70	0.64	SN74180	0.26	0.26	0.26
SN7474	0.73	0.70	0.64	SN74181	0.26	0.26	0.26
SN7475	0.73	0.70	0.64	SN74182	0.26	0.26	0.26
SN7476	0.73	0.70	0.64	SN74183	0.26	0.26	0.26
SN7477	0.73	0.70	0.64	SN74184	0.26	0.26	0.26
SN7478	0.73	0.70	0.64	SN74185	0.26	0.26	0.26
SN7479	0.73	0.70	0.64	SN74186	0.26	0.26	0.26
SN7480	0.73	0.70	0.64	SN74187	0.26	0.26	0.26
SN7481	0.73	0.70	0.64	SN74188	0.26	0.26	0.26
SN7482	0.73	0.70	0.64	SN74189	0.26	0.26	0.26
SN7483	0.73	0.70	0.64	SN74190	0.26	0.26	0.26
SN7484	0.73	0.70	0.64	SN74191	0.26	0.26	0.26
SN7485	0.73	0.70	0.64	SN74192	0.26	0.26	0.26
SN7486	0.73	0.70	0.64	SN74193	0.26	0.26	0.26
SN7487	0.73	0.70	0.64	SN74194	0.26	0.26	0.26
SN7488	0.73	0.70	0.64	SN74195	0.26	0.26	0.26
SN7489	0.73	0.70	0.64	SN74196	0.26	0.26	0.26
SN7490	0.73	0.70	0.64	SN74197	0.26	0.26	0.26
SN7491	0.73	0.70	0.64	SN74198	0.26	0.26	0.26
SN7492	0.73	0.70	0.64	SN74199	0.26	0.26	0.26
SN7493	0.73	0.70	0.64	SN74200	0.26	0.26	0.26
SN7494	0.73	0.70	0.64	SN74201	0.26	0.26	0.26
SN7495	0.73	0.70	0.64	SN74202	0.26	0.26	0.26
SN7496	0.73	0.70	0.64	SN74203	0.26	0.26	0.26
SN7497	0.73	0.70	0.64	SN74204	0.26	0.26	0.26
SN7498	0.73	0.70	0.64	SN74205	0.26	0.26	0.26
SN7499	0.73	0.70	0.64	SN74206	0.26	0.26	0.26
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3. Magnetic P.U. 1.5 mV into 50KΩ
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Scratch (Low Pass) 8KHz
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BP948	27p	26p	24p	
BP961	71p	66p	60p	
BP962	15p	12p	11p	
BP963	44p	42p	38p	
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BP9009	44p	42p	38p	
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SN74184	62.27	62.86	62.75	
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SN74182	62.20	61.98	61.76	
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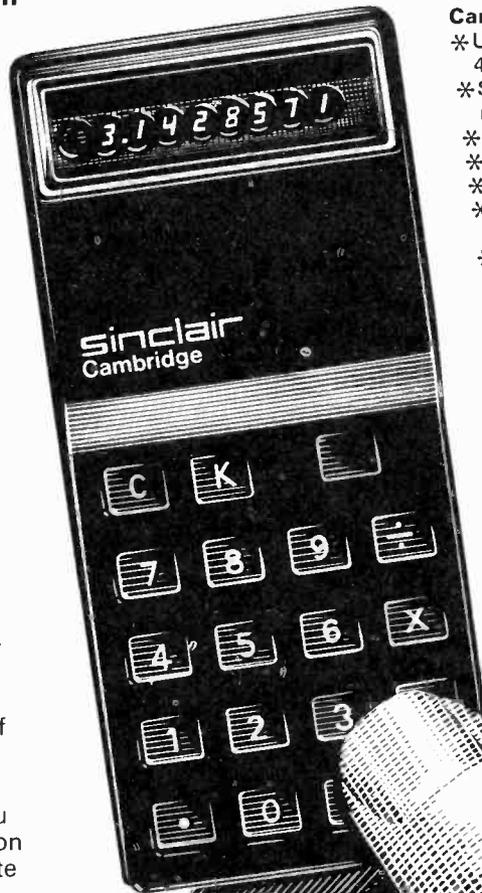
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Features of the Sinclair Cambridge

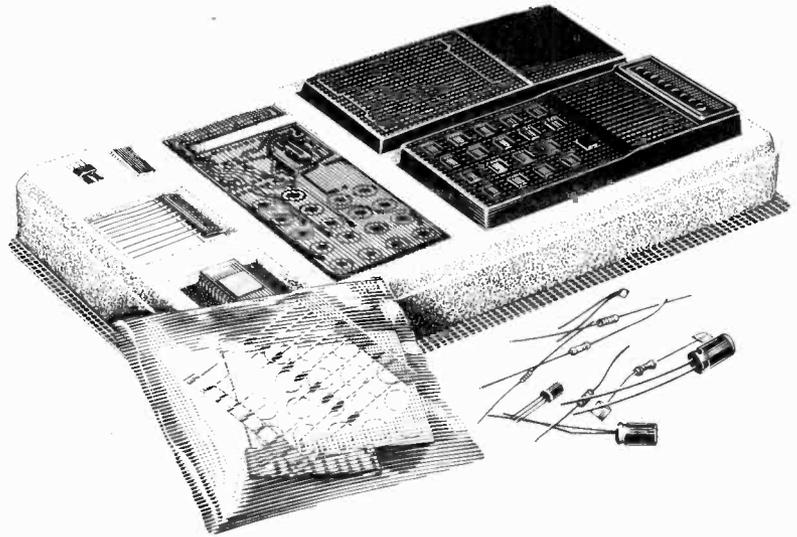
- * Uniquely handy package. $4\frac{1}{2}'' \times 2'' \times \frac{1}{8}''$, weight $3\frac{1}{2}$ oz.
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This valuable book – free!

If you just use your Sinclair Cambridge for routine arithmetic – for shopping, conversions, percentages, accounting, tallying, and so on – then you'll get more than your money's worth.

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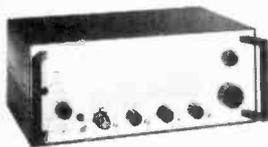
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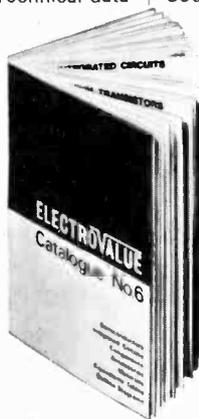


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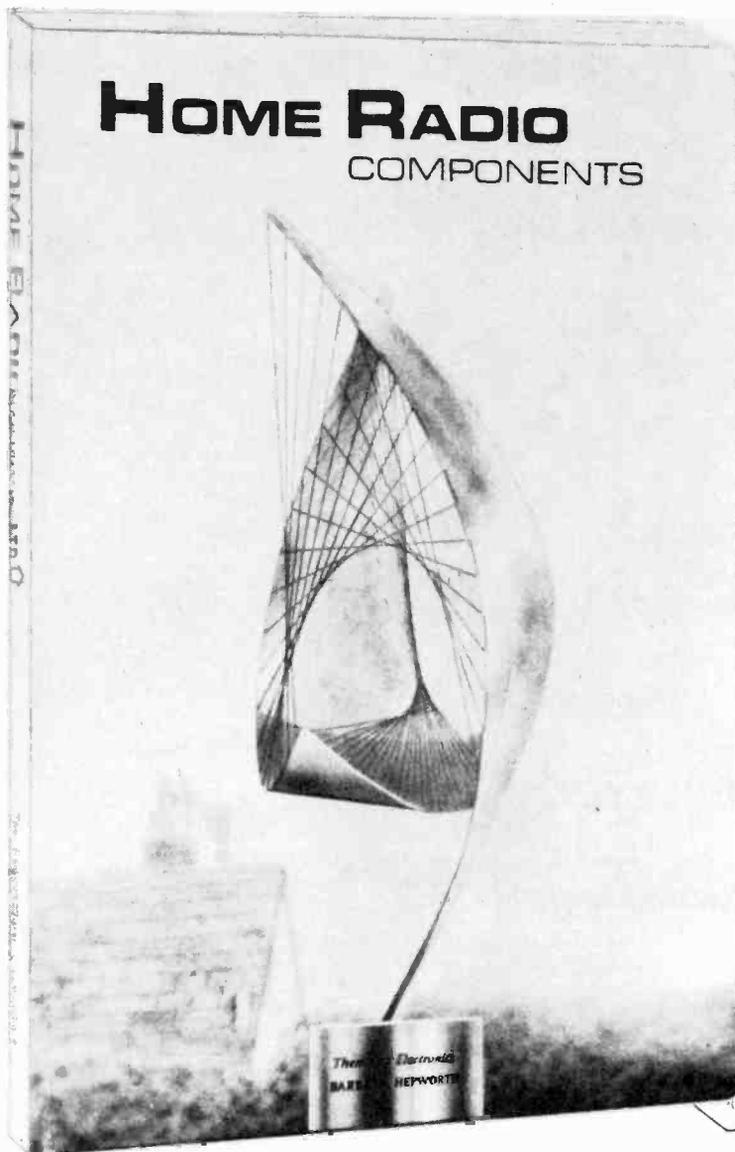
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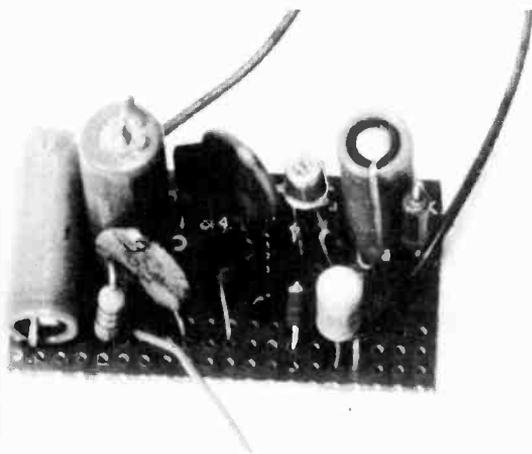
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PUBLISHED ON SEPTEMBER 1st

300mW AUDIO AMPLIFIER



By

R. A. Penfold

Nearly all the components for this simple a.f. amplifier can be assembled on a small piece of Veroboard. The output available is 300mW into a 25 Ω loudspeaker.

THIS AMPLIFIER USES TWO SILICON TRANSISTORS AND an integrated circuit, and will develop an output power of up to about 300mW into a 25 Ω impedance load. The output quality is very good due to the high degree of negative feedback which is applied to the circuit. An input sensitivity of approximately 10mV for full output is obtained with the prototype amplifier.

The use of an i.c. enables the unit to be miniaturised and the prototype amplifier, apart from the volume control, on-off switch, battery and speaker, measures slightly more than 2 by 1 in. with a maximum height of approximately 1 $\frac{1}{4}$ in. It operates from a 9 volt battery and the provision of a Class B output stage ensures good battery economy. The unit is very versatile and has many uses.

CIRCUIT DIAGRAM

The circuit diagram of the amplifier is given in Fig. 1. This consists of the complementary output pair, TR1 and TR2, with an i.c. providing the input and driver stages. The i.c. is a Mullard TAA263, the internal circuit of which is shown in Fig. 2. As may be seen, the i.c. contains three transistors and two resistors. The transistors are direct coupled and the resistors form the collector loads for the first two of them. All three transistors operate as common emitter amplifiers. The TAA263 is housed in a Jedec TO-72 encapsulation, and is therefore the same size as a small standard transistor. There are only four lead-out wires.

Lead-out wire 4 of the i.c. is taken to earth and the negative supply rail. Lead-out wire 2 is taken to the supply rail via a decoupling network. The latter is essential in order to give good stability, and the decoupling components are C2 and R2.

The output of the i.c. is taken from lead-out 3, which must be connected to the positive supply via a suitable load resistance. In Fig. 1 this resistance is provided by R3 and R4 in series. Lead-out 1 connects to the base of the first transistor inside the i.c., and it is to this lead-out that the input signal and biasing current are applied. The input signal is connected to volume control VR1, the slider of which couples to the i.c. via C1 and R1.

In order to obtain stable bias conditions for the i.c. it is necessary to use a large amount of d.c. negative feedback in the bias circuit. Since the input and output of the device are 180° out of phase, this can be achieved by connecting a resistor network between these points. In this type of circuit a bypass capacitor is often included in the network in order to eliminate, or perhaps just reduce, the level of a.c. negative feedback which is introduced by the biasing resistors. In the present circuit, however, nothing approaching the full gain of the i.c. is required, and thus a single biasing resistor without a bypass capacitor can be used.

In Fig. 1 this approach is taken a stage further by taking the feedback point from the emitters of the two output transistors instead of from the output of the i.c. itself. This mode of operation is permissible because both the output transistors are emitter followers,

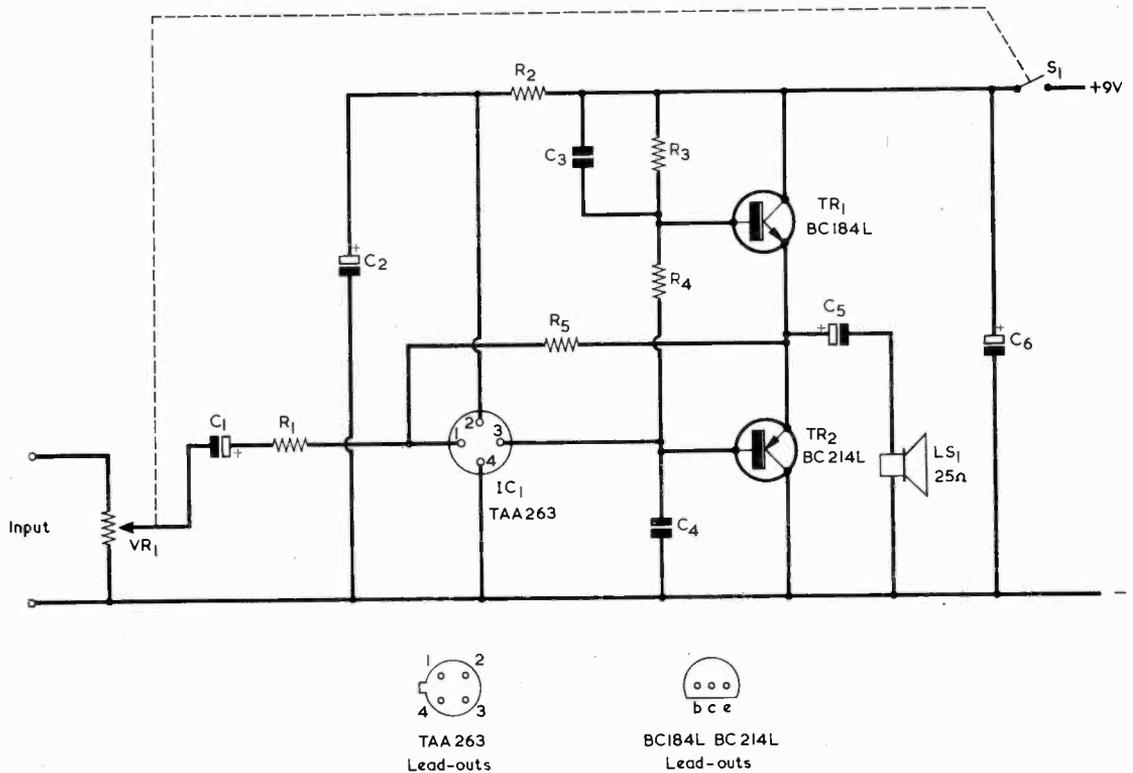


Fig. 1. The circuit diagram for the 300mW a.f. amplifier. The heart of the input and drive circuits is an i.c. type TAA263

COMPONENTS

Resistors

(All fixed values $\frac{1}{4}$ or $\frac{1}{2}$ watt 10%)

R1	2.2k Ω
R2	2.2k Ω
R3	1.5k Ω
R4	470 Ω
R5	330k Ω
VR1	5k Ω potentiometer, log track, with S1

Capacitors

C1	10 μ F electrolytic, 10V. Wkg.
C2	100 μ F electrolytic, 6V. Wkg.
C3	0.01 μ F miniature (see text)
C4	0.01 μ F miniature (see text)
C5	200 μ F electrolytic, 10V. Wkg.
C6	100 μ F electrolytic, 10V. Wkg.

Semiconductors

IC1	TAA263
TR1	BC184L
TR2	BC214L

Switch

S1	s.p.s.t., part of VR1
----	-----------------------

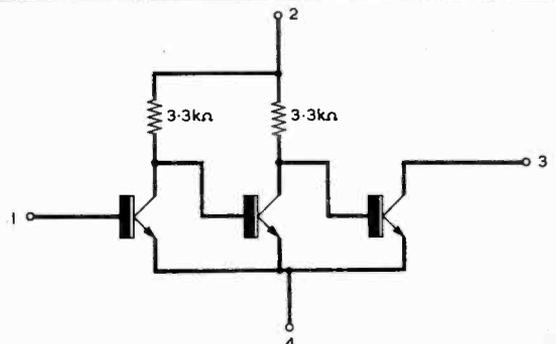
Speaker

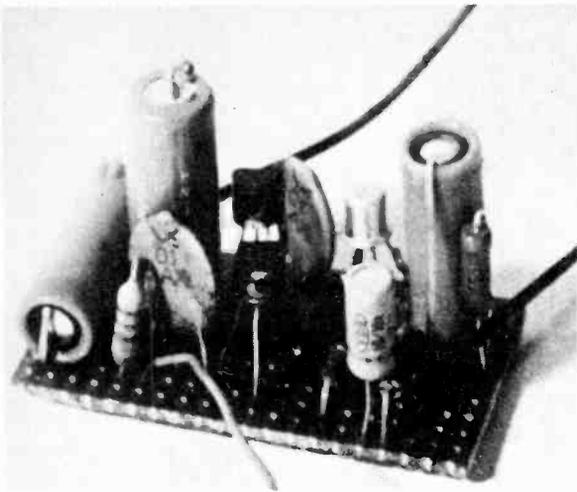
LS1	25 Ω speaker
-----	---------------------

Miscellaneous

	Veroboard panel (see text)
	9-volt battery

Fig. 2. The internal circuitry of the TAA263





The top of the assembly, as seen from a slightly different angle

offering zero phase change and very nearly unity gain. The feedback resistor is R5, and this provides bias together with feedback which extends over the entire amplifier including the speaker. The circuit arrangement, which allows a high level of feedback to be applied over the whole amplifier, results in a very low level of distortion, a low noise level and a very flat frequency response over the audio range. The asymmetry in the output transistor drive circuit, due to the presence of R4 in series with TR1 base, is virtually cancelled out.

The function of R4 is to permit a small biasing current in the two output transistors in the absence of signal, thereby reducing crossover distortion. The value of R4 and, hence, the biasing current, is much smaller than would be employed in a normal Class B output stage, but here again the high level of negative feedback allows such a circuit value to be used. This feedback reduces the crossover distortion which would otherwise be evident to a level which is unnoticeable.

The operation of the output stage is basically quite simple. As already mentioned, TR1 and TR2 operate as emitter followers and thus have approximately unity voltage gain. On the other hand the two transistors have a considerable current gain, and thus produce an output signal at very low output impedance. This signal can be directly coupled to a 25Ω or 35Ω speaker without the necessity for an output transformer. With a 25Ω speaker the available output power is 300mW.

Under quiescent conditions, the voltage at the junction of TR1 and TR2 emitters with respect to the negative rail is approximately half that of the supply. When a signal is applied to the transistor bases, TR1 amplifies the positive half-cycles and TR2 the negative half-cycles. So far as the output transistors are concerned, there is only a small bias current under no-signal conditions. When a signal is applied the current drawn by the output transistors is proportional to the output power. In consequence, maximum battery economy and a minimum amount of heat generation are given in the output stage.

The output signal is coupled to the speaker via the d.c. blocking capacitor, C5. Capacitors C3 and C4 are required in order to reduce the high frequency response of the circuit; without these capacitors the response could extend well into the r.f. spectrum, with a consequent tendency towards instability.

CONSTRUCTION

Assembly is very straightforward, a Veroboard panel having 20 by 10 holes, with the copper strips running along the board length, being employed for most of the components. The prototype used Veroboard with a hole matrix of 0.1in., but it is quite in order to use 0.15in. Veroboard instead. If 0.1in. Veroboard is employed a very compact amplifier results, but construction requires a little more skill and it is essential that the resistors and capacitors be subminiature types. Beginners are advised to employ 0.15in. Veroboard.

Fig. 3 shows the Veroboard panel assembly as seen from the component side of the board. There are no breaks in the copper strips. C1, C2, C5, R2, R3 and R5

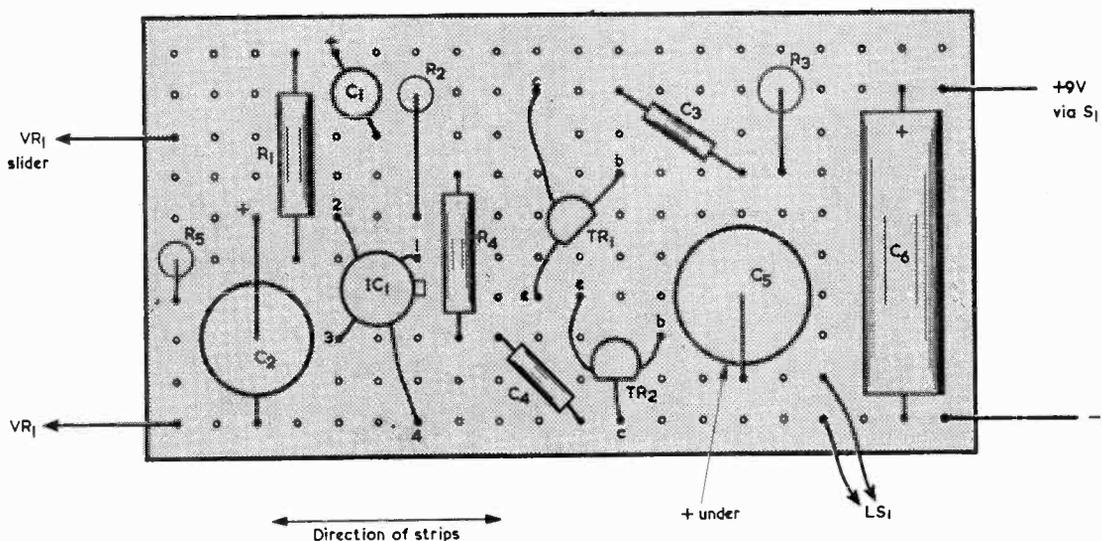


Fig. 3. The component side of the Veroboard assembly. VR1 and S1 are external to the board

are all mounted vertically. C3 and C4 should be types which are suitable for printed circuit mounting and which have their lead-outs projecting from the same side of the component body. Disc ceramic capacitors were employed in these two positions in the prototype.

The lead-outs of the integrated circuit are fairly short. If necessary, extension leads can be soldered to one or more of these to enable Veroboard hole positions to be reached. However, such extension leads should be kept as short as possible.

NOTES ON USE

The optimum speaker load impedance is 25Ω. Speakers having a higher impedance may also be used

but the maximum available output power will then be less than 300mW.

The earth connection for the amplifier is given by the negative supply rail. When the input connection to VR1 is made by screened wire, as is recommended, the outer braiding of the wire connects to the outside tag of VR1 which is common with the negative supply rail. Input impedance is approximately 5kΩ.

No attempt should be made to employ a supply voltage greater than 9 volts, as this might result in the destruction of the TAA263 integrated circuit. Any 9 volt battery can be used to power the amplifier, a small radio type such as the PP3 or PP4 being perfectly adequate. A quiescent current of around 5mA (drawn mainly by the TAA263) is to be expected. ■

SIMPLE SOLDERING SCHEME

by W. J. Gadsby

A trouble-free method of overcoming an occasional workshop problem.

IN THE ASSEMBLY OF HOME-MADE SWITCHING DEVICES IT is sometimes necessary to be able to fit an adjustable screw to materials such as springy brass strip. If the strip forms part of a home-constructed switching assembly, the screw can then be turned to provide an adjustable contact. Other occasions can arise when it is similarly necessary to fit a screw to a thin piece of brass, copper or tinplate.

ADDING A NUT

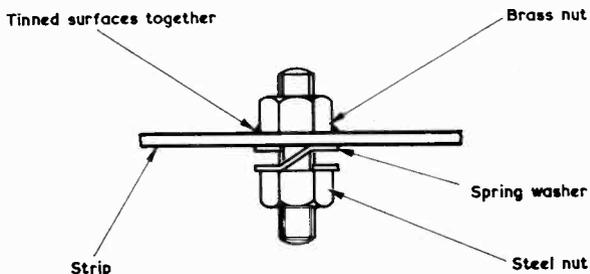
In all these cases, the metal to which the screw is to be fitted is too thin to be tapped, and it becomes necessary to solder a nut to it. The problem then arises of suitably positioning the nut and of ensuring that a reliable solder joint is made.

The accompanying diagram shows a scheme which the author has found to work very satisfactorily.

First drill in the strip, or thin metal, a hole which is clearance size for the screw to be fitted to the nut. Then tin the strip around the hole, using resin cored solder such as Ersin Multicore or Savbit.

Next, take up the nut which is to be soldered to the strip and similarly tin the surface which will be in contact with the strip. This nut must, of course, be brass.

With a steel screw or piece of steel studding fix the nut to the strip with the two tinned surfaces together, as illustrated. Fitted on the opposite side of the strip are a



How the parts are assembled on the strip before soldering

steel nut and a steel spring washer. Tighten the nuts so that the washer is compressed.

Apply a soldering iron to the brass nut. The solder on the two tinned surfaces will then melt and the spring washer will force them together. Allow the assembly to cool and then remove the steel bolt or studding, the steel spring washer and the steel nut. The brass nut will now be firmly soldered to the strip.

This method relies on the facts that a small amount of flux still remains on a surface which has been tinned with resin cored solder and that the solder does not readily form a joint with the steel parts which are temporarily used to hold the brass nut in position on the strip. ■

NEWS . . . AND .

THAMES TV INTRODUCE AIR-TRANSPORTABLE OB UNIT



A portable colour TV control room that can be air-freighted throughout Europe has been built by Thames Television in co-operation with BEA Cargo Department. It is to be used this summer for making holiday programmes in Spain, Portugal, France, Italy and Yugoslavia.

The control-room is constructed within a double-skinned glass-reinforced plastics framework and is about 9 ft long, 7 ft 6 in wide and 6 ft high.

All the auxiliary sound and vision equipment can be stored within the main container which is transported to and from airports on flat-backed lorries.

The unit has two lightweight Philips LDK13 colour camera channels, an Ampex quadruplex videotape recorder and a four-channel sound mixer. The portable 'Skylab' is fully air-conditioned to work in a wide range of temperatures.

This new portable outside-broadcast unit – the first of



its type to be built in the U.K. – is likely to encourage further use of electronic cameras for TV programmes of types formerly made in film. The use of electronic cameras allows the shots to be viewed immediately on playback without processing. The cameras are light enough to be carried on the shoulder or mounted on simple tripods.

ALIEN EARS TUNE IN ON EARTH RADIO

A Space vehicle from another star is circling the earth at the same distance as our moon – that astonishing idea was put forward recently at a meeting of the British Interplanetary Society, covered by BBC World Service.

The idea was originally suggested in 1960 by an American professor to explain some long delayed echoes of regularly spaced radio signals transmitted from earth

These echoes were observed and recorded around 1930 by radio experimenters in Holland, Norway and France. At that time the mechanics of radio propagation were not very well understood, and it was assumed that these echoes were bouncing off the moon or being reflected within the atmosphere.

In a paper published in the British scientific journal *Nature* the professor suggested that if an alien space vehicle wanted to contact the earth, a process of re-transmitting our own signals with a suitable pause would ensure that someone heard them. At the recent BIS conference, however, Mr. Duncan Lunan, a graduate of Glasgow University, told a meeting of the British Interplanetary Society about his own interpretations.

Mr. Lunan claims that by plotting the delay times against the order in which they came in, he is able to obtain several star maps. From these maps Mr. Lunan deduces that a space vehicle from the star Epsilon Bootes is circling the earth in the same orbit as our moon.

He also believes that the vehicle arrived here about 13,000 years ago after completing the 103 light year journey from its starting point.

Most of this information is reasonably easily deduced from Mr. Lunan's charts, assuming they have been accurately drawn up. What is rather more difficult to accept is his interpretation of later, more complex charts.

The ideas are sufficiently stimulating to have persuaded EMI to make available equipment to test the theory.

Suggested Circuits - April issue

In 'High Input Impedance Amplifier' (Suggested Circuit No. 269) published in the April 1973 issue TR4 is shown as an n.p.n. transistor. The emitter arrow for this transistor should point inwards.

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COMMENT

OPEN UNIVERSITY COURSE IN ELECTRONICS AND CIRCUIT DESIGN

Electromagnetics and Electronics, a Post-experience course by the Open University, aims to provide an understanding of the scientific basis of electronics and electronic circuit design.

The course is primarily, but not exclusively, intended for those preparing for higher level university study in science and technology.

It assumes little prior knowledge of electronics or electromagnetics but does assume a background of scientific or technical education beyond GCE 'O' Level.

The first part deals with the basic ideas of electricity, magnetism and electromagnetism, semiconductors and the properties of simple circuits and the remainder deals with electronic circuits.

The course consists of 17 written correspondence units linked to 17 television and five radio programmes. Students are required to attend a one week residential summer school and encouraged to attend evening or Saturday tutorial sessions. There are 12 assignments to complete and an examination at the end of the course.

Applications are now invited for the course which starts next February and lasts until November.

A home experiment kit, including a cathode-ray oscilloscope and a signal generator, is sent to students who are expected to design and build circuits for checking at summer school.

The course tuition fee is £45 plus £37 for the residential summer school. Application forms are available from The Post-experience Student Office, P.O. Box 76, Milton Keynes, MK7 6AA.

IN BRIEF

● Mr. William E. Harvey has been appointed director of operations for the £2 million Television Division of EMI Sound & Vision Equipment Ltd., based at Hayes, Middlesex.

● Torbay Amateur Radio Society is holding a Mobile rally on Sunday 12th August at the All Whites Rugby Football Ground, Newton Abbot.

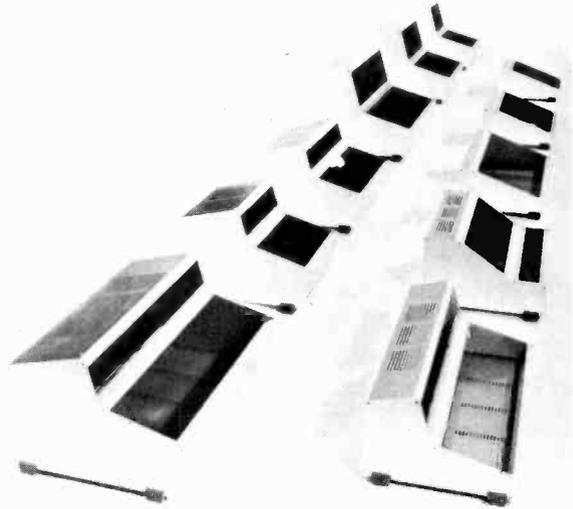
● A contract to supply audio equipment to one of the first local commercial radio stations in this country has just been awarded to Audio Ltd., of Stansted, Essex, by Birmingham Broadcasting Ltd., to be known as BRMB.

● The Committee on Broadcasting Coverage extends an open invitation to anyone, including private individuals, who wishes to submit written evidence, on any matter within its terms of reference, which relate to the coverage of broadcasting services in the U.K. Write to Committee on Broadcasting Coverage, 85 Whitehall, London, SW1A 2NP.

● AMF Venner have now published a technical data sheet describing their Digital Timer, Type TSA 240.

Specially designed for the Post Office at outside locations it may be operated from the mains supply or from external low voltage batteries.

INDUSTRIAL RACKS AND CABINETS



Daturr Ltd. is a new face in the field of racks, cabinets, instrument cases, card frames, consoles and all sheet metal work for the electronics industry.

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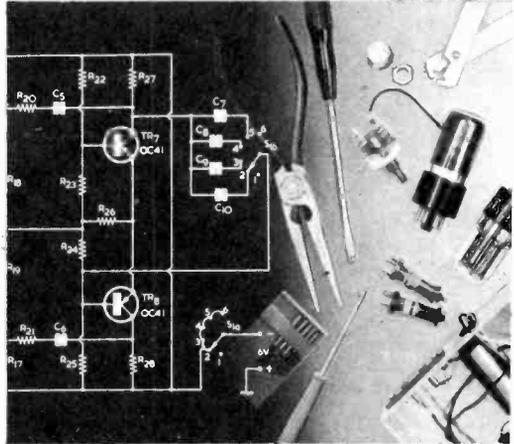
Daturr consoles are despatched packed flat and may be assembled using only a screwdriver in a few minutes. Prices vary according to type and size from £12 to £26 each and delivery will normally be ex stock.



"Don't tell me, young man — That's where you rig your tests!"

CLASS A AMPLIFIER

by G. A. FRENCH



AN INTERESTING TRANSISTOR WHICH has been available on the home-constructor market for some time is the Motorola MJE340. This is an n.p.n. power transistor having an hFE at 50mA collector current of 30 to 240 and a maximum collector voltage rating of 300 volts. This exceptionally high voltage rating makes the MJE340 a very useful device for American domestic entertainment products, since it can function as an audio output transistor in radios and record players which obtain their d.c. supply by direct rectification, without a transformer, of the American 117 volt mains.

This article describes a record player amplifier incorporating an MJE340 and is primarily intended to demonstrate to the more experienced constructor the advantages and disadvantages of this device. The output of the amplifier is in excess of 1 watt and the MJE340 is employed in a Class A circuit. The quality of reproduction, although not in the high fidelity category, is nevertheless quite acceptable. Sensitivity is sufficient to enable full output to be obtained from a crystal or ceramic pick-up.

The circuit will not be attractive to the beginner who simply requires a 1 watt amplifier and intends to buy all the components new, as a conventional Class B design of comparable performance would be cheaper and much less bulky in size when completed. The circuit should, on the other hand, appeal to the experimenter who likes to try something new and who may have most of the components required already on hand.

CIRCUIT OPERATION

The circuit of the amplifier appears in the accompanying diagram, and it will be helpful to commence a description of its operation at the power

supply section. The power for the unit is obtained by way of mains transformer T2 and rectifiers D3 and D4. Capacitor C8 is the reservoir capacitor, R13 the smoothing resistor and C7 the smoothing capacitor. It will be noted that component values here are reminiscent of those encountered in the h.t. supplies of valve equipment. The current drawn by the MJE340, which is in the TR3 position, and by its base bias potentiometer, is such that the positive plate of C7 is about 100 volts positive of chassis.

TR3 couples to the positive rail by way of the primary of T1, which is a valve-type speaker transformer having a step-down ratio of around 30:1. Its secondary connects to a 3Ω loud-speaker. Base bias for TR3 is provided by the potentiometer given by R8 and R9, whilst R10 is the emitter bias resistor. Since the voltage across R10 cannot rise above that at the base of TR3, a safely limited supply voltage becomes available at the emitter of TR3 for TR1 and TR2, which are standard low voltage transistors. About 11 volts is present across R10 and approximately 11.6 volts appears at the base of TR3. A current of slightly less than 12mA flows in R9 and one of around 14mA in R8, the extra current in R8 being due to the collector current of TR2. It is possible that standing current in R8 and R9 could be reduced by giving these resistors proportionately higher values, but the circuit functions satisfactorily in practice with the values shown and exhibits good voltage stability.

The collector of TR2, which is a common emitter amplifier, couples to the base of TR3 via R7. In conjunction with C6, this resistor limits amplification at radio frequencies and prevents instability at these frequencies. The use of a collector-to-base capacitor and a series base feed resistor is encountered in commercially produced equipment

employing the MJE340, and appears to be a common circuit approach when this transistor is used in a single-ended output stage. Base bias for TR2 is obtained, via R6, from the voltage dropped across R10.

Preceding TR2 is the emitter follower TR1, whose function is to provide a high impedance input for the amplifier. The emitter load for TR1 is R4, and the coupling to TR2 is by way of C3. The collector of TR1 connects direct to the upper end of R10, and its base couples to this same source of voltage by way of R3 and R5. The emitter of TR1 is bootstrapped to the junction of these two resistors via C2, with the result that R3 has little shunting effect on the input impedance of TR1 base circuit. There is no decoupling between the upper end of R10 and the base supply to TR2, the collector of TR1 and the base supply to TR1 (although C2 might be considered to provide decoupling in the last case) since this was found to be unnecessary. Even if feedback from the emitter of TR3 did occur this would, in any case, be negative in character.

The input to the amplifier is provided via volume control R1 and the series components R2 and C1. Input impedance is 2MΩ when R1 slider is at the low-volume end of its track and is slightly less than 1MΩ when R1 slider is at the high-volume end. These impedances are suitable for a crystal or ceramic pick-up and similar sources of signal.

TR2 and TR3 provide a relatively high level of gain and a considerable amount of negative feedback is applied from the secondary of T1 via R11 to the emitter of TR1. R12 functions as a top-cut tone control. When its slider is at the end of its track which connects to C4, this capacitor is effectively in parallel with R11. In consequence, feedback increases as frequency rises, causing a reduction in the treble

response. This reduction decreases progressively when the slider of R12 is moved towards the other end of its track. The link shown as a broken line between points 'A' and 'B' denotes the fact that the feedback loop is left open at this point during construction. It is completed after the correct phasing of the connections to T1 secondary has been found, as is explained in greater detail later.

Components which have not so far been dealt with are D1, D2, R14 and C9. D2 causes C9 to charge to the peak value of the half-secondary voltage from T2, and D1 ensures that the collector of TR3 cannot rise above this potential. Without a protection circuit of this nature there is a possibility that transients in the signal being reproduced could cause voltages in excess of the maximum specified value to appear at TR3 collector, with consequent risk of breakdown. R14 is merely a high value bleeder resistor and it ensures that C9 becomes discharged after the amplifier has been switched off.

COMPONENTS

Unless otherwise specified in the diagram, all the fixed resistors are $\frac{1}{4}$ watt 10% types. R2 to R6 inclusive should be high stability components. R1 and R12 are standard panel-mounting potentiometers. If desired, the on-off switch, S1, may be ganged with R12. An additional 100k Ω resistor, not shown in the diagram, is required for setting-up purposes.

The four diodes may be any silicon rectifiers rated at 0.5 amp or more and having a p.i.v. of at least 400 volts. The author used BY100's in the prototype as these happened to be on hand.

Transformer T2 is an R.S. Components 'Midget Mains 125V' type, which is available from Home Radio under Cat. No. TM39 or from stockists of R.S. Components parts. It has an h.t. secondary offering 125-0-125 volts at 50mA and a heater secondary giving 6.3 volts at 1.2 amps. The 6.3 volt secondary is unused, although it could if desired be employed to power a panel light which would indicate when the amplifier was switched on.

Ideally, the primary of speaker transformer T1 should present an impedance of some 2,500 Ω to the collector of TR3, and this would be produced by a transformer having a turns ratio of 29:1. The writer initially checked performance with a miniature 33:1 valve output transformer and then with a larger 36:1 valve output transformer rated at 5 watts. There was little significant difference in performance between the two transformers and it would seem that any transformer ratio reasonably close to 29:1 should be satisfactory in practice. The 5 watt transformer is listed in the Home Radio catalogue under Cat. No. TO44, and is described there as offering a primary impedance of 5,000 Ω at a secondary load of 3.75 Ω .

The primary impedance will be lower than 5,000 Ω when a 3 Ω load is used. It is desirable for T1 to be a fairly large component because, immediately after switch-on, there is a short current pulse through its primary which is limited by R13 to about 120mA. The pulse is caused by charging current flowing into C5. The normal standing current in T1 primary is about 35mA.

The transistor type MJE340 is available from a number of semiconductor suppliers, including Henry's Radio. Despite its high dissipation capability it is a physically small device. It has a metal surface on one side which comes into contact with the heat sink. The lead-out positioning is shown in the inset, this illustrating the transistor with the flash line (i.e. the raised line which is given where, during manufacture, the two halves of the plastic mould meet) below the lead-outs. It is, however, a little difficult to see the flash line and constructors who want to be completely certain of lead-out identification can confirm this with the aid of a testmeter switched to an ohms range. There will be continuity between the central collector lead-out and the base lead-out with the testmeter leads connected one way round. There will be no continuity between the collector lead-out and the emitter lead-out with the testmeter leads connected either way round.

The MJE340 dissipates about 3.2 watts in this circuit and needs to be mounted on a fairly large heat sink. The author employed a flat unpainted mild steel sheet about 4 in. square, mounted vertically, and with the transistor secured to it at the centre. The transistor surface indicated in the lead-out inset in the diagram should be that which is in contact with the heat sink, and the transistor is secured by means of a 6BA bolt and nut. A smaller heat sink could be used if this were of the ribbed variety and was painted black. The MJE340 must not be allowed to become too warm as, apart from any other factors, it may then cause distortion to be introduced.

The heat sink is in contact with the collector of the transistor and must, therefore, be insulated from the chassis. A word of warning is needed here. Experienced constructors who have worked with power transistors in low voltage circuits have, in some cases, fallen into the bad habit of checking transistor temperature with the back of a finger whilst the associated equipment is switched on. In the present design the heat sink has a potential of about 100 volts above chassis, and it must *not* be touched whilst the equipment is switched on since this could result in an unpleasant and possibly even dangerous shock. The amplifier must be switched off before checking heat sink temperature. Incidentally, checking transistor temperature with the back of a finger whilst the associated equipment is turned on

is a bad habit because it is possible for some of the power transistors in such items as television receivers to have cases which are similarly at a dangerously high potential above chassis. The amplifier under consideration must be housed in a suitable cabinet after completion so that the heat sink and, of course, mains and other high voltage points cannot be accidentally touched.

FEEDBACK PHASING

Construction should not raise many difficulties. The layout is not critical provided the signal circuitry around TR1 does not too closely approach that around TR3. It is important that R11 and C4 are *not* connected to the emitter of TR1 at this stage. In the circuit diagram this state of affairs corresponds to the link between points 'A' and 'B' being open.

When assembly has been completed and the usual wiring checks have been made, R1 may be set to the minimum volume position and the amplifier switched on. In its present condition, without feedback, the amplifier has a very high gain, with the input circuitry around TR1 being at high impedance. There should be quite a loud hiss from the speaker and it may be found that there are noticeable crackles from the speaker if the chassis is touched by a screwdriver. The next operation is to check the phasing of the feedback. This is done by taking up a 100k Ω resistor and connecting it across points 'A' and 'B'. These points are both at low impedance and it is quite in order to hold the resistor body between thumb and forefinger. If the hiss drops noticeably when the 100k Ω resistor is connected, the feedback phasing is correct and points 'A' and 'B' can be joined directly.

Should the application of the 100k Ω resistor cause the amplifier to break into oscillation, the feedback phasing is incorrect. The amplifier is switched off and the feedback leads (i.e. those from chassis and from R11/R12) at the secondary of T1 are transposed. The amplifier is switched on again and the check with the 100k Ω resistor at points 'A' and 'B' carried out once more. The hiss should now reduce as the resistor is applied to these two points, thereby indicating that, this time, the feedback has the correct phase. Points 'A' and 'B' may then be joined directly.

It is necessary to adopt the procedure just described because, if points 'A' and 'B' were joined directly when the amplifier had incorrect feedback phase, the consequent oscillation might be sufficiently strong to cause damage to an output component. Using the 100k Ω resistor ensures that oscillation, with incorrect phase, is at a safely low level.

After the feedback connection between points 'A' and 'B' has been finally made, the amplifier is complete and ready for use. ■

New Products

DEVCON RUST JELLY



Devcon Rust Jelly is a versatile rust remover that was developed originally for industrial use, on machinery, tanks, bridges and other structures. It works on any surface – iron, steel or concrete – and will remove tarnish from copper, aluminium and other metals. The substance is applied to the surface and allowed to penetrate the rust for five minutes, before being washed away with clean water. A second application may be used if there are still traces of rust. Rust Jelly contains a neutralizing agent to help protect sound metal. No further treatment is necessary before painting. Devcon Rust Jelly can be used at any angle – even overhead or vertical surfaces can be treated – and it is quite safe to use, being non-flammable and odourless. The retail price is 55p for an 8 fluid oz. plastic applicator pack.

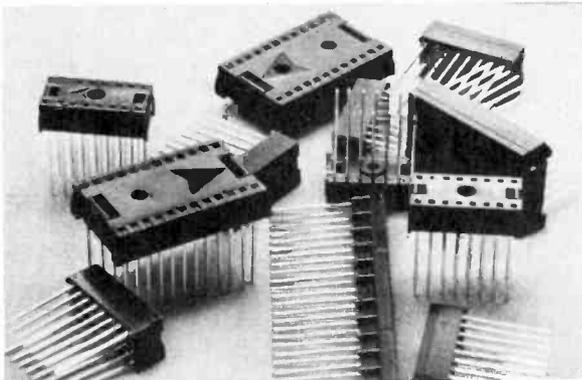
Further details from Devcon Ltd., Station Road, Theale, Berks.

D.I.L. SOCKET RANGE

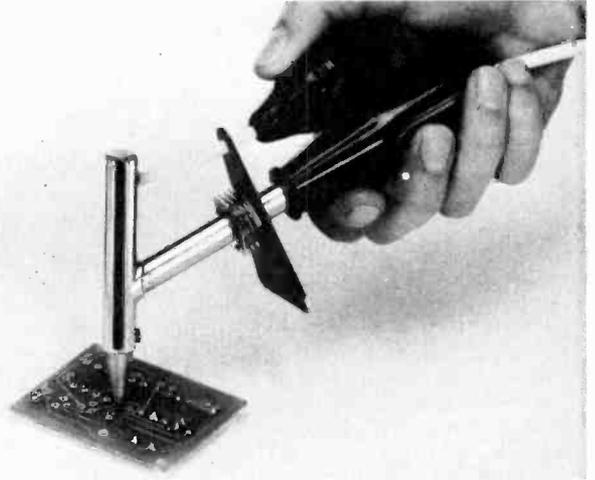
Tekdata (Trading) Ltd. are now offering the Scanbe ME 2 14-, 16- and 24-pin d.i.l. sockets, with either square wire-wrap posts or solder terminals.

The glass-filled nylon body, with latching cap, holds gold-plated phosphor-bronze wire-wrap posts or solder terminals. These have a current rating of 1 ampere in use, and a typical contact resistance (contact surface to pin tip) of 8 milliohms. Unwanted pins can be easily removed, by prising off the cap. Insertion force per pin is said to be 250 g max. (100 g typically) and extraction force 30 g min. (60 g typically). Designed for high-density p.c.b. mounting, the socket pins fall on a 0.1 by 0.3 in. grid.

ME 2 d.i.l. sockets are available ex stock. A sample price of 32p each for the 16-pin wire-wrap types in 100 lots is quoted. Tekdata (Trading) Ltd., Pentagon House, Bucknall New Road, Hanley, Stoke on Trent, Staffs. ST1 2BA.



DE-SOLDERING GUN



A new de-soldering instrument which eliminates the necessity for air or vacuum lines has been introduced by Adcola Products Ltd.

Known as the R 500 the instrument has been designed for vertical operation over the joint to be de-soldered on printed circuit boards, etc.

To allow the operator to hold the instrument in a convenient position while maintaining the barrel vertically over the joint the handle is positioned at an angle to the barrel.

The R 500 has also been designed for simple one hand operation and features an air bulb connected to the barrel by clear PTFE tubing. In use the de-soldering 'gun' reaches operating temperature in two minutes. The air bulb is depressed and the nozzle end of the barrel is then positioned over the joint to melt the solder. Once the solder has become molten the air bulb is released to 'suck' the solder up into the barrel to leave a clean joint.

The barrel can be cleaned of solder by sharply depressing the air bulb to eject the molten solder into a suitable waste container. The tool is suitable for continuous operation and it will not lose the necessary reservoir of heat needed for efficient de-soldering operation, even under the most rigorous rectification work conditions.

The 'gun-like' design of the tool allows for clear vision of the work piece at all times, and it can be balanced on the work bench when not in use. The handle is moulded in glass filled nylon to remain cool over long periods of use, and the interchangeable nozzle can be removed for periodic cleaning.

The R 500 is available in a range of voltages from 6v to 250v and has an element rated at approximately 30w for efficient operation. It is robustly constructed for long term trouble-free operation to meet worldwide safety standards, and is priced at £6.72 plus VAT.

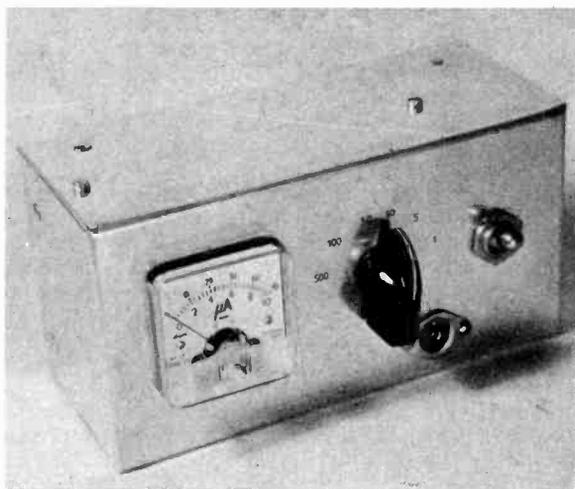
A.C. Millivoltmeter

by A. P. Roberts

This battery operated instrument has a flat frequency response from approximately 25Hz to higher than 100kHz, and offers ranges from 0-1mV to 0-500mV.

IN THE DESIGN AND TESTING OF AUDIO AMPLIFIERS THE two most useful items of equipment are probably a wide-range audio signal generator and an audio a.c. millivoltmeter. With these two pieces of equipment it is possible to make accurate measurements of noise, gain and frequency response. Other useful measurements can also be carried out.

While many enthusiasts own an audio signal generator, a.c. millivolts are a comparative rarity. The unit which forms the subject of this article is an inexpensive millivoltmeter for use at audio frequencies, its frequency response being relatively flat from approximately 25Hz to well over 100kHz. Six ranges are provided, these having f.s.d. values of 1, 5, 10, 50, 100 and 500mV. The input impedance is in excess of 500k Ω on all ranges. As is explained later, an audio signal generator with a calibrated output (or an audio signal generator and some form of a.c. voltmeter) is required for setting up.



Front view of the completed millivoltmeter

Additional numbers were added to the meter scale in the prototype to provide a 0-10 range

THE CIRCUIT

The circuit diagram of the a.c. millivoltmeter appears in Fig. 1. The design consists basically of an emitter follower input stage (TR1), a switched attenuator to select the desired range (S1(a) (b) and R5 to R15), a voltage amplifier (TR2, TR3 and TR4) and finally a moving-coil meter fed via a bridge rectifier to indicate the input voltage (M1 and D1 to D4).

The input signal is fed to the base of TR1 via C1. R1 and R3 form a potential divider and provide bias for TR1, which is used in the emitter follower mode. C2 provides a bootstrap coupling back to the input and this, in conjunction with R2, considerably reduces the shunting effect on the input impedance given by R1 and R3. The input impedance obtained using a 2926G in the TR1 position, as specified, should be found perfectly adequate for nearly all practical purposes. If the constructor so desires, the input impedance can be somewhat increased by employing a BC169C for TR1; however, this article assumes that a 2N2926G is used, as occurred with the prototype.

The emitter load resistance for TR1 is provided by the attenuator network. This is positioned in the low impedance section of the input stage in order to remove the necessity for frequency compensation. Actually, the attenuator consists of a single load resistor for the 0-1mV range and five separate potential dividers for the remaining ranges. The desired range is selected by S1(a) (b). The resistors used in the potential divider sections should be 2% or, if possible, 1% high stability types. Some of the values are non-standard and may need to be made up of two resistors in series, as indicated in the Components List.

C4 couples the output from the attenuator to the voltage amplifier input. An electrolytic capacitor cannot be used in this position since the polarity of the d.c. voltage appearing across the capacitor is not the same on all ranges. Suitable capacitors for C4 are available from a number of suppliers including Marco Trading The Maltings, Station Road, Wem, Salop. The working voltage is unimportant as the maximum voltage appearing across the capacitor will only be of the order of 1 volt or so.

Three transistors, TR2, TR3 and TR4, are used in the voltage amplifier, these all being connected in the

RADIO & ELECTRONICS CONSTRUCTOR

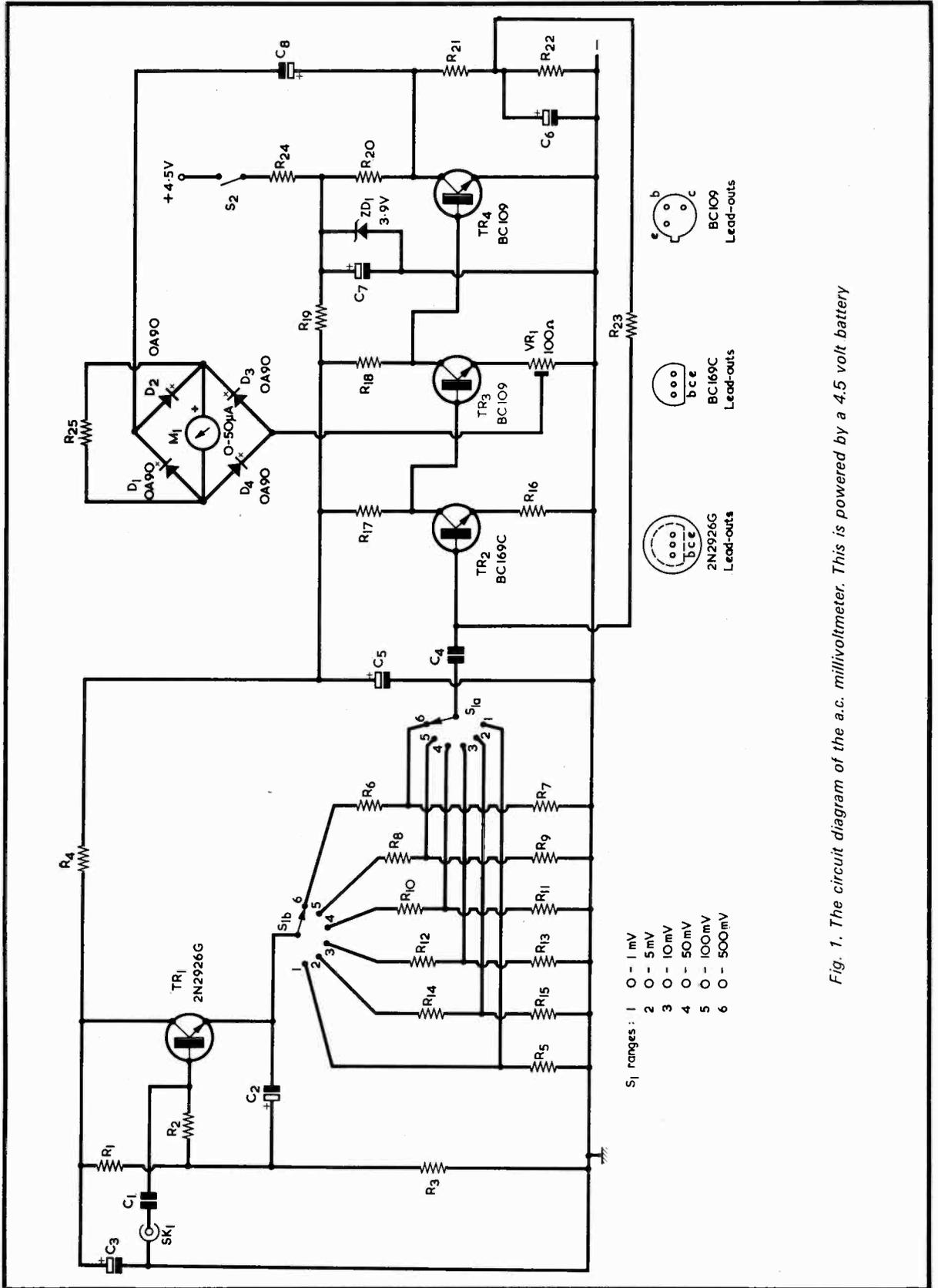


Fig. 1. The circuit diagram of the a.c. millivoltmeter. This is powered by a 4.5 volt battery

COMPONENTS

Resistors

(All fixed values $\frac{1}{4}$ watt 10% unless otherwise stated)

R1	100k Ω
R2	100k Ω
R3	150k Ω
R4	330 Ω
R5	6.2k Ω
R6	6k Ω 2% (4.7k Ω + 1.3k Ω)
R7	12 Ω 2%
R8	6.2k Ω 2%
R9	62.8 Ω 2% (56 Ω + 6.8 Ω)
R10	6.2k Ω 2%
R11	129 Ω 2% (82 Ω + 47 Ω)
R12	6.2k Ω 2%
R13	690 Ω 2% (680 Ω + 10 Ω)
R14	4.8k Ω 2% (2.4k Ω + 2.4k Ω)
R15	1.2k Ω 2%
R16	270 Ω
R17	10k Ω
R18	10k Ω
R19	330 Ω
R20	2.2k Ω
R21	10k Ω
R22	10k Ω
R23	270k Ω
R24	68 Ω
R25	680 Ω (see text)
VR1	100 Ω pre-set potentiometer, miniature skeleton, horizontal mounting

Capacitors

C1	0.47 μ F 100 V.Wkg. (see text)
C2	10 μ F electrolytic, 6 V.Wkg.
C3	100 μ F electrolytic, 6 V.Wkg.
C4	2.2 μ F (see text)
C5	200 μ F electrolytic, 6 V.Wkg.
C6	25 μ F electrolytic, 6 V.Wkg.
C7	50 μ F electrolytic, 6 V.Wkg.
C8	200 μ F electrolytic, 6 V.Wkg.

Semiconductors

TR1	2N2926 green (see text)
TR2	BC169C
TR3	BC109
TR4	BC109
D1-D4	OA90
ZD1	3.9V 5% zener diode, 250 or 400mW

Switches

S1(a) (b)	2-pole 6-way miniature rotary
S2	s.p.s.t. toggle

Meter

M1	0-50 μ A meter, 42 x 42mm (see text)
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Socket

SK1	Flush mounting coaxial socket
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Battery

	4.5V battery type 1289 (Ever Ready)
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Miscellaneous

	2 miniature crocodile clips
	Chassis, 6 x 3 x 2 $\frac{1}{2}$ in., with base plate
	Pointer knob
	Paxolin for component panels

common emitter mode. They are direct coupled, and bias current for TR2 is obtained, via R23, from the potential divider given by R21 and R22 which is connected across the output. This bias network also gives a large degree of d.c. negative feedback, and the presence of bypass capacitor C6 ensures that there is no a.c. negative feedback. The bias current for TR3 is obtained from the collector circuit of TR2, and that for TR4 from the collector circuit of TR3.

The unbypassed resistor, R16, in the emitter circuit of TR2 increases the input impedance of the voltage amplifier. There is, as a result, a negligible loading effect on the attenuator and a fairly low value capacitor can be fitted in the C4 position.

C8 couples the output from the voltage amplifier to the full-wave bridge rectifier, D1 to D4. The rectified output from the rectifier circuit is fed to the moving-coil meter, M1, this being shunted by R25 to give the required sensitivity. The meter used by the author was a Hioki MK38. This is very similar in appearance to the Henelec and Sew 38 series meters and either of the latter may be employed instead. The value of the shunt resistor, R25, should be approximately half the internal resistance of the meter used. The internal resistance of the Hioki meter employed by the author was 1.3k Ω , causing R25 to have a value of 680 Ω . The Henelec 0-50 μ A meter has a quoted nominal internal resistance of 900 Ω , which would argue a value of around 450 Ω for R25. However, it would in practice be better for the constructor to initially employ the specified 680 Ω value for R25 and only reduce this if there appears to be excessive sensitivity when the calibration procedure is carried out.

To compensate for non-linearity in the forward resistance of the diodes in the bridge rectifier, the junction of D3 and D4 is taken to the slider of the pre-set potentiometer, VR1, in the emitter circuit of TR3. This provides a level of negative feedback, the extent of which is controlled by VR1. When the slider of VR1 is at the earthy end of its track there is no negative feedback, and when it is at the TR3 emitter end of the track there is a high level of feedback.

Apart from compensating for non-linearity in the bridge rectifier diodes, this feedback also controls the sensitivity of the instrument. During setting-up, therefore, VR1 is adjusted to set the overall sensitivity of the instrument to the correct level.

In order to ensure that the sensitivity does not alter as the battery voltage drops with age, R24 and ZD1 are included to stabilize the supply voltage. The supply voltage is at the rather low level of 3.9 volts since the transistors must be operated at low voltages and currents in order to obtain a low noise level.

R4, C3, C5, R19 and C7 are the supply decoupling components. S2 is the on-off switch. Power is obtained from a 4.5 volt torch battery, which gives very economical running.

The input coupling capacitor, C1, is shown in the Components List as having a working voltage of 100. This is the working voltage of the component used in the prototype instrument, which is only intended for use with transistor equipment. If the unit is to be used with valve equipment it is possible that direct voltages of 200 or 300 may appear across the input. In this event, the capacitor in the C1 position should have a working voltage of 300, or more if necessary. A suitable component here would be the Mullard 0.47 μ F polyester capacitor with a working voltage of 400, which is available from Home Radio under Cat. No. 2EH41.

CONSTRUCTION

The millivoltmeter is housed in an aluminium case measuring 6 in. wide, 3 in. high and $2\frac{1}{2}$ in. deep. The author employed a standard ready-made chassis having these dimensions, the chassis deck becoming the front panel of the instrument. The chassis base plate functions as a detachable rear panel.

Some retailers of this size of chassis do not also supply a suitable base plate, and it will then be necessary for the constructor to make his own from 18 s.w.g. sheet aluminium. The base plate is secured to the four corner pieces of the chassis by four self-tapping screws. A chassis and base plate of the requisite dimensions are available from Home Radio under Cat. Nos. CU221 CU233 respectively.

Fig. 2 gives drilling and cutting details for the front panel. The large cut-out for the meter can be made with a needle file. Alternatively, a series of closely spaced $\frac{1}{8}$ in. holes may be drilled just inside the perimeter of the hole, the centre piece then being broken out and the edges smoothed off with a large half-round file.

S1, S2 and the input socket may be mounted after work on the panel is completed. Two earthing solder tags are required under the nuts which secure the input socket in position. Connection is made to these later. The meter is not mounted at this stage as some further drilling is required in the case, and it might suffer damage whilst this is in progress.

The emitter follower input stage is assembled on a piece of $\frac{1}{16}$ in. Paxolin measuring $2\frac{1}{2}$ by $1\frac{1}{2}$ in. A full-size diagram illustrating this board, as seen from the components side, is given in Fig. 3. The component lead-outs pass through holes drilled in the board, connecting to other lead-outs on the underside as indicated by the broken lines. The two points marked 'A' are joined together by a short insulated lead.

When completed, the board is mounted by two $\frac{1}{2}$ in. 6BA bolts on the right-hand side of the case, next to the on-off switch, with the C3 end of the board at the top. Two short insulating spacers are employed to space the underside of the board from the inside surface of the aluminium case. If desired further protection against short-circuits to the case may be provided by primarily fitting a piece of self-adhesive plastic to the aluminium under the board area. As shown in Fig. 3, the board takes a chassis connection by way of a solder tag under one of the 6BA nuts which retains it in position.

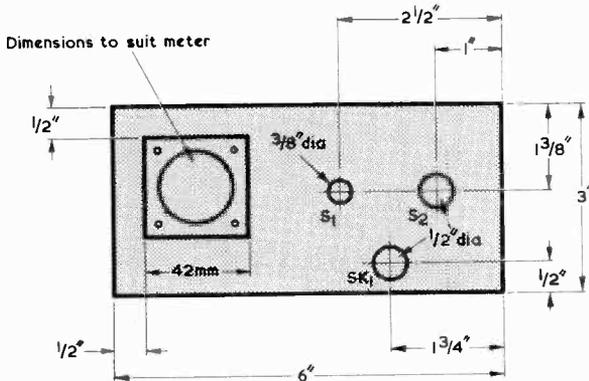


Fig. 2. Drilling and cutting details for the front panel

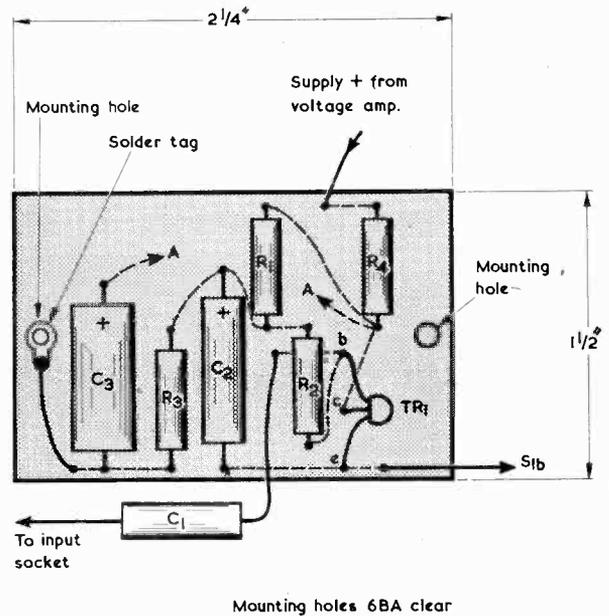


Fig. 3. The emitter follower input board. This is reproduced full-size

There are two flying leads from the board. One of these connects to the arm of attenuator switch S1(b) whilst the other takes a positive supply from the voltage amplifier board, which is not yet assembled. Capacitor C1 is external to the board, and is positioned between the board and the centre contact of the input socket.

The wiring of the attenuator switch, S1(a) (b), is rather complex and, to aid construction, a diagram is given in Fig. 4. In this diagram the two points 'A' and 'A' are joined together, as also are the two points 'B' and 'B', 'C' and 'C', 'D' and 'D', and 'E' and 'E'. The dashed line drawn across the switch indicates its two halves. Practical switches may, in some cases, have the two wiper tags positioned differently, relative to the fixed contact tags, than is shown in Fig. 4. Constructors

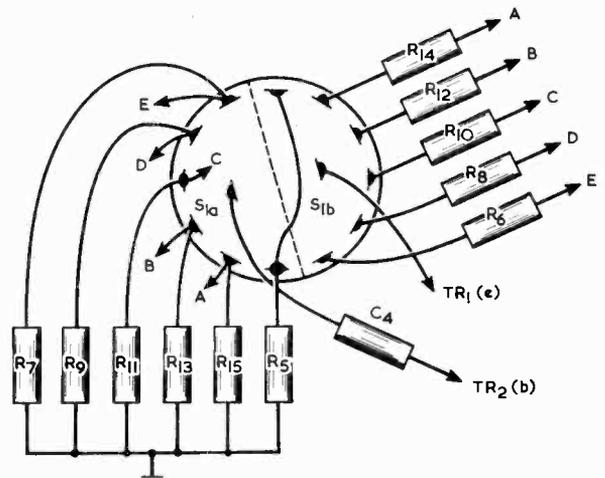
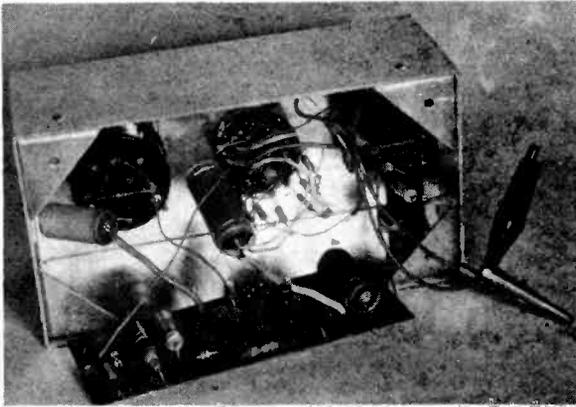


Fig. 4. Wiring the resistors in the attenuator section



The voltage amplifier board partly removed in order to allow the sensitivity potentiometer to be set up

should check this point. Mistakes can be avoided if a continuity tester is used to check the switch tag positions before these are wired. The earth connection in Fig. 4 is made at one of the tags at the input socket.

The attenuator resistors are shown spread out in Fig. 4 for clarity, but in practice they project back from the switch tags. They should not be allowed to project too far backwards, however, or their lead-out wires may touch the back of the case when this is screwed in place. The risk of short-circuits can here, again, be reduced if a piece of self-adhesive plastic is affixed to the inside surface of the case back at the appropriate position.

Capacitor C4 is included in Fig. 4 and it connects to the voltage amplifier board, which is next to be assembled. It may be found easier to connect this capacitor to the voltage amplifier board first, and then solder its other lead-out to the arm of S1(a) after the board has been mounted in position.

The voltage amplifier board, also reproduced full-size, is shown in Fig. 5, where the component side is towards the reader. The board is $\frac{1}{16}$ in. Paxolin and wiring and mounting details are the same as for the emitter follower board. The two points 'A' are joined together by an insulated lead. One flying lead from the board connects to S2, another to the junction of D3 and D4 at the meter, and a third carries the positive supply to the emitter follower input board. The zener diode should be connected into circuit with correct polarity. If it is connected the wrong way round it will function as a normal silicon diode and a voltage of about 0.6 volt only will appear across it. Capacitor C8 is external to the board, one lead-out connecting to R21 on the board and the other to the junction of D1 and D2 at the meter.

The voltage amplifier board is mounted on the top inside of the case towards the left hand side (i.e. away from the emitter follower board) with R24 at the left. As with the emitter follower board, it takes an earth connection from a solder tag under one of its securing nuts.

The meter may now be mounted. The wiring at its terminals is illustrated in Fig. 6. If the actual meter employed has its positive terminal at the left then, naturally, D1 and D2, and D3 and D4, are transposed.

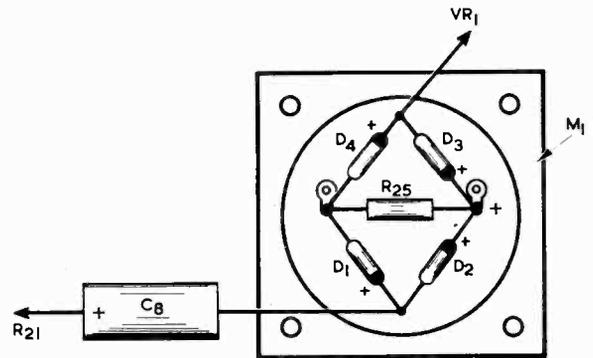


Fig. 6. The components and wiring at the meter

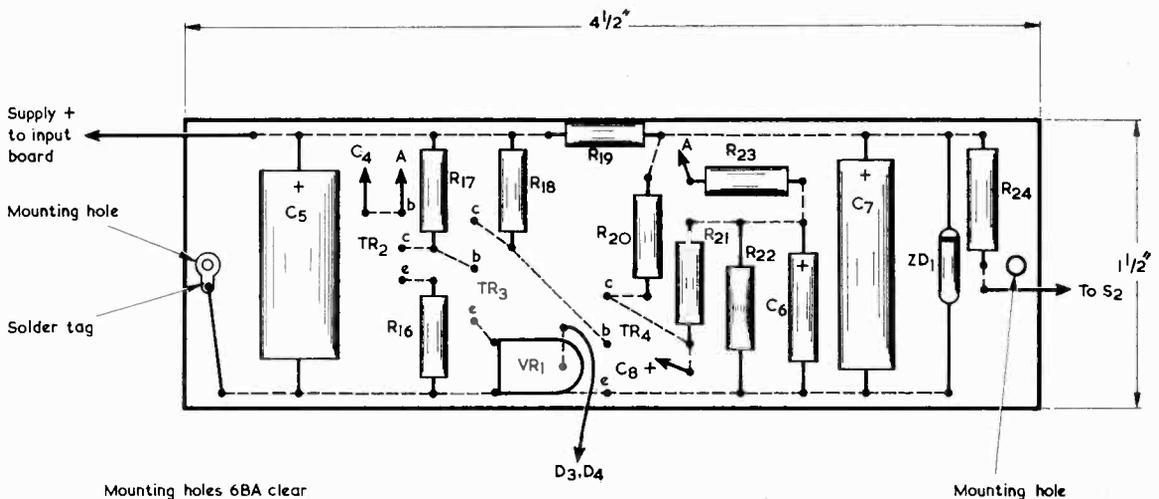
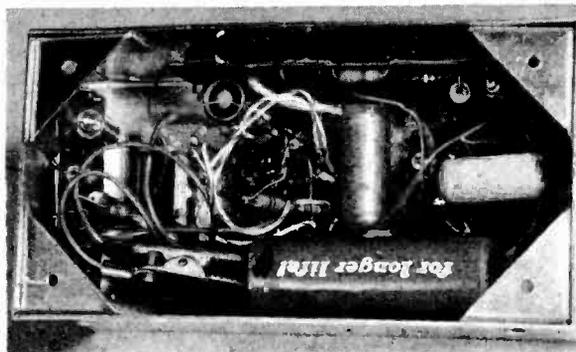


Fig. 5. The voltage amplifier board. This is also reproduced full-size

The unit with the back removed



The battery is positioned on the bottom of the case on the left hand side, opposite the voltage amplifier board. It is held in position under the bodies of the meter and S1(a) (b). If required, a pad of foam rubber or foam plastic can be glued to the bottom of the case to make the battery fit more snugly.

The battery terminals consist of two brass strips, and the battery is positioned so that these are to the right. A pair of miniature crocodile slips can be used as battery connectors. The positive clip connects to the unused tag of S2 whilst the negative clip connects to chassis at the input socket.

When construction is completed, the front panel may be marked with the ranges corresponding to the positions of S1(a) (b). The 0-500mV range is given when the knob of this switch is turned fully anti-clockwise, and the 0-1mV range is given when it is turned fully clockwise.

CALIBRATION

Once the unit has been completed, VR1 must be given the correct setting. It will be necessary to temporarily disconnect the voltage amplifier board in order to allow easy access to VR1. A short lead with a crocodile clip at each end should be used to make the connection between the board and earth (the case).

The unit is set up by applying an audio signal of known amplitude to its input socket. This may be provided by an audio signal generator and another

calibrated audio millivoltmeter, or by an audio signal generator having a calibrated output. If neither of these is available, an audio signal generator and a good quality multimeter capable of giving an accurate reading at 500mV r.m.s. a.c. will be satisfactory.

The method of calibration is to set an output voltage on the signal generator which is equal to f.s.d. on any of the millivoltmeter ranges. VR1 is then adjusted for f.s.d. on meter M1, and this adjustment will hold good for all other ranges. If a multimeter is being used to monitor the signal generator output voltage the latter should be adjusted for 500mV. The operating frequency of the generator should be around 1kHz. Should the millivoltmeter appear to have an excessively high sensitivity the value of R25 may need to be reduced, as was discussed earlier.

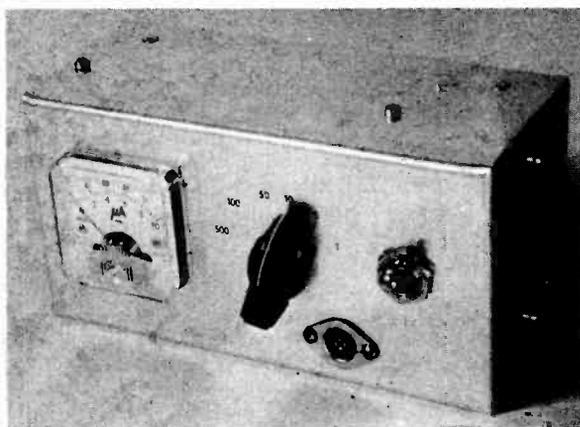
When VR1 has been set up, the voltage amplifier board may be refitted in the case, and the millivoltmeter is then ready for use.

NOTES ON OPERATION

With some units of this type, switching arrangements are provided to enable the meter to measure the battery voltage and thereby indicate when a new battery is required. Such a circuit was not considered necessary in the present instance as the current drawn from the battery is quite small when considered in terms of battery capacity, and even with quite heavy use the battery should have a long life. It should be sufficient if the battery which is a fairly inexpensive type, is replaced about every six months.

Due to the high input impedance and the sensitivity of the millivoltmeter, the test lead which connects to the input socket must be screened to avoid misleading results due to stray pick-up of mains hum and similar effects.

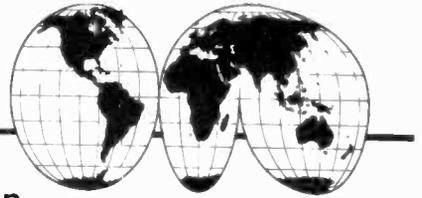
The existing 0-50 scale on the 0-50 μ A meter used in the millivoltmeter is satisfactory for the 0-5, 0-50 and 0-500mV ranges, but a little thought is required when it is used for the other ranges. If desired, the numbers 0, 2, 4, 6, 8 and 10 can be added to the meter scale at the existing 0, 10, 20, 30, 40 and 50 points, but such a modification should only be attempted by the more advanced constructor who has had working experience with moving-coil meters. The inexperienced constructor is warned to leave well alone here, as it is very easy to accidentally damage a meter beyond repair when its movement is removed from its case. ■



Another view of the millivoltmeter front panel.

SHORT WAVE NEWS

FOR DX LISTENERS



By Frank A. Baldwin

Times = GMT

Frequencies = kHz

There are many stations on the LF broadcast bands that attract some attention at times from the dyed-in-the-wool Dxr, either for the simple reason that they represent a Dx 'catch' or, being something of a mystery, identification is required and/or the QSL card is coveted and still required.

Although it is some years since the writer bothered about QSL cards, two Dx stations were sought on the dial, the first mentioned being for reasons of further information and the second simply for the fact that it was Dx only occasionally heard here in the U.K.

In the Indian Ocean, between Mauritius and Malagasy, lies Reunion Island and from the capital, St. Denis, programmes in French are radiated from 0230 through to 1845 on **4807** (62.40 metres) from a 4kW transmitter. The island has an area of 970 square miles and a sugar growing economy. Signals from ORTF Reunion were heard at 1825, Bill Haley at his best with "Rock Around the Clock". However, identification is made in English at 1844 "You are listening to ORTF Reunion Island, greetings to listeners on nearby islands and on passing ships." Then followed announcements in French, a choral rendition of "La Marseillaise" and off at 1855.

The following morning, at 0228, St. Denis could be heard when radiating the interval signal repeatedly till 0230, at which time the National Anthem (again choral version) and announcements in French were logged.

Probably of more interest to the Dxr are the Comoro Islands, lying midway between the continent of Africa and Malagasy, in the Mozambique channel. The islands have a total area of about 838 square miles and the economy is largely based on turtle fishing, vanilla, copra, sisal and plants for perfume manufacture.

ORTF Comoro Islands, Moroni, can be heard on **3331** (90.06m) here in the U.K. when signals rise to peak strength around 1845 to 1900, conditions permitting. The 4kW transmitter has a schedule from 0330 to 0430 and from 1500 to 1930. We logged them several times from 1840 onwards, listening to a dialogue in Comorian, African-type drums and, later, songs with a style reminiscent of Arabia. African style drums and chants are often a feature of the Moroni transmitter.

CURRENT SCHEDULES

● ROMANIA

Radio Bucharest has schedules in English for Europe as follows - from 1300 to 1330 on **9690** (30.95m), **11940** (25.12m) and on **15250** (19.67m). From 1930 to

2030 on **9570** (31.34m) and on **11775** (25.47m). From 2100 to 2130 on **9690** and on **11940**.

● HUNGARY

Radio Budapest, in English to Europe, as follows - from 1745 to 1800 on **6170** (48.62m), **7220** (41.55m), **9833** (30.50m), **11910** (25.18m), **15415** (19.46m), **17795** (16.85m) and on **21505** (13.95m). From 1945 to 2000 on all the foregoing channels except that **6170** is changed to **6110** (49.09m). From 2130 to 2200 on **5980** (50.16m), **7220**, **9833**, **11910**, **15415**, **17890** (16.76m) and on **21505**. From 2245 to 2300 on all the latter channels, on Tuesdays and Fridays, is presented a Dx programme (also featured from 1615 to 1630).

● JAPAN

Radio Japan has an external service, in English to Europe, as follows - from 0800 to 0830 on **17825** (16.83m) and on **21570** (13.90m); from 2030 to 2100 on **9735** (30.81m) and on **11960** (25.08m).

● GERMANY (EAST)

Radio Berlin International, The Voice of the GDR, has the following programmes in English to Europe - from 1815 to 1900 and from 2130 to 2215 on **6080** (49.34m), **6115** (49.05m), **7115** (42.16m), **7185** (41.75m), **7260** (41.32m), **7300** (41.09m) and on **9730** (30.83m). From 2245 to 2330 on the foregoing channels except **7115** and **7300**.

● PAKISTAN

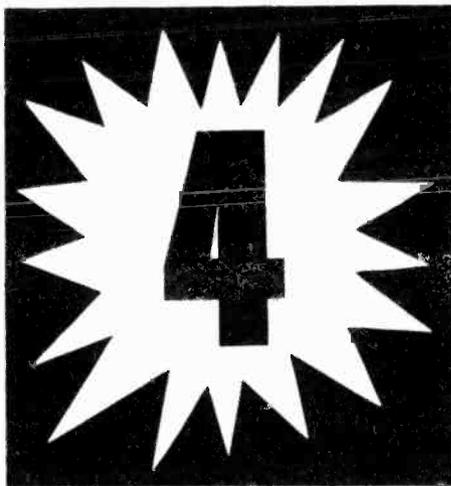
Radio Pakistan has a World Service which directs a programme to the U.K. from 1915 to 2115 - from 1915 to 2000 in Urdu, from 2000 to 2005 newscast in English, from 2005 to 2100 in Urdu (news 2030 to 2040), from 2100 to 2115 in Sylheti (news 2100 to 2108), all on **9465** (31.69m) and **11672** (25.70m).

The General Overseas Service presents the news in English, at dictation speed, from 1715 to 1730 (to West Europe) on **11935** (25.13m) and on **15325** (19.57m).

● POLAND

Radio Warsaw offers programmes in English for Europe from 0630 to 1700 on **7285** (41.18m), **9540** (31.44m), **9675** (31.00m); from 1200 to 1230 on **7285** and **9540**; from 1600 to 1630 on **6095** (49.22m), **7125** (42.10m), **7285** and **9540**; from 1830 to 1900 on **6095**, **7125**, **7285** and **9540**; from 2030 to 2100 on **7285** and **9540** and from 2230 to 2300 on **5995** (50.04m), **6135** (48.89m), **7285** and **9540**.

RADIO & ELECTRONICS CONSTRUCTOR



BAND TRANSISTOR SUPERHET

This article, the first of a 2-part series, describes a fully solid-state superhet receiver covering the medium wave band and three short wave bands extending from 180 to 9.5 metres. The concluding article will be published next month.

THIS FULLY TRANSISTORISED SUPERHET RECEIVER covers four wavebands, these consisting of the standard medium wave band and three short wave bands. The coils employed are plug-in Denco components and the approximate ranges, using the Denco coil range numbers, are: Range 2, 0.515 to 1.54MHz (580 to 194 metres); Range 3, 1.67 to 5.3MHz (180 to 57 metres); Range 4, 5 to 15MHz (60 to 20 metres); and Range 5, 10.5 to 31.5MHz (28 to 9.5 metres). There is a slight overlap between the short wave ranges, so that if the alignment of the receiver is not quite perfect with regard to frequency, there should be no gaps in short wave coverage.

Although by modern standards the receiver would probably be considered rather basic it is still fairly complex, using 13 transistors (11 without the optional S-meter circuit). These are all modern silicon types which give high gain and a low noise level. The set is mains powered.

Fig. 1 gives a block diagram for the receiver. One r.f. stage appears ahead of a single transistor mixer/oscillator stage. A 2-stage i.f. amplifier follows, this incorporating controlled regeneration. A diode detector feeds a 1.5 watt Class B audio amplifier, and also produces the a.g.c. bias voltage. The S-meter circuit is optional.

Plug-in coils are used for simplicity and to save space, since only one set of coils is in the receiver at any one time. The desired waveband is selected by merely plugging three appropriate coils into their respective holders.

MECHANICAL CONSTRUCTION

An aluminium chassis measuring 10 by 7 by 2½ in. and a 10 by 5½ in. front panel form the basis of the receiver. The chassis is commercially available (e.g. Home Radio Cat. No. CU225), but the front panel is cut out from 18 s.w.g. aluminium sheet by the constructor.

Details of the holes required in the front panel are given in Fig. 2, which also indicates the control layout.

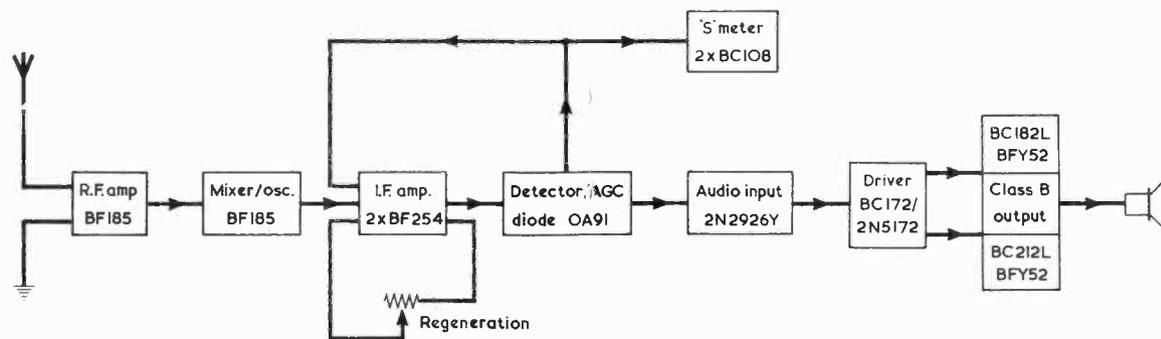


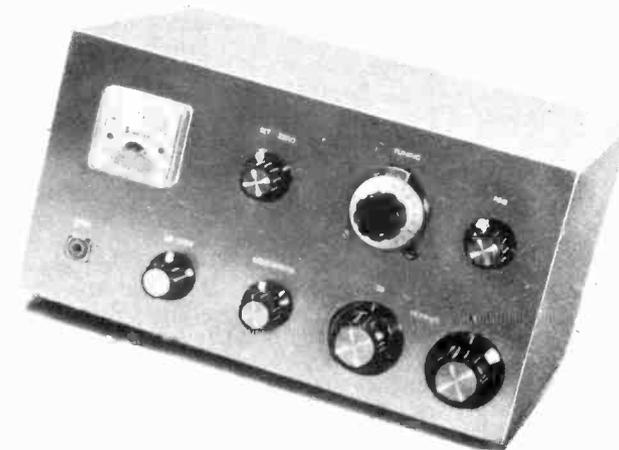
Fig. 1. Block diagram illustrating the stages of the receiver and the transistor types that are employed in them



Cover Feature

Part 1

by R. A. Penfold



The mounting holes for VR5 and meter M1 are omitted as an S-meter is not required. If, however, an S-meter circuit is to be added at a later date, its fitting will be much easier if the holes for the meter and VR5 are made at the same time as the other holes. The prototype employed a Sew S-meter type SR38P, but the S-meter in the Henelec 38 series may be employed instead, if desired. The S-meter movement is 1mA f.s.d. The four mounting holes around the large 1½ in. hole can be marked off with the aid of the meter. The 1½ in. hole can be made with an Abrafile or by cutting out a series of small holes just inside its periphery, removing the central section and then smoothing off with a large half-round file.

The horizontal dimensioning of the ½ in. hole for the 3-gang capacitor VC1, 2, 3 is not given in Fig. 2, as it is

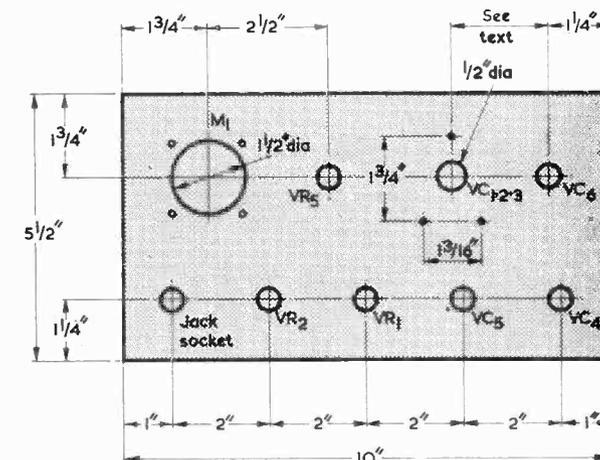


Fig. 2. Details of the front panel. The holes for M1 and VR5 may be omitted if the S-meter circuit is not to be fitted



more convenient, and the fitting is liable to be more accurate, if this hole is marked off with the aid of the capacitor itself. The manner in which this is done is described shortly.

The chassis, as seen from the top and with its sides opened out for clarity, appears in Fig. 3. There is ample space for the components and circuit boards and their exact positioning is not critical. The five control holes on the front chassis apron correspond with those on the front panel, and the panel and chassis are held together by the mounting bushes of the controls at these holes. Holes which are not directly associated with any component or board are intended for the passage of wires through the chassis and should be fitted with grommets. A grommet must also be fitted at the hole in the rear apron through which the mains lead passes.

The i.f. amplifier board is mounted on the inside of the rear apron and the a.f. amplifier board on the inside of the left apron. The mains transformer, T1, the power supply board and the board with the S-meter circuitry are fitted on the top surface of the chassis. The mounting holes are marked out with the aid of the transformer and the boards when the latter have been made.

The two holes marked 'A' are 6BA clear and take the screws which secure the two front lugs of the 3-gang tuning capacitor. The chassis and front panel should be temporarily fastened together and the capacitor positioned so that the holes in its two front lugs are directly behind the 'A' holes. The horizontal positioning of the centre of the corresponding $\frac{1}{8}$ in. hole in the front panel can then be marked off from the capacitor spindle and the hole cut out. This $\frac{1}{8}$ in. hole is for an 8:1 vernier reduction drive type T501, this being mounted by two 8BA bolts and a bolt supplied with it. The complete drive mechanism fits in front of the panel, and a metal bush passes through the panel to couple with

the capacitor spindle. To prevent the mechanism jamming and to provide smooth operation, the drive and the capacitor spindle must be accurately aligned. The capacitor may be fitted temporarily to check for this and two 6BA screws, with plain washers under the heads, passed through the front lug holes and the 'A' holes on the chassis. The holes in the capacitor lugs are larger than 6BA clear and this enables a small amount of sideways movement of the capacitor for final alignment. If necessary, spacing washers may be fitted between the lugs and the chassis to raise the capacitor body slightly. When the capacitor positioning is satisfactory the hole for the rear mounting lug may be marked on the chassis. The capacitor is then removed and this last hole is drilled out 4BA clear.

It is advisable to drill as many of the chassis holes as possible before the 3-gang capacitor is finally fitted permanently, and some constructors may prefer to make it virtually the last item. If holes are drilled with the capacitor mounted in position, its vanes should be closed to prevent their being bent or damaged.

Two screens are required and their dimensions are given in Fig. 4. The material is 18 s.w.g. aluminium. They are positioned between the coil holder holes, as indicated in Fig. 3.

The orientation of the three coil holders is shown in the layout diagram in Fig. 6. The holder for L1 has pins 1 and 9 nearest the 3-gang tuning capacitor, while those for L2 and L3 have pins 1 and 9 away from the capacitor. There is a chassis solder tag under one of the securing nuts for L3 coil holder. A 3-way tagstrip is mounted under one of the screen securing nuts.

Each of the three trimmer capacitors, TC1, TC2 and TC3, require two 6BA $\frac{1}{8}$ in. screws for mounting. A larger hole between the mounting holes allows the adjusting screw to pass through. When the capacitors

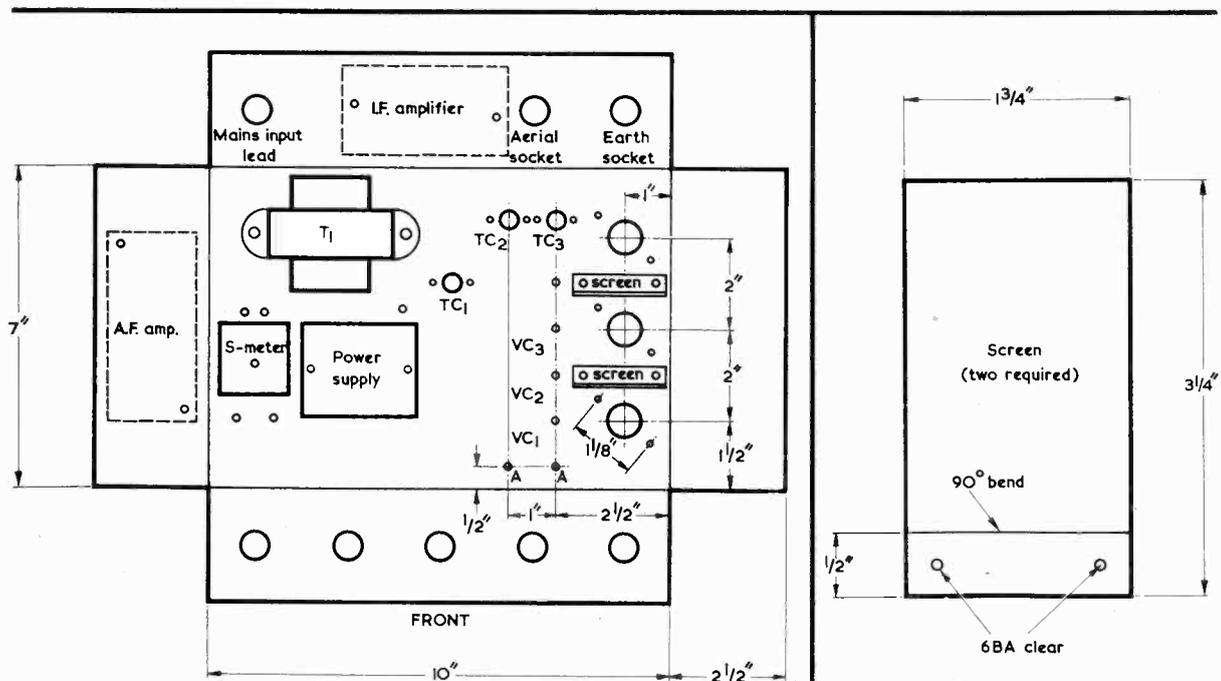


Fig. 3. Top view of the chassis. The sides are shown opened out to assist in illustrating the layout

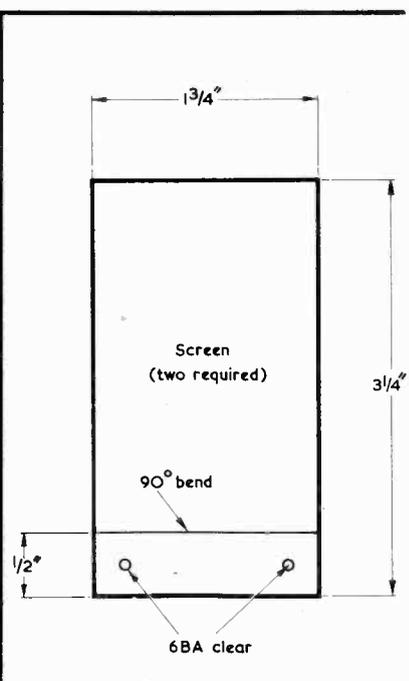


Fig. 4. Two small screens are required. These have the dimensions shown here

● CZECHOSLAVAKIA

Radio Prague at present has the following programmes in English for Europe. From 0700 to 0800 on 11855 (25.30m), 15310 (19.59m) and on 21700 (13.82m); from 1530 to 1630 on 6055 (49.54m), 9605 (31.23m), 11990 (25.02m), 15240 (19.68m), 17740 (16.91m), 17840 (16.81m).

To the U.K. and Eire from 1500 to 1530 on 6055 and 9505 (31.56m), from 1630 to 1700 and from 1900 to 1930 on 5930 (50.59m) and on 7345 (40.84m), also from 2200 to 2230 on 6055.

Radio Prague also has an "Inter Programme" for Europe in English on 6055 and 9505 from 0745 to 0800, 0845 to 0900, 0945 to 1000, 1045 to 1100 and from 1145 to 1200.

● NETHERLAND

From Hilversum, Radio Nederland radiates in English to Europe from 0930 to 1050 on 6130 (48.93m) and 7275 (41.23m); from 1400 to 1520 on 6020 (49.83m), 21480 (13.96m), Madagascar relay on 11740 (25.55m) and 17810 (16.84m); from 1830 to 1950 on 6020, 6085 (49.30m), 11730 (25.57m), 21570 (13.90m), Madagascar relay on 6020 and 9555 (31.39m), all weekdays only.

On Sundays, the "Happy Station" programmes to Europe are from 0930 to 1050 on 6020, 6130, 7275; from 1230 to 1350 on 6020, 11960 (25.08m), 15130 (19.82m) and from 2000 to 2120 on 6020, 6085 and on 9715, all from Hilversum.

● IRAN

Radio Iran has an External Service in English from 2000 to 2030 on 9022 (33.25m).

AROUND THE DIAL

In our wanderings around the dial, we have come across a few stations which may be of interest to readers, here are some of the transmissions we logged.

● PHILIPPINES

The VOA (Voice of America) transmitter with a programme in Chinese, station identification in English and sign-off at 1700 on 9555 (31.39m).

● SRI LANKA

Colombo on 11725 (25.58m) with a programme in English at 1720. Sign-off at 1730 after station identification.

Colombo has also been heard on 4902.5 (61.19m) from 2020 through to 2100 with continuous Buddhist chants, no breaks or identification. This time period is outside the normal schedule but transmissions of this type occur during full moon days and last throughout the night. The programming on this channel is in Sinhala.

● CHINA

Radio Peking on 11695 (25.65m) with Chinese songs and music during a programme in English at 2010, much praise of Chairman Mao!

AUGUST 1973

● INDIA

Near the above channel is that of 11620 (25.81m) where AIR Delhi can be heard with an English programme directed to Africa. News of African affairs at 2015 then Indian songs and music.

AIR Delhi is also to be heard on 3905 (76.82m) at 2350 or so with Indian music and announcements in English.

● LIBERIA

VOA Monrovia with a news review in English at 1915 on 15445 (19.42m), also on 17875 (16.78m).

● SOUTH KOREA

Suwon at 0658 on 15335 (19.56m) with a programme in English for the U.K. and Europe during which the address for reports was given and "we will send you a QSL card". Full identification is given at 0659 so, if a colourful card and a pennant is required, tune to 15335 at 0630, the programme in English ends at 0700.

● ECUADOR

HCJB Quito with station identification and programme in English for Europe at 1900 on 17870 (16.78m), also in parallel on 11925 (25.15m) and 15315 (19.58m).

● USSR

Radio Tashkent with the news in English at 1400 after identification on 15460 (19.40m).

● ISRAEL

Jerusalem at 1200 on 15425 (19.44m) with station identification and six 'pips' timecheck after a programme in English.

● CANADA

Sackville at 1241 on 17820 (16.83m) with news of Canadian affairs in English.

● CLANDESTINE

Peyk-e-Iran at 1500 on 11695 (25.65m) with identification and programme in Farsi (Persian).

● GHANA

Tema at 1520 on 21545 (13.92m) with news of African affairs during a programme in English.

● JORDAN

Amman at 1630 on 9560 (31.38m) with a programme of Arabic-type music, announcements in English and station identification.

● TURKEY

Ankara Police Radio at 1826 on 6340 (47.31m) with a programme of Arabic-type music and songs, sign-off at 1900.

● SOUTH VIETNAM

Saigon at 1410 on 11950 (25.10m) with songs and announcements in Vietnamese.

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BAND

TRANSISTOR

SUPERHET

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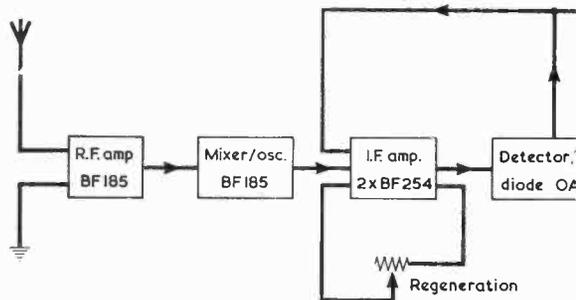


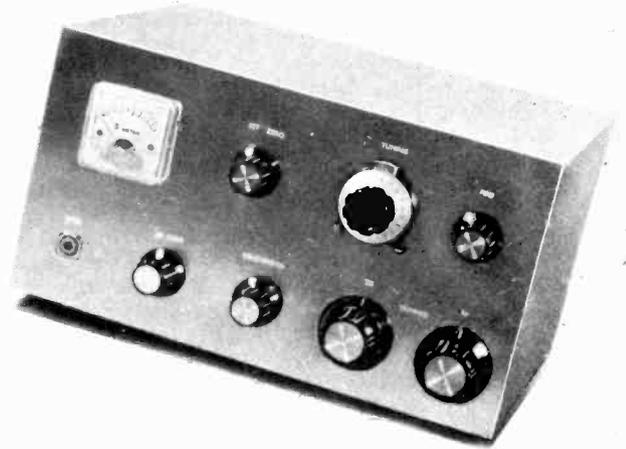
Fig. 1. Block diagram illustrating the stages of the receiver.



Cover Feature

Part 1

by R. A. Penfold



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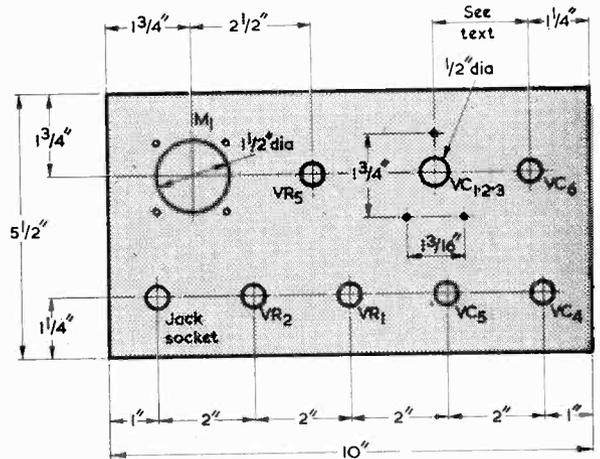
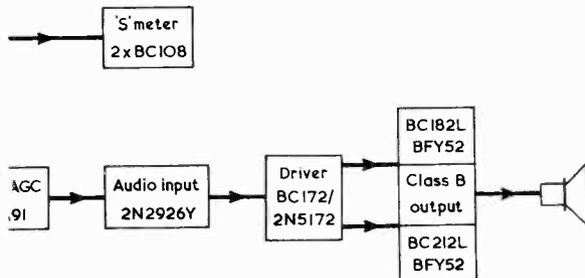


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Driver and the transistor types that are employed in them



more convenient, and the fitting is liable to be more accurate, if this hole is marked off with the aid of the capacitor itself. The manner in which this is done is described shortly.

The chassis, as seen from the top and with its sides opened out for clarity, appears in Fig. 3. There is ample space for the components and circuit boards and their exact positioning is not critical. The five control holes on the front chassis apron correspond with those on the front panel, and the panel and chassis are held together by the mounting bushes of the controls at these holes. Holes which are not directly associated with any component or board are intended for the passage of wires through the chassis and should be fitted with grommets. A grommet must also be fitted at the hole in the rear apron through which the mains lead passes.

The i.f. amplifier board is mounted on the inside of the rear apron and the a.f. amplifier board on the inside of the left apron. The mains transformer, T1, the power supply board and the board with the S-meter circuitry are fitted on the top surface of the chassis. The mounting holes are marked out with the aid of the transformer and the boards when the latter have been made.

The two holes marked 'A' are 6BA clear and take the screws which secure the two front lugs of the 3-gang tuning capacitor. The chassis and front panel should be temporarily fastened together and the capacitor positioned so that the holes in its two front lugs are directly behind the 'A' holes. The horizontal positioning of the centre of the corresponding $\frac{1}{2}$ in. hole in the front panel can then be marked off from the capacitor spindle and the hole cut out. This $\frac{1}{2}$ in. hole is for an 8:1 vernier reduction drive type T501, this being mounted by two 8BA bolts and a bolt supplied with it. The complete drive mechanism fits in front of the panel, and a metal bush passes through the panel to couple with

the capacitor spindle. To prevent the mechanism jamming and to provide smooth operation, the drive and the capacitor spindle must be accurately aligned. The capacitor may be fitted temporarily to check for this and two 6BA screws, with plain washers under the heads, passed through the front lug holes and the 'A' holes on the chassis. The holes in the capacitor lugs are larger than 6BA clear and this enables a small amount of sideways movement of the capacitor for final alignment. If necessary, spacing washers may be fitted between the lugs and the chassis to raise the capacitor body slightly. When the capacitor positioning is satisfactory the hole for the rear mounting lug may be marked on the chassis. The capacitor is then removed and this last hole is drilled out 4BA clear.

It is advisable to drill as many of the chassis holes as possible before the 3-gang capacitor is finally fitted permanently, and some constructors may prefer to make it virtually the last item. If holes are drilled with the capacitor mounted in position, its vanes should be closed to prevent their being bent or damaged.

Two screens are required and their dimensions are given in Fig. 4. The material is 18 s.w.g. aluminium. They are positioned between the coil holder holes, as indicated in Fig. 3.

The orientation of the three coil holders is shown in the layout diagram in Fig. 6. The holder for L1 has pins 1 and 9 nearest the 3-gang tuning capacitor, while those for L2 and L3 have pins 1 and 9 away from the capacitor. There is a chassis solder tag under one of the securing nuts for L3 coil holder. A 3-way tagstrip is mounted under one of the screen securing nuts.

Each of the three trimmer capacitors, TC1, TC2 and TC3, require two 6BA $\frac{1}{2}$ in. screws for mounting. A larger hole between the mounting holes allows the adjusting screw to pass through. When the capacitors

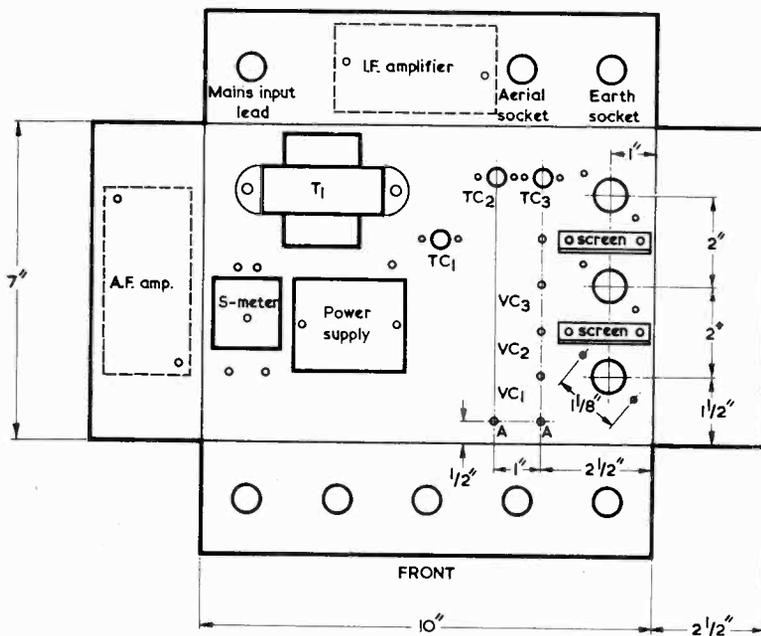


Fig. 3. Top view of the chassis. The sides are shown opened out to assist in illustrating the layout

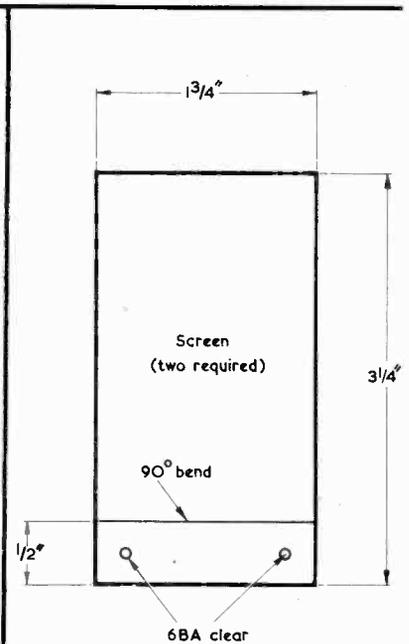


Fig. 4. Two small screens are required. These have the dimensions shown here

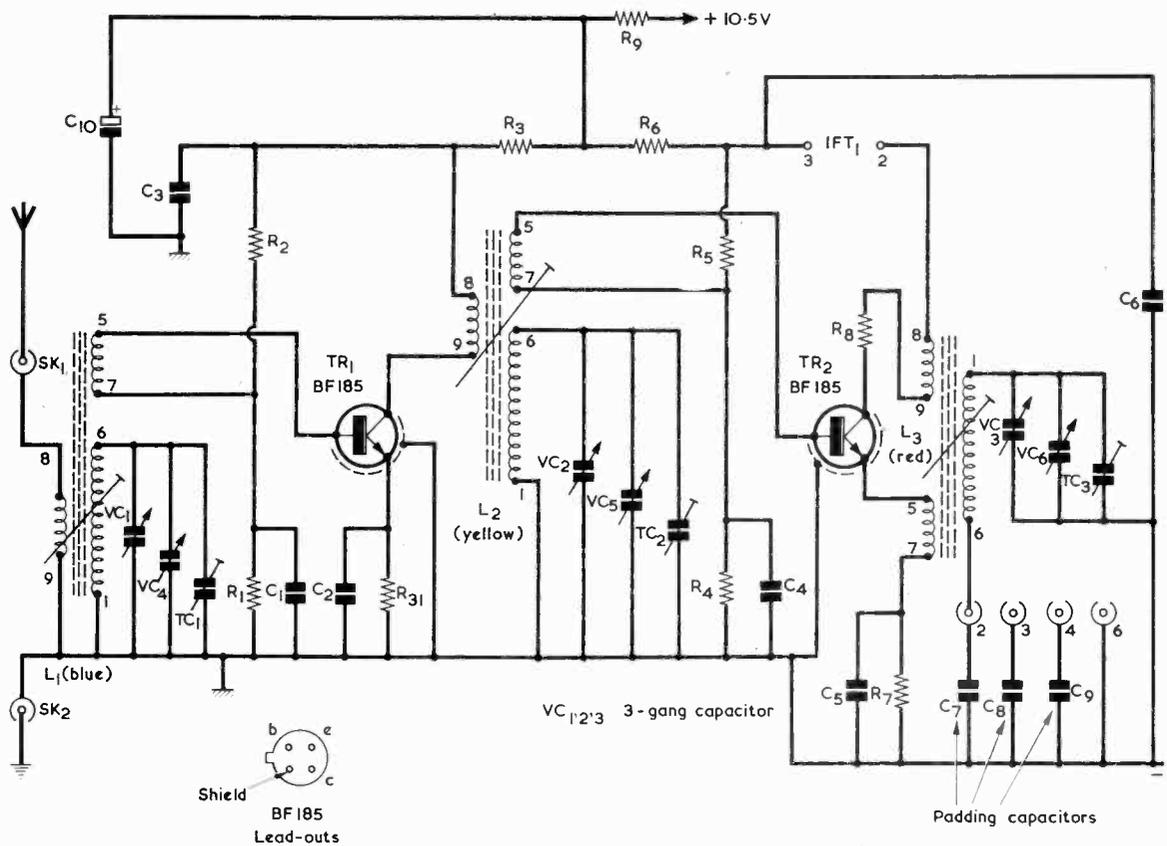


Fig. 5. The circuit of the r.f. amplifier and mixer/oscillator stages

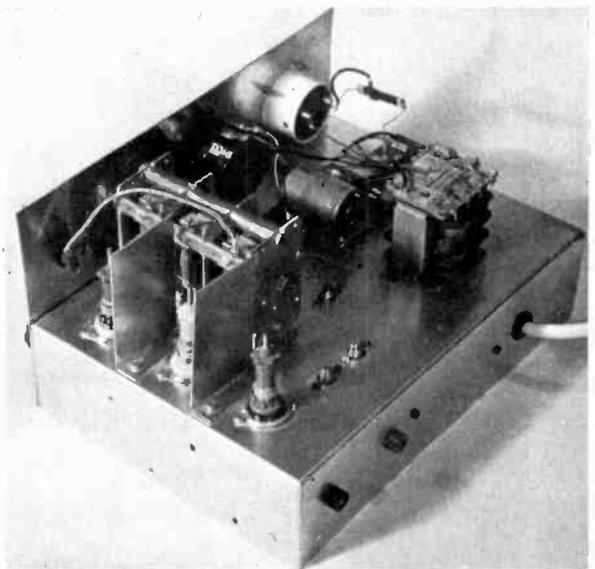
are mounted, a continuity checker should be used to ensure that there are no short-circuits between the spindles and chassis.

R.F. AND MIXER/OSCILLATOR STAGES

The circuit of the r.f. amplifier and mixer/oscillator stages is given in Fig. 5. These stages are quite conventional. The primary winding of L1 couples the aerial signal to the tuned circuit. VC1 is the main tuning capacitor for L1, and VC4 is the fine tuning control. The low impedance secondary coil couples the aerial signal to the base of TR1, and also carries the bias current from R1 and R2. C1 provides a bypass path to chassis for the lower end of L1 secondary. R31 is the emitter bias resistor for TR1, C2 being its bypass capacitor. The primary of L2 forms the collector load for TR1.

VC2 tunes the r.f. stage tuned winding, VC5 being the fine frequency control. The mixer/oscillator stage, incorporating TR2, is very similar to the r.f. stage, but the oscillator coil and its associated components have been added and the primary of the first i.f. transformer (which is mounted on the i.f. amplifier board) forms its collector load.

AUGUST 1973



A view of the receiver from the rear

COMPONENTS

Resistors

(All fixed values $\frac{1}{4}$ watt 5% unless otherwise stated)

R1	2.2k Ω	R17	680k Ω
R2	15k Ω	R18	5.6k Ω
R3	1.5k Ω	R19	1.5M Ω
R4	2.2k Ω	R20	2.2k Ω
R5	6.8k Ω	R21	6.8k Ω
R6	1.5k Ω	R22	330 Ω
R7	680 Ω	R23	820 Ω
R8	330 Ω (see text)	R24	27k Ω
R9	680 Ω	R25	10k Ω
R10	5.6k Ω	R26	4.7 Ω , $\frac{1}{2}$ watt
R11	33k Ω	R27	15k Ω
R12	4.7k Ω	R28	820 Ω
R13	680 Ω	R29	820 Ω
R14	2.2k Ω	R30	330 Ω
R15	1.5k Ω	R31	1k Ω
R16	1k Ω		
VR1	500k Ω potentiometer, linear		
VR2	5k Ω potentiometer, log, with switch S1		
VR3	1k Ω pre-set potentiometer, miniature skeleton, vertical mounting		
VR4	250 Ω pre-set potentiometer, wire-wound slider type		
VR5	10k Ω potentiometer, wire-wound		

Capacitors

C1	0.01 μ F disc ceramic
C2	0.04 or 0.05 μ F disc ceramic
C3	0.02 μ F disc ceramic
C4	0.01 μ F disc ceramic
C5	0.02 μ F disc ceramic
C6	0.02 μ F disc ceramic
C7	350pF silvered mica, 2%
C8	1,100pF silvered mica, 2%
C9	3,000pF silvered mica, 2%
C10	100 μ F electrolytic, 10 V.Wkg.
C11	2,700pF plastic foil
C12	2,700pF plastic foil
C13	100 μ F electrolytic, 10 V.Wkg.
C14	4.7pF ceramic
C15	0.022 μ F plastic foil
C16	0.01 μ F disc ceramic
C17	5 μ F electrolytic, 10 V.Wkg.
C18	5,000pF disc ceramic
C19	100 μ F electrolytic, 10 V.Wkg.
C20	5 μ F electrolytic, 10 V.Wkg.
C21	100 μ F electrolytic, 16 V.Wkg.
C22	100 μ F electrolytic, 16 V.Wkg.
C23	2,500 μ F electrolytic, 12 V.Wkg.
VC1, 2, 3	3-gang capacitor, 310pF per section, type E3 (Jackson Bros.)
VC4	50pF variable, type C804 (Jackson Bros.)
VC5	50pF variable, type C804 (Jackson Bros.)
VC6	5pF variable, type C804 (Jackson Bros.)
TC1	3.8 - 50pF trimmer, type C801 (Jackson Bros.)
TC2	3.8 - 50pF trimmer, type C801 (Jackson Bros.)
TC3	3.8 - 50pF trimmer, type C801 (Jackson Bros.)

Inductors

(L1, L2, L3 all Denco Miniature Dual Purpose, Transistor Usage).

L1	Blue coils, Ranges 2T, 3T, 4T and 5T
L2	Yellow coils, Ranges 2T, 3T, 4T and 5T
L3	Red coils, Ranges 2T, 3T, 4T and 5T
IFT1	I.F. transformer type IFT.18/465 (Denco)
IFT2	I.F. transformer type IFT.14/470 (Denco)
T1	Mains transformer, secondary 8 volts at 0.25 amp (see text)

Semiconductors

TR1	BF185
TR2	BF185
TR3	BF254
TR4	BF254
TR5	2N2926 yellow
TR6	BC172
TR7	2N5172
TR8	BC182L
TR9	BC122L
TR10	BFY52
TR11	BFY52
TR12	BC108
TR13	BC108
D1	OA91
D2-D5	IN4001

Transfilter

TF1	Transfilter type TF-01B
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Switch

S1	S.P.S.T., part of VR2
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Jack

JK1	3.5mm. jack socket
-----	--------------------

Meter

M1	S-meter, Sew type SR38P or equivalent, 1mA movement
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Sockets

3	B9A valveholders
1	Aerial socket
1	Earth socket

Speaker

8 Ω speaker, diameter 5 in. or more (see text)

Drive

Vernier drive type T501, 1.5 in. diameter (Eagle)

Miscellaneous

Chassis, aluminium, 10 × 7 × 2 $\frac{1}{2}$ in.
Panel, aluminium 18 s.w.g., 10 × 5 $\frac{1}{2}$ in.
2 clip-on heat sinks (TO-5 size)
6 knobs
1 3-way tagstrip, centre earthed
1 2-way tagstrip
Paxolin
Bolts, nuts, wire, etc.

TR2 operates in the common emitter mode for the r.f. signal fed to its base, and in the common base mode for the oscillator section of the stage since the base is effectively bypassed to chassis at oscillation frequency via the secondary of L2 and C4. Positive feedback from the collector to the emitter is provided by L3, the tuned winding of which is tuned by VC3 and fine tuner VC6. The r.f. input signal is thus modulated by the oscillator frequency, causing a mixing action to take place. One of

the frequencies appearing at the collector of TR2 is the difference frequency of 465kHz, and this is fed to the subsequent i.f. amplifier, which rejects the other frequencies produced by TR2.

VC3 is the oscillator tuning capacitor and this is ganged with VC1 and VC2. Although the tuning capacitor is fitted with an 8:1 reduction drive to make tuning easier on the cramped short wave bands, the reduction provided on Range 5 is not quite sufficient.

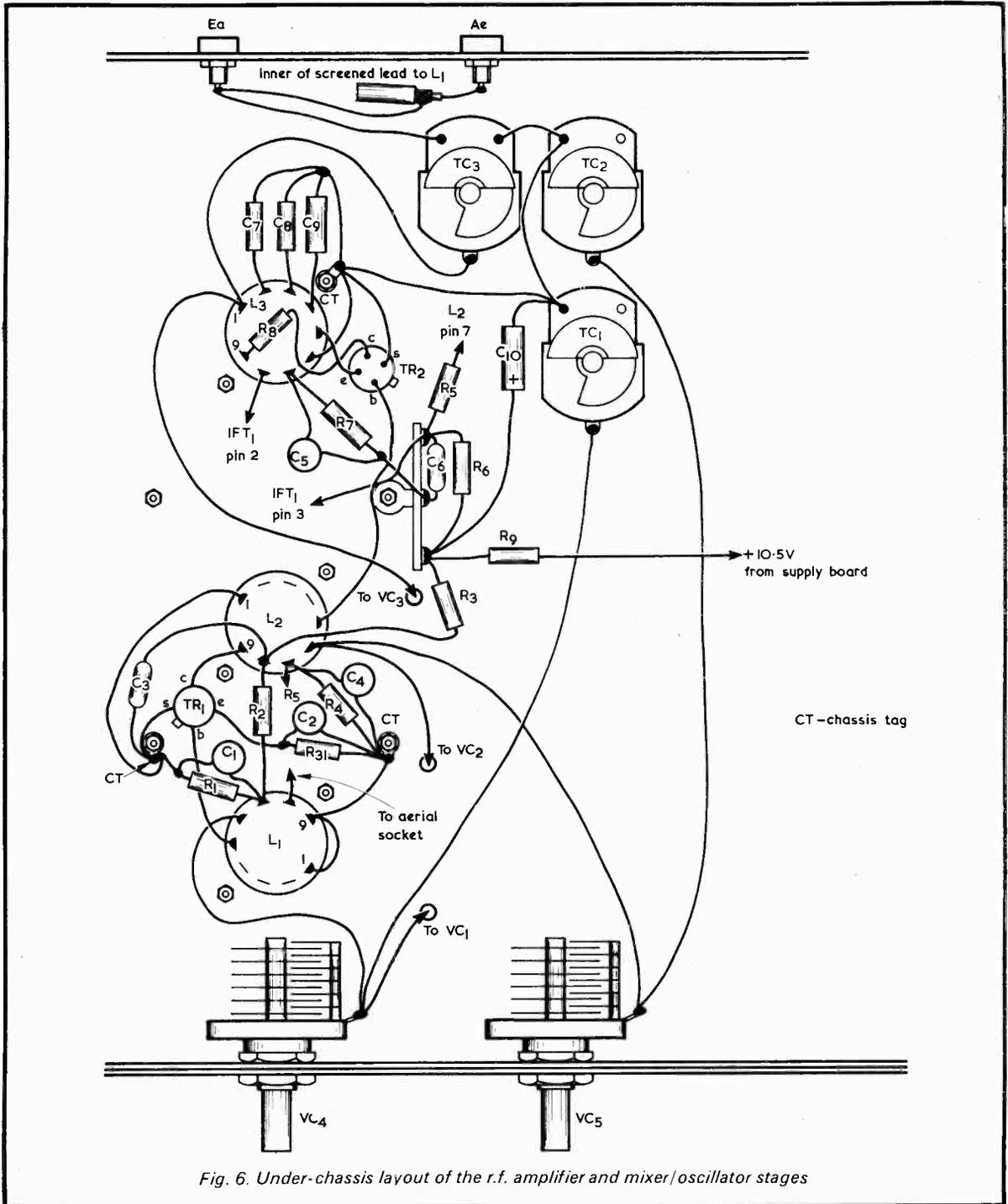


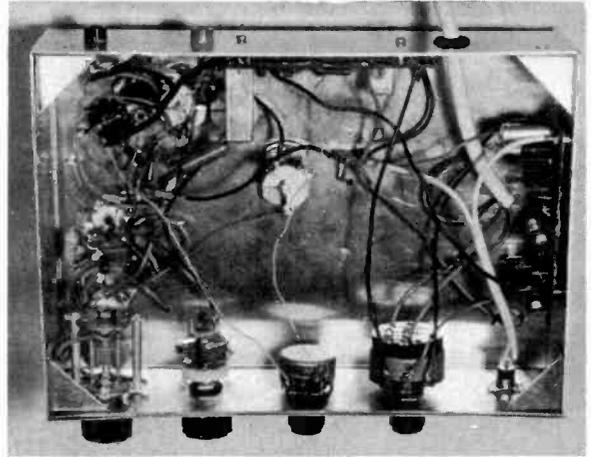
Fig. 6. Under-chassis layout of the r.f. amplifier and mixer/oscillator stages

VC6 is, in consequence, included to provide bandwidth. It gives an effective reduction ratio, in terms of its own maximum capacitance and that of VC3, of 62:1, but it only covers a very small proportion of the band.

Each oscillator coil requires a different padding capacitance. The padding capacitor for each coil connects to a different pin of the coil holder, allowing the correct one to be selected automatically when each coil is inserted. There is no padding capacitor on Range 5, the lower end of the tuned winding being connected directly to chassis. If difficulty is experienced in obtaining a 1,100pF capacitor for C8, this may consist of a 1,000pF and a 100pF capacitor in parallel.

The r.f. and mixer/oscillator wiring is shown in Fig. 6. Note that the lead from the aerial socket to pin 8 of L1 is screened, the braiding being earthed at the earth socket. The lead to pin 2 of IFT1 from pin 8 of L3 is also screened, the braiding being earthed at the adjacent chassis tag. This connection is not shown in Fig. 6. TC1, in this diagram, is in a slightly different position from that visible in the photographs. Its positioning is not critical, but the wire which couples it to VC4 should be kept reasonably clear of the wiring to VC5. The fixed vanes of VC3 connect to the fixed vanes of VC6 above the chassis.

The transistors are modern silicon types and have very short lead-out wires. These should not be shortened further, and it will be necessary to use a small length of insulated wire to extend the base lead of TR2 so that it can reach the appropriate tag on L2 coilholder.



Under the chassis. The leads to VR1 were not screened when this photograph was taken

I.F. AMPLIFIER

The i.f. amplifier circuit is shown in Fig. 7. The input signal is fed from the secondary of IFT1 to the base of TR3 via C11. TR3 has no tuned circuit other than the transfiler, TF1, in its emitter circuit. The a.g.c. bias voltage is fed to the base of this transistor via R18,

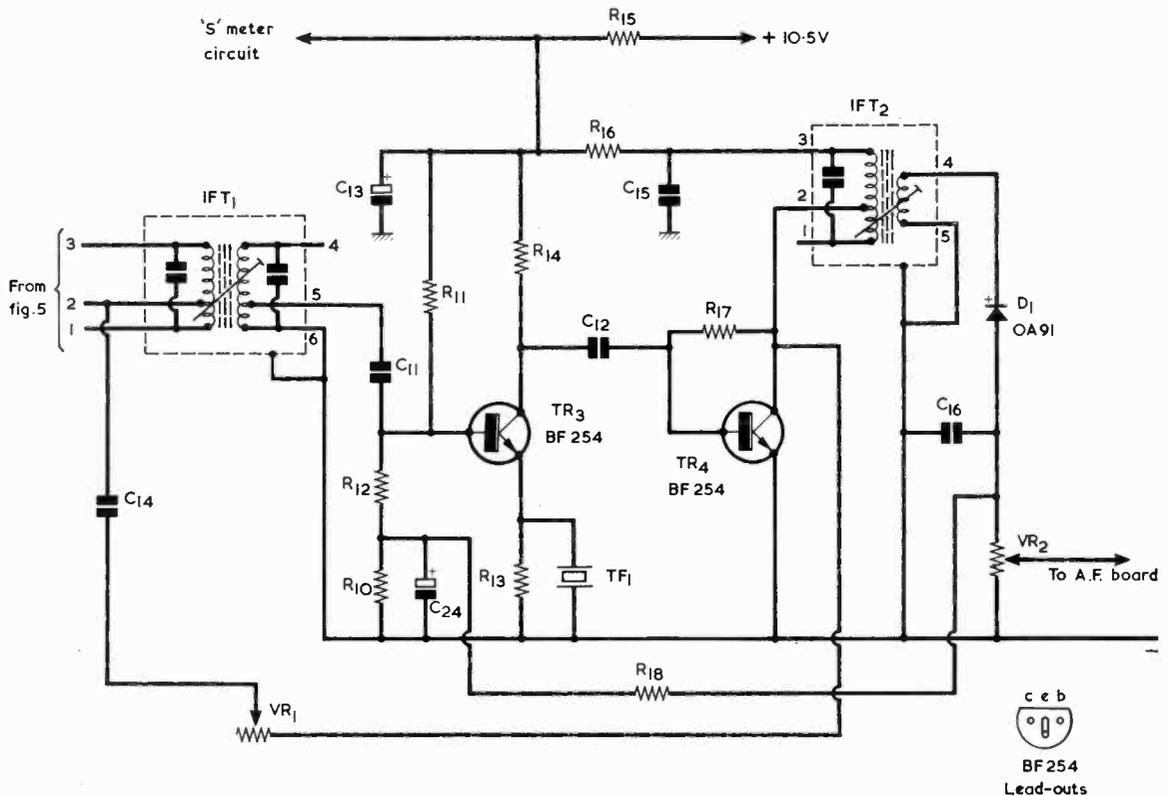


Fig. 7. The i.f. amplifier. VR1 provides controlled regeneration

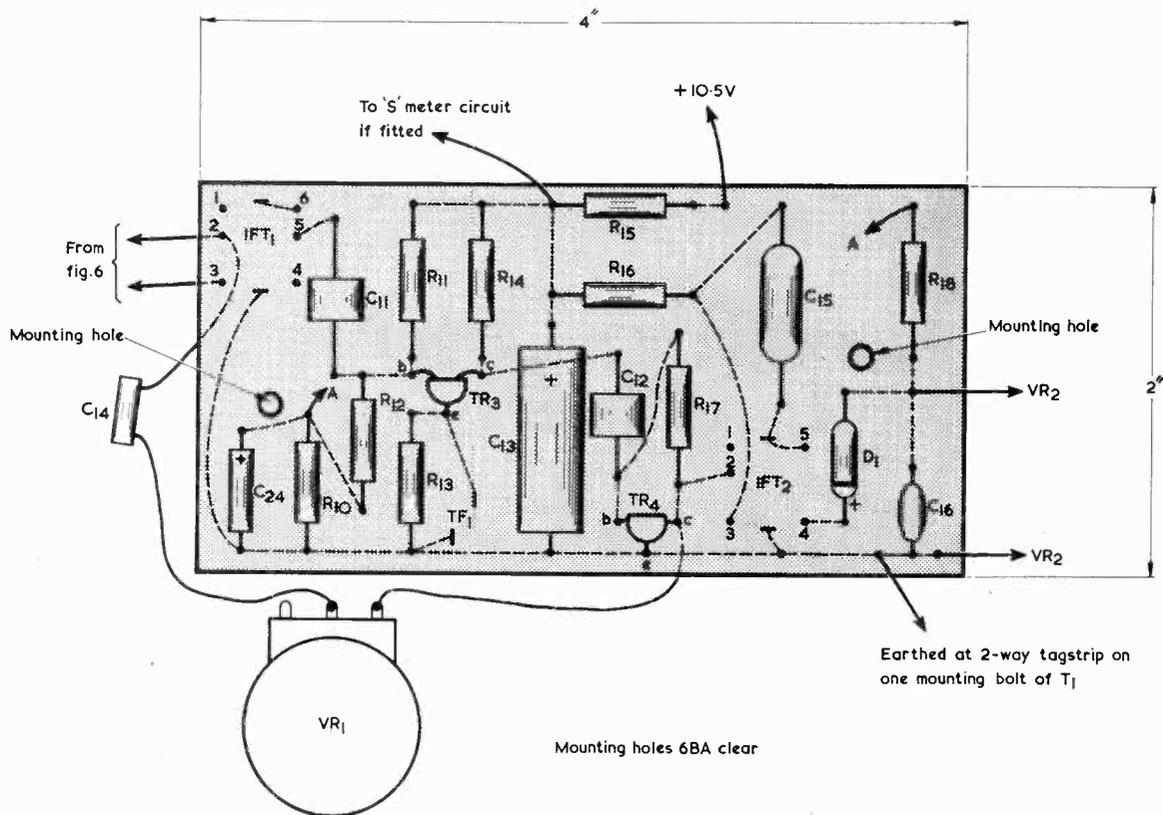


Fig. 8. The i.f. amplifier board as seen from the components side. This is reproduced full size

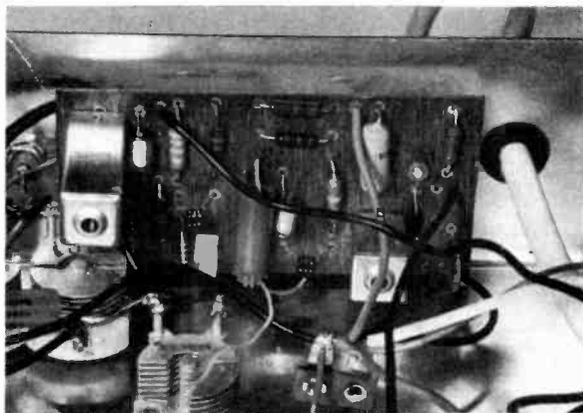
R10, R12 and C24. An aperiodic coupling, given by C12, is used between the two i.f. transistors.

TR4 is the second i.f. transistor, this being biased by R17 and having the primary of IFT2 as its collector load. The output from IFT2 is detected by diode D1, capacitor C16 removing the i.f. signal. The audio signal developed across volume control VR1 is passed to the audio amplifier board, and the d.c. component of the detected signal provides the a.g.c. voltage applied to R18.

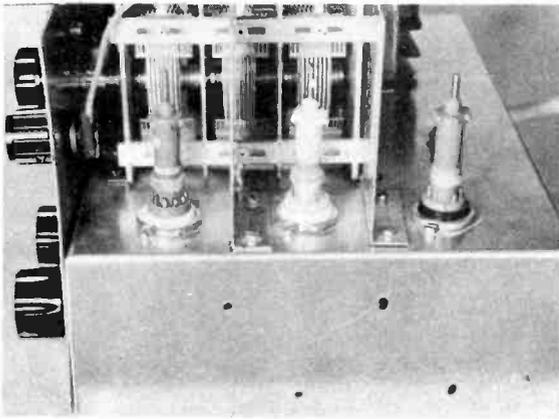
Positive feedback is given between TR4 collector and the input of the i.f. amplifier in order to offer variable selectivity. The feedback path is through VR1 and C14, with VR1 controlling the level of the feedback. Feedback increases as the resistance inserted by VR1 is reduced, with a consequent increase in selectivity. As the resistance inserted by VR1 is decreased a point is reached where the circuit commences to oscillate. C.W. signals can be resolved when the circuit is in this state.

The components required for the i.f. amplifier are standard types. The transistor type BF254 may be obtained from Electrovalue and the transfilter type TF-01B from Home Radio. The i.f. transformer specified for IFT2 is nominally a 470kHz component but it tunes satisfactorily to 465kHz, as is required here. Incidentally, the cores of the i.f. transformers should not be touched until alignment of the receiver commences. They are accurately set up at the factory and should only require a small final adjustment in the receiver.

The i.f. amplifier is assembled on a $\frac{1}{8}$ in. Paxolin panel measuring 4 by 2 in. This is illustrated full size in Fig. 8, which shows the component side of the board. Small holes are drilled in the board for component lead-out wires, these being passed through, bent over through 90°, and then connected together as indicated in the diagram by the broken lines. Where component leads are too short to reach the other leads to which they connect, extension tinned copper leads of around 22 s.w.g. may be added. The two leads marked 'A' are joined together by a short length of insulated wire.



The i.f. amplifier board in position at the rear of the chassis



Side view, with the three coils for a band plugged into their holders

The holes for the i.f. transformer pins may be marked out from the transformers themselves and from the data supplied with them. The holes for the transfilter are marked out with the aid of the component. Note that the i.f. transformer can lugs carry chassis connections by way of the can itself. Thus, the can lugs of IFT1 carry a chassis connection to pin 6 of that transformer, whilst the can lugs of IFT2 carry a chassis connection to C15.

When the board is completed it is mounted on the rear apron of the chassis with its input end nearest the coil holder for L3. It is secured by two 1 in. 6BA bolts, with $\frac{1}{4}$ in. spacers between the board and the chassis to keep the two slightly apart. If desired, additional protection against short-circuits can be given by primarily fitting a piece of self-adhesive plastic to the inside surface of the chassis edge over the area to be covered by the board. As already stated, the lead to pin 2 of IFT1 is screened. This lead should be kept reasonably short in order to keep stray capacitances low. The two leads from the board to VR1 should be screened, the braiding being earthed to chassis at the potentiometer. In Fig. 8, VR1 is shown from the rear, with the spindle pointing away from the reader.

NEXT MONTH

In the concluding article, to be published next month, details will be given of the a.f. amplifier board, the power supply, and the S-meter section. Also to be described will be the setting-up procedure.

For convenience, the full Components List accompanies the present article. Some of the items listed (including those marked 'see text') will be discussed in greater detail next month, and readers will find that the next article should clear any queries they may have about these. Resistors R27, R28, R29, R30, VR4 and VR5, together with transistors TR12 and TR13 and meter M1 are all part of the S-meter circuit. These are not required if the S-meter is to be omitted.

(To be concluded)



KIT REVIEW . . .

CLOSED CIRCUIT TV CAMERA KIT



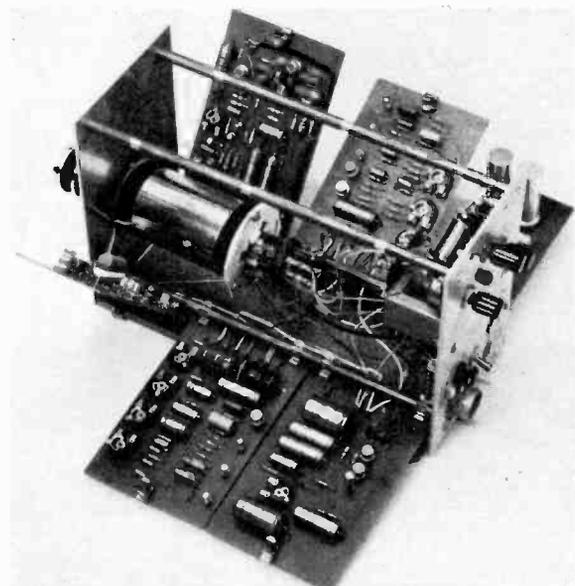
A CLOSED CIRCUIT TELEVISION CAMERA KIT OF HIGH quality and professional performance has been made available by Crofton Electronics, 15-17 Cambridge Road, Kingston-upon-Thames, Surrey, KT1 3NG. Based on a Mullard Educational Service design, the camera may be employed with a 405 or 625 line black and white monitor or, with the addition of a modulator, with a 405 or 625 line monochrome television receiver. A u.h.f. modulator kit can be obtained inside the camera case. Alternatively, Crofton Electronics can provide instructions and the necessary components to enable a black and white television receiver to function also as a monitor. The video output of the camera is compatible with most video recorders.

SERVICING FACILITIES

A particular feature of the Crofton kit is that the performance of a finished camera may, if desired, be brought up to full specification by an engineer who is based in the area in which the customer resides. However, very explicit instructions are provided with the kit and these, in most instances, should enable the completed camera to be set up correctly by the constructor. All components are guaranteed for a year, and Crofton Electronics give an unequivocal guarantee that the camera will produce satisfactory pictures.



The rear panel of the camera. This carries the input fuse and the electronic controls



Inside the camera. The printed circuit boards may be laid open whilst the camera is operating

A clearly detailed and well produced instruction manual is provided and this gives all the information required for assembly. Provided in this manual also are a full circuit diagram, a test card, and the waveforms to be expected at different circuit points. Whilst an oscilloscope is helpful for setting up the camera it is not essential, and the presence of scanning waveforms can be detected with as simple an item of equipment as a headphone. The various parts of the kit can be purchased in modules. If desired, each module may be bought, assembled, tested and roughly aligned before purchasing the next. Yet another alternative consists of having the kit fully built by Crofton Electronics. The complete assembly incorporates 23 transistors and a vidicon camera tube.

The kit does not require any special skills in its assembly, and it will be of particular value in the training of students. The Mullard design on which the camera is based is entitled 'A Simple Closed Circuit Television Camera' and is the latest in the series 'Educational Projects in Electronics'. This booklet costs 60p and requests for copies should be sent with cash to the Mullard Educational Service, Mullard Ltd., New Road, Mitcham, Surrey, CR4 4XY. It should be noted that components for the camera are not available from Mullard Ltd.; these are provided by Crofton Electronics.

MODIFYING THE GC1U RECEIVER. Part 2.

by P. Cairns, R. Tech. Eng., M.I.P.R.E., G3ISP

In this concluding article constructional details are given of the added product detector.

THE ARTICLE PUBLISHED IN LAST MONTH'S ISSUE described a new stabilized mains power supply unit for the popular GC-1U receiver and covered the replacement of the existing fixed resistors with high stability types. The additional product detector circuit was next discussed and a Components List showing the new parts required was published. In the constructional details for fitting the product detector which now follow it will be necessary to refer to Fig. 5, which appeared in the previous issue.

CONSTRUCTION

The construction of the product detector is quite straightforward, the complete circuit, except for C8, being mounted on a piece of Veroboard. Component layout, dimensions and bracket mounting details for the Veroboard are given in Fig. 7. No extra controls or holes are required on the front panel and no drilling of the chassis is necessary.

The completed Veroboard is mounted vertically by means of two U-shaped brackets fitted to the inside edge of the receiver i.f.-a.f. printed board. The existing 6BA screws which fasten this board to the chassis are used. An aluminium bracket along the bottom of the Veroboard fixes the board to the two mounting brackets. Full details are given in Fig. 7. As will next be described, S1 and R47, the latter with its new value of $1k\Omega$, are mounted. The existing b.f.o. frequency control is removed, the wiring on the control and rear switch being first unsoldered.

To make room on the front panel for S1 the aerial trim control is removed. This means that the aerial trimming capacitor has now to be pre-set for optimum matching with the particular aerial used. This control was of little practical use, particularly if an aerial coupling unit was used and, due to its position in the circuit, caused a noticeable frequency shift on the h.f.

bands. The removal of its variable function is, in some respects, something of an advantage. The $\frac{1}{4}$ in. fibre spindle is removed from the capacitor coupler and the brass bush set in the front panel is unscrewed. S1 is mounted in place of this bush.

The 4.7pF b.f.o. coupling capacitor (C60) is removed from under the receiver i.f. printed board. R54, the



The new function switch, S1, is fitted in place of the panel bush for the aerial trim control

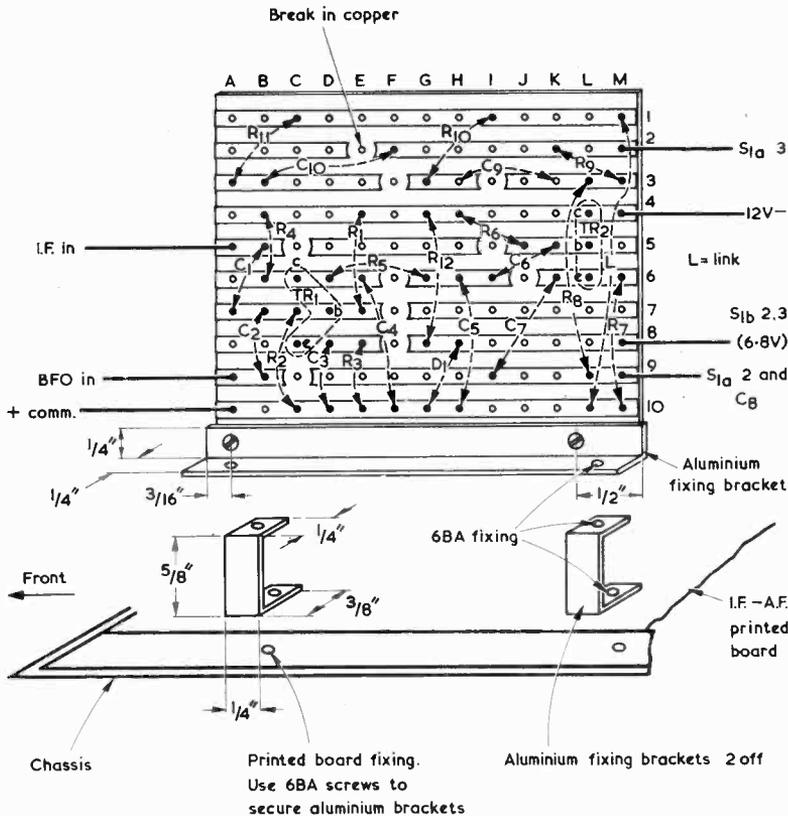
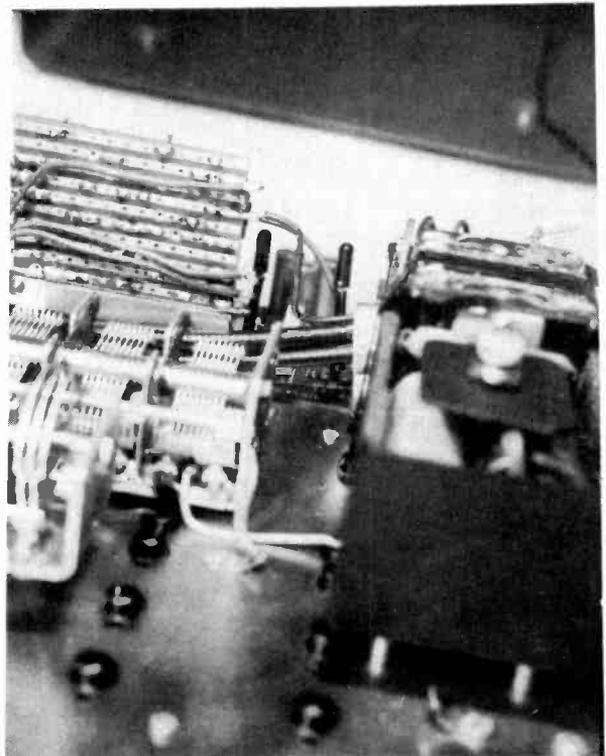


Fig. 7. The copper side of the Veroboard on which the product detector components are fitted. Also shown are fixing details and the mounting brackets required

1.5k Ω resistor which is wired between the b.f.o. potentiometer R47 and an earth tag on the rear of the front panel is replaced by the new 2.2k Ω resistor. The new 1k Ω potentiometer which replaces the old 2k Ω R47 is mounted at the same time. Next, the output from I.F.T.2 (a.m. detector output) which passes to one end of the a.f. volume control is disconnected from this control and wired to the appropriate point on S1(a). The output from S1(a) arm is then wired to this same point on the a.f. volume control. This completes the receiver modifications, leaving only the few interconnection wires to be put in.

The i.f. input to the product detector is taken from the emitter tag of transistor X6 on the top of the i.f. printed board. The b.f.o. input to the detector is taken from the b.f.o. output tag, one end of which can be found on top of the i.f. printed board adjacent to the top-right hand corner of the b.f.o. coil (L16), as seen looking from the front panel. It is next to the 62k Ω resistor (R55) and labelled 'B.F.O.3'. These leads should be short, direct and kept away from other components.

The 12 volt supply is now wired to the Veroboard and the appropriate outputs, including the 6.8 volt b.f.o. supply, taken from this board to S1. The replacement R47 b.f.o. control is also wired in. The wire carrying the old b.f.o. d.c. supply which was disconnected from the old b.f.o. switch is cut short and taped up. The other supply lead from the original switch is extended so as to reach the appropriate point on S1(b). C8 is wired between S1(a) and the adjacent earth tag on the rear of the front panel. (See Figs. 5 and 7.) This completes the additional wiring.



A view of the copper side of the Veroboard panel. Also visible to the right is the new power supply fitted in the battery box

SETTING UP

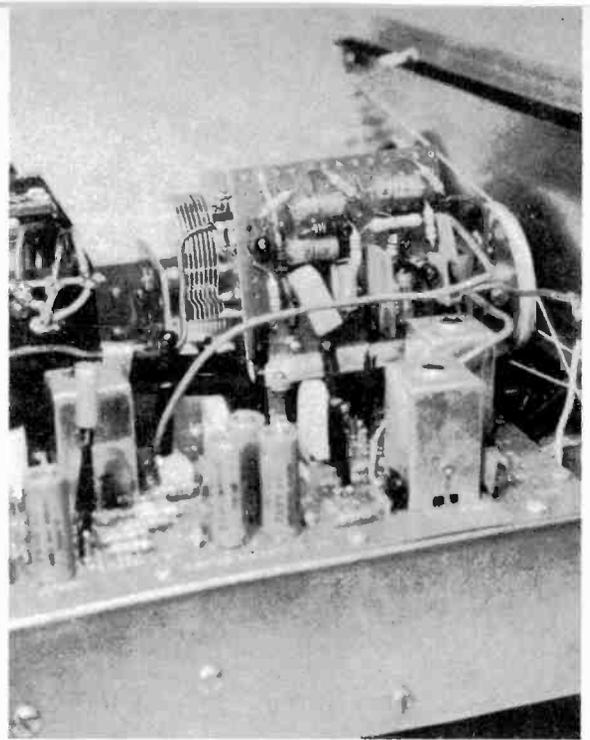
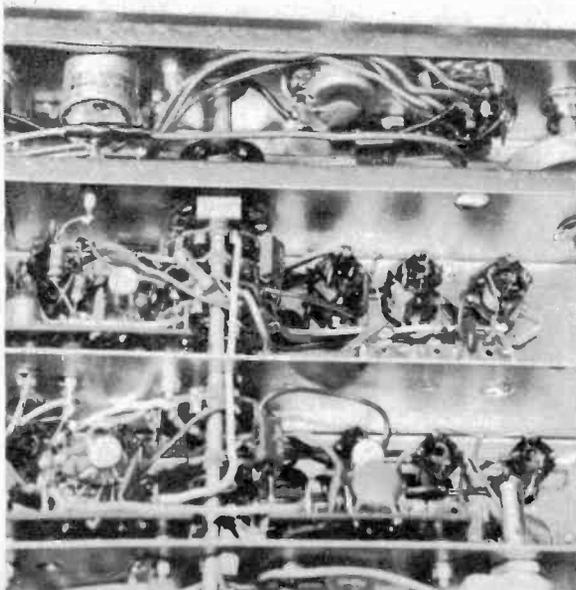
The receiver can now be set up, no instruments being required for this process. Switch on and set S1(a)(b) for a.m., tune in a known station and note that the signal level has not changed compared with pre-modification level. Next switch to the h.f. band most used and adjust the aerial trimmer (which is now pre-set) for maximum output. This should be done using either the internal whip aerial or the external aerial normally employed with the receiver. The tuning indicator meter can be used for signal strength comparisons during these tests.

Now, using the tuning meter, tune in a station with a strong carrier for maximum output. It is essential that this signal be tuned in exactly and not slightly off to one side or the other. Next, set S1(a)(b) to position 2 and reduce the r.f. gain. Set the b.f.o. control (R47) to mid-point and adjust the core in the b.f.o. coil (L16) for zero-beat. This should be done with great care, removing the trimming tool at intervals to ensure there is no shift in frequency due to external stray capacitances. This coil should be tuned for exact zero-beat with R47 in the centre position.

The receiver can now be tried out on some s.s.b. signals. These should be tuned in with R47 in its centre position, a.v.c. off, a.f. gain at or near maximum and r.f. gain adjusted to suit signal strength. With the required station exactly in tune on the main and bandsread controls, the final tuning is resolved by slight adjustment of the b.f.o. control. In practice the writer found that in many cases the b.f.o. control could be off-set and the various stations resolved simply by tuning across the band by means of the bandsread control. Under these conditions the r.f. gain control can be adjusted to suit each signal without appreciable effect on the received signal intelligibility.

The circuit allows for some adjustment if the performance is not quite up to expectations. The overall apparent sensitivity can be increased for s.s.b. and c.w. signals by reducing the value of C8. Values as low as 0.1 μ F can be tried if required. Due to differences which could occur between various receivers, it might be found necessary to increase or decrease the amount of

The fibre spindle for the aerial trim capacitor is removed, and it now becomes a pre-set component



The added product detector Veroboard in position. The relatively bulky rectangular capacitor C7, can be seen near the left edge of the board

i.f. signal input. This is done by increasing or decreasing the value of C1. This capacitor can be safely increased in value to 200pF without having any noticeable loading effect on the i.f. circuit. Increasing C1 too much will, of course, mean that the b.f.o. injection will be insufficient. If it is thought necessary to experiment a little with C1, it should only be increased sufficiently to allow a strong s.s.b. signal to be resolved with the existing b.f.o. injection. Generally speaking, however, C1 should require no adjustment.

OTHER RECEIVERS

As was mentioned at the beginning of the previous article, the product detector circuit of Fig. 5 offers scope for experiment to the interested constructor having another communications receiver which falls into the 'older' category. The circuit should be added to many receivers where s.s.b. facilities are required, though the i.f. and b.f.o. injection levels would have to be adjusted to suit individual sets. Changes in the values of C1 and C2 would effect this. A suitable matching point into the i.f. circuit would also be required, though this should not necessarily prove an insurmountable problem. The other point which is also of importance with any such conversion is frequency stability of both local oscillator and b.f.o. This can often be largely catered for by the use of zener diodes in transistor receivers and neon stabilizers in valve receivers. Any components in these circuits which are temperature sensitive should also be replaced if at all possible.

In conclusion, the modifications described offer a relatively cheap and simple method of up-dating a basically good receiver at a modest cost. The overall improvement in performance is, in the opinion of the writer and several others who have tried the modified GC-1U under operating conditions, well worth the time and expense involved. ■

Integrated Circuit Timebase

by A. Foord

Our contributor commences by discussing general timebase principles, then proceeds to a description of a comprehensive timebase which takes full advantage of modern integrated circuits.

A LINEAR TIMEBASE GENERATOR PROVIDES AN OUTPUT waveform a proportion of which has a linear variation of voltage or current with time. One major application of such a waveform is in a cathode ray oscilloscope. Here the timebase waveform is applied to the X deflecting plates so that the electron beam is swept horizontally across the screen with time. Since this waveform is used to sweep the electron beam it is sometimes called a sweep voltage in this application. Timebase circuits are also used in radar and television indicators, in precise time measurements, in time modulation, and other instrumentation applications.

TIMEBASE WAVEFORM

A typical timebase voltage is shown in Fig. 1. The voltage starts from an initial value, increases linearly with time to a maximum value, and returns rapidly to its minimum value. The time required for the return to the initial value is called the 'return', 'restoration' or 'flyback' time. Normally, the shape of the waveform during the return time is not important, although its duration must usually be short compared with the time taken for the linear sweep part of the waveform.

Timebase waveforms can be generated by vacuum or gas filled valves, transistors, or integrated circuits, depending on convenience, the application, and the required speed.

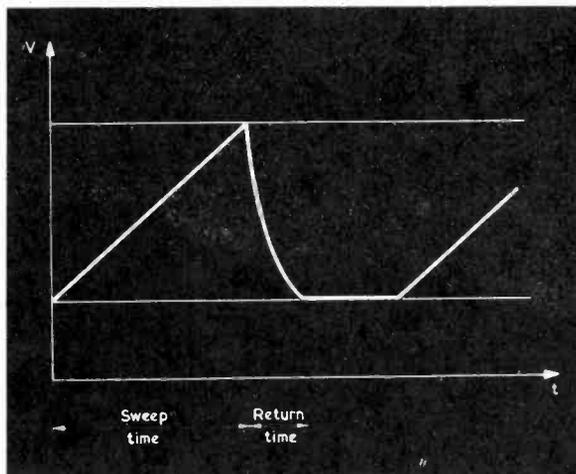


Fig. 1. A typical sweep voltage, showing sweep and return times

SWEEP VOLTAGE GENERATION

There are several methods of generating a sweep voltage of good linearity. Some of these will now be discussed.

Exponential charging. Here, a capacitor is charged through a resistor to a voltage level which is small in comparison with the supply voltage. See Figs. 2 (a) and (b). When the switch in Fig. 2 (a) is opened the capacitor

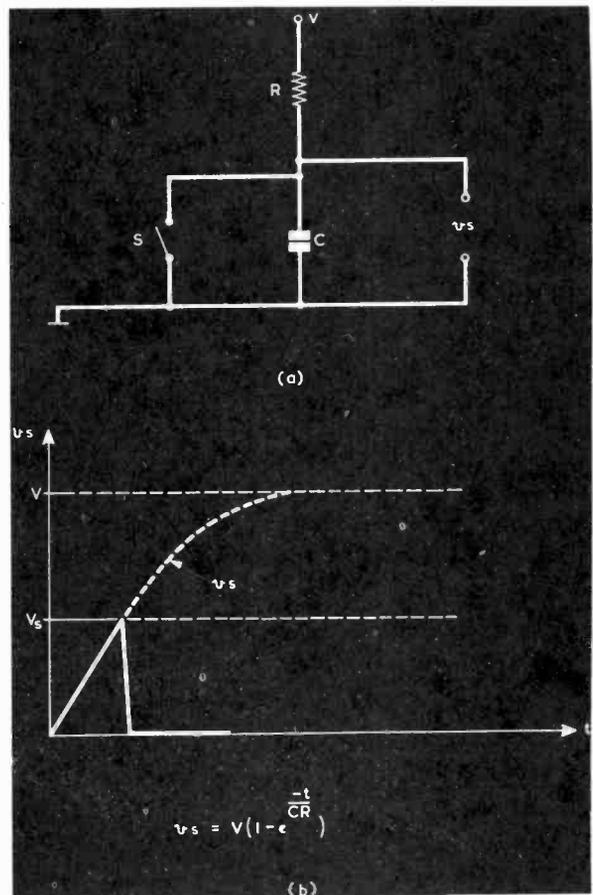


Fig. 2 (a). Charging a capacitor from a fixed voltage via a resistor
(b). The voltage appearing across the capacitor

charges up according to an exponential law, and as the required threshold is reached the switch is closed to discharge the capacitor. In order that the part of the sweep used is reasonably linear, the supply voltage V must be much greater than V_s . For example a 20V sweep can be obtained with a sweep speed error of less than 10% by using a supply voltage of at least 200V. As a result, this simple circuit is only useful in applications where a low sweep voltage is needed. In practice, the switch in Fig. 2 (a) would be replaced by an electronic device which discharges the capacitor.

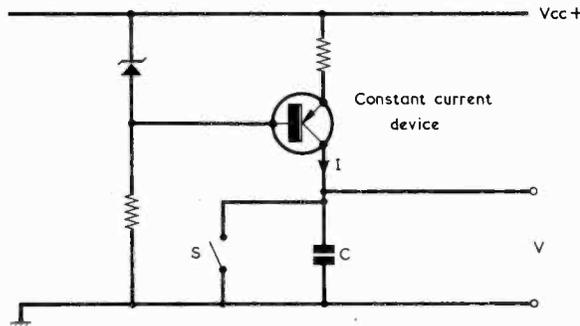
Constant current charging. In this instance, a capacitor is charged linearly from a constant current source. Except for very small values of collector to base voltage, the collector current of a transistor connected in the common base mode is nearly constant when the emitter current is held fixed. This characteristic is used in Fig. 3 (a) to generate a constant current supply to charge a capacitor. The voltage across the capacitor is shown in Fig. 3 (b). When the capacitor, C , is charged by a constant current I , then the voltage across the capacitor is given by:

$$V = \frac{It}{C}$$

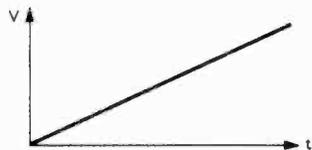
where V is in volts, I is in amps, t is in seconds and C is in farads.

It follows that the rate of change of voltage against time is given by:

$$\text{Sweep speed} = \frac{I}{C} \text{ volts/sec.}$$



(a)



$$\text{Sweep speed} = \frac{I}{C} \text{ volts/sec}$$

(b)

Fig. 3 (a). Constant current charging of a capacitor

(b). The linear voltage change developed across the capacitor

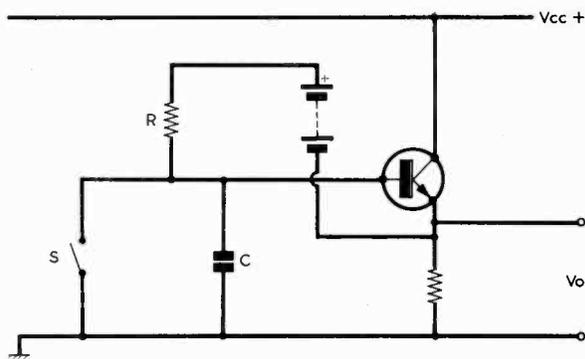


Fig. 4. The basic bootstrap sweep circuit

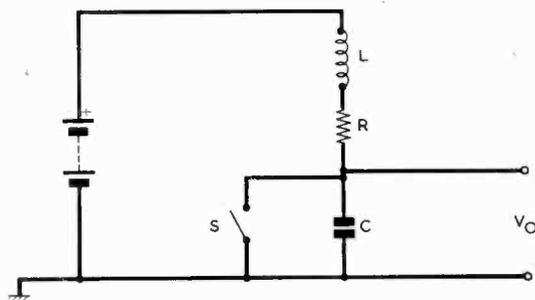


Fig. 5. An LRC sweep circuit

The constant current charging circuit enables a linear sweep to be obtained if only a low voltage supply is available. It suffers from the disadvantage that the circuit cannot be loaded appreciably without seriously deteriorating the linearity, and the sweep voltage must be applied to the load through an emitter follower or a similar type of buffer amplifier.

The Phantastron Circuit. This is a valve circuit based on the Miller Integrator and only requires an input pulse to trigger it, and not an external step or gating waveform. It is limited to the generation of linear sweeps of the order of 10μS or longer because of the effect of stray capacitance to earth at the various valve electrodes.

The Bootstrap Circuit. With this circuit a constant current is approximated by maintaining a nearly constant voltage across a resistor in series with the charging capacitor. This is achieved by using a unity gain amplifier in a feedback configuration, as in Fig. 4.

An Inductor Circuit. An LRC series circuit can be used to give more linear capacitor charging than is possible without the inductor. See Fig. 5. This circuit improves the linearity of a simple RC sweep and also allows a sweep to be obtained whose amplitude is larger than the supply voltage because of the oscillatory nature of the circuit.

In practical applications the most useful circuits are the constant current Miller one and the final one to be considered, the Miller Integrator.

The Miller Integrator. A basic Miller Integrator circuit is shown in Fig. 6 (a), and it consists of a high gain inverting amplifier with overall negative feedback from output to input via a timing capacitor. The input

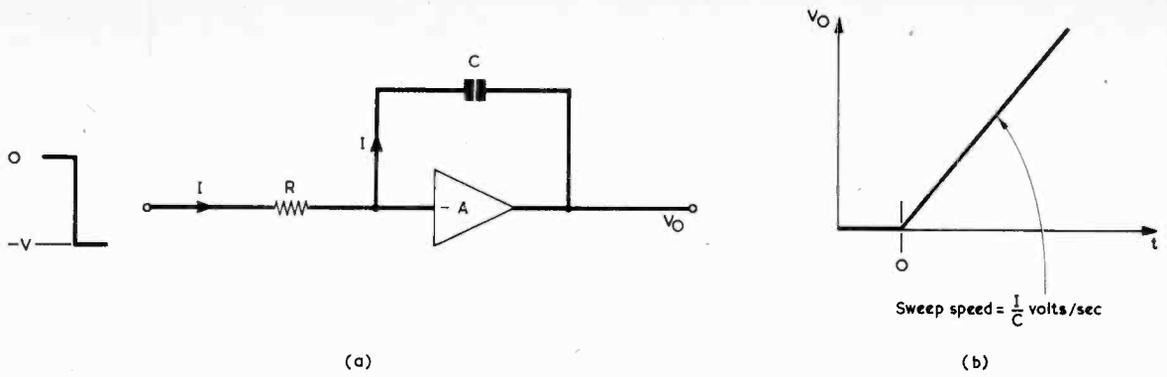


Fig. 6 (a). A Miller Integrator circuit incorporating an op-amp
 (b). The circuit gives a linear sweep, as indicated here

point remains at a 'virtual earth' with current summing at this point, where any input current I is forced to charge the capacitor by feedback action. The output waveform of the Miller integrator is actually the initial part of a large exponential. The higher the amplifier gain the better the linearity. Since the circuit has a low output impedance a buffer amplifier is not required (unlike the constant current supply fed into a capacitor).

If we assume that initially the voltage across the capacitor is zero, then when an input step is applied a constant current flows into the capacitor and the output rises at a rate determined by the current and the capacitor. As indicated in Fig. 6 (b):

$$\text{sweep speed} = \frac{I}{C} \text{ volts/sec.}$$

In order to produce a complete timebase circuit we must determine when the output of the integrator has reached the required level and terminate the sweep and hold the capacitor in a discharged state. This can be achieved with a circuit of the type shown in Fig. 7 (a).

Initially, in this diagram, V_2 is high and the transistor switch is closed, maintaining an effective short-circuit across the capacitor. When the bistable is triggered by the input pulse the transistor switch is opened, and the output of the integrator will run up at a rate depending on the time constant and the negative supply. When the ramp reaches the comparator reference level, shown here as 5 volts, a reset pulse is generated which resets the bistable and closes the transistor switch. This rapidly discharges the capacitor and then maintains the amplifier output at earth potential until the circuit is retriggered by another start pulse.

The ramp slope is equal to

$$\frac{I}{C} \text{ volts/sec, or}$$

$$\frac{V}{CR} \text{ volts/sec.}$$

The duration of the ramp depends on its slope and the chosen reference level voltage, and is equal to

$$\frac{\text{ramp slope}}{VR} = \frac{V}{I} \times CR \text{ secs,}$$

where VR is the reference voltage.

Fig. 7 (b) illustrates the waveforms appearing in the circuit.

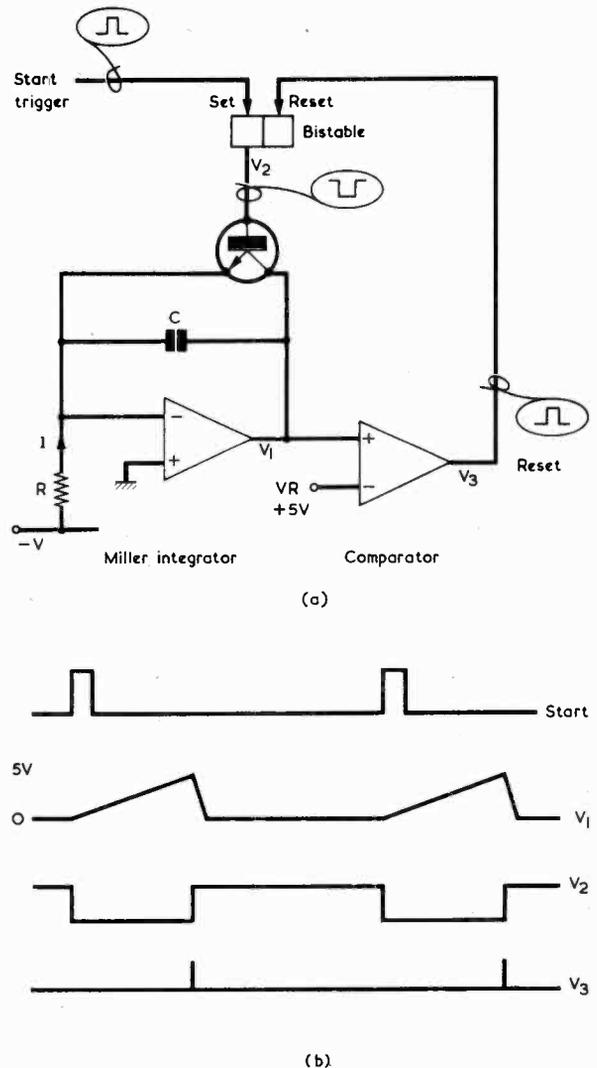
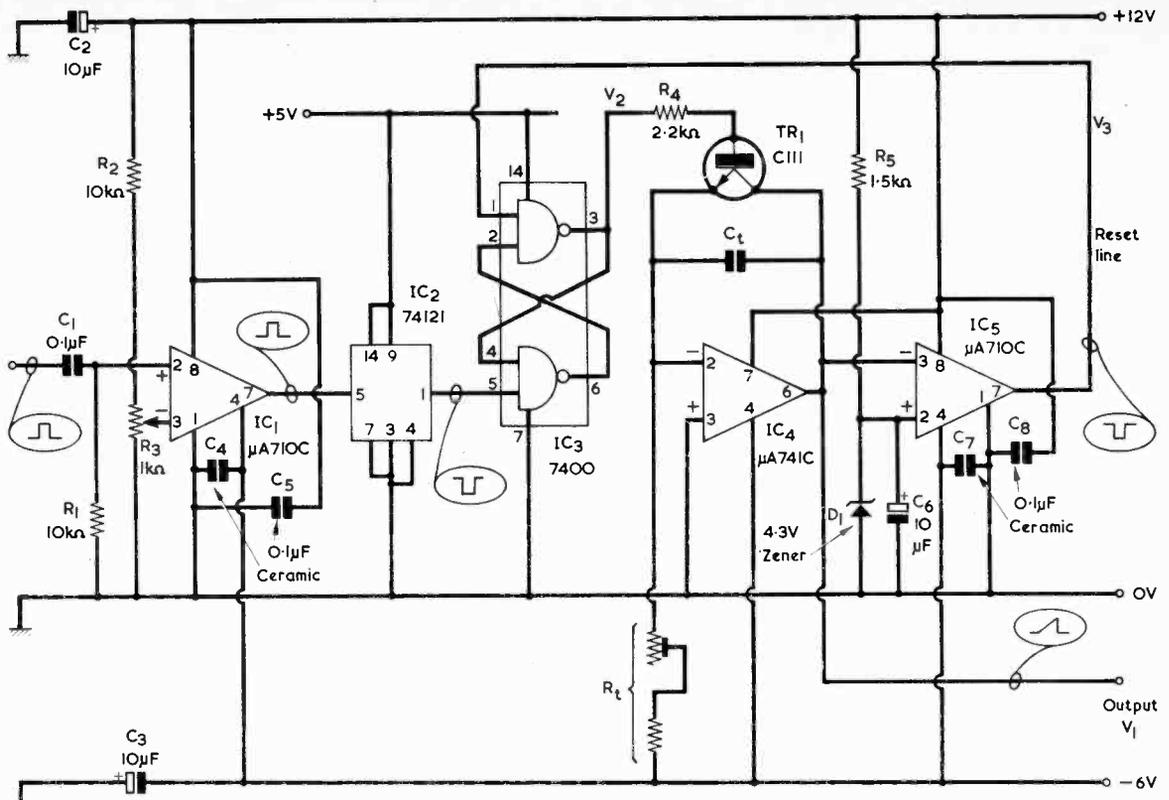
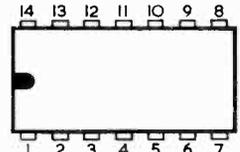
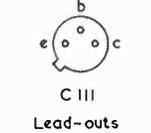


Fig. 7 (a). A more comprehensive version of the Miller Integrator timebase
 (b). Waveforms appearing in the circuit of (a)



Comparator Monostable R_t, C_t - see text Integrator Comparator

R_t, C_t - see text



- 741 D.I.L. 14-pin:
- 3 offset null N_1 (not used)
 - 4 inv. input
 - 5 non-inv. input
 - 6 $V_{CC} - (-6V)$
 - 9 offset null N_2 (not used)
 - 10 output
 - 11 $V_{CC} +$

- 710 D.I.L. 14-pin:
- 2 GND
 - 3 non-inv. input
 - 4 inv. input
 - 6 $V_{CC} - (-6V)$
 - 9 output
 - 11 $V_{CC} +$

Fig 8. Complete circuit of the integrated circuit timebase

COMPLETE CIRCUIT

A complete timebase circuit is shown in Fig. 8. A comparator given by IC1, a $\mu A710C$, is used to shape an input waveform to the t.t.l. logic level for the 74121 monostable multivibrator. This produces a short pulse (30 ns) to trigger the 7400, which is cross-coupled as a bistable circuit. When the clamp on the integrator is released it runs up at the rate previously discussed. On reaching the 4.3 volt level set by the zener diode the $\mu A710C$ comparator in the IC5 position operates, and its negative-going edge initiates the stop action. As the capacitor discharges the comparator input goes below

the 4.3 V threshold and the comparator output returns positive. The comparator reference level must be kept below the 5V limit allowed for its differential input, so that a 4.3V zener diode is suitable. Circuit waveforms are given in Fig. 9.

The speed at which this circuit can operate is limited by the slew rate of the $\mu A741C$, which is typically 0.5V per microsecond. Since the excursion is 4.3V the return time will be about 8 μS . In practice it appeared as a little less because the transistor aids the integrated circuit in its recovery. However the length of ramp which can be obtained also depends on slew rate and should be much slower than the amplifier slew rate in order to maintain

a good linearity. For the circuit shown the minimum time should be a (say) 80 μ S ramp.

R_t should be between 1k Ω and 100k Ω . The maximum duration time for the ramp is limited by drift in the integrator, but 500mS or longer can readily be obtained. If it is required to check that the 74121 is working, then the 30nS pulse may be lengthened to (say) 100 μ S by connecting a 0.1 μ F capacitor between pins 10 and 11.

As an example for calculating R_t and C_t let us suppose that the ramp has to be 500 μ S long. Then, from the last equation given above:

$$500 \times 10^{-6} = \frac{4.3}{6} \times CR$$

$$CR = 0.7 \times 10^{-3}$$

Thus CR has to be 0.7mS.

A 0.05 μ F capacitor and a resistor of 14k Ω are required, so that a 10k Ω potentiometer in series with a 6.8k Ω fixed resistor would be suitable.

The stability of the ramp depends on the 4.3V reference and the constant current through R_t . If the 12V supply is stabilised then the 4.3 reference will have excellent stability and require no further thought. However, changes in the 6V negative line will directly influence the slope of the ramp. The ramp stability and linearity could be improved by using a constant current source in place of the resistor R_t .

(The transistor type C111 shown in Fig. 8 is available from several suppliers, including Henry's Radio Ltd. The 7400 and 74121 can be obtained from most suppliers of t.t.l. integrated circuits. A d.i.l. equivalent of the μ A741C is the '741C D.I.L.' (Henry's Radio) and an equivalent of the μ A710C is the R. S. Components 710-MOPA, which is available from Chromasonic Electronics, 56 Fortis Green Road, London, N10 3HN. Other suppliers also stock 741 and 710 integrated circuits. The pin numbering in the circuit diagram of Fig. 8 for both the 741 and 710 is that for the 8 lead radial version. The pinning insets in this diagram also give pinning for the 14-way dual in-line versions. All i.c. pinning diagrams are with the leads pointing away from

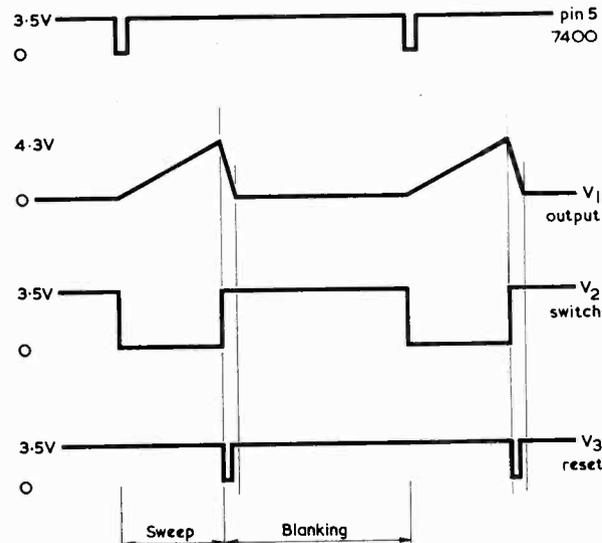


Fig. 9. Waveforms illustrating the sequence of operations in the timebase

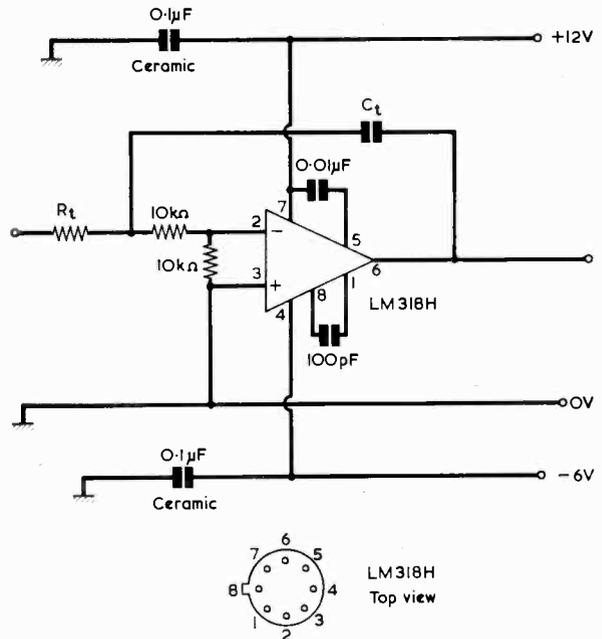


Fig. 10. An alternative integrator circuit incorporating a wide band operational amplifier

the reader. The transistor lead-out diagram is with the leads pointing towards the reader. - Editor.)

A constructional point is that the four 0.1 μ F capacitors, C4, C5, C7 and C8, should be wired close to the i.c. pins to which they connect.

IMPROVED TIMEBASE SPEED

Fig. 10 is included for the professional reader since, at the time of writing, the integrated circuit employed is not widely available in the retail market. It does show, however, what speeds can be obtained with the latest devices.

As was previously discussed, the maximum timebase speed is limited, by the slew rate of the amplifier used, to about 80 μ S long. Where the expense is justified a wide band operational amplifier can be used as the integrator. The National Semiconductor LM318H (distributed by Athena Semiconductor Marketing Company, 140 High Street, Egham, Surrey - Editor) may be employed in a similar circuit, and it was found that the recovery time was better than 0.25 μ S, so that a ramp duration time of 2 μ S is possible. The circuit for an integrator section using an LM318H is given in Fig. 10. With this circuit, the 100pF, 0.01 μ F and 0.1 μ F capacitors should all be positioned close to the integrated circuit.

If the 2 μ S long ramp is used for an oscilloscope display of 10 cm, length then this represents 0.2 μ S/cm., which is adequate for many applications.

CONCLUSION

In this article we have shown how a timebase circuit can be developed for simple applications. The manner in which the sweep and blanking pulses are used will depend on the application, but the circuits as they stand are compatible with the linear and digital integrated circuits normally used in modern circuit design. ■

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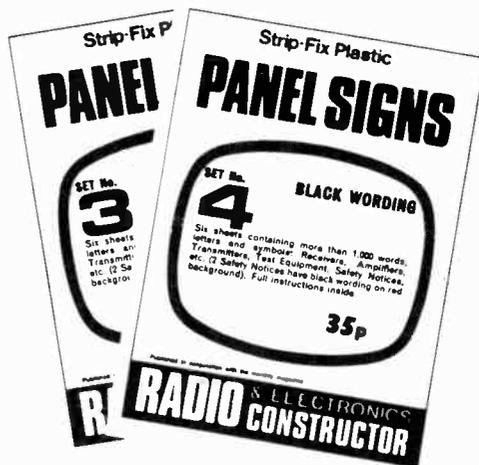
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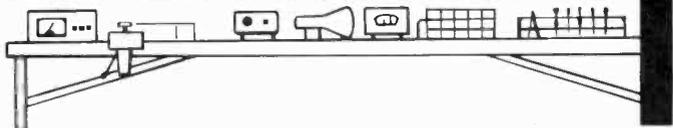
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In your workshop



This month Smithy the Serviceman and his able assistant, Dick, take their annual August holiday away from the Workshop. We find them relaxing on a sunny beach where, despite the surroundings, Smithy still finds time to explain to Dick the mysteries of the programmable unijunction transistor.

"Ah," said Smithy contentedly, "this is the life."

Dressed only in a bathing costume and with his clothes in a heap beside him, he sat forward on the sand. Screwing up his eyes against the morning sun, he gazed happily at the scene in front of him. Before him stretched the golden sands of the beach which he and Dick had chosen to patronise on their annual day off together, the sand breaking away in the distance to a sparkling silvery sea. He listened to the distant shrieks and shouts of holiday-makers as they swam and played in the cool water. All was as it should be, and he lay back, allowing the sun to warm his body.

"It's all right for you," grumbled Dick, who lay alongside him and was similarly clad in swimming trunks. "But who did all the donkey-work bringing the scoff down here?"

"Now please don't start spoiling things already," remonstrated Smithy mildly. "All you had to do was to carry a hold-all bag - which, incidentally, I provided - containing the food you brought for yourself, the food I brought for myself, and a little something to drink."

"A little something?" repeated Dick incredulously. "Blimey, there are at least four thermos flasks in that bag for a start."

"Ah yes," concurred Smithy, lying back comfortably on the sand, "but don't forget that it's a hot day today and we'll need the odd spot of tea every now and again to keep the body fluids in a state of equilibrium. In any case," he concluded magnanimously, "I'll carry the bag on the way back."

PROGRAMMABLE UNIJUNCTION

"Blow me," snorted Dick indignantly. "That won't be much of a hardship,

will it? After we've eaten all the nosh and you've drunk all the tea, there won't be any weight to carry at all. Hallo, what's that dog up to?"

Smithy turned on one side and glanced in the direction of Dick's pointing finger. A large shaggy dog of indeterminate breed was wandering morosely around the beach.

"Perhaps he's lost," volunteered Smithy. "Perhaps he's wondering where his owners have put themselves."

"Could be," concurred Dick disinterestedly. "By the way, it's funny that you should mention the word 'put'."

"Why?"

"Because I've just been reading something about electronic devices which are called 'puts,'" explained Dick. "I can't say I was any wiser after I'd finished reading than when I started!"

Smithy sat up and regarded his assistant balefully.

"Don't you," he queried irritably, "ever ease off on your questions about electronics? Dash it all, we're supposed to be having a day free from technical things today."

"All right," said Dick equably. "But at any rate it won't hurt you to tell me what a 'put' is."

"To begin with," replied Smithy shortly, "you don't call the device a 'put', you call it a 'p.u.t.'. And those letters stand for 'programmable unijunction transistor'."

There was silence for a moment.

"How does it work?"

Smithy sighed.

"In rather the same way as a thyristor," he said resignedly. "It's a four-layer device, like the thyristor, and you trigger it off by increasing the circulating current inside the layers."

There was a further silence. Smithy prepared himself for the inevitable.

"I'm not too certain," persisted

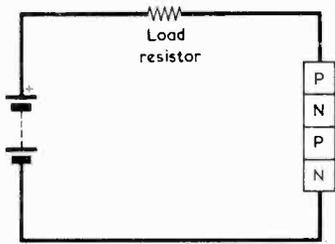
Dick, "how a thyristor works, either. And what exactly do you mean by a four-layer device?"

Giving up the unequal struggle, Smithy leaned forward and, with his finger, drew out on the sand a rectangle with four sections. (Fig. 1.)

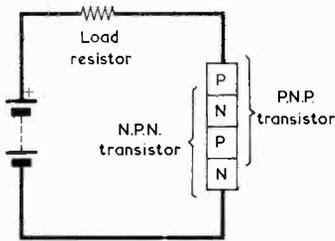
"Here we are," he stated. "Now here are the four layers which you find in a thyristor or, to give it its earlier name, a silicon controlled rectifier. All the layers are silicon and they are p-type, n-type, p-type and n-type respectively. In an initial examination of the device we can start by applying a positive voltage to the p. end via a load resistor



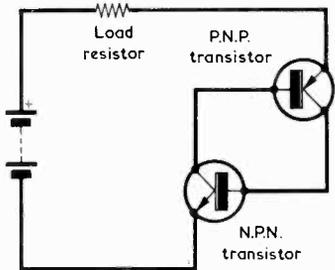
Fig. 1. The four layers which appear in a thyristor



(a)



(b)



(c)

Fig. 2 (a). Applying a voltage to the four-layer device
(b). Two effective transistors appear within the four layers
(c). Redrawing the circuit with the transistors shown as discrete components

and a negative voltage to the n. end." (Fig. 2 (a).)

"A p.n.p.n. device, eh," commented Dick musingly. "So far as I can see, it just consists of three diodes in series, these being a p.n. diode, an n.p. diode and another p.n. diode."

"True," agreed Smithy. "But if you think a little more deeply about it, you'll see that there are, in actual fact, two transistors lurking away in those four layers. The first of these is a p.n.p. transistor and the second is an n.p.n. transistor. See what I mean?"

Smithy indicated the two effective transistors existing in the four-layer device. (Figs. 2(b) and (c).)

"These two transistors," he went on,

"are going to act like any other transistors. They both share the n. and p. sections in the centre and it is quite easy to look upon them as two separate transistors with their shared sections joined together. Now, let's say that the voltage applied across the four-layer device is relatively low. What current will flow through it?"

"At a guess," hazarded Dick, "I'd say it would be leakage current."

"And that's the right answer," replied Smithy. "This leakage current flows in the n.p. junction in the middle. The two outside p.n. junctions, which are the base-emitter junctions of the two transistors, are forward biased. As you can see, the middle n.p. junction is the base-collector junction for both transistors. If the applied voltage is small the leakage current will be very low and the two transistors will offer hardly any current gain."

"What happens if you increase the voltage?"

"Naturally," said Smithy, "the leakage current increases also. So also, in consequence, does the current gain offered by the two transistors. When this leakage current passes a certain level, the transistors offer sufficient current gain for a sudden regenerative process to take place. The current flowing in the base of the p.n.p. transistor is amplified by that transistor, the amplified current at the collector flowing into the base of the n.p.n. transistor. And the current flowing in the base of the n.p.n. transistor is similarly amplified by that transistor and caused to flow in the base of the p.n.p. transistor. So both transistors are now amplifying, with the amplified collector currents flowing in the bases of the opposite transistors. As you can imagine, this constitutes a regenerative

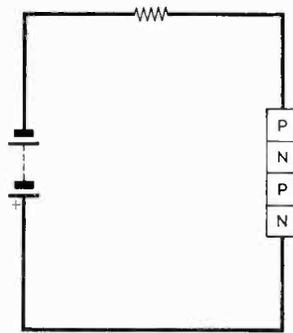
loop and the transistors quickly become fully saturated. In consequence, the voltage across the four-layer device suddenly drops to a value which is of the same order as that given across two forward biased silicon diodes in series."

"And this drop in voltage across the device occurs at a particular applied voltage level?"

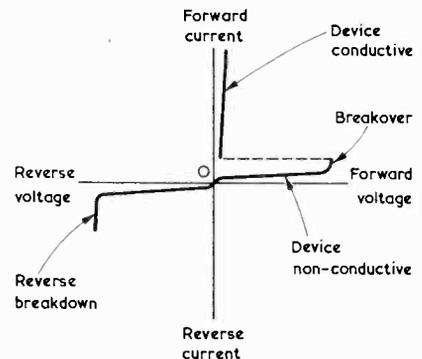
"It does," confirmed Smithy. "The voltage level at which the effect occurs is known as the 'breakover' voltage and it varies for different specimens of the device. To recap, the device passes hardly any current at all for applied voltages below the breakover value. As soon as the breakover value is reached the device suddenly becomes almost a direct short-circuit. A further point we must now consider is that, after breakover, the device stays in the conductive condition provided that the current flowing through it is not reduced below a certain very low level. The minimum current level at which the device remains conductive is called the 'holding current'."

"What happens," asked Dick, "if you apply the voltage the other way round?" (Figs. 3(a) and (b).)

"In this case," said Smithy, "the device acts like three diodes in series because the current which flows is in the wrong direction for transistor action. The n.p. diode in the middle is forward biased and the two outside p.n. diodes are reverse biased. If the applied voltage is taken high enough, the outside diodes go into avalanche breakdown, rather like a zener diode. But in this case the voltage across the device doesn't go down to a low level as occurred previously, because there is no transistor regenerative action. Come here, boy!"



(a)



(b)

Fig. 3 (a). If the polarity of the applied voltage is reversed, there is no transistor action within the device

(b). Graph illustrating the relationship between voltage and current. As soon as forward voltage reaches breakover level the device changes to the conductive mode

ADDING A GATE

"I am here," responded Dick, puzzled.

"I'm talking to the dog not you, you nit. There's a good boy!"

The large shaggy dog of indeterminate breed had now wandered over towards them, and was yawning back and forth in front of the Serviceman, wagging a conciliatory tail furiously from side to side. He approached sufficiently close to allow Smithy to pat him on the head. With a sudden burst of confidence he flopped down beside the Serviceman and rested his muzzle on Smithy's thigh.

"How about that?" said Smithy, obviously pleased. "Now that's what I call a *sensible* dog."

"Blow the dog," retorted Dick. "Keep on about thyristors."

Ignoring his assistant, Smithy reached into the hold-all bag and produced a neat packet wrapped up in grease-proof paper. The rustling of paper caused the dog's ears to perk up, and he threw an enquiring glance at the Serviceman's face.

"Are you hungry, old boy?" said Smithy, addressing the dog. "Here, have one of these."

He produced a sandwich and offered it to the dog. The latter sniffed suspiciously, then took it in his mouth. After several gulps, it had disappeared. Smithy gave the dog another sandwich.

"Hey, what the devil are you up to?" expostulated Dick. "Those are my ham sandwiches."

"Surely," said Smithy reproachfully, "you wouldn't deny a little food to a hungry animal?"

"Wouldn't I just," replied Dick indignantly. "I didn't cart those sandwiches all the way down here just to see them go down the gullet of *that* rapacious mongrel."

But Smithy's attention was centred on the dog and he gave it several more sandwiches, each of which was demolished with similar dispatch.

Reaching over, Dick attempted to pull the heavily depleted packet from Smithy's hand but the Serviceman snatched it out of reach. This action caused the packet to come within reach of the dog, who stuck his nose inside the paper and sniffed eagerly at its contents.

"You might as well give him the lot now," said Dick in disgust. "I don't fancy them after he's been slaving his great chops all over them."

"Fair enough," replied Smithy, as he passed the sandwiches, one by one, to the apparently insatiable dog. The last sandwich was consumed as quickly as the first, and the dog, seeing that the packet was patently empty, ran his tongue appreciatively around the outside of his mouth. He then contentedly rested his head once more on Smithy's thigh.

"That's a fine state of affairs. I must say," complained Dick bitterly. "All my ham sandwiches have gone."

"Well, you've brought some other things to eat as well, haven't you?"

"I have," confirmed Dick heatedly, "and it's a jolly good thing I did, too, or I'd be starving today. Anyway, go on a bit more about thyristors."

"All right," said Smithy cheerfully. "Well, we've got as far as the four-layer device and we've seen that, if the p. end is positive and the n. end is negative, the device changes abruptly, as the applied voltage is increased, from offering a high resistance to behaving in a manner analogous to a diode. We can use the diode terms 'anode' and 'cathode' here. The end of the device to which we apply the positive potential is referred to as the 'anode', and the end to which the negative potential is applied is known as the 'cathode'."

"Fair enough," remarked Dick. "I'm a bit puzzled here, though. Working from the little I know about thyristors, I don't seem to remember any applications in which they are meant to become conductive when the voltage across their outside terminals

exceeds a certain level."

"You don't meet such applications very often in practice," agreed Smithy. "Normally, a thyristor is employed with voltages across the anode and cathode which are much lower than the breakover voltage. However, when explaining the functioning of a thyristor, it's convenient to first of all refer to its operation as a two-terminal device, because this helps you understand the way in which it functions when it appears, in its usual form, as a three-terminal device."

"Ah," said Dick, his interest mounting. "That sounds a bit more like it."

"The third terminal," said Smithy, "is known as the 'gate', and it connects to the p. layer near the cathode end."

Smithy added a line in the sand to the four-layer device he had already traced out. (Fig. 4(a).)

"Let's think," he continued, "in terms of the two transistors which make up the device. Let us say that the voltage applied to the anode and cathode is below breakover level, whereupon the two transistors don't have sufficient current gain to turn the device on and make it conductive. The only current which passes under these conditions is a low leakage current. Now, see if you can tell me what happens if we next apply, via a limiting resistor, a current to the gate which is obtained from a voltage source that is positive with respect to the cathode." (Figs. 4(b) and (c).)

Dick contemplated this circumstance.

"Well," he said musingly, "that gate current will flow in the base-emitter junction of the bottom n.p.n. transistor. Its collector current must then increase. I suppose that this will cause increased base current in the upper p.n.p. transistor, with the result that *that* transistor will also pass increased collector current."

Dick abruptly smote the sand with the flat of his hand. The dog jerked his

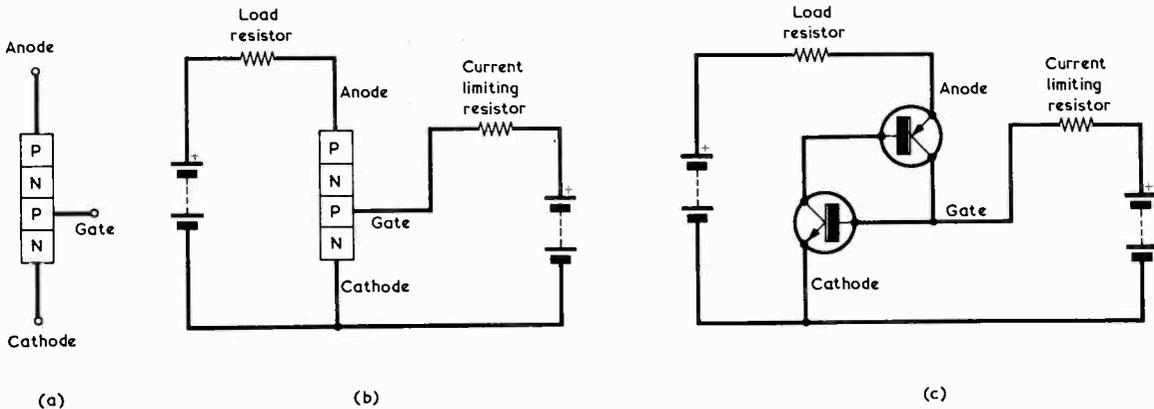


Fig. 4 (a). The thyristor also incorporates a gate terminal
 (b). The thyristor is made conductive under the conditions shown here
 (c). Thyristor operation is easier to visualise if considered in terms of the integral transistors

head at the sudden sound then settled down again, gazing reproachfully at Smithy's assistant.

"Why, of course," went on Dick excitedly. "What happens is that the gate current causes the thyristor to become conductive in just the same manner as it does when it reaches breakover voltage."

"Exactly," confirmed Smithy. "We know that the thyristor is triggered on at breakover voltage because the leakage current then given is just sufficiently high to enable the transistors in the thyristor to offer the requisite current gain. At voltages below breakover voltage, we can still cause the thyristor to be triggered on, but this time we have to give the transistors a little bit of outside assistance. And this we do by feeding into the gate a current from a source which is positive of the cathode. This makes the bottom n.p.n. transistor pass more collector current and this initiates the whole regenerative action between the two interconnected transistors. The thyristor comes on, and it then stays on even when the gate current is removed. The thyristor can only be turned off again, in the absence of gate current, by reducing the anode-to-cathode current below the holding level. This may be done by removing the supply voltage."

"Would the thyristor also turn off if the anode and cathode were shorted together?"

"It would," agreed Smithy, "provided there was no gate current, of course. If gate current was present, the thyristor would come on again as soon as the short-circuit was removed. This ability to remain turned on is one of the advantages of the thyristor, incidentally, and it is possible for a thyristor to be turned on by a gate pulse which only lasts for a microsecond or even less. There is also a great deal of amplification, too. A thyristor rated at around 25 amps can be turned on by a gate current of only 50mA or so, which represents an effective amplification of - let me see now - 500 times."

UNIUNCTION TRANSISTOR

"Not bad for a power device," commented Dick.

"Not bad at all," agreed Smithy. "And that tells you how a thyristor works, whereupon I can next turn to the programmable unijunction transistor which started this whole business off. Before leaving the thyristor, though, I'd better just mention that its circuit symbol consists of a conventional diode symbol with a sloping line coming out of the cathode line. That sloping line represents the gate connection." (Fig. 5.)

"Right," said Dick briskly, "let's carry on to the p.u.t. then."

Now that he had completed his dissertation on the thyristor, Smithy momentarily turned his gaze towards

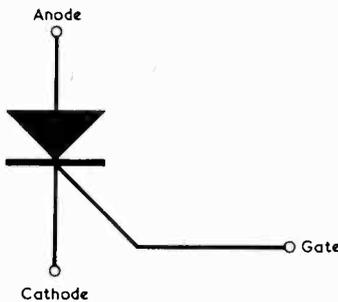


Fig. 5. Circuit symbol for a thyristor

the dog, which was still settled comfortably with his head on the Serviceman's thigh. The dog looked back at him with melting, beseeching eyes.

"The poor thing," said Smithy compassionately, "he must be starving."

"He's not starving," pronounced Dick contemptuously. "Dash it all, Smithy, can't you see you're being taken? That's no ordinary hound, that's a con-dog!"

But Smithy's heart was moved and he took another paper package from the hold-all. He unwrapped this, to reveal a large piece of fruit cake which he passed over to the dog. The dog wolfed it down in three gulps.

"Ye gods," commented Dick, glancing venomously at the dog. "That cake hardly touched the sides at all. At any event, I see you gave it a bit of your own food this time."

"I don't know whose food it was," said Smithy absently, as he stroked the dog's back. "I didn't bring any cake myself."

"Blow me," raged Dick, as realization came flooding in, "you've given that voracious flea-bag my cake."

But Smithy was luxuriating in the inner content of one who, at no cost whatsoever to himself, has perpetrated a Good Work.

"Tut, tut," he remarked, blandly ignoring his assistant's complaints. "Let's press on to the p.u.t. now."

Leaning over, he traced out on the sand another rectangle with four sections. Fig. 6(a.)

"Now this," he remarked, "shows the basic make-up of the programmable unijunction transistor. It's almost identical to the thyristor and it has anode and cathode connections at the same ends. The only difference is that the gate connection now goes to the n. section next to the anode instead of to the p. section next to the cathode, as with the thyristor."

"Can you," asked Dick, forgetting for the moment the deprivation of his food supplies by the do-gooding Serviceman, "think in terms of two transistors, as with the thyristor?" (Fig. 6(b).)

"You can," confirmed Smithy.

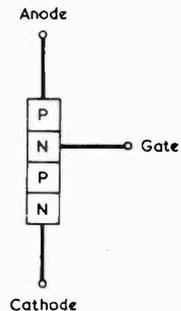
"Then," said Dick slowly, "I suppose you turn the p.u.t. on by making its gate go negative. This will

increase the base current in the upper p.n.p. transistor, whereupon this transistor will have an increased collector current, thereby turning on the lower p.n.p. transistor, and so on."

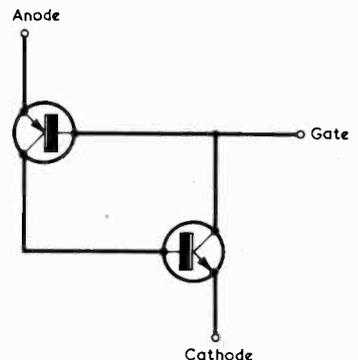
"That's the general idea," confirmed Smithy. "The device will come on when the gate is sufficiently negative of the anode to cause the p.n.p. transistor, and hence the n.p.n. transistor, to go into the regenerative condition. Once the device has triggered it stays turned on, in the same way as a thyristor stays turned on."

"Why," asked Dick, "is it called a programmable unijunction transistor?"

"Because it replaces the ordinary unijunction transistor in relaxation oscillator circuits and in similar applications," replied Smithy. "Actually, it's rather a pity that the word 'unijunction' has been chosen to describe it, because its internal operation is quite different from that of a standard unijunction transistor. The 'programmable' bit arises from the fact that you can use external components to 'program' the voltage at which it fires."



(a)



(b)

Fig. 6 (a). Basic structure of a programmable unijunction transistor

(b). The programmable unijunction transistor has the gate connection made to the base of the internal p.n.p. transistor

RELAXATION OSCILLATOR

"Thank you very much," said Dick sarcastically, "I know as much now as I did before I asked that last question!" Smithy glanced irately at his assistant.

"I'll try and explain it in a bit more detail then," he stated irritably. "Let's smooth up this sand a bit."

Smithy brushed his hand over the sand until he had a flat even surface. Subconsciously, he noted that the sounds of the swimmers in the sea had become much closer. Carefully, he traced out a simple diagram with his finger. (Fig. 7.)

"This," he remarked, "is the standard unijunction oscillator circuit. The material between the base 1 and the base 2 is n-type silicon and the emitter consists of a p-type spot about a third, say, of the way up the base material. When the supply is switched on the capacitor is discharged, and the n-type silicon acts like a resistor. The capacitor begins to charge via the resistor above it until the voltage on its upper plate is sufficient to cause the p-n junction given by the emitter and the n-type silicon with which it is in contact to become conductive. There is then a negative resistance effect which causes the capacitor to discharge rapidly into the resistor connected between the base 1 and the negative supply rail. The negative resistance effect ceases when the capacitor is nearly fully discharged, and the transistor reverts to its previous condition. The capacitor then commences to charge up once more until the voltage on its upper plate is at triggering level. The transistor once more exhibits the negative resistance effect and the capacitor again discharges rapidly into the resistor connected to base 1. This process continues indefinitely and results in a series of positive-going pulses across the base 1 resistor."

"That seems simple enough," commented Dick. "What's the snag with an oscillator circuit of this nature?"

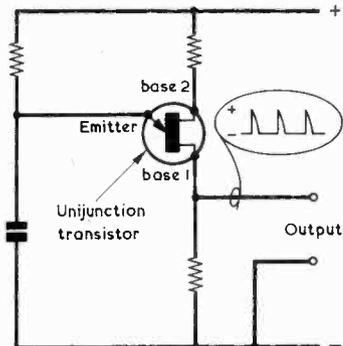


Fig. 7. A unijunction transistor relaxation oscillator

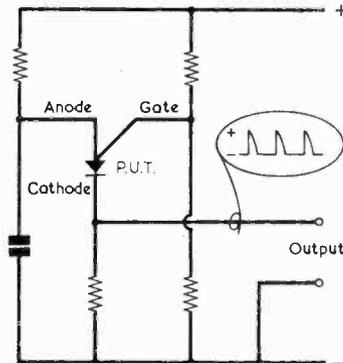


Fig. 8. A p.u.t. relaxation oscillator

"The snag," replied Smithy, "is that the emitter potential at which the device triggers depends upon the physical positioning of the p-type emitter along the n-type base material. Before triggering takes place the latter acts like a resistive potential divider, so that the voltage at the point where the emitter is located must obviously depend upon its physical position. Unfortunately, it is difficult to manufacture unijunction transistors with precise positioning of the emitter, with the result that different unijunction transistors of the same type number can have varying trigger voltages."

"I see," said Dick thoughtfully. "I should imagine that this could be a nuisance with mass-produced assemblies incorporating unijunction transistors."

"Exactly," concurred Smithy. "And this is where the p.u.t. scores. It's used in a relaxation oscillator circuit which is nearly as simple as that for the unijunction transistor."

He smoothed over the sand once more, and traced out another circuit. (Fig. 8.)

"There we are," he remarked. "As you can see, the circuit symbol for a p.u.t. is that for a diode plus a sloping gate line which, this time, comes out of the anode section. Now, we said just now that we turn the p.u.t. on by causing the gate to go negative with respect to the anode. When the p.u.t. is used in a relaxation oscillator we cause it to turn on by keeping the gate at a fixed potential and taking the anode positive of the gate. This is, of course, the same thing expressed in a different way. In this oscillator circuit, the gate potential is decided by the two resistors which couple it between the positive and negative supply rails. The capacitor is discharged until the supply is switched on, after which it commences to charge, taking the anode of the p.u.t. positive. When the anode passes the potential of the gate a gate current commences to flow, and when this is high enough the device becomes triggered and turns on, causing the

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capacitor to discharge into the resistor between the cathode and the negative rail. When the voltage across the capacitor is sufficiently low the device turns off again, allowing the capacitor to charge and another cycle to commence. A series of positive-going pulses is then given across the cathode resistor."

"That's knobby," said Dick. "I suppose you can fix the gate voltage at any level you like within reason by giving suitable values to the two resistors which connect to it."

"That's correct," stated Smithy, "and that's where the p.u.t. has the edge over the unijunction transistor. Since the gate and the anode form a silicon junction the device fires when the anode is about 0.6 volt positive of the gate. The minimum gate current required to cause firing, the 'peak-point current' as it is called, is of the order of microamps only. In consequence it becomes possible to have a relaxation oscillator circuit which gives virtually the same frequency with any p.u.t. of the correct type number, since the triggering level is 'programmed' externally by the values of the two gate resistors. You couldn't have the same consistency of performance with unijunction transistors."

"What sort of supply voltages are required by p.u.t.'s?"

"Anything from about 4 to 40 volts," replied Smithy. "The p.u.t. is not very fussy in this respect. In the relaxation oscillator circuit I've just shown you, the anode resistor and the two gate resistors could all be 100k Ω each, and the cathode resistor could be 100 Ω . The capacitor could have a value of around 0.05 μ F, this being altered for different frequencies of oscillation. A supply of 9 volts would be suitable."

"I don't," remarked Dick, "seem to have seen many p.u.t.'s on the home-constructor market yet."

"That's because they're fairly new," replied Smithy. "But they should be appearing in some profusion pretty soon, whereupon you'll be well primed up on how they work."

rose to its feet and shook himself vigorously to get rid of the sand on his body. Smithy turned over to face his infuriated assistant. The latter scowled thunderously at him then, for no apparent reason, his expression changed first to wonderment and then to almost hysterical jubilation.

Smithy felt a warm stream flowing down his back.

He sat up abruptly and the dog, once more in a state of equilibrium with all its four paws properly on the ground, scampered away out of sight across the beach.

"Well, that's gratitude for you, I must say," snorted Smithy angrily. "After all the scoff I gave him, too!"

"He's left a marker on you for the benefit of his mates," chortled Dick, almost delirious with laughter. "It's like those signs that tramps leave outside houses where they're given cups of tea. As soon as any of his mates have a sniff at you they'll say to themselves: 'Ah, here's a right generous twit so far as scrounging food is concerned'."

Dick subsided into helpless giggles which abruptly changed to a smothered gasp when the pair of them were soaked by an outside wave from the hitherto unnoticed incoming sea.

"Quick," yelled Smithy, "get the clothes!"

They picked up their clothes and rushed further up the beach where, panting, they watched the advancing sea. They were on a relatively flat section of the bay and the water moved in at a correspondingly swift rate. It was then that Smithy noticed that they had forgotten the hold-all bag with the food and the precious containers of tea.

Smithy strode into the water and reclaimed the bag which, apart from the thermos flasks, contained food that was completely soaked and uneatable. After which he walked back into the sea again for a very long and fully immersed bathe.

In the circumstances there wasn't much else he could profitably do. ■

VISITING CARD

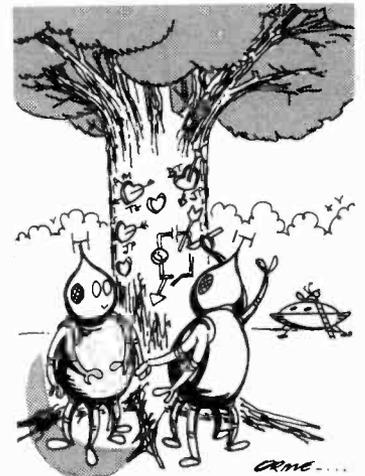
The sounds from the bathers in the sea were very close now. The dog nuzzled his nose into Smithy's side and gave a little waine.

"We haven't forgotten you," chuckled Smithy. "Here, let's see what else we can find for you."

He fished into the hold-all bag and produced another paper bag. He extracted two sausage rolls and passed them to the dog, who devoured them almost instantly.

"Corluvaduk," fumed Dick as Smithy lay back again. "Those sausage rolls were mine. That blasted mutt has eaten pretty well all my lunch!"

The dog, sensing perhaps that the chance of further offerings was low,



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TECHNIQUES

The methods of construction themselves are quite diverse. We can, for instance, build all our projects on sturdy metal chassis, using tagboards and tagstrips for connections. Or we can make up our own printed circuit boards and mount our components on these. Or we can use Veroboard. Or we can use plain perforated board with turret tags. Or, for temporary items, we can use S-DeC. So far as metalwork is involved this can be as extensive as we care to make it, or it can be reduced to the minimum by employing Lektrokit and any of the many ready-made metal boxes that are nowadays available in various sizes.

Projects? Here, receivers and amplifiers are among the most popular. Making one's own test gear is another favourite with many constructors. So far as gadgets and electronic devices are concerned, the field is wide open indeed. Simple logic circuits form an engrossing subject, as also do devices which function in relation to time, such as metronomes, process timers and units which switch a piece of equipment on or off after a long delay. Light-operated circuits can offer both amusement and practical usage, as also can sound-operated units. Electronic musical instruments can be completely absorbing, as also can items of equipment which modify musical sound signals. And we haven't yet caught up with tape recording, for which the constructor may make up simple accessories or, even, complete recorder units.

AUGUST 1973

Interested in short wave listening? Then you can build your own receiver or, if using one that is ready-made, knock up aerial tuning units and other ancillary gear in addition to playing around with the aerial system itself. Or you can indulge in hi-fi and the pleasures of music that is really well reproduced. Then, again, there are such interests as radio control of models, amateur transmitting, power supply design and electronic robots.

ACTIVE COMPONENTS

When we come to think of active components we once more encounter a very wide choice. It is instructive here to look upon these from the historical point of view.

The older amongst us bit our constructional teeth on valves. Cumbersome as they are, both with regards to size and power requirements, you can do no end of things with valves. Valves have the advantage of *simplicity*: you apply a voltage change to a high impedance grid and get a corresponding current change at the valve anode. I know that transistors are simple, too, but transistors don't seem to have the almost baby-faced type of simplicity that valves have. Quite a few constructors still like to make up valve equipment and there is no reason why they shouldn't continue to do so with complete success. The only real snags with valves are the heating-up time, the relatively heavy current requirements, the bulk and the fact that they are not as long-lived as transistors.

Another old-timer which won't lie down is the relay. Making up relay circuits is quite an art in itself, as anyone who has had experience of these will at once agree. The relay has the exceptional advantage that the current which operates it is completely remote from the circuits into which its contact set or contact sets connect. Relays have been doing logic operations for very many years now, and they still continue to do so. In its most modern form the relay appears, of course, in the form of the dry reed relay, which has its contacts hermetically sealed to prevent oxidation.

Whereupon we next come to the transistor. Nobody can say that there isn't a wide variety here! The transistor types available from the different manufacturers run into many tens of thousands. And from these we can select a.f. transistors, r.f. transistors, power transistors, switching transistors f.e.t.'s, m.o.s.f.e.t.'s and so on. Further semiconductor devices, such as light-emitting diodes, varactors, thyristors, triacs, and many more, continue to come to mind. After which we pass on to integrated circuits . . .

Need I say more? All of this rich diversity undeniably makes our hobby one of the most rewarding, both in terms of interest and of achievement, that could ever have been devised.

SOLDERING IRON TIP

Finally, a little dodge for those who like to have a soldering iron quickly available, but who may work for long periods without actually using it. The idea I'm going to describe isn't new, but I haven't seen any references to it for quite a long time and so it should be worth bringing up again.

If a soldering iron is left switched on and unused for a long time, it is liable to overheat a little. Also, it suffers a corresponding shortening of its useful life.

The dodge consists of running the iron at half-power when it is not required, whereupon it remains just below operating temperature. On being switched to full power it achieves operating temperature very quickly. The half-power condition is achieved by inserting a silicon rectifier in series with one of its leads, together with a switch across this rectifier. When the switch is open the rectifier allows only alternate half-cycles to pass to the iron, thereby giving the half-power condition. When the switch is closed, full power is applied to the iron.

The switch may be fitted on the bench near the mains socket for the iron. If a wall-mounting switch is used, there may well be sufficient room inside it to take the rectifier. The latter can be any small type capable of passing the iron current and having a p.i.v. of 400 or more. A 1N4004 would be a good choice. ■

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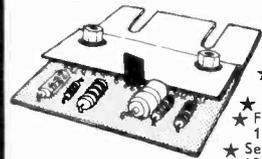
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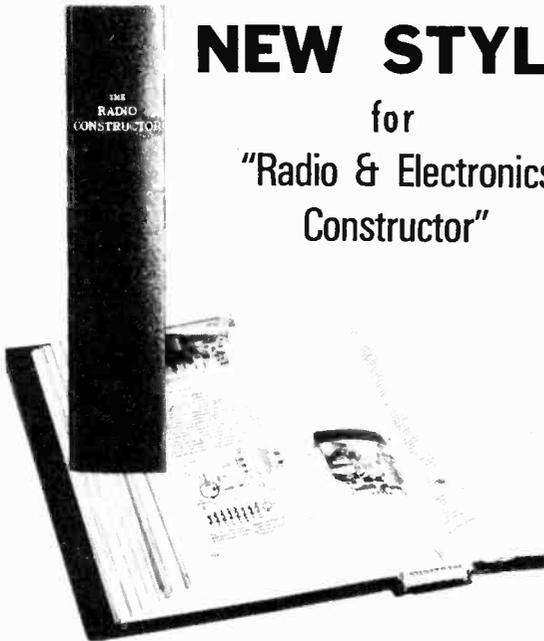
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1.5	6,500	4,110	2,600	2,050	1,300	822	650	411	260	130
2	5,630	3,560	2,250	1,780	1,130	712	563	356	224	113
2.5	5,030	3,180	2,010	1,590	1,010	637	503	318	201	101
3	4,600	2,910	1,840	1,450	919	582	460	291	184	91.9
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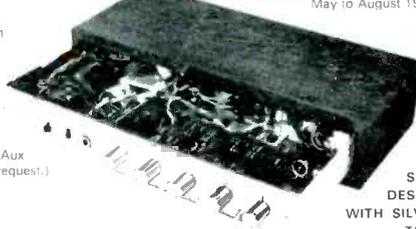
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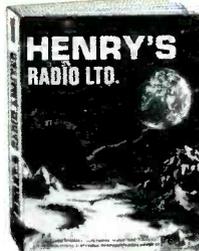
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