


Eight optional accessories including an r.f. probe, a peak-to-peak probe, a temperature probe, and a coaxial T-connector, make the TF 2650 Solid State FET Multimeter one of the most comprehensive general purpose multimeters available. Such versatility makes the TF 2650 ideal for use in servicing, production, technical education, research, design and many other applications, while the battery/mains option makes it equally suitable for field, laboratory or workshop

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1.5 mV and 0.15 mA f.s to 1500 V and 1.5 A a c. and d.c. The accessories extend the ranges up to 30 kV and 150 A , r.f. up to 1 GHz and temperatures up to $500^{\circ} \mathrm{C}$. Resistances can be measured from $100 \Omega$ to $100 \mathrm{M} \Omega$ mid-scale with a facility to make in-circuit measurements on solid-state devices. A centre-zero facility is available on most ranges.

The basic instrument is supplied complete with co-axial leads, crocodile grips, test prods and a leather carrying case

For further information write or phone:

## mi MARCONI INSTRUMENTS

[^0]
# wireless world 

## Electronics, Television, Radio, Audio DECEMBER 1977 Vol 83 No 1504

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[^1]

Front cover, showing a microcomputer using Intel devices on a single p.c. board, introduces our second article on microcomputer design in this issue.
Photographer Paul Brierley

## IN OUR NEXT ISSUE

Traffic information broadcasting. Latest developments in the BBC's proposed system using a multiplicity of low power m.f. transmitters on $a^{\text {a }}$ single frequency.

Fuses - their physical functioning, design and characteristics. A detailed look at a somewhat neglected component.

Power into loads: why we don't apply the maximum power transfer theorem to active devices in linear circuits.

## 



When dealing with a variety of projects, electronic filtering requirements change rapidly and need to be met with minimum fuss and maximum flexibility. The Barr \& Stroud EF3 Modular Filtering System is designed around the most compact of basic main frames containing the power unit and function switching with capacity for two slide-in filter units. The modular concept allows you to begin with the minimum of a mainframe and one filter unit. Thereafter you can extend your 'library' of filter capability as requirements dictate and budgets make possible.

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[^2]
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## DTARII

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Switzerland: Audio Bauer AG, CH-8048 Zurich, Berner strasse-Nord 182, Haus Atlant
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It's an exception of compact recorders. Specially designed for critical professional applications from the ground up. It leaves nothing to be desired. 68 dB signal-to-noise and great-er-than-60dB crosstalk. Variable speed DC-servo capstan motor for less than $0.05 \%$ wow/flutter and $\pm 7 \%$ pitch control. +19 dBm headroom before clipping. Motion sensing control logic Front panel edit and cue; stepless bias adjustability; built-in test and cue osciallator; all front accessible. 600 ohm , +4 dBm or $-10 \mathrm{dBm} \cdot$ fixed-level output and XLR connectors. Remote controllability for all transport functions. In short, it's a sheer professional masterpiece to produce desired 15 or $7-1 / 2$ ips masters.

The performance and reliability have been fully proven since its original version was introduced in 1973, in more than one thousand practical applications by broadcasters, studio recordists, audio-visual professionals and musicians all over the world. For the full story of this unique and compact professional machine, ask anyone who uses it or get in contact with your nearest Otari distributor.


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The illustrations show typical extreme low frequency response characteristics of three cartridges in the Series // /mproved arm.

Note the substantial reduction in the $Q$ of the low frequency resonance. Although
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tance to external shock and reduction of spurious low frequencies. The prior art has been to apply damping at the bearings but this method is inherently inefficient and liable to migration of the damping fluid. The FD200 overcomes these problems as it is applied at a radius of 1.45 inches ( 36.8 mm ) making it several times more effective.

It is suitable for use with all Series II and Series II Improved arms and offers a choice of three damping rates to suit all cartridge compliances.

The attractively presented kit includes all parts necessary for the conversion which is easily carried out by the user following the explicitly illustrated instruction booklet. It is recommended for all cartridges, particularly those of relatively fow compliance which might otherwise require a more massive arm.

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WIRE TIES are a flexible means of fastening wires and small cables into orderly, compact looms. They are quick and easy to fit and can be re-used, greatly reducing re-looming times. Wire ties are made from nylon and are available in various sizes each determined by a different colour.
The P.C. BOARD GUIDE is a self-retaining edge support for printed circuit boards. It has good panel retention and grips p.c. boards firmly and securely. The guide is available in two types of material - yellow acetal or grey Noryl, for high temperature and voltage applications.
 P.C. BOARD SPACERS are simple to fit, onepiece mouldings for use with p.c. boards. They have a self retaining shank for fastening into panels and a $T$-shaped anchor for securing p.c. boards of $0.062^{\prime \prime}$ thickness. They have good resistance to vibration and are suitable for board-to-board or board-tochassis use
P.C. BOARD STAND-OFFS are quickly assembled, self-retaining panel supports for p.c. boards. Made from natural (off white) nylon and have good resistance to vibration. Suitable for panels up to $0.079^{\prime \prime}$ thickness. Stand-Offs accept a No. 4 self-tapping screw


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$5 \mu \mathrm{~V} / 5 \mu$ Tesla $(50 \mathrm{~Hz})$. Polar Pattern': Hypercardioid. Output Impedance: $200 \Omega$. Load Impedance: $>1000 \Omega$, Connections: M $201 \mathrm{~N}(\mathrm{C})=$ Cannon XLR-3-50 T or Switchcraft: $2+3=$ $200 \Omega_{1}, 1=$ ground. $\mathrm{M} 201 \mathrm{~N}=3$-pin DIN plug T 3262: $1+3=200 \Omega$ $2=$ ground. $\mathrm{M} 201 \mathrm{~N}(6)=6$ pin Tuchel.
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$(100 \times 50 \times 25 \mathrm{~mm})$ $(112 \times 62 \times 31 \mathrm{~mm})$ $(120 \times 65 \times 40 \mathrm{~mm})$ $(150 \times 80 \times 50 \mathrm{~mm})$ $(190 \times 110 \times 60 \mathrm{~mm})$

## MINI DESK BIMCONSOLES

Moulded in Orange, Blue, Black or Grev ABS and incorporating guides on all sides for holding 1.5 mm thick pcb's. 1 mm Grey Aluminium panel sits recessed into front of console and held by screws running into integral brass bushes. Stand-off bosses in base for supporting small sub-assemblies etc. 4 self adhesive rubber feet also included. BIM1005 $(161 \times 96 \times 58 \mathrm{~mm})$ £1.97*
BIM1006
( $215 \times 130 \times 75 \mathrm{~mm}$ )
£2.70*

Also available in Grey Poly
screws BIM2007/17 £0.82

Diecast BIM2002/12 £ $0.87^{*}$ BIM2003/13 £0.97* BIM2004/14 f1.05* BIM2005/15 £1.18* $\begin{array}{ll}\text { BIM2006/16 } & \text { £1.84* }\end{array}$ BIM5002/12

Hammertone £1.20* £ $1.50^{*}$ £ $1.86^{*}$ £2.38* £2.38*

LOW PROFILE BIMCONSOLES

1 mm Grey Aluminium panel sits recessed into front of console base, which is moulded in Orange, Blue, Black or Grey ABS and sits on 4 self adhesive rubber feet. Incorporating guides for holding 1.5 mm thick pcb , the base also has stand-off bosses for supporting small sub-assemblies etc. and ventilation slots. Front panel is held by 4 screws which run into integral brass bushes.
BIM6005 ( $143 \times 105 \times 55.5[31.5] \mathrm{mm}) £ 2.14 *$ BIM6006 ( $143 \times 170 \times 55.5[31.5] \mathrm{mm}) £ 2.73^{*}$ BIM6007 (214×170×82[31.5] mm) £3.75*

## Natural

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## ALL METAL BIMCONSOLES

MULTI-PURPOSE BIMBOXES
Moulded in Orange, Blue, Black or Grey ABS with 1 mm thick Grey aluminium recessed front cover which is retained by 4 screws running into integral brass bushes 1.5 mm pcb guides are incorporated on all sides and as with all ABS boxes they are $85^{\circ} \mathrm{C}$ rated. 4 self adhesive rubber feet also included.
BIM $4003(85 \times 56 \times 28.5 \mathrm{~mm})$
£1.13* BIM $4004(111 \times 71 \times 41.5 \mathrm{~mm})$ £1.42* BIM $4005(161 \times 96 \times 52.5 \mathrm{~mm})$ £1.87*

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Bimboards accept all sizes of DIL packages as well as resistors, diodes, capacitors and LED's etc. They have integral Bus Strips running up each side for carrying Vcc and ground as well as Component Support Brackets for holding lamps, fuses and switches etc. Available as either single or multiple units, the latter mounted on 1.5 mm thick, matt black aluminium back plates which stand on non slip rubber feet and have 4 screw terminals for incoming power.
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BIM 71.51 ( $102 \times 140 \times 51[28] \mathrm{mm})$ £ $7.66^{*}$ BIM 7152 ( $165 \times 140 \times 51(28) \mathrm{mm})$ £ $8.51^{*}$ BIM $7153(165 \times 216 \times 51[28] \mathrm{mm})$ £ $9.35^{*}$ BIM7154 ( $165 \times 211 \times 76[33] \mathrm{mm}) £ 10.21 *$ BIM $7155(254 \times 211 \times 76[33) \mathrm{mm}) \quad £ 11.05 *$ BIM $7156(254 \times 287 \times 76[33] \mathrm{mm}) £ 11.92^{*}$ BIM $7157(356 \times 211 \times 76[33] \mathrm{mm}) £ 12.76^{*}$ BIM7158 ( $356 \times 287 \times 76[33$ ] mm) £13.60*
$30^{\circ}$ Sloping Panel
BIM 7301 ( $102 \times 140 \times 76[28] \mathrm{mm}$ ) £ $7.66^{*}$ BIM 7302 ( $165 \times 140 \times 76[28] \mathrm{mm})$ £ 8.51* BIM7303 ( $165 \times 183 \times 102[28] \mathrm{mm}) £ 9.35^{*}$ BIM7304 ( $254 \times 140 \times 76[28] \mathrm{mm}) ~ £ 10.21^{*}$ BIM7305 ( $254 \times 183 \times 102[28] \mathrm{mm}) € 11.05 *$ BIM7306 ( $254 \times 259 \times 102[28] \mathrm{mm})$ £ $11.92^{*}$ BIM 7307 ( $356 \times 183 \times 102\left[28\right.$ ] mm) $£ 12.76^{*}$ BIM7308 ( $356 \times 259 \times 102(28] \mathrm{mm}) £ 13.60^{*}$

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(Deputy Chairman and Managing Director of Standard Telephone and Cables Lidd, and Senior Officer of ITT in the United Kingdom).
The Microprocessor in the Home, by Dr Steve Forte (Managing Director of General Instrumen: Microelectronics Led since 1971 and has many years experience of the Semiconductor industry). The Microcomputer in Industry and Commerce, by Alex d'Agapeyeff, OBE, (Chairman ot Computer Analysts and Programmers (1d).
The Impact of Microelectronics on Employment, by Dr Alfred Prommer (Vice l'resident of
Siemens AG, West Gertrany, and head of sales and marketing in the company's componens group). Lord Orr-Ewing, OBE. C. Eng. (Chairman of Ultral Elecaronics Lid): Sir leuan Maddech, CB OBE, FRS (Deputy Chairman of the National Electronics Council and Lord Thorneycrott (Chairman of Fye of C.mbridge Led) have alf agreed to chair the sessions.
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## Frequencies, technology and society

To judge from all the preliminary discussions about WARC 1979 one would think that radio systems began and ended with frequency space. Of course, when there's a shortage of it the various users making their claims and counter-claims become preoccupied with the subject and develop an almost pathological concern for spectral lebensraum. But if we are going to need more communication, broadcasting and other radio systems in the future we can't afford to ignore the other, equally important, dimensions of a communication channel: space, time and signal-to-noise ratio. And to make use of these, our traditional thinking and methods of transmission and reception will have to be supplemented by fresh ideas and new technology.

By "space" is meant simply the physical volume occupied by a radio transmission. If you physically confine transmissions you can have many using the same frequencies without interference. Furthermore, signals can be kept within coaxial cables, waveguides, leaky feeders and optical fibres, and it makes sense to use such methods when transmitters and receivers are fixed. There are people, for example, who maintain that broadcasting is a misuse of the radio spectrum because the transmitters and many receivers can be connected by cables. The dimension of time can be used in a variety of ways. For example, where pauses occur in a message, as between spoken phrases, the gaps can be occupied by portions of other messages automatically switched into them. Redundancy in certain signals can be used to reduce the maximum information rate and hence the bandwidth required. Television broadcasting is a notorious consumer of time and bandwidth. Since many tv programmes are little more than homogenised pap dished out of cans, why not send them out at night, to be recorded on video tape machines fitted in receivers, so that the spectrum space could be used by other services during
the day? Signal-to-noise ratio can be "traded" by modulation and encoding methods so that channels are utilized more efficiently. Spread spectrum techniques are one possibility and more attention could be given to determining human tolerance to noise-type background interference from pseudo-randomized signals as against background interference from intelligible signals.

But all this assumes a continuation of present trends in society. A fundamental question affecting all communication technology is what will happen in the future. Can the present process of industrialization and economic growth continue indefinitely in a finite environment, or shall we be forced into a state of "global equilibrium" at a lower level of activity as predicted by the famous MIT study "The Limits to Growth"? The well-known economist the late Dr E. F. Schumacher thought that our present system could collapse as a result of three impending crises: a revulsion of human nature against "1984" technological, organizational and political patterns; a breakdown of the living environment which supports human life; and exhaustion of the world's non-renewable resources, in particular, the fossil fuels. Energy, on which depends all industrialization and economic growth, could indeed be a decisive factor. Ichiro Miura, the director of communications policy in Japan's ministry of posts and telecommunications, thinks that because of this the widely predicted "information-oriented society" will not in fact succeed industrial society and that we may well revert to the "resources society" which preceded industrialization (Telecommunication Journal, September 1977).

If the natural limits of the environment do restrict economic growth and as a result the structure of society is modified, then obviously the requirements for electrical communication - both in amount and pattern - will be greatly changed.

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# Teletext decoder modifications 

# New circuits to take advantage of the latest facilities 

by Richard T. Russell

Since the original articles on the Wireless World Teletext decoder appeared (November 1975 to June 1976 issues), a new teletext specification has been published ${ }^{1}$ which describes a number of extra facilities and allocates some of the hitherto unused control characters for use with four new display modes. These are: graphics hold, double height, separated graphics and background colour, and were described in the February 1977 issue of Wireless World, p. 61. These new facilities are intended to enhance the appearance of teletext pages and remove some of the limitations imposed by the original specification. The photographs show typical examples of their use.
Another facility not provided by the original design, although present in an earlier specification and used by both Ceefax and Oracle, is the "concea-led-display" mode. This allows, by the action of a control character, selected characters to be displayed as spaces until "revealed" by the viewer, and is useful in a question and answer situation. All these new facilities are provided by the additional circuitry to be described.
At the time of writing, Oracle (the IBA's teletext service) makes extensive use of these facilities on test pages with some use on normal information pages, whilst Ceefax (BBC) is just beginning to use some of the new facilities on an experimental basis.

## New board

The new cirćuitry comprises 25 t.t.l. integrated circuits, which decode the apropriate control characters and implement the new display modes. These i.cs have been numbered 101 to 125 to avoid confusion with i.cs 1 to 90 used in the original decoder design. There is sufficient room in a decoder using the cabinet and printed boards described in the original articles to mount an extra board containing the new circuitry, and the number of connexions between this board and the rest of the decoder has been kept to a minimum to simplify installation. In a decoder of this sort, the new board
mounts above digital board 1 in the area between the analogue board and the left-hand end of the cabinet. The new board, which will be referred to as digital board 3 , is principally intended to work with a decoder using the 74S262 (X887) character generator r.o.m., which provides the full upper and lower case teletext character set. A small modification will allow the board to be used with a decoder using the 2513 r.o.m., although since it occupies the area used by the add-on lower-case board, an alternative location for this will have to be found if the new board is to be fitted into a decoder of this sort.

Most of the connexions to the new board are to the existing edge contacts linking digital boards one and two, and therefore installation is particularly simple in a decoder using these boards. The only external modification required

Fig. l. Control character codes.

| Bits | $\begin{aligned} & b_{7} \\ & b_{6} \\ & b_{5} \end{aligned}$ | $\mathrm{O}_{0}$ | 0 <br> 0 <br> 1 |
| :---: | :---: | :---: | :---: |
| $b_{4} b_{3} b_{2} b_{1}$ |  | 0 | 1 |
| 0000 | 0 |  |  |
| 0001 | 1 | $\begin{gathered} \text { Alphan } \\ \text { rea } \end{gathered}$ | Graphics red |
| 0010 | 2 | $\begin{aligned} & \text { Alphan } \\ & \text { green } \end{aligned}$ | Graphies green |
| 0011 | 3 | Alphan yellow | Graphics yeflow |
| 0100 | 4 | Alpha ${ }^{\text {n }}$ blue | Graphics blue |
| O101 | 5 | A)phan magenta | Graphics magenta |
| O 110 | 6 | Alpha ${ }^{\text {n }}$ cyan | Graphics cyan |
| 0111 | 7 | $\begin{aligned} & \text { Alphan } \\ & \text { w, white } \end{aligned}$ | Graphics white |
| 1000 | 8 | Flash | CONCEAL DISPLAY |
| 1001 | 9 | Steady | CONTIGUOUS GRAPHICS |
| 1010 | 10 | End box | SEPARATED GRAPHICS |
| 1011 | 11 | Start box |  |
| 1100 | 12 | NORMAL HEIGHT | BLACK BACKGROUND |
| 1101 | 13 | DOUBLE HEIGHT | NEW BACKGROUND |
| 1110 | 14 |  | $\begin{aligned} & \text { HOLD } \\ & \text { GRAPHICS } \end{aligned}$ |
| 1111 | 15 |  | RELEASE GRAPHICS |

is the provision of a push-button "reveal" switch on the front panel.

## Control codes

The latest list of control characters, showing their binary equivalents, is given in Fig. 1. The control codes which are detected and implemented by digital board 3 are shown in capitals. There are nine of these in all. Usually these control codes are allocated in pairs, one of each pair to establish the specified mode and the other to end it. The exception is the conceal mode. There is no reveal control character; instead the normal revealed condition is established on the occurrence of any character in the shaded portion of the table (i.e. characters $0 / 1$ to $0 / 7$ and $1 / 1$ to $1 / 7$ inclusive).
A significant point, which has considerable importance in the detail of the circuitry adopted, is that before the advent of graphics hold, all control characters were displayed as spaces. This meant that the transition between two display modes was invisible, and could take place anywhere within the control character rectangle. Now that the control character rectangle may be filled with a "held" graphics character, it is important that a change in display mode (e.g. a colour change) takes place at the boundary of the character rectangle, not part way across the character. Certain of the display modes are said to be "set at" the occurrence of the control character, meaning that the mode changes at the left-hand boundary of the control character rectangle. Others are "set after" the control character, meaning that the mode does not change until the right-hand edge of the character rectangle.

## Circuit description

As mentioned, it is required to change certain of the modes at the left-hand edge of the control character rectangle. To achieve this, it is necessary that the data at the output of the page store have been stable for a period sufficient to decode the control character. In the
original design the left-hand edge of the character rectangles corresponded to the point at which the data latches on the outputs of the page store were clocked. This does not provide the necessary period, so with the new board the character rectangle is redefined as being one clock-pulse later than before. Conveniently this means that the rectangle edges correspond to the negative edge of the signal on $\mathrm{IC}_{1}$, pin 8 (January, 1976 issue), and all mode changes are made synchronous with this edge. For simplicity, the alphanumeric characters are not moved by a corresponding amount, and this means that these characters are displaced slightly to the left with respect to the character rectangles. This is of no consequence, however, as there is still a gap between the character and each edge of the rectangle.

As can be seen from Fig. 1, all the control codes of interest have bits b6 and b7 at logic 0 , and bit b4 at logic 1 . These bits are gated together by $(106,13)$ and ( 101,3 ), and gated additionally with a mixed blanking signal at $\mathrm{IC}_{101}$, pin 6 , giving a logic 1 at $\mathrm{IC}_{10 f}$, pin 10 for valid control characters (Fig. 2). This leaves bits b1, b2, b3 and b5 to be decoded into a separate signal for each control code.

Examples of the new facilities, obtained using the modified decoder.

A 4-line-to-16-line decoder could have been used for this purpose but, since only nine of the control codes are of interest a saving can be made by inverting bits b3 and b5 and using a 4-line-to-10-line "decoder $\mathrm{IC}_{113}$, (7442). Bits b3 and b5 are inverted so that the inputs to $\mathrm{IC}_{113}$ corresponding to the wanted control codes are in the range 0000 to 1001 ( 0 to 9 ). Binary inputs in the range 1010 to 1111 res' it in none of the outputs being enabled.

As explained, the display modes must change at the edge of the display rectangle. This is achieved by feeding the inputs to $\mathrm{IC}_{113}$ from a D-type latch $\mathrm{IC}_{112}$, clocked by the signal from $\mathrm{IC}_{1}$, pin 8 inverted in $(118,8)$. Bits bl and b2 are fed directly to the inputs of this latch from the page store output latch $\mathrm{IC}_{28}$ on digital board 1 (February 1976 issue). Bits b3 and b5 are fed to $I_{112}$ via NAND gates $(101,8)$ and $(101,11)$ which perform the dual function of inverting them, and gating them with the valid character signal at $\mathrm{IC}_{106}$, pin 10 . If this signal is at logic 0 (i.e. "invalid") pins 12 and 13 of $\mathrm{IC}_{113}$ are clocked to a logic 1 , thereby disabling all its outputs.

There remains the reveal signal to be generated, which as mentioned corresponds to codes $0 / 1$ to $0 / 7$ and $1 / 1$ to $1 / 7$ inclusive. Gates $(106,1),(118,2),(111,3)$, $(116,11)$ and $(116,8)$ provide this signal, which is synchronised with the character rectangles by $D$-type latch $(117,15)$.

Graphics generator. A completely new graphics generator is included on board 3 , the original on digital board 2 being made redundant. The reason for this is twofold. Firstly, the necessity to provide the held graphics function means that the graphics character has to be stored in a latch so it can replace a control character when required. Secondly, the separated (or "non-contiguous") graphics facility requires a more complex graphics generator. To have incorporated the original graphics generator circuit would have entailed an excessive number of interconnexions.

The graphics generator operates as follows. The six-bit latch $\mathrm{IC}_{102}$ contains the held graphics character. This is defined as the last character having bit b6 at logic 1, so the clock (pin 9) is gated with b6 in $(116,3)$. This latch also synchronizes the graphics character with the character rectangle. Data selector $\mathrm{IC}_{107}$ selects either bits bl, b3, b5 or bits b2, b4, b7 according to the state of pin 1 , which is a logic 0 for the left-hand half of the character and a logic 1 for the right-hand half. On the outputs of $\mathrm{IC}_{107}$, therefore, are the three bits corresponding to the three vertical cells in that part of the graphics character (Fig. 3). $1 C_{104}$ is a 16 -line to 4 -line data selector (multiplexer), the address inputs of which (pins 11 to 15) are driven from a vertical (line) address which runs from 15 at the top of the

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Us:hou: HOLD:
an
Yith HOLD:

character rectangle, through 0 , to 8 at the bottom ( 10 lines in all, or 20 counting both fields). This is the same address which feeds the character generator r.o.m. on digital board 2 . Assuming $\mathrm{IC}_{-121}$, pin 9 is at logic 1, the gates in $\mathrm{IC}_{109}$ act simply as non-inverting buffers. The inputs to $\mathrm{IC}_{104}$ are connected to the appropriate outputs of $\mathrm{IC}_{107}$ to give the correct allocation of lines to the three graphics cells - 3 to the top cell, 4 to the middle cell and 3 to the bottom cell. This symmetrical arrangement gives a slightly better appearance than the 4-3-3 scheme adopted in the original decoder.
The decoded control signals for contiguous and separated graphics at

Fig. 2. Circuit diagram of the new digital board 3 .
the outputs of $\mathrm{IC}_{113}$ are fed to a latch comprising $(120,8)$ and ( 119,8 ). This latch is additionally set to the contiguous condition by the inverted mixed blanking signal at $\mathrm{IC}_{124}$, pin 8 as all rows start in the contiguous state. The specification states that the contiguous/separated graphics mode shall be part of the structure of the held graphics character, so the contiguous/ separated signal is fed to D-type flip-flop $\mathrm{IC}_{121}$, clocked by the signal at $\mathrm{IC}_{102}, \operatorname{pin} 9$.
When separated graphics are in use there are gaps between the graphics cells in both the vertical and horizontal directions. In the vertical direction this is achieved by $\mathrm{IC}_{109}$, the outputs of which are held at 0 in the separated mode thereby inhibiting the graphics on lines $1,4,7$ and 10 of the character
rectangle (counting from top to bottom). When separated graphics characters are vertically adjacent this gives a larger gap between the rows (2 lines) than between the cells within a character (1 line) but this is unavoidable.
In the separated-graphics mode $\mathrm{IC}_{106}$, pin 6 goes to a logic 0 , thereby allowing the signal on $(124,6)$ to be fed to the strobe input of $\mathrm{IC}_{104}$, pin 9 . This signal provides the gaps between the graphics cells in the horizontal direction (Fig. 3).

Display colour. To simplify the background colour circuitry, and to arrange that the display colour changes at the boundary of the character rectangle, a new display-colour selection circuit is included on the new board. For convenience the aphanumerics /

graphics selection is also included. The circuits on digital board 2 which originally performed these functions are no longer used.

If any control character in the shaded portion of Fig. l occurs, then the red, green and blue components of the display colour obtaining for the next and subsequent characters (display colour is a "set after" display mode) correspond directly to bits $\mathrm{b} 1, \mathrm{~b} 2$ and b 3 respectively. For example, if bit bl is 1 then the colour includes red. It will be remembered that this group of control characters is the same as that defining the reveal condition, and a corresponding signal is available at $\mathrm{IC}_{117}$, pin 15. By gating this signal with the charac-ter-rate square wave in $(111,6)$ a suitable signal is obtained for clocking the display colour latch $\mathrm{IC}_{115}$. To the

D-inputs of this latch are applied bits b1 b2 and b3, all delayed by one character and inverted. By inverting the inputs and using the $\overline{\mathrm{Q}}$ outputs of the latch, the clear input (pin 1) may be used to set the display colour to white at the beginning of each row. the fourth latch in $\mathrm{IC}_{115}$ is used to store the alphanumerics/ graphics signal (which is a function of the state of b5) in the same way.

Background colours. It may be helpful at this point to explain how the set at and set after requirements are met. The occurrence of a control code of interest causes one output of $\mathrm{IC}_{113}$ to go low for exactly one character period (or more if two or more identical control characters occur in succession). Both edges of this signal are synchronised to the edges of the character rectangle. If $\mathrm{a}^{\prime}$ set at
function is required, the signal is fed to a logic-0 sensitive preset, clear or enable input, causing a change to take place immediately it goes low. Examples of this are hold and conceal. For the set after function, the signal at the output of $\mathrm{IC}_{113}$ is fed to a positive-edge sensitive input causing the mode to change on the trailing edge of the signal. Examples of this are double height and release which are fed to the clock inputs of D-type flip-flops.

On the occurrence of a new background control character the current display colour is adopted as the new background colour. Both new background and black background are "set at" modes, so what is required is a 3 -bit latch to store the background colour with level-operated preset and clear inputs. One device which meets this



Fig. 3. Waveforms in the graphics generator. In the separated-graphics mode, only the small squares are illuminated.
requirement is the four-bit counter 74177, and this is used as $\mathrm{IC}_{110}$. It is used simply in its parallel load mode and the clock inputs are disregarded. The black background signal is combined with inverted mixed blanking in $(123,8)$ to ensure that all rows start with a black background.

The data selector $\mathrm{IC}_{105}$ switches between the background colour on its A inputs (in r,g,b form) and the display colour on its B inputs according to the white output signal from digital board 2 ( $\mathrm{IC}_{67}$, pin 6). Mixed blanking is fed to the strobe input, pin 15.

Concealed display. Conceal is a set at mode and reveal a set after mode. Consequently, the conceal signal is fed to the clear input of a D-type flip-flop ( 114,1 ) and the reveal signal to its clock input, pin 3. Inverted mixed blanking is fed to the preset input (pin 4) to ensure all rows start revealed. The signal at pin 6 , which is logic 1 for conceal, is gated with the signal from the manual reveal switch in $(120,6)$ and fed via $(119,6)$ as a blanking signal to digital board 2 .

Held graphics. Graphics hold is a set at and graphics release a set after mode. The other half of $\mathrm{IC}_{114}$ is used as the hold/release latch with the hold signal from (113,3) fed to pin 13 and release from $(113,4)$ to pin 11 . The connexion to pin 10 presets the latch to the release condition at the start of each row.
the signal at $(19,13)$ is a logic 1 during a control character and logic 0
otherwise. This signal, fed via $(119,12)$ and ( 119,6 ) normally blanks control characters. However, if graphics have been selected (and during a control character this can only occur in the held graphics mode) the blanking is disabled by the signal at $(118,6)$.
The select graphics signal at $(122,10)$ can only go to a logic 1 under the following circumstances. Gate input $(122,8)$ must be at $\operatorname{logic} 0$, that is, the display must be in the graphics mode, and neither a 'blast-through' alphabetic character ( 122,11 ) nor a control character in the graphics release mode $(122,12)$ must be selected. Blast-through characters are identified by b6 being 0 and $b 7$ being 1 . During a control character in the graphics hold mode $(116,4)$ and $(116,5)$ are both at logic 1 . Output $(116,6)$ is therefore also at logic 1 and $(122,13)$ is forced to logic 0 . If in graphics rather than alphanumerics mode, $(122,8)$ is at logic 0 and $(122,10)$ goes to a logic 1 to select the held graphics character for display. At the same time the normal control character blanking is inhibited by $(118,6)$ as mentioned.

Earthing the test-point $(119,1)$ disables the control character blanking so that all control characters are displayed (except when hidden by being in the same colour as the background). This can be useful for fault diagnosis, especially with the 74 S 262 character generator which includes special characters for all the control codes (Fig. 4). With the 2513 character generator the control characters will appear as various alphanumeric symbols.

Double height. This is the most complex of the new display modes as it involves two distinct processes. The first is the


Fig. 4. Symbols generated by the Texas 74S262 character generator.
switching between single and double height characters across a particular row, and the second is arranging that any row containing double-height characters is read from the page store twice in succession, the row which would normally occupy the position of the second of the pair of rows being ignored, i.e. not read from the store. Additionally, on the second row of the pair any characters which are not double-height characters must be displayed as unboxed spaces.

Double height is a set after mode and the signal at (113,11) is fed to the D-type clock input $(121,3)$. Normal height is a set at mode and, gated with inverted mixed blanking in ( 123,11 ), the signal at $(113,10)$ is fed to the clear input $(121,1)$. The data selector $\mathrm{IC}_{108}$ switches the vertical (line) address of a character from the normal $0,1,2,3$.. to one changing at half the rate (i.e. $0,0,1,1 .$. ) thereby expanding the top-half of the character to fill the whole row height. On the first of a pair of double-height rows, and on all normal rows, the only purpose of $\mathrm{IC}_{103}$ is to subtract 1 (add 15)
from the vertical address in order to move the alphanumeric characters down one line. If this is not done, characters (from the 74 S 262 r.o.m.) can run into graphics in the row above. This does not apply to the 2513 r.o.m.s and a small modification of this part of the circuit to inhibit the subtract 1 function will be described later.

The section of the double-height circuitry described so far results in the first row of the pair being displayed correctly - normal height characters being displayed conventionally and double-height characters having their upper-half stretched to fill the rectangle.

The function of $\mathrm{IC}_{125}$ and the associated gates is as follows. If there are no double-height characters in a row, $(125,8)$ and $(125,12)$ remain at logic 0 . Although both flip-flops are clocked after every row, their J and K inputs are at logic 0 in each case, and in this condition the outputs remain unchanged (Table 1.). Pin 1 of $\mathrm{IC}_{123}$ is therefore at logic 1 and the most significant bit of the line address is fed, inverted by $(122,4)$, to $\mathrm{IC}_{11}$, pin 11 on digital board 1 . The positive-going edge of this signal clocks the row address once per row in the normal way.

If a double-height character is present, $\mathrm{IC}_{125}$, pin 6 will pulse to a logic 0 , thereby changing $(125,8)$ to a logic 1 . This has no immediate effect but at the end of the last line in the row, when the signal at $(122,6)$ goes low, this is prevented from reaching $\mathrm{IC}_{11}$, pin 11 and the row address is not incremented. Instead, as $(125,14)$ is now at logic 1 , pin 12 of $\mathrm{IC}_{125}$ changes from logic 0 to logic 1. This signal feeds $(108,2)$ and, when a double-height character is selected, it becomes the most significant bit of the vertical character address. That is, instead of the address counting $0,1,2,3$. it counts $8,9,10,11 \ldots$ What is required, however, is for the address to count $5,6,7,8$ to display the lower half of the character correctly. This is achieved by $(103,7)$ and $(103,11)$ going to logic 0 , thereby subtracting a further three from the vertical address. The double-height characters are now displayed correctly.

The signal at $(125,12)$ is also gated with the output of the normal/ double-height latch to give a signal $(120,3)$ which is zero for all nor-mal-height characters in the second row of a double-height pair. This blanks the display via $(119,5)$ and also inhibits the cut-hole signal at $(123,6)$.

| CLK | $J$ | $K$ | $Q$ | $\bar{Q}$ |
| :---: | :---: | :---: | :---: | :---: |
| $\Gamma L$ | 0 | 0 | $Q_{0}$ | $\bar{Q}_{0}$ |
| $\sqrt{L}$ | 0 | 1 | 0 | 1 |
| $\sqrt{L}$ | 1 | 0 | 1 | 0 |
| $\sqrt{L}$ | 1 | 1 | $\bar{Q}_{0}$ | $Q_{0}$ |



At the end of the last line in the second double-height row $\mathrm{IC}_{125}$ is again clocked. This time ( 125,7 ) is at logic 1 as is $(125,14)$. $(125,8)$ changes back to a logic 0 but $(125,12)$ remains unchanged. At this point $(120,12)$ is at logic 0 so $(123,3)$ changes to a logic 1 , thereby clocking the row address. Shortly afterwards $(120,12)$ goes to logic 1 . This causes $(120,11)$ to go to 0 and $(123,3)$ to return to 0 . At the same time (122,1) goes to $1,(124,3)$ goes to 0 and $(125,12)$ is cleared back to a zero via. $(120,11)$ this causes $(123,3)$ to go back to 1 . In this way a narrow negative-going pulse appears at $(123,3)$ of a duration equal to the total delay around the loop ( 120,11 ), 122,1 ), ( 124,3 ) plus the clear-to-Q delay of $\mathrm{IC}_{125}$. This narrow pulse is sufficient to clock the row address again, for the second time in quick succession. In this way the hidden row is skipped as required. Figure 5 shows the waveforms associated with the double-height circuitry.
The teletext specification states that after a change in either the alphanumerics/graphics or the normal/ double-height modes the held graphics

Fig. 5. Waveforms for the double-height mode.
character should be taken to be a space. This is arranged as follows. When in the alphanumerics mode, $\mathrm{IC}_{115}$, pin 7 is at $\operatorname{logic} 0$. If $(118,3)$ is at $\operatorname{logic} 0$, i.e. the next character is not a valid held graphics character, $\mathrm{IC}_{111}$, pin 11 goes low and the held graphics character is cleared to a space character. On a change from double to normal-height, or from normal to double-height, pins 12 and 13 of $\mathrm{IC}_{124}$ will be momentarily at the same logic level due to the delay through the RC network. A short negative-going pulse will occur at $(124,11)$ which will similarly clear the held graphics character via $(111,8)$ and $(111,11)$.

## Reference

1. Broadcast Teletext Specification, September 1976. Published jointly by the B.B.C., I.B.A. and B.R.E.M.A.

Notes on installation and testing will appear next month

## Literature Received

Two programme reference guides, published by Rapid Recall. list the assembly language in both ri:emonic and hex. machine language form for the 8085 and 8048 microcomputers. There is also a table of the ASCII character set and controls with hex. values. Rapid Recall Ltd, 9 Betterton Street, Drury Lane. London WC2H 9BS WW401

Static, uninterruptible power supplies for use with electronic equipment whose performance must not be affected by transients, voltage fluctuations or power failures are produced by Chloride Transipack. A brochure describes the systems, which rectify mains a.c., charge
batteries and convert back to a.c. by means of inverters. Chloride Transipack Ltd, Stanley Road, Bromley, Kent BR2 9JF

WW402
A chart illustrating many varieties of audio and power connector is obtainable from Beacon Audio Components, 282 Hatfield Road, St. Albans, Herts AL1 4UN .. WW403
More than 3000 components for use at r.f., including connectors, adaptors, leads and probes, are completely specified in a catalogue from Greenpar Engineering Ltd, P.O. Box 15. Station Works, Harlow, Essex

WW404
Microprocessors and i.cs for use in telecommunications are described in a booklet from National. Other equipment mentioned includes tone receivers, modems, touch-tone dialling. companders, filters and memories. National Semiconductors (UK) Ltd, 19 Goldington Road, Bedford MK40 3LF
. . . . . . . . . . . . . . . . . . . . . . . . WW 406

## Hall effect heads

A new type of tape recorder head has been developed which will give higher output, better signal to noise ratio and improved distortion figures, say the makers, Hitachi. The head uses the Hall effect principle, whereby a current flowing through a material along the $y$ axis and subjected to a magnetic field acting along the $z$ axis will produce a potential difference across the $x$ axis. The effect is most marked in a semiconductor. The induced $x$ axis Hall electric field, if the current along the $y$ axis is constant, is proportional to the magnetic field strength. If the magnetic field strength is varying, as when a recorded tape passes over the head, then the electric field appearing across the semiconductor will vary accordingly.

In the Hitachi stereo cassette head there are four sections, enabling simultaneous stereo recording and playback. In other words, cassette users will be able to monitor off tape as they record, as reel to reel machines have allowed for years. According the provisional specification the record and playback heads, with casing, are only 10 mm wide along the direction of tape travel, and the distance between record and playback heads is a mere 2.6 mm .

According to a New Scientist report the head is the subject of over a hundred patent applications round the world. The improvement in signal-to-noise ratio is 3 dB over conventional inductance coilheads. The semiconductor used is a thin film of indiumantimony. Hitachi's provisional specification says that at a flux level of -20 dB ref. 250 n Wbian the output level at 1 kHz would be around $280 \mu \mathrm{~V}$.
Hail effect devices are frequently used in measurement but it is only recently that they have been applied to domestic equipment. Matsushita, for example, have developed linear and switch-type silicon monolithic Hall i.c.s with a Hall element and amplifier on a single chip. This can be used for keyboard switches, microswitches, rotation detectors for tachometers, tape recorders and so on, and for position detectors for movie cameras and sewing machinies.

## Can you hear the picture?

Another spectacular tape recording development, again from Japan, is the use of video recorders to tape high quality sound. Sony are about to sell in Japan a black box for attachment to their Betamax video cassette recorder which will encode sound signals serially and put them on the half-inch video tape. Pre-recorded cassettes will be available which, when decoded through the playback section of the circuit, will give a dynamic range of 95 dB . The dynamic range for selfrecorded material will be about 85 dB , say Sony. Other measurements are: distortion $0.03 \%$ from 2 Hz to 20 kHz ; unmeasurable wow and flutter (because of storage and time-base correction); and frequency response 2 Hz to $20 \mathrm{kHz} \pm 1 / 4 \mathrm{~dB}$. The sampling rate is 44.1 kHz and, for the domestic version of the device, three words are put into each line, each word having two 13 -bit information portions and six parity bits. The box accepts line-level audio inputs on the record side. The selling price of the equipment, which can be attached to any conventional helical-scan recorder, will be around the same as the player, around $£ 800$.
It is reported that the Japanese broadcasters may transmit high quality sound signals after normal transmission hours. These will be received by the television set and decoded to the amplifier and speakers through the black box. Sony expect that the Betamax will now attract a great deal of professional attention since, although it would not normally be good enough for professional video use, the new technique will make it one of the best sound machines available, and one which offers a three-hour playing time. It was first introduced in Japan in May 1975. In November 1964 Sony introduced what they say was the world's first home use video recorder, the CV2000, and in October 1969 they announced the first video cassette system, the U-matic.

- The IBA announces that its director of engineering since 1966, Mr Howard Steele, is leaving to join Sony Broadcast at the beginning of 1978.


## Electronics takes two prizes

Two of the four prizes awarded this year by Technical Development Capital Ltd, a funding organisation, for outstanding innovation went to electronics companies. BSH Electronics (Manchester) Ltd shared the runnerup prize with two other firms, one of them SEMA Electronics Ltd of Dundonald, Ayrshire.

BSH's development was of a device for turning a rear windscreen-heater into a car aerial. The inventor, Mr J. J. Kropielnicki, got the idea for his Bi-Fi aerial when he was having trouble with his own, conventional, car aerial. His wife came and asked why he didn't use "the aerial in the back window," and he set about designing a system that would allow him to do just that. The device has the advantage that it cannot be torn off the car, like conventional whip or rod aerials, though we have no information about its efficiency with a variety of rear windscreenheaters.
SEMA's prize was for developing a light,
portable gas detector. According to its inventor, Mr R. L. Dries, the idea for this came to him when he had taken the top off a transistor 12 years ago, and found that the air contaminated it.

The first devices he made which used this phenomenon were too heavy to carry and consumed too much current, he said. He had to wait until the i.c. arrived before he could make the device as portable as he wanted. So far he had tested about 400 gases and it had worked on all of them. Each detector will only respond to one kind of toxic or inflammable gas, so the customer has to say what he wants the device to do. The detector and the electronics are then made to suit.

The detector and electronics can be in the same case, or a remote sensor can be fitted. "You can put half a mile of cable on the detector, it makes no difference." He could not say anything about the detector other than that it was a $\mathrm{p}-\mathrm{n} \mathrm{p}-\mathrm{n} \mathrm{p}-\mathrm{n}$ multiple sandwich. The detector reverted back to normal after the gas went away, so there was no ageing effect. Full scale deflection was a contamination of one part per million. "It will detect the unburnt combustibles in smoke. It will work in an inert atmosphere and doesn't require oxygen to make it work."
At the presentation of the awards Lord Seebohm, chairman of Technical Development Capital Ltd, a subsidiary of Industrial and Commercial Finance Corporation Ltd, said that 155 people had made submissions. "Before introducing the prizewinners I want to stress that the TDC award is not for the innovation itself. The new products are important ingredients ... but they are far from being the whole story ... In choosing the winners of our competition we have taken all aspects of the business into account." The winner, who showed a method of injecting concrete blocks with insulation, received $£ 10,000$ and each of the runners-up received $£ 1,667$.

## Ferrograph swap

The impending closure of the Ferrograph factory in South Shields has forced the National Enterprise Board to arrange a marriage between Ferrograph and North-East Audio Ltd (NEAL). Wilmot Breeden, who have owned Ferrograph since 1968, sold the company for nearly $£^{1 / 2}$ million. The announcement came in mid-October.
Ferrograph had been struggling for some time. Although the old Ferrograph Series 7 tape recorder had been well thought of, the BBC, for example, using it extensively, successors to the machine, the Super 7 and Logic 7, had met with a less than enthusiastic response. Had Wilmot Breeden closed the South Shields factory, moving production down to Bognor Regis, only the Ferrograph test set and the Studio 8 tape recorder would have remained in production. There were also long lines of communication between South Shields, Bognor and Wilmot Breeden's Birmingham headquarters.

Ferrograph's difficulties were compounded by problems elsewhere in the electronic division of Wilmot Breeden: the Wayne Kerr factory at Bognor Regis, for example, went on a three-day week last January "due to a run down in orders." The electronics division made losses of $£ 1 / 2$ million last year attributable partly to Rendar and partly to Ferrograph.
The Bognor factory went back to full-time working after 12 weeks, and a spokesman
told Wireless World, performance had now improved in other parts of the division.
The National Enterprise Board was anxious that Ferrograph's South Shields factory should not close. Its 140 or so employees live in an area which is suffering from even worse unemployment than other parts of the UK. The NEB approached NEAL with an offer to take a $49 \%$ stake in NEAL if they would take on the factory. The NEB holding means a new injection of $£ 400,000$ into NEAL, whose founder and managing director, Alan Helliwell, was chief engineer or chief executive at Ferrograph for the four years to 1972.

As well as audio tape recorders, Ferrograph make audio test equipment and marine echo sounders. NEAL will take over the whole range except for the ARAl cathode ray tube response unit, though this may be supplied in future under an o.e.m. arrangement with Wayne Kerr. The Studio 8 recorder is a professional machine designed to compete with the $£ 3,000$-plus Studer machines. It was developed in Bognor Regis and is now made under subcontract. All other Ferrograph manufacture, NEAL's own factory in Newcastle, and the sales office in London will be moved to South Shields.

Mr Helliwell told Wireless World that after a slow start the sales of the Studio 8 were doing "very well." As for the rest of the company: "We have quite a long job in front of us. We're not going to do it overnight. Our aim is to try to get it back to what it was ten years ago within three to five years, though if it takes five years I shall be disappointed." One of his first tasks would be to make the existing product range more attractive

He expected that, at the end of next year. NEAL would contribute half of the company's turnover. His approach would be to attack several markets from the base of the same technology rather than, as the Japanese had done, attacking one market with several technologies. The latter course was, in any case, rather expensive. It would be little more expensive to market Ferrograph and NEAL combined than NEAL alone since, for example, they only had to buy space for one exhibition stand to show both products.

## Light on records

Teac showed a record player that used a laser beam instead of a stylus at the recent AllJapan Audio Fair in Tokyo. Production won't begin for five years but the price is expected to be around $£ 320$ a unit. The development is expected to result in a large reduction in record wear.

## BBC's Wood commended

The Society of Motion Picture and Television Engineers (SMPTE) has awarded MrC . B. B. Wood, head of engineering infor mation at the BBC, a special commendation award for "his many outstanding contributions to motion pictures and television." Wood worked at the BBC research department for 26 years, during which time he had worked on television film recording. After the introduction of colour tv he was awarded the MBE in 1971. In 1968 he received the

Geoffrey Parr award of the Royal Television Society for his physics group's work in improving tv reproduction of colour films. In 1972 he won the Pye Travelling Scholarship for work on the standardisation of reproduction chromacities. He is a fellow of the SMPTE, an honorary fellow of the BKSTS, and a fellow of the Royal Television Society.

## Radio and the danger of explosions

A gas terminal receiving supplies from the Frigg field is so close to a new Royal Navy communications base that the radio waves from the base might cause a serious explosion, according to the British Gas Corporation. Yet the Admiralty, who decided to go ahead with the project, for which they have received a Nato grant, in 1969, before the St Fergus gas terminal three miles away was even contemplated, say they have no intention of closing the base down. The terminal will eventually supply $40 \%$ of Britain's gas.

The Navy's plans for the base at Crimond, Grampian, according to the Ministry of Defence, were known to Total and the British Gas Corporation before they went ahead with the terminal. The base is due to open formally next January, but test transmissions have been carried out and are continuing though at reduced power while an investigation is carried out.

The Navy told Wireless World that although the frequencies and powers to be used were not publicly available, this information had been given to the parties concerned with the building of the gas terminal when planning permission for the base was going through.

It has now been well established that radio waves, particularly those received near to a powerful transmitter, can cause the explosion of inflammable gases. Currents induced in metal pipes by the waves can cause a dangerous spark, and a British Standard, BS4992, has been published on the subject.


The two winners of the TDC award see "Electronics takes two prizes." Top: the inside of the Bi-Fi car aerial device. and (below) the SEMA gas monitor.

That standard places the onus for the protection of flammable gases on the user of the material so hazarded, and that is one reason why the Navy feel the British Gas Corporation should carry out any necessary protective work themselves.

British Gas say that when they first thought of building the terminal the hazards of radio waves were not much known or thought about by the scientific community. It was only after construction began in 1972 that enough work on the subject had been done to make it clear that it could be a hazard, and this was long after the Navy had sought planning permission, in 1970. The British Standard was published in April, 1974. Ironically, when British Gas sought a site for their terminal they investigated Crimond, and discovered the Navy had already got it.
Since "hundreds of millions of pounds," according to BGC, have already been spent on St Fergus, and Shell and Esso will also build terminals there for passing products on from the Brent field, there is no question of closing that down either. A committee of representatives from the Energy Department, Defence Ministry, British Gas and Total will now have talks and safety measures will be agreed, though it is not known how long this will take.

## Sinclair pushing tv

Production problems at the beginning of the year caused Sinclair to depart from the target dates they announced for the pocket television set when it was launched at the beginning of January. (See WW March, p.37.) Although Clive Sinclair had said that the sets would be in the shops in February, that did not happen until June, four months later.

The delay was partly attributable to faults in the case moulding, each one of the first batch of which had to be altered by hand There is also some doubt about the quantities now in production. By August they were making about 800 a month, and 1,000 a month after that. During September they had hoped to make 1,500 a month but the failure of delivery of tuner circuits cut that figure by a third. At an October press conference to launch a new calculator range and pocket multimeter, Clive Sinclair announced that production was "now 2,000 a month," and he expected it to reach 4,000 a month within two months.

Even allowing, however, for a little hyperbole in these figures the assertion that "demand exceeds supply" is probably correct. All the sales are being made either here or in the United States, roughly half each, and Sinclair expects the tv to account for half the company's turnover eventually.

The calculator market which built the company up is now very competitive, and Sinclair executives were finding this year's going a bit hard, which is why they welcome the success of the television and are keen to get production up to its (as yet undisclosed) maximum. There are immediate plans to increase capacity to about double the present figure.

There were reports that there had been some difficulty in the supply of the AEG Telefunken tube, which the German company are contracted to supply only to Sinclair for this application. Clive Sinclair denied this, and there do not at the present appear to be any plans to find second sourcing for the tube, though this may change next year if, as Sinclair hopes, the production of the sets
goes well ahead and the market opens up. After Christmas test marketing of the set will start in Europe.

Sinclair said at the October press conference that the investment by both the NEB and the company, which now totals over £2 million, had resulted in large increases in turnover; in November, he said, turnover would be "at least double" that for the same month last year. And profits were also "increasing very rapidly."

The cost of producing a colour version of the set may be prohibitive, at least for the present, but, in view of Sinclair's growing interest in the instrument market, it would not be all that surprising to see a pocket oscilloscope being launched at some future date.

## Beeb's light drama to be lighter

The BBC is conducting a one-year experiment in electronic news gathering. The first use of the new equipment was for an interview with Mrs Margaret Thatcher on October 10 , just before the Conservative party conference and it now averages two or three stories a day.
A Range Rover has been equipped with a Philips LDK11 camera, Sony $38003 / 4 /$ in Umatic cassette video recorder and Sony Trinitron monitor, radio telephone, and transmission links. An intermediate Microwave Associates 13 GHz "window" link, with a range of about $1 / 2$ mile, will be able to transmit to the e.n.g. unit. From there a more powerful $21 / 2 \mathrm{GHz}$ link will carry the signals to the top of London's Millbank Tower, and cable takes them to Television Centre. Other base stations, which have already been tested, will be used when necessary. Where a cable link already exists for outside broadcasts, at such places as Downing Street or Heathrow Airport, the video cassette can be played straight down the cable. Another choice is to send the cassette to the Centre by dispatch rider, as already happens for cans of exposed film.
The BBC plan to do live inserts into television news broadcasts. This has been done in the past with the o.b. units, sending signals back over temporary links.

The BBC have been carrying out tests with various types of equipment for the past two years, and talks with the unions have been going on for the past 18 months. Film camera crews have been retrained to operate the electronic equipment. The Range Rover will have a crew of two, plus a reporter who will travel separately and the despatch rider, if needed. The LDK11 camera is rather larger than the latest cameras available for such uses. Nevertheless its weight compares well with that of a 16 mm camera. Crews can either connect the camera to the recorder on the van, or carry the recorder with them for playing back later from the vehicle. The reporter can play back the whole tape and send cues and editing instructions to Television Centre via the radiotelephone. Cue numbers are visible on an elapsed-time readout, and are generated by an SMPTE code on the tape.
The BBC are not giving any figures for the cost of the experiment, though it is believed that the equipment of the van cost about $£ 70,000$, and another $£ 30,000$ was spent on associated equipment at the centre. This total of $£ 100,000$ will be written off over five


The BBC's experimental e.n.g. unit. See "Beeb's light drama to the lighter."
years. A film unit costs about $£ 20,000$ to put on the road, but the operating costs of the electronic unit are believed to compare favourably with those of film units, since there are no processing costs and the "stock" can be re-used almost indefinitely. Ten minutes of film cost about $£ 20$. All in all the comparative total costs of e.n.g. as against film are about the same over the five-year period.
The small new cameras have other advantages besides being easily carried. According to BBC engineers the $2 / 3 \mathrm{in}$. tubes are more sensitive than those of larger cameras, giving a picture even in bad light. In this respect they perform about as well as film cameras, but they have the added advantage of giving a better colour balance. Editing is allelectronic, and will take about the same time as film editing does now, though no time has to be spent on film processing, which can take from 40 minutes to an hour.
The Corporation is doing a great deal more lightweight production for drama and current affairs than on news, which represents only about $9 \%$ of output. According to a BBC spokesman, $67 \%$ of $t v$ output was now being produced electronically, and they aimed to enable drama producers, for example, to use locations as easily as they now did the studio.

This can have important benefits on productivity. Whereas a production unit might only shoot four minutes of film a day because there might be doubt about the light conditions, for instance, with electronic production this can be increased to 12 minutes a day or more because the results can be seen immediately without processing. Twocamera units using lin tubes have been operating in drama for about three years. The next step would be electronic field production using lin helical scan tape recorders instead of the heavy 2 in tane machines now. in use, which do not offer still-framing.

The BBC has just bought three Ampex $V$ PR1 recorders for news and current affairs in Newcastle and Manchester, and these and other machines may be bought for drama.

## Solar power nearer

Bell labs are developing solar cells made from liquids and solids which may be more economical than all-solid cells. They are also easier to make and may last longer, say Bell.

Efficient but expensive solar cells may be made from joining two single crystal solids, and less expensive but less efficient cells can be made from the junction of polycrystalline materials. The latest liquid junction Bell cells also use polycrystalline materials but are cheaper to make: "Junctions in all-solid cells are difficult to form since layers of crystals must be aligned precisely," say Bell. "Liquids conform to solids easily."
The new cells comprise two electrodes in a water-based solution. One of the electrodes is a semiconductor, the other carbon or any of a number of common metals. When light falls on the semiconductor current flows from one electrode to the other via the liquid. The solution is of sulfide polysulfides in water, and it remains relatively unaffected by the process. This accounts, say Bell, for the long life of the cell, up to four years in one case. In one experiment using cadmium selenide as the semiconductor electrode over a twentieth of the incident light was converted into electricity, about two-thirds the efficiency of the equivalent crystal cell.

An $8 \%$ efficient solar cell is reported to have been made by annealing with a single 30 ns laser pulse, according to a report from the 1977 Photovoltaic Solar Energy conference in Luxembourg. The result is a silicon ion-implanted solar cell. Another cell, a 50 mm thick silicon cell uniformly thinned by etching in sodium hydroxide has a claimed efficiency of $14 \%$.

A Rugby engineer, Mr A. T. Freeman, has built a solar powered car which does not store the energy in conventional batteries. The solar cells, made by Ferranti, are mounted in a canopy above the vehicle's seat and produce a maximum of 35 W . Eventually he wants to produce a commuter vehicle which will have a roof area of $50 \mathrm{ft}{ }^{2}$, producing 250 W , and a maximum speed of $15 \mathrm{~m} . \mathrm{p} . \mathrm{h}$. The cost at the moment would be $£ 5,000$ for the panels but he is hoping this will eventually come down to $£ 50$.

## The electronic arm of the law

Suffolk Police háve installed over $£ 100,000$ worth of computer equipment at their headquarters at Martlesham, near Ipswich. The equipment, to control the force's more than 200 vehicles, to record incidents, to switch messages between teleprinters in a communications network, and to hold local criminal, firearm, and stolen property records, was commissioned in November 1975 and installed beginning in the summer of the following year.
Currently 96 of the police vehicles are fitted with terminals which link them with the computer. Fourteen v.d.u.s are in divisional control rooms at Ipswich, Bury St Edmunds and Lowestoft, and at force headquarters. The message switcher controls 18 teleprinters distributed among the 13 sub-divisions and force HQ , and also a journal printer. The processor used is the GEC 4080 , supported by 256 kbytes of core store, two 35 Mbyte discs, magnetic tape, line and logging printers, and communications interfaces operating at 2,400 baud to the v.d.us. and 75 baud to the teleprinters.

The Suffolk police regard the installation as one of the most advanced of its type, even though they are aware that computer technology has advanced some way since the system was commissioned. The force serves a community whose industry is expanding rapidly and includes the port of Felixstowe. The new police headquarters, housing 250 police and civilian workers, is a showpiece, and, like the computer system, has been designed to meet Suffolk's needs well into the future.

Suffolk is one of the latest forces to embrace the computer. Recent reports in the specialist and daily press have mentioned the growing police use of computers in Glasgow, Staffordshire, the West Midlands, Devon and Cornwall, Lincolnshire, Tayside, the Thames Valley, and, of course, Scotland Yard. Many more of the country's 43 police forces are taking advantage of the computer, and there is no doubt that one day they will all be computerised. Those not already so may be using their local authority's computer for the same purposes.

## Stations linked

The arrival of the blue serge computer is mainly attributable to the shortage of police manpower, and to the related switch in emphasis in recent years from crime detection to crime prevention, or, as the bobbies inimitably put it, "pre-emptive policing." But while this is a desirable end, serious doubts have been raised by the means.

There is nothing a computer can store that could not be stored on paper. The difference is in the amount of storage and the ease with which it can be made available. Data banks can hold enormous amounts of information with very little trouble, and when what was previously held in paper files is so easy to store there is an overwhelming temptation to add all sorts of other details as well.

All of the 800 police stations in England and Wales are linked to a national police computer, based on a Burroughs main frame, at Hendon. This project went ahead in 1969 and began to operate in 1974. According to the Government white paper on computers and privacy, "its purpose is to automate the storage and retrieval of certain of the records
held by the central and regional criminal record offices and by police forces: these comprise indices to records of persons convicted of serious offences, wanted and missing persons, stolen vehicles, disqualified drivers and persons with suspended sentences."

In addition, each day a new tape is carried from the vehicle registration computer at Swansea to Hendon for feeding into the computer. This is necessary because the Swansea computer works on tape and that at Hendon on disc, for speedy access. This transfer means that, by 1979, the names of 26 million people owning licensed vehicles will be on the file, along with address, date of birth, details of the vehicle and other codes saying whether, for example, the car is "of long term interest to the police." The total number of entries will be over 36 million.
This contradicts a government assurance that the information used for one purpose would not be transferred for use in another. There are some 220 central government administrative tasks which are computerbased. The public sector has around 3,500 computers at its disposal, roughly a quarter of the private and public sectors' combined total. The Home Office and Metropolitan Police joint automatic data processing unit, for example, has five central processors, 500 staff and a capacity of 768 kwords. The Home Office has spent $£ 14$ million on computers in the last ten years. In January, 1973, Computer Weekly reported that detectives on a murder enquiry had searched the memory banks of a Department of Health and Social Security computer, looking for a letter. Also, the Balcombe Street bombers were partly tracked down by tracing a computer dating form from a Time Out advertisement which one of the bombers had filled in as a joke.

The police national computer also has a fingerprint file for those convicted and sentenced over the last 40 years (the Rehabilitation of Offenders Act, 1974, is supposed to ensure that a conviction vanishes from the record after five years) as well as those awaiting trial, a total of 2.25 million people. Those detained under the Prevention of Terrorism Act also had their fingerprints taken and stored even though 19 out of 20 of them were acquitted. (See Wireless World, September 1975, p.406.) A Police Review editorial once described the information in police files as "frequently libellous".

## BBC

This is why the government white paper stresses that the disclosure of criminal and other records from the computer is not "given to anyone, however responsible unless considerations of public interest justify it." Yet the number of people with authorised access includes public bodies or authorities about to employ doctors, dentists, nurses, youth leaders or teachers, civil servants, atomic energy and Post Office temporary or permanent staff, magistrates, justices of the peace, barristers, solicitors and their clerks. Astonishingly the paper also admits that "the police also maintain the practice, which does not have specific statutory authority. of helping social services departments in considering applications from adoptive and foster parents." Anyone
who applies to join the police, or for an award under the Criminal Injuries compensation legislation, or for a gaming licence, is also checked.
In addition, a recent book* by Patricia Hewitt of the National Council for Civil Liberties points out that criminal records are also available to anyone who knows how to work the system. For this reason the practice, not confined to security firms, of hiring ex-policemen has become widespread. The most outstanding example must be the BBC's appointment of Robert Huntley, former head of the bomb squad, as security adviser. Anyone applying for a BBC post might therefore expect the corporation to have access to a great deal of information which would otherwide be closed to them. Yet when the police computer was being set up, in 1971, the then Home Secretary, Mr Reginald Maudling, told the Commons that access to it would be restricted to the police.

## Passport details

Scotland Yard's computer provides a good example of the dangers computers may present. In February this year The Times revealed that nearly 1.5 million names of criminals "and their associates" will have been fed into the computer by 1985. Stewart Tendler wrote that Scotland Yard refused to say whether this would include the names of those suspected of subversion. A $£ 900,000$ contract for the computer was announced last year but no other details, including the name of the winner of the contract, for which 25 companies competed, have been released. However, Computer Weekly revealed in December last year that the contract had gone to Computer Technology Ltd, one of their biggest ever orders.
Notably, the records include passport details and details of bankruptcies. "By 1985," said The Times, the computer could be storing information equivalent to one-fifth of the population in the area of the Metropolitan Police's jurisdiction." Although the kind of information stored is supposed to be subject to the guidelines in the white paper, and therefore not secret, Scotland Yard refused to say why the information was classified, or whether it was true that an astonishing two-thirds of the names on the biggest section of the records was of suspects and associates rather than people with criminal records.

A further report in September said that the names of tens of thousands of those suspected by the special branch "for reasons of national security" were to be fed in. The special branch was allocated up to 600,000 of the total capacity of $1,300,000$ names for such purposes.
No one doubts that the tracing of bombers and murderers after the event, still more the prevention of their atrocities beforehand, is of benefit to the community, though more often the beginning of the spiral is the mere tracing of stolen cars. But, the objectors say, these worthy objects may become the excuse for producing just the kind of peephole society the bombers would resort to if they ever found themselves in control. Be it noted that, after nine years' work, the FBI has abandoned a national police computer it was to set up in the United States, largely because of the privacy issue.

[^4]
# Low-cost pickup arm using mono-filament suspension needs no special tools 

by Ernie Lowinger

Many pick-up arm designs for home manufacture have already been described in the journals. Most very beautiful, all very sophisticated and requiring the use of a lathe and appropriate precision. For some time I have sought the design for an arm that could be made with a minimum amount of costly precision tools.
My aim has been to produce an arm with performance as good as there is, with a reasonable appearance, from inexpensive materials which are easily found, using tools I have to hand: electric drill, drilling stand, assorted drill bits and a few BA taps.

I have tried miniature ball races and found them too temperamental. A unipivot is out, hydraulics and knife edges, too. For some time I toyed with the idea of a thread or wire suspension. The idea finally took form when I saw two mentions of a thread-suspended bias compensation device in some old WW and HFN \& RR copies ${ }^{1}$. This was not my first acquaintance with this form of

First design, in whch the arm is carried by the signal wires.
thread suspension. It defeated me the first time I met it as an A-level maths problem years ago. This time I derived the equation $M=W a^{2} / h$, where $M$ is the moment, W the total mass of the arm, 2 a the distance between the threads, and $h$ the suspension height (see Appendix 1). The principle is simple enough; the arm is supported by only two threads, Fig. 1. The resulting friction is due only to the torsional hysteresis of the threads and the lead-out wires. I have made two arms utilising this principle. One uses fine nylon fishing line for suspension. The other uses the signal wires themselves to support the arm.

In addition to having low inherent friction the principle also offers automatic bias compensation. The moment on the arm is proportional to the sine of the angle of rotation of the arm, i.e. $M^{\propto} \sin \theta$. If $\theta$ is centred on $90^{\circ}$ then for a sweep of $23^{\circ}$ of the 9 -inch arm there is only $4 \%$ variation in M .

## Bias compensation

To arrive at some concrete dimensions for the threads and the values of $a$, $h$ and W it is necessary to look into this bias business.


If a cartridge is offset $\phi$ degrees in a pick-up arm, Fig. 2, but is tracking a groove tangentially, there will be a force $F$ due to friction along the cartridge's longitudinal axis. This force can be resolved into a pull along the arm P from the pivot, and a pull $S$ at right angles to the arm which tends to make the stylus ride the inner wall of the groove harder than the outer

$$
\begin{aligned}
& S=F \sin \phi \\
& P=F \cos \Phi
\end{aligned}
$$

(see Appendix II)

Now $\mathbf{F}$ is proportional to the tracking weight and it has been shown ${ }^{2}$ that the constant of proportionality can be anything between 0.2 and 0.9 , depending on the record and this can be increased by $80 \%$ more on highly modulated grooves in the case of certain cartridges. It is quite likely that a resulting variation of $10: 1$ can occur across one record. One might well ask, is it worth the bother?
Anyway, to start somewhere the equation has become

$$
S=k G \sin \phi
$$

where $G$ is the tracking weight and $k$ a constant. Taking a mean value for k of 0.5 , a 9 -inch arm with an offset angle of $23^{\circ}$ and a range for G between 1 and 3 grams, S ranges between 0.2 and 0.6 grams. This means a moment at the pivot of a 9 -inch arm of between 1.8 and 5.4 gram-inches, if you will forgive the mixed units. This is the range of the values for M in the equation $\dot{M}=W a^{2} / h$.

## The crunch

The effective mass $M_{e}$ of a pick-up arm (moment of inertia divided by the square of its length) and the compliance C of the cartridge combine to give a resonant frequency $f$ of bounce on the stylus suspension, according to

$$
f=\frac{1}{2 \pi \sqrt{ } M_{\mathrm{e}} C}
$$

I have read ${ }^{3}$ that $f$ should be kept between 6 and 14 Hz and to achieve this with the range of cartridge compliances likely to be met, the arm less cartridge should have an effective mass of 7 to


10 gm . Generally speaking, this implies a fairly massive counterweight kept close to the pivot point. This is because inertial moment and therefore effective mass is proportional to the mass of the counterweight but the square of its distance from the pivot.

There is, however, a counter constraint. If $M=W a^{2} / h$, we may have to keep $W$ down to keep a and $h$ convenient and still have the right M. In the event, it has been quite easy to keep the effective mass in the 7 gm region with an all-up W of 58 gm , and if we take a as $3 / 16$ in (i.e. the threads are $3 / 8 \mathrm{in}$ apart), then $h$ becomes about half an inch. This can be increased to lin for a value of 1.8 for M. However, if you want to increase M, I think it would be better to increase ' $a$ ' than reduce $h$ much more, otherwise the stress on the wire may be excessive (see Appendix 3).

## It wobbles

You might think that, dangling there on the end of its thread, the arm would be rather unstable. You are right. However, analysis shows that the wobbles are predictable and amenable to control.
Going back to the first principle, there are three modes of oscillation in the system that cause trouble, Fig. 3.
$A$ - The lower rod $A B$ can rotate about its own longitudinal axis.
B - The lower rod AB can swing at the end of the two threads moving in tandem about an axis at right angles to the projection of the two threads in plan, Fig. 4.
$C$ - The lower rod $A B$ can swing about the upper rod $C D$ and its longitudinal axis.
Not sure which would be the best orientation for the threads, I made two arms. In the first, Fig. 5, the lower rod $A B$ forms a part of the rod itself. The four signal wires emerge from small holes in the arm tube. Two of the wires actually carry the arm. The remaining two are loosely coiled to contribute as little torsion as possible. The wires are held firm in the arm by simply tying a large knot which jams in the hole, just behind the cartridge, through which the wires enter the arm. At the other end the wires are 'knitted' into a tubular hollow cross tree. The suspension height can be varied by tweaking the wires in and out of the holes. This cross tree is now the upper rod CD. It acts as the pivot axis for vertical movement of the arm. So we have no trouble from the type (c) wobbles. Type (a) wobbles do happen. This is because the centre of mass has to be below the line joining the stylus tip and the support point on the arm, to stop it falling over. So you end up with a sort of compound pendulum. This wobble is difficult to eliminate. By keeping the centre of mass quite low down the period of oscillation is kept long and partially damped by the sogginess of the support wires. The method used for damping type (b) wobbles helps a little too, but I found


Second arm has its own cross-member and uses nylon threads.
that jolts on the turntable can set it wobbling. However, the movement only lasts a second or so and, more important, does not cause any audible modulations. Wow.

Type (b) wobbles are the worst. I found the easiest way to control them was to drape some cotton thread over a piece of wire and fix one end to the pivot point of the arm. The other carries a small weight. The two or three $1 / 8 \mathrm{in}$ lead split shot do quite well. The friction of the thread running over the wire seems to be enough to damp them out completely. As the arm is only free to wobble in one direction, one thread and weight is enough.

The disadvantages of this arm are that the vertical pivot point can be rather high above the surface of the record for low tracking weights and small anti-skating forces, Fig. 6. Also bias adjustment is rather fiddly and so rarely undertaken. On the other hand it is simple and neat and performs well with a G820SE tracking at $1 \frac{1}{4} \mathrm{gm}, 2 a$ $3 / 8 \mathrm{in}$ and h lin.

In the case of arm number two, the suspension is turned through $90^{\circ}$ about a vertical axis, Fig. 7. The arm itself has to have a cross tree to take the ends of the support threads. These threads are $11 / 2-\mathrm{lb}$ breaking strain nylon monofilament. This has very little inherent damping property so I had to be a bit more careful. The cross tree acts as the axis for vertical movement of the cartridge, and so type (a) oscillations do not happen. Both types (b) and (c) occur in this arrangement, the last-mentioned taking the form of the whole arm swinging about a line joining the stylus tip and the upper end of the support threads. These wobbles caused considerable wow and flutter when left undamped and required two threads of the type already described, at right angles to each other, to damp all
tendency to movement in the horizontal plane. They are completely effective but the wire frame to carry them is a bit of a contorted shape. With its easily adjusted suspension, height and therefore bias compensation, very low friction and complete lack of wobbles this arm is magnificent but looks very Heath-Robinson-esque. I use it at present with an ADC Q36 tracking at $1 \frac{1}{4}$ gm again.

Both arms were simple to make but incredibly fiddly to assemble and set up. The horizontal pivot point does not stay in one place as the arms move across the record, variation in the friction component P along the arm causes some change. This, however, is not a problem as long as the arm is set up to give zero tracking error at the end-ofside, as is usually the case.

## Friction

To keep friction low the important thing is to use sufficiently fine lead out wire, and finding it was a bit of a job. Writing to SME for a free sample would be admitting defeat so I kept looking. For arm number one I found some four-way screened and sleeved wire supposedly for pickups, but stiff as a salami. However, when you strip off the sleeving and screening, you get four pretty coloured p.v.c. covers each containing 10 strands of 47-gauge copper wire, untinned, and these are quite flexible.

For the second arm I used $3 \times 48 \mathrm{~g}$ Litz wire, enamelled and silk covered. This is about as fine as one would wish to use, somewhat fragile and a trifle tricky to solder. Four pieces are cut to length and then given different colours with fibre tip pens.

Measuring lateral and vertical friction is a bit of a problem because of the constant lateral moment. However, I must confess to not having seriously tried. Inspection of the only sources of friction, the lead-out wires and nylon support threads, would I am sure convince the reader as it did me that in the case of arm number one the friction at the stylus is approximately sweet F.A. and with arm number two, even less.

## Materials

Arm: You can buy 12 -inch lengths of 10-gauge fine-wall aluminium. tube from any good model shop for about 10 pence. It weighs about 5 gm and can be bent with bare hands. It is quite strong, too. It is $3 / 16-\mathrm{in}$ o.d. and nine-inches serves for the arm, the remaining three inches take the counterweight.
Counterweight: 1 used about $1 / 4 \mathrm{in}$ of $11 / 4$ in brass bar. Aim for about $1 \frac{1}{2} 02$ weight.
Support pillar: $1 / 2$ in aluminium tube from Band I TV aerial.
Cartridge retaining clip and finger lift: 16-gauge aluminium sheet.

Support base: I happen to have some green perspex $1 / 8$ in thick and some clear 1/4in.

Arm 1
Cross tree: 12-gauge brass tube from model shop.

Arm 2
Thread carrying platform: 1/4in clear perspex and 6BA threaded brass rod
Cross tree in arm: 10 -gauge welding rod.
Damping frame: 18-gauge steel wire
Various 8, 6 and 4 BA screws and nuts, and that is about it.

## Construction

I can say nothing that will improve on the constructional information published by $H F N \& R R$ and $W W$ in the past ${ }^{4}$ and construction of this arm is clearly much indebted to the work of Bickerstaffe. The March and April 1971 articles are particularly useful as regards the arm itself and I will not try and improve on them. Suffice it to say that the arm must be bent to give a $23^{\circ}$ offset angle and the pivot region should be on the same horizontal plane as the stylus tip. As cartridges tend to vary a bit in dimensions it will have to be set up for the individual case. The head is undercut, and various holes are started off with a sharp metal spike. The tube wall is very thin; once it is penetrated the actual-size hole can be made with an appropriate drill worked between finger and thumb. In the case of very small drills, wrap the shank in scotch masking tape to a thickness of $1 / 4 i n$. I found this the most accurate and least damaging way to cut holes in fragile tubing.

A hand vice is required for drilling all the other components. Brass and perspex both tend to snatch when being drilled. Low revs and a very slow feed rate are essential. Even so I frequently saw the hand vice whirling round the drill shank at $900 \mathrm{rev} / \mathrm{min}$. The drill would be well and truly bent and the vice and workpiece then hurled through the window, or worse into one's guts.

Threading the signal wires into the arm is fairly straightforward if one uses nylon thread or 5 -amp fuse wire to pull through. Similarly the hollow cross tree in arm number one. In arm two, the solid cross tree is glued into the arm with Araldite.
The support platform in arm two is fiendishly hard to set up the first time but easier when you know how. Assemble the height-adjustment screw in the platform. Take a good foot of nylon line. (You only need a foot but you have to buy a hundred yards; still, it's cheap enough.) Form a blob at one end, about $y_{11}$ in diameter, by holding the end near a gas flame. This is a finnicky task and you burn through a few foot lengths of line before you get a good secure blob. Now pass the unblobbed end down
through one hole in the screw, a hole in the perspex platform immediately below, then up through the adjacent hole $3 / 8$ in away and up through the appropriate hole in the screw. Now form another $1 / 16$ in blob at the other end of the line. Carefully turn the adjustment screw and the nylon line should fit neatly in the grooves in the screw. Three full turns gives about an inch adjustment.

Fit the platform to the support pillar and pass the nylon loop under the cross tree in the arm. The arm will 'already have its signal wires inserted. The wire can now be threaded through the support pillar.

It would be nice to terminate the wires in a smart plug-socket arrangement at the bottom of the support pillar. After a long search I found a miniature seven-pin plug/socket that does reasonably well, Fig. 8. I filed the threads of the plug part and soldered the arm wires to it. It just fits in, and is retained in the bottom of the pillar by an 8BA screw. The screw also carried a fly lead to earth the pillar itself. (The arm is earthed by a fly lead from one of the cartridge earth tags.) The socket part is soldered to the twin screened lead which leads to the amplifier.

There is a lot of unscreened signal wire floating about. I was worried about this and made up some screening cans from aluminium film canisters. However, during use I found that they made absolutely no difference and with the cartridges I use and $68-\mathrm{k} \Omega$ input impedance at the preamp there is no hum at full volume. So I threw the screens away.

The pillar base is a sandwich of green perspex top and bottom and clear in the middle. Total thickness $1 / 2 \mathrm{in}$. The whole is solvent welded with chloroform. You can get this from your chemist but you will have to sign the poisons book.

Drill the $5 / 8$ in mounting hole $83 / 8$ in from the record spindle.

## Further development

The pick-up arm can be suspended by any number of threads. If their upper ends are regularly spaced about the
circumference of a circle of radius a centred on the upper support, and the lower ends of the threads similarly fixed about a circle centred on the pivot point of the arm, then the couple on the arm, ceteris paribus, remains $\mathrm{Wa}^{2} / \mathrm{h}$.

The theoretical pivot for both lateral and vertical movement of the arm, is a point halfway between the centres of the upper and lower circles.
With regard to stability, there will always be one direction in which such an arm would be free to wobble. It will therefore require one stabilizing thread arranged as in the Type I arm. The lateral stability will be enhanced however.

A simple way of making such an arm will be to use a modified Type I arm with all four signal wires doing the supporting. The stress on the wires, and their slight tendency to creep will be correspondingly reduced.

## Conclusion

They might not require a lathe, but instead require extensive wrestling with almost invisible bits of wire and thread. However, they work, and better than I had ever hoped. They are not beautiful, but they have a certain rustic charm. They are certainly cheap.

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Type 1 arm in use.



## The power game

The power race that has taken such a firm grip on m.f. and h.f. broadcasting in so many countries (appropriately enough Babel, Iran now has a 2MW m.f. station) has not left British amateurs unaffected. Without wishing to be a spoil-sport one cannot help feeling that the authorities never foresaw the power levels that have been made possible by exploitation of (or misunderstanding of?) the Post Office ruling some years ago that British s.s.b. stations could use up to 400 watts p.e.p. output, although this was clearly meant to be directly comparable with the 150 W d.c. input for a.m., f.m. and c.w. modes.

Today some British amateurs are using power amplifiers that for any mode other than s.s.b. would be rated at over lkW d.c. input; for example the Heathkit SB220 which is usually listed as 2 kW p.e.p. and the popular K2RIW design of a 432 MHz stripline amplifier with two ceramic tetrodes, considered in the United States as a "kilowatt" amplifier. Such amplifiers often have two 4CX250B or 8930 valves for u.h.f. or t wo $3-500 \mathrm{Z}$ or similarly rated valves for h.f., with power supplies built to provide over 2.5 kV at up to $800-1000 \mathrm{~mA}$ on peaks, occasionally with heavy r.f. speech clipping to increase significantly the duty cycle of the transmissions.

While such equipment may be operated under legal conditions for s.s.b. it must be extraordinarily difficult to get them down to anywhere near l50W d.c. input when used for other modes. 'On u.h.f. such equipment, combined with high-gain aerials, can provide effective radiated powers as high as $50-100 \mathrm{~kW}$, without taking into account the clipping "gain"

Again, the more stringent power limits imposed by our licences for 1.8 MHz ( 10 W d.c. input and on s.s.b. 26.67W p.e.p. output) do not appear to prevent many amateurs using on that band medium power h.f. transceivers that have no facility for reducing power and which, I am assured, often cannot readily be operated within the authorised power levels. An American 1.8 MHz enthusiast, recently visiting the UK, was prepared to identify certain

Top Band signals he hears regularly and is convinced do not emanate from 10-watt transmitters.

If only to sort out the current confusion, and if the Home Office have no objection to the use of high-power transmitters for s.s.b., should they not authorise at least a similar legal 400 W p.e.p. output for c.w., f.s.k., and n.b.f.m.? Or is this a case where it is left to "self-regulation"?

Many amateurs believe that our radio and tv set makers could do more to make receivers, broad-band mast-head amplifiers and audio equipment far less susceptible to high r.f. fields - but it is asking a lot for domestic equipment to be capable of operating directly in a 100 kW e.r.p. beam of a nearby transmitter.

At the other end of the scale, David Johnson, G4DHF, recently made more than 40 contacts on the 144 MHz band during a low-power contest at distances up to 300 km : he was using a home-made transceiver based on the SL600 series of integrated circuits with an r.f. output of about 25 mW .

## Scanning the bands

The British Amateur Radio Teleprinter Group reports a membership increase of 40 per cent during the past year to over 550. John Jones, GW3IGG, suggests that the advent of visual display units for "teleprinter" display has brought many new operators to r.t.t.y., even though most stations are currently still using hard copy machines for two-way contacts, and he wonders if the increasing interest in such display units and microprocessors is beginning to make the very term "radio teleprinting" seem too restrictive for moden usage. Reginald Wigg, G6JF, has worked some 136 different countries on r.t.t.y. and is the top British station in BARTG's "quarter century award" listings. Leading station is the Belgian ON4BX with 154 countries. Brian Hodgson, G3YKB, has taken over the editorship of the group's newsletter (now appearing in an attractive new format) in succession to Dennis Goacher, G3LLZ who has produced the newsletter for eight years.

The Home Office has now resumed licensing the additional amateur repeater stations forming "Phase 2 " of the RSGB's current plans but has stated that it is not prepared to consider any applications beyond this present phase for the time being. It has expressed concern at the high sites being chosen for some of the repeater stations. However, preliminary planning for Phase 3 is continuing. With London now served by three u.h.f. repeaters, the UK FM Group is considering closing down the GB3LO v.h.f. repeater which continues to be subject to many abuses by a group apparently wishing to bring repeater operation into disrepute.

French beacon stations have been allocated the prefix "FX" and will
include: FX3TEN on 28.227 MHz ; FX3VHF on 50.1 MHz ; FX0THF on 144.741 MHz ; FX3THF on 144.905 MHz ; FX7THF on 144.985 MHz ; FX9UHF on 432.5 MHz ; FX4UHF on 432.83 MHz ; and FX6UHF on 432.870 MHz . French v.h.f. activity generally appears to continue to increase rapidly as indicated by the recent story of 200 stations on 144 MHz , 36 on 432 MHz and 3 on 10 GHz for their 1977 National VHF Contest.

A novel constructors' contest has been announced by the East London RSGB Group. This is for home-constructed items of audio, electronic or radio equipment "totally enclosed by a $20 z$ tobacco tin with lid." Optionally the power supply need not be enclosed in the tin.

## H.F. transmitting aerials

Bob Haviland, W4MB, recently analysed the aerials used by 500 stations contacted on 21 MHz . Just over half were using Yagi beams ( $80 \%$ of them 3-element, $5 \%$ 2-element); second most popular group were the verticals (mostly multiband types using traps); just under $15 \%$ were various forms of "quad" loop arrays (nearly all of them 2-element); dipoles (including invertedvees) formed $12 \%$ of the total; "long wires" only $1.8 \%$. Height of the elements varied from 10 ft to over 120 ft ( $12 \%$ over 80 ft , average 10 ft ). In Europe the simpler aerials form a higher percentage of the total. A check on 100 stations on 14 MHz and above showed 38 using ground-planes and other "verticals"; 31 were using various types of dipole (including W3DZZ trapped dipoles and G5RV multi-band dipoles); only 13 were using 3 -element Yagi arrays; 7 were using 2-element "quads".

## In brief

The 1978 president of the RSGB will be Dr Dain S. Evans, G3RPE, who for a number of years has been one of the leaders of the "more activity on microwave bands" group that has done much to popularise 10 GHz in the United Kingdom .... By August 31, the number of UK amateur licences had risen to over 22.750 (Class A 16,205; Class B 6559) .... ITU international prefixes now include J 3 for Grenada and H 4 for the Solomon Isles . . . . The Braun SE401 series of 144 MHz multi-mode transceivers is believed to be the first commercially-built amateur equipment to use a v.f.o. which used a $64 \mu \mathrm{~s}$ PAL-type delay line as a integral part of a stable phase-locked-loop system It was exactly 50 years ago, at the Washington Radio Conference of October-November 1927, that 350 delegates of 74 countries first defined internationally the harmonically related amateur bands from 1.7 to 56 MHz .

PAT HAWKER G3VA

## CONTROL OF SOLAR HEATING

Congratulations to Mr K. D. C. Passey (Letters, October issue) for highlighting the importance of an electronic control system for solar energy pre-heating of domestic water. However, it is also important that the complexity of any control/recording system be kept within practical limits, so that the extra cost of the control/recording system and its maintenance does not exceed the extra saving over a less sophisticated electronic control.
The necessity for limiting pressure drop due to lime deposition may of course be avoided completely by using an indirect solar heating system.

We are at present conducting tests on a prototype electronic control system specifically designed for solar applications, which uses a true double differential technique, where the pump "turn-off" condition is determined completely by the pump requirement and the selected flow rate. It would be quite possible to monitor various functions by connections to points within the circuitry and to have a display panel some distance from the control proper.

We plan to start work in the near future on a control system based on microprocessor techniques which should certainly afford the user the kind of assistance advocated by Mr Passey.
F. B. McKee

McKee Solaronics L.td.
Southminster
Essex.

## MATRIX H SURROUND SOUND DECODING

In commenting upon certain statements in Michael Gerzon's multisystem and J matrix decoder (July issue), might I point out that not everyone can afford room-space or expenditure for two stereo speakers, let alone the four or six proposed for the J matrix. Popularly, stereo listening is being accomplished on headphones (two or four channel).

Since the phase-shift between left and right channels does not significantly widen the subjective "stereo-head-phone" stage width in the same way as when listening on conventional stereo loudspeakers, it is thus important that the encoding locus should pass through left-only and right-only if only to achieve good compatibility for the many thousands who listen to f.m. stereo ( H "quad") on headphones. It is thus not an "unnecessary design restriction" as Mr Gerzon seems to suppose. At least SQ and (in theory) RM, QS and BMX (Nippon Columbia UD4) also give full headphone separation in stereo at least for pop-surround and dramatic programme material, some broadcasts of which have been surprisingly impressive on "binaural" listening.

Secondly, the deliberate curvature of the H matrix locus allows non-conjugate decoding or at least some measure of compatability when using decoders of other systems, e.g. even a simple " $10-40$ " blend SQ decoder may be used if the connections to speakers are rearranged. Connections are: $\mathrm{SQ}-\mathrm{L}_{\mathrm{B}}$ becomes approximately equal to $H$ front, $S Q-R_{H}$ becomes H centre-back (two speakers in series in the left-back and right-back positions can be fed with this "ambience" signal, which in theory contains little matrix H centre-front since $S Q$ (blend) right-rear is almost $160^{\circ}$ from the front on the phase-amplitude sphere), $S Q-L_{F}$ becomes $H$ due left, and $S Q-R_{F}$ becomes H due right.

The deliberate curvature of the H-matrix locus is by no means caused by a departure from "optimal" three-information-channel source encoding. For instance, the bent locus is produced by the matrix below from only three microphones: (i) omni-directional plus (ii) figure-of-eight oriented left-right plus (iii) another figure-of-eight oriented front-back, approximately as below, $\theta$ measured clockwise from centre-front.
(The equation is covered by the BBC patent.) Thus it follows that since H does not have to be produced from pairwise-panpot fourchannel sources, the "third-matrixed (narrowband) channel" technique to restore most of the $3 \rightarrow 2$ channel matrix "lost" information is as applicable to H as to J ; and is not therefore a function of locus-straightness as has been suggested by Mr Gerzon et al.

Finally, the difference in rate-of-change of curvature (phasebending) between the front-half H locus and the rear, could well be the reason that stereo headphone listeners can get some impression of front-back stereo perspective.
T. W. J. Crompton,

Crawley,
Sussex.

## RADIO AND AIR SAFETY

Letters printed in your August, October and November issues brought to my attention the editorial in your June issue "Radio and air safety." As an experienced broadcast engineer with direct involvement in v.h.f. broadcasting and as an experienced and active airplane pilot, I tend to agree with the letters which you published. Your editorial is

$$
\left[\begin{array}{l}
\mathrm{L} \\
\mathrm{R}
\end{array}\right]=\left[\begin{array}{l}
0.53 \mathrm{e}^{-175}, 0.36 \mathrm{e}^{+773},-0.53 \mathrm{e}^{175} \\
0.53 \mathrm{e}^{+175}, 0.36 \mathrm{e}^{177}, 0.53 \mathrm{e}^{175}
\end{array}\right]\left[\begin{array}{l}
1=\mathrm{omni} \\
\cos \theta=\text { fig. } 8 \text { (iii) } \\
\sin \theta=\text { fig. } 8 \text { (ii) }
\end{array}\right]
$$

based on an inadequate knowledge of aeronautical voice communications systems and equipment and the role of voice communications in air traffic control.
There is nothing inherently wrong with a.m. mode or with 2.7 kHz bandwidth. My experience with audio quality on 747 class civil airliners and all the control towers that I hear is that audio quality is quite good and background noise level is low. There may be problems with aircrews whose command of the English language may be less than is desirable. There may be faults in the system, i.e. the architecture of the communications system, which is a "party line" - no control on "access" to the communications channel. Basic aeronautical transmitters and receivers are OK. We may not be using them properly all the time.
I suspect the final Tenerife accident report will show that the reception of one of the control instructions was partially "blocked" by another aircraft transmitting at the same time. This is not a fault of the equipment but rather one of "system architecture." The Tenerife communications channel was not noisy or significantly bandwidth limited.

1. Switzer

Switzer Engineering Services Ltd
Mississauga
Ontario. Canada

## USING A MICROPROCESSOR

There are some difficulties in using the 8080 -based microcomputer discussed in J. Skinner's article, "Using a Microprocessor" (June and September issues)

The interrupt input to the 8080 is wired to a switch (START) which is used to allow the 8080 to get past the halt instruction (HLT) at address 5 . For this to operate properly it is necessary to have an interrupt instruction port wired to input a single-byte, eight-bit instruction to the 8080 during the interrupt acknowledge cycle. This is not shown. It is readily accomplished with another 8212 integrated-circuit. A no-operation (NOP $=00_{16}=000_{R}$ ) should be the instruction hard-wired at the interrupt instruction port.

Mr Skinner's 8080 configuration may be useful in some applications, but it eliminates the use of the interrupt for other, more sophisticated tasks. Perhaps there will be further articles which cover this important topic. If interrupts are to be preserved a sense flag could be used to replace the interrupt connection. In this way a single bit is input and tested under software control. This requires the use of a spare bit position at an input port. If this is not available, a single input port is quickly constructed. Mr Skinner's circuit is simple and just what may be needed for a simple application where thousands are to be manufactured without further changes or add-ons. Readers should be aware, however, that there are other uses of interrupts.
After providing the register codes in three-bit octal (base eight) notation it is sad to see that the programme is encoded in hexadecimal (base sixteen) format. This is not Mr Skinner's fault, but rather the fault of all 8080 manufacturers. The octal format is much easier to encode and understand.

As per the information in Table I in the article, each of the 8080's registers is given a
three-bit binary code which is easily translated into an octal equivalent, $A=111=7 \% \quad \overline{-}=010=2$, etc. The data transfer instructions provided in the article are good examples of the use of octal notations.

The data transfer between registers is broken down as follows:

$$
01 d d d \operatorname{ss}
$$

where the ddd is the binary code of the destination register and the sss is the binary code of the source register. Each of the three groups shown above is then translated into an equivalent octal code:
MOVAD $=01111010_{2}=172$;
MOVHA $=01100111=147$
MOVLA $=01101111=157$
MOVAE $=01111011=173$
MOVEA $=01011111=137$
It seems much easier to code the instructions in octal than in hexadecimal. The last two examples prove the point by showing that when destination and source registers are interchanges, the octal digits representing the registers are interchanged in the instruction code. This is not apparent in hex notation; 7 B to 5 F .

Memory addressing is also simple, remembering the rule that memory is treated as 256 word blocks with a high address and a low address with the same grouping of bits as shown above:

$$
X X \quad X X X \quad X X X
$$

The rule is that when the low address goes from 377 to 000 the high address is incremented by one; 001377 to 002000,125 377 to 126000 , etc.

I was pleased to see that Mr Skinner's programme listing is on a line-by-line basis as opposed to other programme listings which tend to cram too much on each line.
Jonathan A. Titus
Tychon Incorporated
Blacksburg
Virginia, USA.

## DIRECT SENSING OF RADIO WAVES?

For some two years I have been plagued by clearly defined environmental "noises" which I have come to believe to be of electromagnetic as opposed to acoustic origin. Advertisements in the national press have yielded dozens of letters from people all over Britain who appear to experience similar phenomena which are often attributed to medical/psychological causes. My own observations, however, indicate that my sensation of "noise" is due neither to causes within myself nor to acoustic stimulation from outside.

In order to test my theory I wish to construct a tuner-amplifier to feed a loudspeaker or cathode-ray oscilloscope but since the frequencies are in the audio band ( 400 to 1100 Hz ) the design of a suitable tuner presents some difficulty. Since my knowledge is limited and rusty, I would be most grateful to any interested reader who could offer me technical assistance. I shall be only too pleased to answer any letters.
Donald Wood
Oaklands
Dorstone
Hereford
Editor's note: Mr Wood's letter was accompanied by a confidential report from a highly reputable research organization which lends some support to his belief.

## MINIMISATION IN LOGIC DESIGN

I have been following your series of articles on logic design with interest, but find some of the underlying assumptions puzzling. The methods are said to be mainly for the use of inexperienced designers, although specialists may use them to improve their technique in dealing with more sophisticated assemblies. To this end a trade-off is presented where reliability, simplicity and transparency of design are won at the expense of a certain extravagance.

Minimisation is said to be relatively unimportant in the days of i.cs, where what counts is to minimise chips, not gates. However, since the sort of circuit discussed can often be part of something larger, a single gate saved here and there can quite easily lead to the elimination of a chip. Nor do the extravagances seem to be always small. Take Example 2 in the July issue (p.65), a circuit which allows a pulse on input M to gate a single pulse from a train on input $X$ to input Z. It uses, besides, a dual J-K flip-flop, a 3 -input NAND, two 2 -input NANDS and two inverters. These can be replaced by a single OR gate as below (assuming t.t.l.| clock polarities):


This configuration would never stem from the advocated design method for two reasons. First, it uses the Clear inputs. They were dealt with in connection with RS flip-flops but not included in the J-K equations - which hardly meets the practicalities of the t.t.l. and c.o.s.m.o.s. families. Secondly, the method assumes common clocking.

Example 1 is more debatable. Unless I have misunderstood the problem, state S3 is stable because its effect is to inhibit further clocking pulses. If so, the authors have demonstrated the vulnerability of their method in even the most expert hands by wasting four gates (quite possibly a chip) in providing redundant stability, since their circuit would work with $K_{A}=6$ and $J_{B}=4$. A further gate can be saved as shown below.

This is achieved by altering the assignments of $A$ and $B$ to the various states. The authors skate over. that topic as not concerning the inexperienced designer: presumably they are not addressing themselves to their specialist readers at this point.
I imagine that in practice the inexperienced designer means the infrequent designer like myself. I have not found minimisation as
unimportant as the articles suggest. On the other hand, while I agree that the mathematical steps advocated are simple enough when you are familiar with them, familiarity involves remembering a lot of highly forgettable detail. Each occasional foray into logic is likely to involve mugging the whole thing up again, which defeats part of the object of the exercise. In contrast I have found mapping methods, which are not developed at length in the series so far, easy to remember and usefully graphic and instinctive.
R. M. Hutton

London SW4
The authors reply:
First of all we would like to point out that at no time have we suggested that minimisation of the number of gates or the number of chips used in a logic system is unimportant. Clearly, considerations of space and power consumption are two important aspects which the designer must balance against the charge for his time occupied in producing a simpler solution. However, what we are saying is that a formal method of approach to logic design is essential to the inexperienced designer and also to the student, whose difficulties we have frequently encountered.

For example, the solution presented for Example 2 seems to us to be an intuitive solution. A design method has not been presented and we can only assume therefore that there isn't one. Intuitive solutions are fine if the designer can come up with one that fits the specification, but it is very useful to have a formal method of approach available when one's intuition fails. As the logic problem becomes more complex intuition becomes more difficult to apply and it seems to us that under these circumstances a formal approach to design through the state diagram is absolutely essential. Our experience with students has been that once they have grasped the design method described in these articles they are very quickly able to develop quite complex logic systems with a minimum amount of difficulty.

Obviously we are aware that the Clear line of a JK flip-flop can be used as an additional control line. It is not possible to cover all these points in a series of articles of this nature. However it is easy to set up a truth table for the JK flip-flop incorporating the Clear signal which leads to the following equations

$$
\begin{aligned}
& Q^{t+n t}=[C l(J \bar{Q}+\bar{K} Q)]^{t} \\
& \bar{Q}^{t+n t}=(\overline{\mathrm{C}} l+\overline{J Q}+K Q)^{t}
\end{aligned}
$$

and similarly it is equally possible to draw a state diagram for the flip-flop which

incorporates the Clear signal as shown above.
B. Holdsworth and L. Zissos


# PROGRAMMABLE CALCULATORS as REACTION TIMERS 

My letter in the August issue on calculators as stopwatches aroused a little interest. Here is a project which grew out of the programme for the digital clock. The question arose: what are the minimum intervals of time to which the Texas SR56 can respond? My guess was: the time required to go from one operation to the next without performing any operation (a NOP instruction). How could this be used? In a reaction timer.

A programme was written in which the calculator displayed a count-down sequence: $5,4,3,2,1$, using the "pause" instruction. We then had a counting loop in order to have a pause of length different from that of the intervals between the previous digits. " 0 " was then displayed for about $1 / 2$ second, using the "pause" instruction. The person whose reaction time is under investigation has to press the R/S key immediately he realises that the " 0 " has disappeared. The programme has a string of NOP instructions after the instruction to display " 0 ." When the programme has been stopped by pressing the R/S key, to find the score it is necessary to press the LRN key. This shows the location in the programme memory at which the programme was stopped. An operator who is quick on the draw scores a low number. If he cheats, and presses the R/S key while " 0 " is still in the display, he reads the location after the "pause" instruction, which in my programme is stored at location 14.

Locations 15 to 94 have NOP instructions. It takes between 1 and 2 seconds to run through them; this gives an idea of the resolution of which the calculator is capable. At location 95 we have a few instructions to set up the calculator for another run. When the operator is ready for another test, it is only necessary to press the R/S key to start the count-down sequence.

In a group of students, the calculator can find the one with the best reaction time. Those with poor scores (including me) simply cannot - despite practice - bring them down to the score of a good sharpshooter. My interest was in writing the programme. I have not used it to study the effect of alcohol on my reaction. This one may be new to the wife.

The programme is entered after storing 5 in Register 0. The programme starts at location 00.

RCL; 0, PAUSE; dsz; 0; 0; I; 0; STO; 0; dsz; 1 ; $0 ; 0$; PAUSE; NOP.

NOP; 5; ST0; 0; R/Ș: RST at location 99.

If we wish to find the time necessary for a NOP instruction, we can write a programme with 95 NOP steps and a few instructions to add I to Register 1. We can run the programme for 60 seconds and stop it with the R/S key; we then note the contents of Register 1 (and the line at which the programme stopped). We repeat the programme with 50 NOP steps. We can then write two simult-, aneous equations from which we can find the time for a NOP instruction and the time required to add I to Register I. In my calculator, the NOP instruction takes 0.0137 seconds.
Instead of a count-down sequence preceding the display of zero, we could arrange a slightly more demanding test. A programme is written which displays random numbers in
the range 0-99. Person $A$ controls the test: person $B$ is tested. A quotes a certain number, e.g. 57 , which he has stored at a certain register. $B$ observes random numbers appearing and, as soon as the chosen number has appeared and disappeared, he must press the R/S key as quickly as possible. After the chosen number has appeared, the programme branches to a series of NOP instructions; B's score is found, as in the first method, by pressing the LRN key. Of course, if the random number generator is arranged to give number up to 10 digits, the test is all the more exacting. With my programme for random numbers which may have up to 10 digits, we may have 18 numbers displayed in a minute, each display lasting between $1 / 4$ and $1 / 2$ second. (Part of the time, when the calculator is crunching numbers, the display is blank.) Reading a 10 digit number in the available time, and deciding that it is, or is not, the specified number, requires a certain mental speed.
T. Palmer

Acton Technical College
London. W3.

## SIMULATING BIRDS' GEOMAGNETIC SENSE

With some trepidation I refer to the magnificent correspondence entitled "Electrodynamically induced e.m.f.," which spanned nearly two years from its inception in "Cathode Ray's" articles "Electricity and Magnetism?" (September and October 1974) via the spirited introduction of the aeroplane problem by Todd and Taylor of Brunel University (July 1975). May I engage the attention of those readers who would agree with C. R. Masson (Cambridge), "Cathode Ray," D. H. Preis (Harvard), B. J. C. Burrows (Culham), D. Midgley (Hull) and me! in order to help solve what seems like a very nonWireless World problem - the geomagnetic sense in birds?

I have made a computer simulation of an "electrodynamic" model of bird flight. Due to wing-flapping the voltage-gradient varies, depending upon the instantaneous direction of the wing with respect to the direction of the geomagnetic field. The simulation resolves wing movement in three axes and the earth's field in two axes.
There are two effects, however, which I am unable as yet to incorporate:

1. What is the effect of the vertical electric field ( $\approx 100 \mathrm{~V} . \mathrm{m}^{-1}$ ) upon the small computed voltage gradient induced in the wing (varying between $\approx-200 \mu \mathrm{~V} \cdot \mathrm{~m}^{1}$ and $+100 \mu \mathrm{~V} \cdot \mathrm{~m}^{\prime}$ )? A simplistic argument runs like this: the external electric field is confined to the bird's surface, in the manner of Faraday's ice-pail, by induced charge. This charge gives rise to a leakage current which is limited by the low conductivity of the surrounding air to $\approx 10^{-1} \mathrm{~A}$, and which varies with movement of the wings in common mode for both wings. I fancy that a more advanced treatment would suggest that movement normal to an electric field gradient invokes a magnetic field at right angles which would modify the apparent direction of the geomagnetic field. 2. A varying voltage gradient in a bird's flapping wing demands ion movement to provide a "displacement" or "polarisation" current along the wing. If one uses values for wing "external" capacitance of the order of 10 pF , one is left with a value of displacement
current of $\approx 10^{-14} \mathrm{~A}$ which is uncomfortably low although possible. I would like to think that there is some analogous internal capacitance, which describes the separation of charge carriers normal to the planes of equal internal voltage, which might have the effect of increasing this small displacement current. At any rate, the phase of this current appears to vary with the bird's heading, at a frequency of twice the flap frequency of about 7 Hz in pigeons (i.e, narrow band f.m.).
As a matter of interest New Scientist describes bird navigation as "a central problem of avian biology!" Any offers?
B. Whatworth,

Addlestone,
Surrey.

## LOSS OF INFORMATION CONCEPT

I have noted the comments in your August issue both in the leader and in Mr Greenbank's letter.

The first point you both make is that I did not express the "loss of information" concept (Letters, June 1977) in engineering terms, i.e. those that can be objectively quantified. However, in the context of a letter to a magazine I do not feel that this was the place to expand these ideas any further. This does not mean that we do not or can not measure "loss of information" objectively. Now, as in my previous letter, I do not propose to go into great depth on the measurement of "loss of information" but perhaps if any of your readers are both interested and suitably equipped they may care to try the following test which is one of many ways of ascribing a figure to "loss of information".

Submit the equipment under test to two signals, one of large amplitude which reasonably stresses the equipment and a second which is of higher frequency and smaller amplitude. At the output of the equipment under test the large signal is filtered out so that the smaller signal may then be examined. In this test one is concerned with distortion of the small signal during and after stress conditions caused by the large signál - for example, in the case of an amplifer, driving an electromechanical load.

The difference in dynamic range between the two input signals may be as large as 90 dB . This test is the basis of a range of tests which can characterise the ability of a piece of audio equipment to reproduce a given dynamic range at one instant in time. This ability is crucial to the reproduction of music. I would propose however that conducting these tests on a piece of equipment after it has been designed is rather like shutting the garage door after the car has been stolen.

I was most interested by Mr Greenbank's disclosure that during amplification "latch-up" periods of $100 \%$ intermodulation occur. Intermodulation is a term that describes what occurs when two signals modulate each other, i.e. produce input dependent intermodulation products e.g. $f_{1}$ $-f .+f_{1}, 2 f_{i}-f_{2}$ etc. As far as I have uecil able to ubserve when an amplifier is "latched-up" it does not respond to input signal in any predictable way.
J. Vereker

Naim Audio Ltd
Salisbury
Wilts.

## AURAL SENSITIVITY TO POLARITY

I suspect that Dr Lipshitz (October Letters) and I are getting round to dealing with apparent misunderstandings rather than real differences of opinion on the question of polarity maintenance.

The polarity effects that Dr Lipshitz described do exist and have been known to exist for some 30 years and I discussed the generally accepted explanation. The waveforms of speech are known to be asymmetrical and either by coincidence or evolution the asymmetry just about compensates for asymmetry of the opposite polarity in the ear drum system. To maintain the compensation that nature apparently intended, it is necessary that our radio transmission systems maintain this polarity relation, a positive going sound pressure wave at the studio microphone producing a positive going sound pressure wave in the listening room. Polarity changes anywhere in the system will produce a change in the quality of the reproduced sound. However, the effect is subtle and I think that it requires equipment of professional quality if it is to be detectable.

I think Dr Lipshitz will agree that the experiments that he describes only tend to support this explanation.
James Moir
Jumes Moir \& Associates
Chipperfield
Herts

## ADVANCED <br> PRE-AMPLIFIER DESIGN

I found Mr Jung's letter in the September issue most interesting; it is gratifying to encounter someone who constructs and measures the circuit under discussion before commenting on it. However, I think it is important to distinguish clearly between the two types of restriction of output swing that occur at high frequencies in the type of disc pre-amplifier being discussed. It is, I think, better to stick with the accepted nomenclature and reserve the term "slew limiting" for that effect arising from the open-loop behaviour of an amplifier, and caused by finite currents charging and discharging compensation capacitance.

The other form of output restriction, which Mr Jung deals with under the same heading, is rather different, being peculiar to closedloop amplifiers with significant shunt capacitance in the feedback arm. This is of course precisely the situation that occurs in an RIAA equalised input stage where the gain is designed to be relatively low so that a high overload margin may be obtained (assuming that a gain control of some kind is then placed before any further voltage gain). The core of the problem is that the feedback-loop shunt capacitance falls in reactance as the frequency being handled increases, and so an increasing current demand is placed on the output section of the amplifier; if this cannot be satisfied then a form of clipping results, and the output capability (and hence the input overload
performance) is restricted at the top of the audio spectrum.
The output structure of the disc input stage of the "Advanced pre-amplifier" is a simple emitter-follower; this is much better at sourcing current than sinking it, and so Mr Jung's graph shows a curtailment of output capability at full drive and high frequencies, indicated by the abrupt rise of harmonic distortion that is typical of clipping. Examination will show that deformation of the output waveform only takes place on the downward half-cycle, due to the limited current-sinking capability, and in this respect the effect is quite different from what is normally known as slew-limiting.

It is at this point important to note that "full drive" is some 40 dB above the nominal operating level of the stage, so the effects discussed here are unlikely to be obtrusive in the day-to-day performance of the preamplifier. Mr Jung's graph shows that if the test signal amplitude is reduced by 12 dB there are no output-restriction effects in the audio band.
Finally, I have tested the effect of Mr Jung's modification (reduction of $R_{\mathrm{e}}$ to $1 \mathrm{k} \Omega$ ), and while the graph he displays is certainly correct in its essentials*, I feel it would be more meaningful to plot maximum available output swing against frequency. If this is done, it will be seen that the modification has its maximum effect at about 6 kHz , where another 3.2 dB of output voltage is available, giving a corresponding increase in input overload margin. However, the improvement diminishes either side of this frequency, falling to 1.0 dB at IkHz and to 2.4 dB at 10 kHz . Readers must judge for themselves whether this is worth the extra 14 mA drawn from the power supply; confirmed lily-gilders may care to note that the same improvement can be implemented without increase in the current drawn by replacing $R_{e}$ with a constant-current source delivering 6 mA .
D. R. G. Self

London E. 17
*The distortion figures shown for below 3 kHz seem rather high - in particular it is most suspicious that the t.h.d. at 1 kHz is shown as being higher at $1.25 \mathrm{Vr} . \mathrm{m} . \mathrm{s}$. than at 5 V r.m.s. I assume that the data shown includes the imperfections of the test equipment.

## CEE22 MAINS CONNECTORS <br> STANDARD OR FIASCO?

Do manufacturers of electrical equipment using the CEE22 mechanical size and shape connector have any requirement to use a particular pin configuration? Many of the mains leads for these equipments (if not the majority) are of the moulded variety, and consumers tend to implicitly trust these leads.

Although it is becoming less common, single pole switching is still in use and production. The danger lies in the fact that while the equipment is switched off or when the mains fuse is blown the internal circuit is still live if the live and neutral leads are reversed. God forbid the results if the earth lead is transposed!
To date I have found three different manufacturers issuing equipment with live/neutral transposed leads. Two of these put no names on the leads.

I hope more people can be made aware of the moulded lead quality control/nonstandard hazard.
K. A. Yates,

Glenrothes,
Fife.

## ELIMINATING ADJACENT-CHANNEL INTERFERENCE

I find the July 1977 issue article on eliminating adjacent channel interference by P. L. Taylor to be most interesting. I have been attacking the problem for some time and have also developed a system to attenuate in-band interference on double sideband transmissions. This has been demonstrated to operate well with in-band modulated carriers and numerous in-band tones. Noise is also reduced. However, I have been unable as yet to satisfactorily eliminate cross modulation between noise and the wanted signal, and, of course, depending on how noise theory is interpreted and extended, this may or may not be possible.

As the system is somewhat complex this is not the place for its description. However, I find the reactions that I have been receiving to it to be surprising and rather depressing, and unfortunately some of these reactions would also apply to Mr Taylor's design.

The first reaction from Canadian Government officials is that interference and noise do not pose any problem as current equipment provide noise-free reliable links. Next, the system only applies to double sideband transmissions. This form of radio communication is now obsolete and is being legislated out of existence to be replaced by single sideband. A reaction from Canadian industry is that it is too complex to warrant risking development money and would necessitate synchronous receiver operation which has proved unpopular in the past. From Canadian universities and research establishments comes the comment that the system cannot possibly work for noise because Shannon set the God-given limits twenty years ago and any suggestion that his theory can be developed to show more than 3 dB advantage for double over single sideband transmission is rank heresy; common interference reduction is of no interest. Incidentally, the one exception here is McMaster University in Hamilton, Ontario.

Double sideband amplitude modulation produces a unique signal having a "mirror image" frequency spectrum and constant phase. Interference can be detected in very much the same way as used to be employed in old movies for a man to tell whether or hot he was real. If he saw his reflection in a mirror he was real, if no reflection was there he had to conclude that he was a ghost.

There is the possibility that a double sideband signal can be lifted out of interference and noise to an extent that is an order of magnitude greater than current communications theory implies. It is not that the theory is wrong, it is that it is limited. Mr Taylor's system shows one approach, my own shows another.
L. Illingworth

Cerman \& Milne
Montreal
Quebec. Canada

# Microcomputer design 

## 2 - Practical hardware and software

by Phil Pittman, B.Sc., in association with NASCO Ltd

Having now looked at the various elements which constitute a microcomputer system we shall move on to examine the detailed construction and operation of a practical system. The example chosen is based on the microcomputer kit shown as a block diagram in Fig. 1 (see panel in November issue).

This system is intended as a "home" computer kit for amateur and educational use. By using commonly available domestic equipment as peripherals, for example a standard television set for a display and an audio cassette player for storing and loading programmes, a low cost system has been produced. The Z80 c.p.u. connects to the memory and the i/o components via the three bus arrangement described last month. The $1 \mathrm{~K} \times 8$ e.p.r.o.m. is programmed with a system monitor programme. This programme automatically starts running
when power is applied and allows the user to communicate with the system. Commands may be entered via the keyboard and interpreted and executed by the monitor programme. This then allows the system to function as a general purpose computer where user programmes may be keyed in to the r.a.m. memory for subsequent execution, again under control of the monitor programme. The monitor supports such functions as: entering information into memory, displaying memory contents on the tv screen, loading memory from cassette tape, storing memory contents on cassette tape, starting programme execution from any given memory address and stopping programme execution when a predetermined point is reached.
Although the entire $2 \mathrm{~K} \times 8 \mathrm{r} . \mathrm{a} . \mathrm{m}$. is available to the user of the system as a

Fig. 1. Block diagram of the commercial microcomputer kit.
programme or data store, $1 \mathrm{~K} \times 8$ of it is generally reserved as a character store for the tv screen information. However, because this screen memory is accessible by any programme in the system it is possible to achieve some interesting graphic effects by this conventional video interface.
A serial interface is implemented in the kit in order to provide a serial data stream to and from the cassette system. The keyboard is connected via a parallel interface controlled by the monitor software.

The essential parts of this hardware system are shown in detail in Fig. 2. Before considering the operation of the system it is useful to examine the timing of a general Z 80 system. Fig. 3(a), (b) and (c) show the bus timing relationships for a programme memory access, data memory access and i/o access respectively. Note that each c.p.u. machine cycle is made up of a number of timing states. A complete instruction cycle may consist of one or more machine cycles, each consisting of at least three clock cycles, depending on the complexity of the particular instruction. The simplest Z 80 instruction requires four clock cycles (or "T" states). During the $T_{1}$ state the memory or $\mathrm{i} / \mathrm{o}$ address is placed on the address bus. The "memory request" or "input/output request" signal indicates whether the address is for a memory or $\mathrm{i} / \mathrm{o}$ operation. $\mathrm{T}_{2}$ is the time allowed to retrieve the memory or i/o data. The data travels on the data bus during $\mathrm{T}_{3}$ time. The "read" and "write" signals indicate whether a read (or input) or write (or output) operation is in progress. This control structure, although described for the Mostek Z 80 microprocessor, is fairly general for most processors. Other signals, more specific to the Z80 operation, will not be described here.

Referring to the circuit of Fig. 2, notice that because each bank of memory is $1 \mathrm{~K} \times 8$, the address decoding is relatively straightforward. To address 1024 memory locations requires 10 address bits and so lines $\mathrm{A}_{0}-\mathrm{A}_{9}$ are common to all banks. In order to select


Fig. 2. Partial circuit diagram of the commercial microcomputer kit.
Integrated circuits are labelled with their type numbers and other annotation corresponds to Mostek's technical literature. Negated names, e.g. $M R E Q$ indicate that the function is active when the signal is low. At this stage it is not necessary to follow the circuit in detail.
the appropriate bank, lines $\mathrm{A}_{10} \mathrm{~A}_{12}$ are decoded via a 3 to 8 line decoder. The resulting memory map is shown in Fig. 4. The monitor p.r.o.m. is located in address range $0-1 \mathrm{~K}$ because on po-wer-up or system rest, programme execution is forced to resume at address zero. Consequently this is a convenient method to automatically begin execution of the monitor programme.
Since there very few i/o circuits in the system an address decoding scheme is not necessarily required for these, i.e. port addresses may be chosen such that individual address bus bits can select the appropriate device.

So far we have looked at the basic hardware components of a microcomputer system, showing how they relate to one another. Although one particular application has been considered, the hardware arrangement is capable of being universal. However, without software the hardware has no "personality". Software gives life to the hardware and will form the subject of the following discussions.

## Stored programme concepts

The previous section has presented the hardware of the microcomputer kit after developing some of the concepts involved in its design in the previous article. In order to perform any useful function, the hardware must be given a programme to be stored in the microcomputer's memory. This then gives the system a unique "personality". In the following paragraphs some of the basic principles of programme execution and programme flow are introduced. A summary of the use of the "operations monitor' programme of the microcom-
puter kit is included in order to show some of the facilities available for verification of programmes, once written.
Any computer programme exists as a sequence of instructions within the main memory of the machine. These instructions are sequentially executed by the central processing unit to perform the desired task. Each instruction is represented within the memory as a binary number, which is decoded by the c.p.u. in order that the instruction may be executed. The instructions to be used are selected, by the human programmer, from what is called the "instruction set" of the c.p.u. The instruction set is the repertoire of binary codes which the c.p.u. is capable, by virtue of its design, of "recognising". The Z80 microprocessor, for example, has an instruction set consisting of 158 basic instructions.

In order to run a complete programme the c.p.u. must go through a process of repetitively fetching instructions from the memory into an internal store called an instruction register (see Fig. 5 November issue) and then decod-



Fig. 4. Microcomputer system memory map.
ing and executing the instructions. This "fetch-execute" cycle is the basis for all programme operations.
The "fetch" part of the cycle is accomplished by the processor's placing the memory address of the next instruction in sequence on to the address bus and initiating a "memory read" operation. The instruction at this address then travels via the data bus into the processor, where it is subsequently executed. Upon completion, the next instruction must be fetched, and so on. Consequently there must be some mechanism provided within the c.p.u. for supplying sequential instruction addresses for successive fetches. This is in fact provided by the programme counter register. It is the contents of the programme counter which are placed on the address bus at the start of an.

Fig. 3. System timing of the Z80 microprocessor, for (a) programme memory access, (b) data memory access and (c) i/o access.
instruction fetch. Then, before another fetch occurs, the programme counter is incremented so that the following instruction in sequence will be fetched next.

Once an instruction is in the c.p.u., the instruction decoding logic (Fig. 5, November) activates the appropriate internal circuits and an execution phase begins. Executing the instruction may be a completely internal operation within the c.p.u., e.g. performing an arithmetical, logical or data transfer operation on data contained in the microprocessor's registers. Alternatively, the execution of an instruction may require the movement of data in another area of system memory. If this is the case then the c.p.u. must put the new memory address on the bus for the transfer. This state of affairs may be further complicated by the fact that the data memory address may either exist in registers within the c.p.u. or may first have to be supplied to the c.p.u. from the "instruction memory", i.e. the programme memory may, in fact, consist of a mixture of instructions and data or addresses. The instruction contains a binary "operation code" which tells the c.p.u. exactly what to do during the instruction phase and whether to interpret subsequent information as data, address or a new instruction.

The following examples illustrate some of these possibilities. The c.p.u. has several internal 8 -bit general purpose registers which will be referred to as A, B, C, D etc. Suppose we want to construct a programme to simply add the contents of $B$ to $A$ and then put the result into register $C$. A typical sequence of instructions could be

1. Add B to A
2. Load $C$ from $A$

This simple programme requires two instructions as shown, each residing in one location of the programme memory.

Since the source and destination of the data for each instruction are contained wholly within the c.p.u.'s internal registers, no additional information need be supplied during the execution phases of the instructions.

Suppose now that it is desired to add a constant, $n$, to the contents of register $A$ and then again put the result into register C. The programme instructions could be:

## 1. Add $n$ to $A$ <br> 2. Load C from $A$

This programme must supply the number, $n$, to the c.p.u. from the programme memory. Consequently the programme memory would appear as follows:

Address


This time, the number, $n$ is embedded within the instruction stream and so a total of three memory locations is required for the two instructions. In the above examples the "load" instructions are identical, having the same data source and destination. However, the "add" binary instruction codes must be different in order to instruct the c.p.u. to get the data from the appropriate place. In the second example the programme sequence would be as follows: first, the programme counter contents (p) would be issued by the c.p.u. in order to fetch the "add" instruction. By decoding this the c.p.u. would then "know" that it must fetch data from the next sequential memory location. Now, during the execution of any instruction the programme counter will be advanced by one. Consequently the data address will be given by the new value in the programme counter. When this is fetched the c.p.u. completes the instruction execution and proceeds to fetch the next.

Another variation of this situation is given in the next example. Suppose it is required to add the contents of register $B$ to those of register A and then store the result in another external memory location specified in the programme. The programme could be:

1. Add B to A
2. Load memory address $m n$ from $A$.

The memory address $m n$ may be typically a 16 -bit number, therefore requiring two 8 -bit memory. locations. Consequently, the programme may appear as shown below:


This time the "load" instruction code instructs the c.p.u. that the destination address is contained in the next two memory locations and so the programme counter is incremented and sent out two more times before the c.p.u. has fetched all the information it requires. Now in order to complete the execution of this instruction, the address value ( $m n$ ) must be sent out from the c.p.u., on the address bus, in order to access the required data memory location. During this cycle the data (contents of register A) is placed on the data bus so that it may be written into memory.

Table 1: Microcomputer kit software commands
\(\left.$$
\begin{array}{llll}\hline \text { T } & \text { ssss } & \mathrm{ffff} & \begin{array}{l}\text { Tabulate memory contents from address ssss to ffff } \\
\text { D } \\
\text { Dump memory to cassette from address ssss to ffff }\end{array} \\
\text { L } & \text { ssss } & \text { ffff } & \\
\text { M } & \text { nnnn } & & \begin{array}{l}\text { Load memory from cassette }\end{array}
$$ <br>

Modify memory location nnnn\end{array}\right]\)| Execute programme (from address nnnn) |  |  |
| :--- | :--- | :--- |
| B | nnnn | or E |

These simple examples have illustrated two important points: the way in which data and/or addresses may be included in programme instructions and how the c.p.u. is guided by the instruction operation codes into interpreting the sequence of information in the programme memory. The examples have shown how instructions are executed one by one, from start to finish of a programne. However, one of the principal features of a digital computer is its decision-making ability. In other words, the c.p.u. has the ability to select its own path through a programme depending on the results it gets from processing en-route.
In general this is achieved by certain instructions being able to examine the logical state of various bits within the a.l.u. Then, depending on these states, the c.p.u. may either continue with the next instruction in sequence or be diverted to a new area of programme memory. Diverting the c.p.u. in this manner is accomplished by loading a new value into the programme counter.
More specifically, the Z80 c.p.u. has, as part of the a.l.u., several single-bit "status registers" which remember conditions relating to the previously executed arithmetic or logical operation. These include the carry bit from the adder, an arithmetic overflow bit, an indication of a zero result, the sign (if relevant) of the a.l.u. result and an indication of the parity of the result in the a.l.u. Instructions in the c.p.u's programme "jump" group are able to interrogate any of these bits and either load the programme counter with a new value or leave it unchanged, depending
on the binary state of the particular status bit.
In the programme shown below the c.p.u. tests the value stored in register A to see if it is less than 10 . If so it subtracts 6 or otherwise leaves the number unchanged:

1. Compare A with 10
2. Jump if carry $=1$
3. Subtract 6 from $A$
4. Next instruction

Here the "compare" instruction has a similar effect to that of subtracting 10 from A although the A register contents will remain unaltered. However, 'the carry bit of the a.l.u. (effectively a 9 th a.l.u. bit) will be set to 1 if the number 10 is greater than the contents of the $A$ register. The conditional jump instruction can examine the carry bit and thereby decide whether to continue normally or to jump out of the normal flow to another point in the programme. The programme memory would look as shown below.

When the jump instruction is fetched, all three memory bytes, i.e., including the 16 -bit jump address, will be read into the c.p.u. However, if the test is true then the value $(p+7)$ will be put into the programme counter causing the "subtract 6 " instruction to be missed.
The instructions used in the above examples are but a few of the many which are common to most microprocessors.
Before continuing to look at further programme writing techniques it is relevant to complete the discussion of the microcomputer kit facilities by

| Address |  | Compare instruction operation code |
| :---: | :---: | :---: |
| p | Compare |  |
| $p+1$ | 10 | Operand (10) for compare instruction |
| $\mathrm{p}+2$ | Jump if carry | Conditional jump operation code |
| $p+3$ | $p+7$ | 16 bit jump address to $p+7$ |
| $p+4$ |  |  |
| $\mathrm{p}+5$ | Subtract | Subtract operation code |
| $p+6$ | 6 | Operand (6) for subtract instruction |
| $p+7$ | Next instruction | Next instruction in sequence |

Table 2: Hexadecimal number system

| Hex | Decimal | Binary |
| :--- | :--- | :--- |
| 0 | 0 | 0000 |
| 1 | 1 | 0001 |
| 2 | 2 | 0010 |
| 3 | 3 | 0011 |
| 4 | 4 | 0100 |
| 5 | 5 | 0101 |
| 6 | 6 | 0110 |
| 7 | 7 | 0111 |
| 8 | 8 | 1000 |
| 9 | 9 | 1001 |
| A | 10 | 1010 |
| B | 11 | 1011 |
| C | 12 | 1100 |
| D | 13 | 1101 |
| E | 14 | 1110 |
| F | 15 | 1111 |
|  |  |  |

examining the features offered by the kit's own software package.

## A practical software system

The lKbyte e.p.r.o.m. programme of the kit is intended as an aid to debugging and executing programmes written by users of the kit. This software supports seven basic commands as outlined in Table l.

Before proceeding with a description of these commands it is useful to introduce the concept of the hexadecimal number system. When working at machine code level with computers it is obviously very tedious to work in pure binary notation since the numbers are very cumbersome and difficult to visualise easily. Similarly, it is inconvenient for the computer to work with a decimal system since binary bit patterns are not readily convertible to and from decimal numbers. Consequently a suitable compromise is the hexadecimal system which works to a base of 16 . Here the symbols 0 to 9 are no longer adequate by themselves and six additional symbols are required for representation of hex numbers. These are chosen to be letters $A$ to $F$. of the alphabet. Table 2 shows the equivalent binary and decimal values for the hex digit set. The advantage now is that numbers can be easily converted between binary and a notation which is less tedious to write because each hex digit corresponds directly to 4 bits of the binary value. Table 3 shows examples of a 16 -bit and an 8 -bit binary conversion to and from hex.

The software of the kit uses this
number system for both displaying and entering numerical information. Each command entered via the keyboard by the user must consist of a single letter identifier followed in most cases by one or two 4 -digit hex numbers. If two such parameters are required they must be separated by a space. The kit automatically generates the space between the letter identifier and the first parameter. To cause the command entered to be executed, the user must type a "carriage return" (cr) character When the system is ready to accept a new command it "prompts" by issuing a full stop (.).

The "tabulate" ( T ) command allows the user to display the contents of the system's memory by specifying the upper and lower address limits concerned as part of the command. An example of this is shown in Table 4. Note that eight hexadecimal memory bytes are displayed per line, preceded by the address of the first byte on that line.

The "dump" (D) command works in a similar way but causes the information to be recorded on to the cassette tape system. As it does so, an additional item of data, called a "checksum" is calculated for each line of data. This checksum is not displayed but is recorded on tape for subsequent checking by the "load" (L) command.

The "load" command reads programmes or data from cassette and stores the relevant memory values in the system's memory at the addresses specified at the beginning of the information blocks. Checksums are again calculated by the processor and compared with those on tape. If they differ, the computer assumes that there has been a tape reading error and stops further loading. At this point the user can take whatever action is desired.

The "modify" (M) command allows memory locations to be examined or modified via the keyboard. Variations on this command enable the user to examine a single byte, replace it with a new value, delete a wrongly entered value, continue to the next consecutive memory address or terminate the command.

A programme "breakpoint," as it is called, may be set using the "B" command. A breakpoint is a very valuable facility when initially checking out a programme to see if it runs correctly. The address value given as part of the breakpoint command represents the point at which it is desired to stop a programme being executed. At

Table 3: Examples of binary/hexadecimal conversions

| (a) | Binary | 0001 | 1010 | 1110 | 0110 |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Hex <br> Decimal | $\left.\begin{array}{c} 1 \\ (1 \times 16 \end{array}\right)$ | $\begin{gathered} A \\ (10 \times 16) \end{gathered}$ | $\stackrel{E}{14 \times 161}$ | $\begin{array}{r} 6 \\ \text { (6) } \end{array}$ |
| (b) | Hex | $\overbrace{0011} \leftarrow 3 F>\overbrace{1111}$ |  |  |  |

Table 4: Typical display format from kit

| (.) | T | 800 | 813 |  | (Entered by user) |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0800 | 00 | 11 | 22 | 33 | 44 | 55 | 66 | 77 |
| 0808 | 88 | 99 | $A A$ | $B B$ | $C C$ | $D D$ | $E E$ | $F F$ |
| 0810 | $A 1$ | $B 2$ | C3 | D4 |  |  |  |  |

this time, the kit software prints out the current contents of the c.p.u.'s programme counter and accumulator registers. Also, the remaining c.p.u. registers are automatically saved in r.a.m. so that they may be examined later with the " M " or " T " commands. In this way the programmer is able to "see inside", the c.p.u. chip at any desired stage in the programme and then modify the registers accordingly, if required, before continuing.
The "execute" ( E ) command is used to start execution of a programme from a desired address. If no address is specified with the command then execution will continue from the address which was in the programme counter before the previous breakpoint was encountered. Register values saved at a breakpoint will be restored automatically prior to execution.
The "single step" (S) command is similar to the "executive" command but causes only one instruction to be executed before stopping.

The above facilities enable programmes to be debugged on the kit in a way that would not be possible otherwise. The value of these will be illustrated in a more practical way in a future article on programme writing and debugging.

## SPECIAL TERMINOLOGY

To access. The noun "access" used as a verb. meaning to gain access to a memory location in which binary information is already stored or can be stored To open up a set of connections to allow reading from or writing into this location.

Instruction. An expression that defines a computer operation and identifies its operands.

Programme. A prepared list of instructions, written in a special "language" or code, to be carried out in sequence by a computer or other programmable device.

Instruction set. The total list of instructions that can be performed by a given microprocessor

Operation code. The symbols within an instruction that represent the particular operation to be performed (e.g. add).

Register. A small-capacity store intended for temporarily holding a small number of binary digits such as a word.

Jump. A departure from the normal sequence of instructions in a programme to a different part of the programme of Article 7, which appeared in the September issue.

# More synchronous and ripple-through counters 

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Decade binary 'up' counter. Examination of the unused states in Fig. 5(a) shows that they can be represented by the Boolean function $B D+C D$

The flip-flop equations are the same as for the scale-of-16 'up' counter, namely, $J_{A}=K_{a}=1, \quad J_{B}=K_{B}=A$, $\mathrm{J}_{\mathrm{C}}=\mathrm{K}_{\mathrm{C}}=\mathrm{AB}$ and $\mathrm{J}_{\mathrm{D}}=\mathrm{K}_{\mathrm{D}}=\mathrm{ABC}$, with the modifications shown below, which are required to inhibit the $S_{9}$ to $S_{10}$ transition and initiate the $\mathrm{S}_{9}$ to $\mathrm{S}_{0}$ transition.
The transitions from $S_{9}$ to $S_{10}$ and $S_{9}$ to $\mathrm{S}_{0}$ are shown below:
pulses and trip an alarm, using the Boolean function representing these states, $\mathrm{f}=\mathrm{BD}+\mathrm{CD}$. A suitable circuit for suppressing the clock pulses is incorporated with the counter implementation in Fig.5(b).
A decade of binary 'down' counter can be designed using the same technique and the corresponding flip-flop equations are: $J_{A}=K_{A}=1$, $\mathrm{J}_{\mathrm{B}}=\overline{\mathrm{A}} \mathrm{C}+\overline{\mathrm{A}} \overline{\mathrm{D}}, \mathrm{K}_{\mathrm{B}}=\overline{\mathrm{A}}, \mathrm{J}_{\mathrm{C}}=\overline{\mathrm{A}} \mathrm{D}, \mathrm{K}_{\mathrm{C}}=\overline{\mathrm{A}} \overline{\mathrm{B}}$ and $J_{D} \cdot=K_{D}=\bar{A} \bar{B} \bar{C}$. The output of a
binary decade counter can be converted to a decimal number using a 4-10 line decoder as shown in Fig.6.

Consider the transition in such a counter from 0001 to 0010 and assume that flip-flop B changes faster than flip-flop $A$. The sequence of changes that take place are:

[^5]

To inhibit the set of flip-flop $B, S_{5}=$ $\mathrm{ABS}_{9}$, where $\mathrm{S}_{9}=\mathrm{ABCD}+(\mathrm{BD})+$ (CD). Simplifying: $S_{9}=A D$, hence: $S_{B}$ $=A \bar{B} \bar{A} \bar{D}=A \bar{B}(\bar{A}+\bar{D})=A \bar{B} \bar{D}$. Therefore, $J_{B}=A \vec{D}$. To initiate the: reset of flip-flop $D, R_{D}=S_{9}=A D$. Therefore, $\mathrm{K}_{\mathrm{D}}=\mathrm{A}$.

If the counter should assume one of the unused states due to circuit misoperation then a suitable corrective action might be to suppress the clock


Fig. 5(a) shows the state diagram for a decode binary 'up' counter, and at (b) is the circuit implementation.



Fig. 6. A 4-10 line decoder.

Table 8. The XS-3 Gray code.

| $D$ | $C$ | $B$ | $A$ |
| :--- | :--- | :--- | :--- |
| $O$ | 0 | 1 | $O$ |
| 0 | 1 | 1 | 0 |
| $O$ | 1 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 0 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 1 |
| 1 | 1 | 1 | 0 |
| 1 | 0 | 1 | 0 |



Fig. 7. State diagram for a XS-3 Gray code counter is at (a) and (b) shows the circuit.


Hence a spike will occur on the output line marked 3 during the transient state. Clearly this can occur at any point in the binary counting sequence where more than one flip-flop is required to change state during a transition. The difficulty can be eliminated by using a Gray code counter, in which only one flip-flop changes state at each transition.
Decade Gray code 'up' counter. As an example of the design of a Gray code counter, the XS3 code will be converted to a Gray code which will be used as the basis for the counter design. The conversion is carried out by obtaining the exclusive-OR sum of each pair of digits in the code starting with the two
least significant digits first, as shown below. It is assumed that there are five digits in each code combination, the fifth and most significant, always being 0.


The complete XS3 Gray code obtained using this procedure is shown in Table 8. A convenient procedure for designing a Gray code counter is:
(1) Determine the $S$ and $R$ expressions for each flip-flop and, using the * equations $\mathrm{S}_{\mathrm{Q}}=\mathrm{J}_{\mathrm{Q}} \overline{\mathrm{Q}}$ and $\mathrm{R}_{\mathrm{Q}}=\mathrm{K}_{\mathrm{Q}} \mathrm{Q}$,
obtain the corresponding expressions for J and K .
(2) Optional products defined by the unused states, (if there are any.) can now be used to reduce the J and K expressions. For the state diagram and codes, see Fig.7(a).

Examination of the unused states in Fig.7(a) shows that they can be represented by the Boolean expression $\overline{\mathrm{B}} \overline{\mathrm{C}}+\mathrm{A} \overline{\mathrm{C}}$.

The flip-flop equations are:

$$
\begin{aligned}
\mathrm{S}_{\mathrm{A}}= & \mathrm{S}_{1}+\mathrm{S}_{5}=\overline{\mathrm{A}} \mathrm{BC} \overline{\mathrm{D}}+\overline{\mathrm{A}} \overline{\mathrm{~B} C D} \\
\mathrm{~J}_{\mathrm{A}} & =\mathrm{BC} \overline{\mathrm{D}}+\overline{\mathrm{BCD}}+(\overline{\mathrm{B}} \overline{\mathrm{~B}})+(\mathrm{A} \overline{\mathrm{C}}) \\
& =\overline{\mathrm{B}}+\mathrm{BCD}
\end{aligned}
$$

$\mathrm{R}_{\mathrm{A}}=\mathrm{S}_{3}+\mathrm{S}_{7}=\mathrm{ABCD}+\mathrm{ABCD}$
$\mathrm{K}_{\mathrm{A}}=\overline{\mathrm{B}} C \overline{\mathrm{D}}+\mathrm{BCD}+(\overline{\mathrm{B}} \overline{\mathrm{C}})+(\mathrm{A} \overline{\mathrm{C}})$
$=\bar{B} \bar{D}+B C D$
$S_{B}=S_{6}=A \bar{B} C D$
$\mathrm{J}_{\mathrm{B}}=\mathrm{ACD}+(\overline{\mathrm{B}} \overline{\mathrm{C}})+(\mathrm{A} \overline{\mathrm{C}})$
$=\mathrm{AD}$
$\mathrm{R}_{\mathrm{B}}=\mathrm{S}_{2}=\mathrm{ABCD}$
$K_{B}=A C \bar{D}+(\bar{B} \bar{C})+(A \bar{C})$
$=A \bar{D}$
$S_{C}=S_{0}=\bar{A} B \bar{C} \bar{D}$
$\mathrm{J}_{\mathrm{C}}=\overline{\mathrm{A}} \overline{\mathrm{A}} \mathrm{B} \overline{\mathrm{D}}+(\overline{\mathrm{B}} \overline{\mathrm{C}})+(\mathrm{A} \overline{\mathrm{C}})$
$=\bar{A} B \bar{D}$
$\mathrm{R}_{\mathrm{C}}=\mathrm{S}_{8}=\overline{\mathrm{A}} \mathrm{BCD}$
$K_{C}=\bar{A} B D+(\overline{\mathrm{B}} \bar{C})+(\mathrm{A} \ddot{\mathrm{C}})$
$=\overline{\mathrm{A}} \mathrm{BD}$
$S_{D}=S_{4}=\bar{A} \bar{B} C \bar{D}$
$\mathrm{J}_{\mathrm{D}}=\overline{\mathrm{A}} \overline{\mathrm{B}} \mathrm{C}+(\overline{\mathrm{B}} \overline{\mathrm{C}})+(\mathrm{A} \overline{\mathrm{C}})$
$=\bar{A} \bar{B}$
$\mathrm{R}_{\mathrm{D}}=\mathrm{S}_{9}=\overline{\mathrm{A}} \mathrm{B} \overline{\mathrm{C}} \mathrm{D}$
$\mathrm{K}_{\mathrm{D}}=\overline{\mathrm{A}} \mathrm{B} \overline{\mathrm{C}}+(\overline{\mathrm{B}} \overline{\mathrm{C}})+(\mathrm{A} \overline{\mathrm{C}})$
$=\overline{\mathrm{C}}$
The circuit implementation of the counter is shown in Fig.7(b).
The output of the counter can be converted directly to decimal with the aid of a 4-10 line XS3-Gray-to-decimal decoder, which is available as a chip.

If the above procedure is adopted for the design of an XS3-Gray code decade 'down' counter the following flip-flop equations are obtained:
$\mathrm{J}_{\mathrm{A}}=\overline{\mathrm{B}} \overline{\mathrm{D}}+\mathrm{BCD}, \mathrm{J}_{\mathrm{B}}=\mathrm{A} \overline{\mathrm{D}}, \mathrm{J}_{\mathrm{C}}=\overline{\mathrm{A}} \mathrm{BD}$, $\mathrm{J}_{\mathrm{D}}=\overline{\mathrm{C}}$.
$K_{A}=\bar{B} D+B C \bar{D}, K_{B}=A D, K_{C}=\bar{A} B \bar{D}$, $K_{D}=\bar{A} \bar{B}$.

The unreduced, and hence the reduced, $J$ and $K$ values of the flip-flops in one direction are the same as the K and J values of the flip-flops in the reverse direction, i.e. to reverse the direction of count it is only necessary to interchange the J and K inputs of each flip-flop.
It should be noted that the method of design employed does not always produce the simplest flip-flop equations but it has advantages when applied to the implementation of 'up-down' Gray code counters.

## 'Up-down' control

'Up-down' counters are counters in which the pulse count is stepped up or stepped down by each input pulse according to whether Yhe value of an external control signal R is 0 or 1 . In practice, the input signals that step the count up or down will appear on two separate lines X and Y , as shown in Fig.8(a). The designer has to generate the clock pulses that will drive the counter flip-flops and the 'up-down'

(a)


Fig. 8(a) shows the input/output characteristics required of an up/down counter, with the state diagram at (b). The control circuit is shown in (c).

control signal R. It will be assumed that step-up and step-down signals do not appear simultaneously.

The control signal must not be allowed to change during the presence of the input data. In this case, the control signal will be generated from the input data and a race condition can be prevented by using the first pulse in each pulse train to change the value of $R$. Since each time the value of $R$ is changed an input pulse is not counted this method results in a maximum count
error of 1 for an odd number of changes in $R$ and no error for an even number of changes in $R$.

The logic circuit used to perform the function described above is event driven and the methods used in the third article of this series will be employed in its design.

Step 1 Figure 8(a) shows the input/output characteristics.
Step 2 A suitable state diagram is shown in Fig.8(b).

Step 3 State reduction is not attempted so that clarity of design is maintained.
Step 4 Turn-on set of $Q=R \bar{Y}$
Turn-off set of $Q=\bar{R} \bar{X}$
Turn-on set of $\mathrm{R}=\overline{\mathrm{Q} Y}$
Turn-off set of $\mathrm{R}=\mathrm{QX}$
The sequential equations are:
$Q=\underline{R} \bar{Y}+Q(\underline{R}+\underline{X})$
$\mathrm{R}=\overline{\mathrm{Q}} \mathrm{Y}+\mathrm{R}(\overline{\mathrm{Q}}+\overline{\mathrm{X}})$
and $c=S_{0} X+S_{2} Y=\bar{Q} \bar{R} X+Q R Y$
The circuit implementation is shown in Fig.8(c).
'Up-down' XS3-GRAY code counter. Combining the flip-flop equations for 'up' counts when $R=0$ and for 'down' counts when $R=1$ the following results are obtained for the XS3-Gray code counter:
$\mathrm{J}_{\mathrm{A}}{ }^{\prime}=(\overline{\mathrm{B}} \mathrm{D}+\mathrm{BC} \overline{\mathrm{D}}) \overline{\mathrm{R}}+(\overline{\mathrm{B}} \overline{\mathrm{D}}+\mathrm{BCD}) \mathrm{R}$
$=J_{A} \bar{R}+K_{A} R$
$K_{A}^{\prime}=(\bar{B} \bar{D}+B C D) \bar{R}+(\bar{B} D+B C \bar{D}) R$
$=K_{A} \bar{R}+J_{A} R$
Similarly
$J_{B}^{\prime}=J_{B} \bar{R}+K_{B} R$
$K_{B}{ }^{\prime}=K_{B} \bar{R}+J_{B} R$
and so on for $\mathrm{J}_{\mathrm{C}}{ }^{\prime}, \mathrm{K}_{\mathrm{C}}{ }^{\prime}, \mathrm{J}_{\mathrm{D}}{ }^{\prime}$, and $\mathrm{K}_{\mathrm{D}}{ }^{\prime}$, where $\mathrm{J}^{\prime}$ and $\mathrm{K}^{\prime}$ are used to denote the flip-flop inputs in the 'up-down' mode:

## Asynchronous binary counters

For counts of powers of 2 the basic arrangement consists of $T$ flip-flops, (or alternatively JK flip-flops with J and K permanently connected to 1 ), connected in cascade as shown in Fig.9(a). As can be seen from the diagram the output of each flip-flop provides the clock signal for the next. The input signal X is used as the clock pulse for the first flip-flop.

The time-diagrams for a scale-of-8 (up) counter are shown in Fig.9(b), where all changes of state are assumed to take place on the trailing edge of the clock pulses. Examination of the time diagrams shows that flip-flop A changes state on each trailing edge of the input pulses X. The output of flip-flop A is used as the clock pulse for $B$ and a change in state of this flip-flop occurs on the trailing edge. of the $A$ pulses. Similarly the output of $B$ provides the clock pulse for $C$ and this changes state on the trailing edge of the $B$ pulses.

The various states of the counter are indicated on the time diagram and the binary digits associated with each state are marked on the time diagrams for the signals $A, B$, and $C$.

It is a simple matter to show that the above circuit will count down if the signals $\bar{A}$ and $\bar{B}$ are used as the clock pulses for flip-flops $B$ and $C$ respectively.

## Scale-of-ten 'Up' Counter.

This circuit requires four flip-flops, as shown in Fig 10(a). The associated time diagrams are displayed in Fig.10(b). Starting with all the flip-flops in the 0


Fig. 9(a) is a 3-stage ripple-through counter, and at (b) are the timing diagrams.

(a)

(b)

Fig. 10 is a scale-of-ten ripple-through counter. Timing diagrams are at (b).
state, the count follows the normal binary sequence up to and including the count of eight. On the trailing edge of the tenth input pulse, flip-flop A makes a transition from 1 to 0 , which would normally induce a transition in flip-flop B, changing its state from 0 to 1 . However $\mathrm{J}_{\mathrm{B}}=\overline{\mathrm{D}}=0$ at this instant and consequently flip-flop $B$ remains in the reset condition. At the same instant it is also necessary to reset flip-flop D and it changes state from 1 to 0 . All the flip-flops are now in the reset condition and are ready for the arrival of the first pulse of the next counting cycle.

## Scale-of-twelve 'Up' counter

The basic circuit of a scale-of twelve asynchronous counter is shown in Fig.11(a), whilst the time diagrams describing its behaviour are shown in Fig.11(b). Flip-flops A, B, and C count from 000 to 101 inclusive. With $\mathrm{D}=0$ the counter reaches the state $\mathrm{ABCD}=1010$ ( $\mathrm{S}_{5}$ ), and when the next X input pulse is received it must go to the state $\mathrm{ABCD}=0001$.

Flip-flop $A$ is controlled by the $X$ pulses and changes state to $A=0$ on the trailing edge of the sixth of these pulses. Flip-flop B remains in the $\mathrm{B}=0$ state since $\mathrm{J}_{\mathrm{B}}=\mathrm{C}=0$ and flip-flop C takes up the state $\mathrm{C}=0$ since $\mathrm{J}_{\mathrm{C}}=\mathrm{B}=0$ and $\mathrm{K}_{\mathrm{C}}=1$. The change of C from 1 to 0 represents the trailing edge of the clock pulse for flip-flop D and hence there is a change of state for this flip-flop such that $\mathrm{D}=1$.

After another six X pulses the state of flip-flop $D$ is restored to 0 and the counting cycle of twelve states is completed.


Fig. 11 shows the form and timing diagrams of a scale-of-12 asynchronous counter.

Continued from page 49

## Appendix 1

To arrive at a general solution, take two threads of equal length y. By their top ends they hang from the ends, C and D of a fixed bar 2 a long, Fig. 9. The lower ends are fixed to the ends of a moveable bar AB, 2 b long.
The bar has a mass W and requires horizontal couple of moment $M$ to rotate it through $\theta$ degrees about a vertical axis passing through the centres of $A B$ and CD. There will be a tension T in the threads, which will be at an angle $\alpha$ to the vertical.
$W=2 T \cos \alpha$
$M=2 b T \sin \alpha \sin \beta$
$M=b W \tan \alpha \sin \beta$

Now $\mathrm{Y} \sin \alpha \sin \beta=\alpha \sin \theta$ and substituting for $\sin \beta$

$$
M=\frac{W a b \sin 0}{y \cos \alpha}
$$

This is the general case. In our case we assume $a=b<1 / 3$, so $y \cos \alpha$ becomes $h$ and constant. $\theta=90^{\circ}, \sin \theta=1$ and the equation becomes

$$
M=\frac{W a^{2}}{h}
$$

## Appendix 2

The stylus only tracks a groove tangentially at the end of a side. This is in fact how the arm is set up. At the start of a side there is a tracking error $\gamma$, which in a well-designed arm should not exceed two degrees. This decreases across the record. The equation on page becomes

$$
S=F \sin (\phi+\gamma)
$$

## Appendix 3

-typical values, in grams

| Component | Mass-W | Me |
| :--- | :---: | :---: |
| Cartridge | 7 | 7 |
| Arm | 5 | 1.5 |
| Wire | 2 | 0.5 |
| Clip and screws | 2 | 2 |
| Counterweight | 42 | 3 |
|  | - | - |
| Totals | 58 | 14 |

For weighing things I use ordinary kitchen scales which turn nicely at $1 / 1, \mathrm{Oz}$. A tuppenny piece weighs $1 / 40 z$, one penny weighs $1 / 8$ oz and $1 / 2$ pence weighs $1 / 16 \mathrm{oz}$. And, of course, 1 oz is 28 grams. For objects weighing four grams or less, I use a stylus scale: hopelessly inaccurate but can be recalibrated.

## Books Received

Tower's International Fet Selector by T. D. Towers and N. S. Towers starts with a brief introduction to f.e.ts followed by a list of about 2,600 devices together with chracteristics and ratings. Four appendices provide information on the tabulations, package outline and pin configuration manufacturers house codes, and manufacturers address. Price $£ 4.50$ Pp.57. W. Foulsham \& Co. Ltd, Yeovil Road, Slough, Bucks

Microphones - How They Work \& How to Use Them, no. 875; The Complete Handbook of Vidocassette Recorders, no. 811; The Complete Handbook of Slow-Scan TV, no 859; and Practical Solid-State DC Power Supplies, no. 891, are recent paperback publications from Tab Books. A hardback book entitied TV Lighting Handbook is also available from the same company. For further details and UK prices contact Tab Books, Blue Ridge Summit, Pa. 17214, U.S.A.
Microcircuit Device Reliability: Digital Generic Data and Linear/Interface Data are two publications which are now available from London Information. The data has been compiled from technical reports and information from various manufacturers. Each publication contains reliability details together with information of the field and test conditions. London Information (Rowse Muir) Ltd, Index House, Ascot, Berks.

# High-speed analogue-to-digital conversion 

## Recent developments and applications

by O. J. Downing, Ph.D and P. T. Johnson, B.Tech., University of Bradford

Digital signal processing and data transmission, particularly for television and radar systems, have stimulated a demand for faster and more accurate analogue to digital converters. Much of the current research and development in this field has given Britain a significant technological lead. Converters capable of coding to 8 -bit accuracy with sampling rates greater than $\mathbf{3 0 M H z}$ have been demonstrated and have opened up many new applications. The two authors have spent several years researching a-to-d convertors at Bradford University with the intention of producing a cheap manufacturable converter particularly suitable for tv signals. This article describes some of the most recent systems including the author's prototype $2+2+2+2$ serial coder design, opposite, and suggest ways in which gross coding rates greater than $400 \mathrm{Mb} / \mathrm{s}$ may shortly be achieved.

It has been said that the solution to one problem begets ten more. Certainly, in the realms of digital signal processing, the inherent attractions of digital operation have led to a steadily growing range of more demanding applications for the devices and interfaces.

One recent example of the power of digital signal processing is the television signal standards converter DICE ${ }^{1}$ (Digital Intercontinental Conversion Equipment). In this equipment a complex and extensive sequence of operations are performed on a digitized tv signal using high-speed logic. This system has resulted in corresponding improvements in mean signal quality, in the amount of maintenance and setting up required, and in the general effectiveness of the equipment when compared with the analogue counterpart. Digital techniques are now being used in the design of television, radar and communication systems, and in the area of transient signal measurement with similar benefits.

In all of these applications, however, the major attractions of digital operation depend on speed, accuracy, stability, reliability and cost of the analogue-to-digital conversion process.


Thick-film integrated and hybrid a-to-d and d-to-a convertors have been available for some time. In these devices the conversion techniques such as rampcounting, feedback successive approximation, and dual-slope integration require a trade-off between the rate at which input signal samples are translated into binary digits (the conversion rate), and the accuracy to which the output bits represent the instantaneous value of the input signal (the dynamic accuracy). Although developments in component technology are relieving these restrictions to some extent, the majority of existing high-speed converters use combinations of serial and parallel conversion techniques. Integration, however, does offer mainly trouble-free and stable circuit operation. The aim of many a-to-d converter designs therefore, has been to obtain higher conversion rates and dynamic accuracy using integration techniques which require few precision components and do not need a great deal of adjustment.

## Pulse-code modulation

The derivation of a pulse-codemodulated output from an analogue
input signal requires three basic operations. Firstly, the input is sampled, according to the Nyquist criterion, at a rate greater than twice the highest spectral component contained in the input signal. A low-pass filter in the input path is often used to ensure this condition. The instantaneous value of the sample must be accurately stored for a length of time which enables the converter to decide which output code it should produce. Secondly, each sample amplitude is compared with a number of preset reference levels to determine the amplitude. This quantization process commonly uses linearlyrelated levels, although expansion or compression of the dynamic signal range by the use of non-linear quantization law has been used in the logarithmic encoding of speech signals. Finally, the digital outputs of the comparison circuitry are re-coded into a more convenient form. The resulting parallel bit streams are latched to remove time-skew between them.
Typically, a converter producing an n-bit parallel binary output for each input sample requires $2^{n}-I$ accurate reference levels. The processes of sampling, quantization and coding are


Fig. 1


Fig. 2


Fig. 1. Sampling quantization and coding processes for pulse-code modulation of video signals.

Fig. 2. (a) Diode gate sampling switch and (b) m.o.s.f.e.t. switch with feedthrough cancellation.

Fig. 3. Parallel quantizer using comparators and a precision resistor chain for the reference levels.
shown in Fig. 1. Subjective tests with digitalized and reconstructed System I PAL tv signals suggest the need for at least eight bits per sample at sampling rates around 13 MHz . For many radar applications much higher sampling rates are required, often with similar quantization accuracy.

## Sample and hold techniques

In principle, the process of sampling and holding a signal is accomplished by switching the signal into a capacitor store which charges to the current signal amplitude. The switch is then opened whilst the quantizer operates on the sampled value. In practice, the time taken for electronic switches to change from low and high resistance states, the parasitic reactances associated with these devices and, in high speed samplers, slewing of the input signal all combine to limit the maximum speed and accuracy.

To reduce the decay of the stored charge during the holding interval, the capacitor is buffered by a highimpedance amplifier and its capacitance is maximised. However, to ensure that the capacitor charges to the signal amplitude during the sampling period its value must be minimized. In general, the sampling period must also be minimized because quantization cannot begin until sampling is complete. Because the input signal continues to slew during the time taken for the sampling switch to open, any shifting of this point caused by noise or sampling clock jitter will cause an error in the sample value. Furthermore, the relative magnitudes of the input and switching signals can shift the sampling period and cause a further error called "aperture uncertainty". If any coupling exists between the switch and the hold store, a proportion of the sampling pulse can be fed to the store capacitor and cause a corresponding error in the voltage.
Two types of sampling switch that have recently become popular are the diode gate and the m.o.s.f.e.t. switch, see Fig. 2. In the first-mentioned the diodes are reverse biased during the hold interval but are forward biased by a current pulse from the transmformer during the sample interval. Voltage sources are included to ensure that the diodes do not become forward biased by high signal slewing during the hold interval. The use of matched lowresistance high-speed Schottky diodes

minimizes sample pulse feedthrough and charging time, but aperture errors are still determined by the slew drive rate of the driving pulse and drive transistors. Aperture errors of less than 100ps at sampling rates in excess of 50 MHz are possible with samplers of this type. A recent development has made use of the m.o.s.f.e.t. possible in high-speed samplers ${ }^{2}$. In the circuit shown, sampling pulse leakage caused by gate-source capacitance is cancelled by feeding a complementary pulse directly onto the sampling capacitor. Cancellation levels up to 40 dB have been achieved with an aperture error below 300 ps .

## Serial and parallel quantizers

An obvious technique for quantizing a signal is to apply it to the commoned inputs of several comparators, the reference levels for which are derived from a precision resistor chain as shown in Fig. 3. Because the comparators have

Fig. 4. Serial-successive approximation quantizer. This system only uses one comparator and precision reference level per output bit.
to settle, their outputs are latched some time after the sampling period. Recoding of the output into binary can be accomplished by simple combinational logic as shown. Although this technique is accurate and using e.c.l. comparators conversion rates approaching 100 MHz with four bits per sample are feasible, the component cost is high; for an n-bit binary converter, $2^{n}-1$ comparators and resistors are required.
At the other end of the scale, the serial successive approximation converter shown in Fig. 4 uses only one

Fig. 5. Single stage of a folding encoder. This system uses a cascade of fast precision rectifiers.
comparator and precision reference level per output bit. The result of each comparison is used to decide whether a voltage, equal to the reference level for that comparator, should be subtracted from the stage input signal. The output from the subtractor is used as an input to the following stage. A successively more accurate representation of the input sample amplitude is built up by subtraction of binarily related reference voltages as the residue propagates along the cascade. Variations in propagation delay are removed by latching all outputs on the leading edge of the next sampling pulse. Because each stage must complete its operation before the next commences, the available time for stage operation is severely restricted. For example, a tv converter may require accurate stage operation in less than 8 ns . One solution is to add a delay line into the signal path of each stage at the point marked $\times \times$ on Fig 4. By correctly timing the latch pulse

to each comparator and re-designing the latch it is possible to arrange that whilst the first stage is determining the most significant bit of the present sample, the second stage is determining the second m.s.b. of the preceding sample and so on. This allows each stage to have nearly a complete sampling interval to settle. Because each stage must be accurate enough to drive the rest of the cascade, only the first stage requires maximum accuracy. Furthermore, this design has the advantage of automatically producing a binary-coded output.

A further modification, which allows the use of a common reference voltage for all stages, is to give the subtraction process a precise gain of two. Unfor-
tunately, the precision subtractor with a stable zero-offset is a problematic part of this design.

## Folding encoder

An alternative form of serial converter which has been successfully used commercially, the so-called folding encoder, uses a cascade of fast precision rectifiers. Fig. 5 shows that the transfer

Fig. 6. Eight bit series/parallel a-to-d converter with digital error correction.

Fig. 7. Recirculating parallel coder. Residue from the first four-bit quantization is fed back to the same quantizer/coder.

function of the system has a typical repetitive form, but, in this case, produces a Gray-coded output. Therefore, conversion errors occurring at major output transistions, say between binary 01111 to 10000 , are less noticeable bè́cause only one bit changes per least-significant transition. The circuit is also susceptible to a high degree of integration.

## Hybrid series-parallel converters

A solution to the compromise between speed, accuracy and cost is a combination of serial and parallel conversion techniques. An eight-bit 15 MHz design developed by the $B B C^{3}$ uses a four-bit quantizer/coder, producing the four m.s.bs of the output, feeding a four-bit d-to-a converter, see Fig. 6. The output of the converter is subtracted from a delayed version of the input sample to produce a voltage equal to the error caused by quantization to $2^{4}$ levels. This residue, after amplification, is coded in a second four-bit quantizer/coder to produce the least-significant four bits of the output. Errors caused by propagation delay in the two sets of four-bit outputs are removed by suitably timed latches. This design is interesting because it uses extra comparators in the second stage to detect under-range and over-range input voltage errors produced by a marginally accurate first stage. The comparators also correct the l.s.b. outputs by digital subtraction or addition. ${ }^{4}$.

The recent development of an integrated comparator ${ }^{5}$, latch, precision current switch and binary-coding logic has enabled the speed and accuracy of this technique to be greatly improved. Use of a current-summing d-to-a converter coupled with current subtraction allows significantly faster operation, 30 MHz , than the corresponding voltage-based design. We have experimented with a modified version of this design at the University of Bradford, see Fig. 7, where the residue from the first four-bit quantization is fed back via a m.o.s.f.e.t. re-sampling switch into the same quantizer/coder. It is possible to code to eight-bit dynamic accuracy with sampling rates almost as high as the $4+4$ converter but using only half as many comparators, and precision resistors.

Another home-produced design which is faster, more accurate and more economical, uses a cascade of four high-speed two-bit coders. Inter-stage voltage gain and $I$ to $V$ conversion by current summing are not required. The maximum conversion rate of this system exceeds 30 MHz . Fig. 8 shows the circuit and the residues from each stage produced by the quantization of a slow ramp sampled at 20 MHz . Dynamic accuracy measurement of this converter suggests that the addition of a simple error corrector to the final stage could allow a further one- or two-bit stage to be added which would reduce the

(a)

Fig. 8. (a) Single stage of a
$2+2+2+2$ serial coder. (b)
Analogue output after coding a slow ramp.

(b)

Fig. 9. Fast d-to-a conversion using binary-weighted switched currents which are summed using (a) virtual earth technique, (b) a $R-2 R$ precision resistor network. The photograph shows a practical design which combines the two techniques.

(a)

maximum conversion rate to around 25 MHz and offer a, so-far rather elusive, ten-bit high-speed converter.

## Digital error correction

The technique of error correction can be used in most forms of a-to-d converter although it is usually only used in high speed designs where the increased accuracy and stability justify the added complexity. The principle is to measure the conversion error in terms of either positive or negative least significant bits and to correct the coder output accordingly. Error detection is usually incorporated in the final stage of serial quantization where the converter is attempting to resolve to a least significant bit. Any errors that have been introduced earlier in the cascade will cause the signal amplitude at the final stage to lie outside its nominal range. These errors can be measured precisely by the addition of extra comparators. Digital arithmetic controlled by the outputs of these comparators is then used to subtract the error from the coder output.

## Digital-to-analogue conversion

Re-conversion of digital signals into analogue form presents few problems for the levels of accuracy and word rates quoted so far. Precision highstability resistors and thick-film networks have been available for some time although commercial integrated high-speed d-to-a converters are still thin on the ground. Two similar techniques, shown in Fig. 9, are popular for high-speed designs. In the first, precision binary-weighted switched currents are summed at a virtual-earth point. In the second, similar precision switched currents are summed binarily in a precision resistor network. The first technique has the advantage that the summing node is a virtual-earth and hence, stray capacitance associated with the current source has less effect in slowing down current transitions. Unfortunately, precision binary-weighted current generators are difficult to produce. In the second technique the advantage and disadvantages are reversed. A logical solution to the compromise is to combine the techniques and sum groups of binarily related switched currents in a reduced resistor network. A circuit which sums three groups of three precision-switched currents $I, I / 2$ and $I / 4$ in an $R$ to $8 R$ resistor ladder can be used to decode eight-bit binary data at word rates above 50 megawords per sec. However, dynamic errors have proved to be troublesome in high-speed converters. Even if data propagation error is limited by latching the input data with a delayed sampling clock, different propagation delays in the resistor or current networks, and parasitic reactances associated with components and circuit layout produce transient spikes or "glitches" on the analogue output waveform when the input data changes.

Resampling this waveform, using a second delayed clock, after the glitches have settled, reduces the problem and also simplifies the design of the subsequent low-pass reconstruction filter.

## Future developments

The commercial development of any new technique depends on market conditions. At present, although a considerable range of exciting and attractive applications have been proposed for high-speed digital signal processing, particularly the prospect of digital broadcast tv systems, the cost of existing a-to-d converters is prohibiting their large-scale exploitation. Of the converter types described in this article, the serial cascade is perhaps the most attractive for future development. The idea of an integrated $n$-bit coder with subtractor, buffer, and latch on a single chip, which could be cascaded to any length, has obvious merits.
Techniques will shortly be capable of producing up to four-bit units with settling times of less than 8 ns . On this basis, converters with gross bit-rates approaching $300 \mathrm{Mb} / \mathrm{s}$ are feasible. The inclusion of an external delay line in each stage may push this limit closer to $400 \mathrm{Mb} / \mathrm{s}$.
Acknowledgement. - The authors would like to express their gratitude to Dr C. Davis of Cambridge Consultants Ltd, and Mr N. Green of Independent Television Companies Association Ltd for their encouragement during the course of over three years research.

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## Microcomputer seminar

Readers interested in our current series on microcomputer design may like to know that a seminar is being held to introduce the Nascom I minicomputer kit (November issue, p.45) at the Wembley Conference Centre, London, on November 26, starting at 9.50 a.m. Organizers are: Lynx Electronics (London) Ltd, 92 Broad Street, Chesham, Bucks (tel: Chesham (02405) 75154).

## HF predictions

Circuit reliability is the product of the probability of ionospheric reflection and the probability of achieving a desired signal to noise ratio and is thus at a maximum somewhere between FOT and LUF. The term FOT, which is the French equivalent of OWF (optimum working frequency), is thus a misnomer since it relates only to skywave probability. However since LUF is dependent on many factors which cannot be generalised it is found satisfactory in practice to take FOT as being what it says it is.





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# Analogue function generator 

## A straight-line approximation design

by G. J. Phelps, B.Sc.

This article describes a versatile function approximator, whose analogue input and output signals are related by a segmented characteristic. The function characteristics are made up of a number of straight lines (segments), each one joining the next, at "break points,' to form a continuous line. A variable characteristic is obtained by adjusting the position of intersection of any two segments.

The generation of an analogue function from an input variable has many applications, especially in the fields of measurement and process control. An example would be the linearization of a signal from a non-linear transducer in a control system.

The circuit described can be used to generate many functional relationships between its input and output signals, using a straight-line "fit" technique to produce the required characteristic.

Straight line approximators are not new. The diode function generator', which is a typical example of past designs, consists of the type of circuit shown in Fig. 1(a). As more of the
feedback diodes are brought into conduction, so the effective amplifier gain alters. A typical characteristic for the circuit is shown in Fig. 1(b).
This scheme has two major drawbacks. Firstly, diode action affects its temperature stability, and secondly, altering one of the feedback resistors means the resetting of all of the resistors that follow it. A different approach is needed in order to obtain versatility.

## Mark-to-space-ratio averaging

If we were to apply a zero-to-five-volt square wave to a simple single-timeconstant $C R$ smoothing network, any variation of the mark-to-space ratio (m.t.s.r.) would cause a change in the output of the CR smoothing network. This output change has two extreme limits, namely zero and five volts, which correspond to a total absence of a pulse (zero percent m.t.s.r.), and a pure d.c. level of five volts ( $100 \%$ m.t.s.r.), being applied to the input of the CR network. Indeed, the output of the network varies linearly with respect to the m.t.s.r.

Now consider three d.c. levels: zero,

five, and seven volts, for example. Switching between the first two and filtering, as above, will produce a zero-to-five-volt signal depending on the m.t.s.r. However, if we now consider switching in the same way between the last two (five and seven volts), the output of the filter will be somewhere between five and seven volts, depending on the m.t.s.r. This process may be expanded still further.

The circuit described in this text has ten adjacent d.c. levels. Each level can be switched on and off, and there is a criterion that only adjacent pairs of levels can be switched, as in the simplified three-level case above. Interpolation between each level, by the m.t.s.r. process, is the basis of the function generator design.

Signal-to-time averaged b.c.d. circuit
The input to the signal-to-time averaged binary-coded-decimal (b.c.d.) circuit lies between set limits. If we choose this input to vary over zero to nine volts, relative to the circuit common, and consider the 0 to $100 \%$ variation of the input, we may divide it into nine equal intervals defined by ten input voltages (i.e. $0,1,2,3,4,5,6,7,8$ and 9 V ). Call these ten voltages, break voltages.

The ten break voltages correspond to the ten break points of the straight-line function generator, which composes its required function out of a nine-segment line ("fit"). As will be seen later, each break point of the function can be individually adjusted (without affecting any other), so that many different functions may be generated in a highly versatile manner.
The input to the circuit is scaled and fed into the non-inverting inputs of a string of nine comparators (see Fig. 2). Each comparator is set to "trip" (change state) at successively increased voltage levels.

Fig. 1. (a) A typical diode function generator in which the amplifier gain alters as more of the feedback diodes are brought into conduction. (b) shows a typical characteristic for this circuit.


Fig. 2. Signal-to-time-averaged b.c.d. circuit consisting of nine comparators, each set to change state successively as the input voltage-level increases. The output from the encoder i.c. is in inverted-b.c.d. (see text).

The scaling is set such that, as the input voltage passes the break voltages of $1,2,3,4,5,6,7,8$ and 9 volts, comparators 1 to 9 trip in turn.

The output of each comparator passes into an encoder i.c. (SN74147) and the resulting inverted-b.c.d. output from this section of the circuit contains information about the input signal and the input break voltages. The exact value of the input signal relative to the ten input break-voltages is obtained by using a time averaging technique. A triangular wave, of peak-to-peak amplitude equal to the intervals (in this case 1 V ) between the ten break voltages, is added to the input signal. This resulting signal, which is applied to the input of the comparators, will lie between two break voltages (assuming that the input signal is not mid-way between two break voltages), see Fig. 4.

Consider now one period of the triangle waveform, and observe the
time within the period that the combined signal spends between break voltages. It can be seen that this time, relative to the period of the triangular wave, is a direct function of the magnitude of the input signal. Each time the combined input voltage crosses a break voltage, the comparator for that break voltage will trip and change the b.c.d. output of this section of the circuit. It can therefore be seen that the average state of the b.c.d. output, in terms of time, will yield the exact position of the input signal relative to the ten input break voltages.

Note that for a static input signal, the typical b.c.d. output will consist of two adjacent b.c.d. states; the m.t.s.r. of each state being determined by the input magnitude relative to the ten break voltages.

## Output and function generation section

The output circuit converts the time averaged b.c.d. signal of the previous state into a meaningful output. Basically, each b.c.d. state fed into the output section switches on one of ten voltage levels, all of which are pre-set by potentiometers to the functional characteristic required by the user. The
voltage levels are then summed up by a summing amplifier (see Fig. 3). After passing through a second-order RC filter, the resulting signal appears at the output of a buffer amplifier $\left(\mathrm{IC}_{17}\right)$. Finally, the signal is scaled by the output amplifier.

The actual function generation of the circuit is achieved by mark-to-space ratio averaging, as mentioned earlier. However, here the magnitude of the input signal determines which voltages are switched on and off and what m.t.s.r. is applied to the switching voltages. The user, however, dictates the magnitude of each of the voltages switched, and therefore the way the average of the voltages changes as the m.t.s.r. of each voltage varies. Theretore; the final output of the circuit (after scaling) consists of a nine segment characteristic having ten break points, see Fig. 5. Note that each segment joins smoothly with its adjacent segments. Note also, that the break points are all independently adjustable. This means that virtually any (but not every) characteristic/function may be approximated by a nine segment fit using this circuit.
It would not be difficult to expand on



Fig. 6. Main power supply circuit.
the above idea, and produce a fit composed of more than nine segments.

## Design considerations

For the circuit to be able to resolve small changes in the input, and convey the information to the b.c.d. signal, the hysteresis effect at the input (differential) of the comparators at small differential signals (specifically at the break points) must be considered. The design should also allow for the slew rate of the comparators so that the relatively fast t.t.l. can respond to the fast-changing differential input signals to the comparators.
In this case a resolution of $0.1 \%$ was desired and, with a maximum considered input hysteresis voltage of 5 mV , this set the maximum signal to the comparators, for $100 \%$ input to the complete circuit, at five volts. The slew rate of the comparators was effectively speeded up by lowering the frequency of the triangular wave to about 200 Hz . 'This gives ample time for the comparators to respond to a 5 mV input differential.

The non-inverting input to the comparators is protected by using zener diode tied to the -5 V rail (see circuit). The choice of comparators and b.c.d. encoder i.c. also necessitates the incorporation of diodes $D_{2}$ to $D_{10}$ inclusive. This arises due to the lack of input protection diodes on the l.s.i. chip, and the possible 'harmful' voltage surges at the comparator outputs. The final output of the circuit is provided with full zero and span adjustments so that the output can be calibrated.

The function generator is considerably accurate, because the conversion is largely digital. The prototype, which used metal film resistors and cermet
potentiometers, showed a negligible change due to ambient temperature fluctuations.

There are many applications for this circuit. They include linearity correction of non-linear signals, generation of mathematical functions, and the generation of voltage programming functions.

## Generation of periodic waveforms

The following is as an application example of the analogue function generator being used to produce continuous functions, of variable period
and complex shape. This may sound difficult, but it is really very easy.

Consider the arranged input/output characteristic (or transfer function) of the analogue function generator to be a single period of the first waveform in Fig. 8. Remember, that this complex function has been pre-programmed into the analogue function generator by use of the adjustable trimpots $\mathrm{R}_{68}$ to $\mathrm{R}_{77}$.
Now, by using a ramp generator attached to the input of the analogue function generator, the output of the last-mentioned generator will follow
Fig. 7. Power supplies for the mother board.

the programmed characteristic as the ramp rises, returning back to the start of the characteristic as the sharp edge of the ramp falls. Therefore the output of the analogue function generator will be a continuous complex function, of programmable shape and period equal to that of the ramp input. Fig. 8 shows examples of repetitive waveforms generated in this manner.

## Component list

Resistors (all 2\% metal oxide unless otherwise stated)

|  | 39 k |
| :--- | :--- |
| 1 | 100 k |
| 2 | 22 k |
| 3 | 3 k |
| 4,5 | 6.8 k |
| 6 | 1.2 k |
| 7 | $825,1 \%$ or better |
| $8-17$ | 1.5 k |
| 18 | $39 \mathrm{k}, 10 \%$ carbon |
| $19-37$ (odd) | 4.7 k |
| $20-38$ (even) | 2 k |
| $39-48$ | 220 k |
| 49 | 1.2 k |
| 50 | $220 \mathrm{k}, 10 \%$ carbon |
| 51,52 | 1 k |
| 53,54 | 2 k |
| 55 | 200 |
| 56 | $120,10 \%$ carbon |
| 57 | 1.2 k |
| 58 | $130,10 \%$ carbon |
| 59 | 1.2 k |
| 60,61 | 1 k |
| 62 |  |



Fig. 8. Examples of periodic waveforms which can be produced by the analogue function generator

| 63 | 470, $10 \%$ carbon |
| :---: | :---: |
| 64 | 1k |
| 65 | 1.5k |
| Variable resistors (Cermet trimmers) |  |
| 66, 67 | 10k |
| 68-77, 79 | 1k |
| 78 | 50k |
| Diodes |  |
| ${ }^{1} 1$ | 6.8V $5 \% 400 \mathrm{~mW}$ zener |
| 2-20 | 1 N914 |
| 21, 22 | 100V 2A bridge rectifier |
| 23 | 20 V , |
| 24 | $5.6 \mathrm{~V}\} 5 \% 400 \mathrm{~mW}$ zeners |
| 25 | 6.2 V |

## Integrated circuits

| $1-3$ | $\mu \mathrm{~A} 741 \mathrm{C}$ |
| :--- | :--- |
| $4-12$ | $\mu \mathrm{~A} 710$ |
| 13 | SN74147 |
| 14 | SN7404 |
| 15 | SN7442 |
| $16-18$ | $\mu \mathrm{~A} 741 \mathrm{C}$ |
| 19,21 | $\mu \mathrm{~A} 7815$ regulator (1A) |
| 20 | $\mu \mathrm{~A} 7805$ regulator |
| 22,23 | $\mu \mathrm{~A} 741 \mathrm{C}$ |

All available from Bi-Pak Electronics, Ware, except IC $1: 3$ which can be obtained from Aries Electronics, Maidenhead

Transistors

| $1-10$ | 2N3709 |
| :--- | :--- |
| 11 | 2N3055 |
| $12-14$ | 2N3053 |
| 15 | 2N2904 |

## Transformer

Primary: 240 V r.m.s.
Secondary: $2 \times 20 \mathrm{~V}$ r.m.s. at 300 mA
Barrie Electronics, London
Capacitors ( $\mu \mathrm{F}$ unless otherwise stated)

| 1 | 220 n |
| :--- | :--- |
| 2,3 | 2.2 |
| 4,7 | 680 E |
| 5 | 10 E |
| 6 | 22 E |
| 8 | 6.3 E |
| 9,10 | 10 n |
| Reference |  |

1 Crump, A. E. Diode function generators, Wireless World, Dec. 1967, pp. 594-598.
A set of two p.c.bs for the function generator and power supply is available for $£ 7$ inclusive from M. R. Sagin at 23 Keyes Road, London NW2.

## Matrix HJ: technical refinement and political jostling?

Last month the BBC strongly reacted to a recent FCC subjective evaluation of surround reproduction system by publicly criticising the FCC Laboratory "not for what they have done so much as the limitations that they pose". The report, part of an FCC Inquiry into "Quadraphonic" broadcasting, showed SQ was preferred to other two channels systems and $H$ and QS in musical preference tests for "quad", stereo and mono. And in what could be viewed as preparation for a united front to the FCC Inquiry, as well as a response to recent criticism of Matrix H broadcasts, the BBC Matrix H surround formula has been changed.

From about half way through the promenade concerts the centre front phase difference - until then $48^{\circ}$ - was changed "fairly significantly", C. B. B. Wood head of engineering information told WW "and the sort of figures we are talking about are $28^{\circ}$, $29^{\circ}$ and $30^{\circ}$.

The change from Matrix H to HJ follows criticism of the stereo compatibility of Matrix H broadcasts, particularly of the proms, now no longer described as completely "unimpaired".

The BBC had kept the change quiet to avoid prompting listeners but at an IEE lecture on the 13th October, the change was made public. They almost got through the proms without any response: the 30 letters received were largely "self cancelling" they said. At the meeting David Meares described
the change to HJ as "slight" and combining "most of the worthwhile features of H and J ". The tentative HJ is actually in the form of tolerance zones on the phase-amplitude or energy sphere. "Zones are really the only way" the BBC now say, because of the variety of microphone techniques. The zone broadly encompasses those points covered by phase-reduced centre-front H and J loci. Front left and right points are reduced in phase from $75^{\circ}$ to $60^{\circ}$ and also in amplitude. "To give credit where it is due" said the lecturer, "the format was iointly agreed with Michael Gerzon and the BBC". They hope to sort it out by the end of the year and plan further experiments within its confines. If this is "firmed up" it would then appear to provide an opportunity to argue the FCC tests to be invalid.

First discussions with the J team were kept a secret at the BBC's request, a move that could have an advantage in giving a better impression of unity than might actually be the case. There is certainly not much apparent unity between the two broadcast organizations. Whilst not disagreeing so much with Meares' analysis of the FCC report, they certainly were at odds over the feasibility of a narrow band third channel system. With the BBC refusing to take the issue any further and the IBA revving to go, given the manpower. the pointed remark by Meares, "We look forward to hearing more
details of this", drew comment from the IBA engineers at the meeting.
The attitudes of the two organizations are well illustrated in two recent pronouncements. "The penalty of the extra channel may be very much smaller than initially supposed" (Alan James, IBA, IEE meeting, October 13th). "We are filled with doubt as to whether a three channel system would ever get off the ground in Europe. Stranger things have happened of course. But the way to -prove the pudding is well known. We would like to see someone conduct a one-year test of the three channel system and we would like to know at the end of the year how many three channel tuners and three track cassette decks the service had attracted" (C. B. B. Wood, AES Annual Dinner, 13th September, 1977).

In answer to a query by David Read on circuit alterations to the BBC Matrix H decoder design (June issue, page 34) Meares said that although he had not put pen to paper on the effect, he couldn't tell the difference between an HJ encoding and an H encoding through an H decoder. In any case further changes may still be made. BBC Research Department tell us that due to unequal loading on the phase shift circuits in the BBC H decoder circuit, they recommend a directly-coupled emitter follower be inserted following each of the three summering circuits. Emitter load should be $10 \mathrm{k} \Omega$.

# Circuit Ideas 



## Variable slope low-pass filter

An important feature of audio pre-amplifiers is a low-pass filter to limit the bandwidth at high frequencies. It is desirable to incorporate a variable slope device as this allows the amount of filtering to be selected for a particular programme source. A Sallen-Key design is suitable for second order filtering and the circuit may be realized with any suitable form of unity gain amplifier. This circuit uses an enhanced source follower which offers a high input impedance, low input bias current, and low distortion. The low bias current enables the gate of the f.e.t. to be directly coupled to the slider of the potentiometer. Resistor $R_{4}$ prevents any

noise occurring from the slifer and $\mathrm{R}_{3}$ restricts the fractional setting of $\mathrm{R}_{2}$ to about 0.1. This ensures that there is an ultimate roll off above audible frequencies, and also prevents any stray capacitance at the amplifier input from forming a high Q filter.

With the values shown the turnover frequency is about 6 kHz and the attenuation with minimum slope is about 2 dB at high frequencies. Combined with a simple CR network, the slope can be adjusted between 6 and 18dB/ octave but the author's preference is to use a second switched Sallen-Key filter with fixed slope to give a total variation from 0 to 24 dB /octave.
R. J. Tidey,

Oxford.

## Long duration c.m.o.s. monostable

The duration of a pulse from standard c.m.o.s. monostable i.cs is affected by temperature changes and other short term variations. When accurate pulse duration is required, a more stable circuit can be made using a 4060 14stage counter and oscillator, and half of a 4027 dual JK flip-flop.
Initially, the dividers are held at 0 V by a high on their reset line. A monostable pulse is triggered by a positive edge which sets Q to a high, clears the reset line, and allows the counters to operate. After $2^{n-1}$ counts, the $\div 2^{n}$ output goes

## Inductance bridge

This circuit was developed to replace a high cost differential amplifier used with a.c. bridge circuits. A pulse transformer with 3 windings is connected in series with the lower limbs of the bridge. Decade resistance box Rv, and variable inductor Lv , are used for fine balancing while the ratio $R_{1} / R_{2}$ provides a course adjustment. As the bridge is brought towards balance, the inductance of the transformer drops rapidly to its leakage value which causes an increase in sen-
sitivity of the system. The third winding is followed by an amplifier with a gain of about 500 . If this is fed into a transistor threshold detector it is possible to detect inductance changes of less than $0.1 \mu \mathrm{H}$ in $30 \mu \mathrm{H}$.
If the unknown limb is used in a noisy environment the interference can be filtered and fed into the known limb through capacitor C to produce a cancellation.
G. C. Kervill \& T. Austin,

Sacol Controls,
Southampton.

high to reset the bistable and zero the counters. Pulse duration is approximately $2^{\mathrm{n}-1} \times 2.2 \times R t \times \mathrm{Ct}$ seconds and the variation, in normal room temperatures, is about $0.5 \%$. Variation in supply voltage will increase this figure.
Apart from stability, this circuit has the advantage of long variable pulse times with good linearity. Application notes ICAN 6086 and 6539 from RCA give details of a crystal control which can be used to produce even more stable, fixed durations.

## R. Price,

Medical Physics Department,
Leeds University.


## Microprocessor 5V clock generator

Many microprocessors require a clock with a full 0 to 5 V swing. Standard c.m.o.s. circuits are unable to provide sufficiently fast rise and fall times and, although t.t.l. has the required speed, the high level needs forcibly pulling up to the 5 V rail. The 74121 monostable has a $\bar{Q}$ output which may drive a fast switching transistor, which in turn pulls the Q output up to 5 V during a logic 1 as shown. An appropriate threshold for $\mathrm{Tr}_{1}$ is provided by the potential divider $R_{2}$, $\mathrm{R}_{3}$. Capacitor $\mathrm{C}_{2}$ ensures that the transitions are shorter than 50 ns , and excessive overshoot or ringing is prevented by the damping resistors $\mathrm{R}_{4}$ and $R_{5}$. For the M6800 microprocessor, two non-overlapping clock phases are required with a frequency of approximately 1 MHz . These waveforms may be obtained by connecting two monostables in a ring. The values shown for the timing components give approximately 500 ns pulse lengths. If a single 74123 is used for $\mathrm{M}_{1}$ and $\mathrm{M}_{2}$ there

will be no spare A inputs to 5 V , but the spare clear inputs must be tied to the supply rail. Also, $R_{1}$ and $R_{6}$ should be increased to $3 \mathrm{k} 9 \Omega$. The circuit will drive loads of 150 pF and keep within Motor-
ola's specified requirements for the M6800.
Tim Perkins,
MRC Neurological Prostheses Unit, London S.E.5.

## Line charged h.v. pulse circuit

On the positive half cycle of the mains, $\mathrm{C}_{1}$ charges to peak line voltage through $\mathrm{D}_{1}$. On the negative half cycle, $\mathrm{C}_{2}$ is charged through $D_{2}, D_{3}$ and $R_{1}$, to the peak voltage of diac $D$. When the diac fires at about $30 \mathrm{~V}, \mathrm{C}_{2}$ discharges partially through the gate circuit of the thyristor. The thyristor then turns on and discharges $\mathrm{C}_{1}$ through the primary of a car ignition coil, which generates a 20 kV pulse in $L_{2}$. This sequence repeats 50 times per second.

The light sensitive resistor prevents the circuit firing if illuminated by a light source such as the flame in an oil burner which is ignited by a h.v. spark.
A. Refsum,

The Queen's University of Belfast.


## Earth referenced V to I

Circuits often require a voltage controlled current source in which a variable load is connected directly to earth. Most operational amplifier configurations produce either a current source or sink referenced to the supply rail. This circuit produces a voltage controlled current which may be injected to the earth rail via the load.

The operational amplifier and $\mathrm{Tr}_{1}$ act as a voltage controlled current sink. The balanced tracking of the current mirror $\mathrm{Tr}_{2}$ and $\mathrm{Tr}_{3}$ is used to convert this sink current to a source current. The circuit has good thermal stability and a linearity to within $3 \%$ for output currents up to 3 mA . This circuit also develops the full supply voltage across the load. B. Wilson \& K. Patel, University of Technology, Baghdad.


## "No loss" capacitor

This simple circuit provides a "perfect" capacitor at frequencies below about 100 kHz . The principle has been used to construct fixed-frequency capacitance standards for use in an accurate capacitor bridge. An oven mounted prototype provided a stability of one p.p.m. and a residual phase angle difference from pure capacitance of one micro-radian. All capacitors are silver mica types.
B. J. Frost,

Paignton,
S. Devon.

(a)

## Automatic micropower battery charger

In micropower equipment it is sometimes necessary to switch between an intermittent power source, such as a solar cell, and a storage battery which must be kept charged. The circuit shown offers this facility with very few components. When the solar cell voltage is 0.2 V below the battery voltage, the circuit is powered through the forward biased diode. When the cell voltage is greater than the battery voltage, the battery is charged by an approximately constant reverse leakage current from the diode. The diode, which may be a Germanium point con-
tact or junction type such as an OA90 or OA73, should be selected for a suitable reverse leakage current. The battery can be a manganese-alkaline type or a Zn -AgO watch type cell.
M. Hadley,

University of Southampton.


## Sequence generator

This sequence generator uses a gated shift register made from a 7475 D-type latch, and four exclusive OR gates. A

simple programmer can also be constructed using two 7493 binary counters. The outputs can be used as a signal source for a synthesizer, or as a sequencer for a lighting unit.

The clock pulse should be narrow to avoid race-round effects. If desired, a 74175 can replace the 7475 , as this has a clear input which may be useful for resetting.
P. D. Maddison,

Portswood,
Southampton.


## Books Received

Modern Electronics Made Simple by George H. Olsen is suitable for $O$ and A level students or those teaching themselves general electronics. This book is one from a series of 67 different titles and, judging by some of the other titles such as Electronic Computers, Mathematics, and Electricity, the areas which are not covered in this book can be found in one of the others. Fifteen chapters deal with components, amplifiers, radio and television, digital electronics, power supplies, oscillators, and high-fidelity. Each chapter has a few questions at the end, and a final chapter describes ten projects. Price $£ 2.95$. Pp. 306. W. H. Allen \& Co. Ltd, 44 Hill Street, London W1X 8LB.
Advanced Data-Transmission Systems by A. P. Clark is based on research, and lecture courses in digital communications at Loughborough University of Technology. The book is intended for practising or student engineers and deals with principles and techniques rather than practical considerations. Mathematical analysis is used where necessary together with worked examples. The emphasis is on future systems and, although many of the systems described have not been built, they have all been studied by computer simulation and theoretical analysis. The main purpose of these techniques is to enable the maximum transmission rate to be achieved over a linear channel, for an acceptable tolerance to noise, and an acceptable degree of equipment complexity.
After an introductory section, chapter 2 develops the theory of the discrete Fourier transform using matrix algebra and then applies it to the analysis and distortion in a sampled baseband signal. Chapter 3 develops the theory of optimum detection and estimation of a sampled baseband digital signal and applies the theory to cases where the noise is Gaussian. The final three chapters cover techniques for detecting distorted digital signals in synchronous serial data-transmission systems and parallel systems, which use code-division multiplexing. Price $£ 12.50$. Pp. 427. Pentech Press, 4 Graham Lodge, Graham Road, London NW4 3DG.

Manual of Solid State Circuit Design and Troubleshooting by Vester Robinson starts by covering the characteristics and biasing of semiconductor devices, together with design considerations. Subsequent chapters deal with audio and radio frequency circuits, signalgenerating circuits, and power supplies. Design examples and graphical information are given where appropriate. The final two chapters describe bench testing procedure and troubleshooting techniques. Price £15.15. Pp. 413. Prentice/Hall International, 66 Wood Lane End, Hemel Hempstead, Herts HP2 4RG.

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Note: 10 Mr input impedance.
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# Synthesized f.m. transceiver - 2 

## The receiver and transmitter sections

by T. D. Forrester, G8GIW

Part. 1 of this article described the synthesizer and oscillator sections of the transceiver and discussed the method employed for channel and mode selection. This second part describes both the transmitter and receiver sections and concludes with some tips on construction and the alignment procedure.

The receiver part of the transceiver uses standard and well-proven circuitry and should have no alignment or constructional problems. Figure 5 shows the circuit diagram for the receiver.
$\mathrm{Tr}_{5}$ is a dual-gate m.o.s.f.e.t. which is used as a r.f. preamplifier. It has just enough gain to overcome the mixer
noise and consequently it helps to ensure a good dynamic range. $\mathrm{Tr}_{6}$ is another dual-gate m.o.s.f.e.t. This device converts the 145 MHz signals to the i.f. frequency of 10.7 MHz . It has a resistive drain load and feeds the 10.7 MHz crystal filter, which gives the receiver all its selectivity. It must therefore be a top quality item.
The bulk of the receiver gain is produced after the filter by the SL612 and the HAll 137 f.m. discriminator. This also helps to ensure a good dynamic range combined with excellent sensitivity. The squelch control is a built-in function of the HA1137 and is sensitive
enough to be lifted by a $0.2 \mu \mathrm{~V}$ signal at 145 MHz .
$\mathrm{Tr}_{7}, \mathrm{Tr}_{8}$ and $\mathrm{Tr}_{9}$ act as the a.f. power amplifier, developing 200 mW into an $8 \Omega$ load.
$\mathrm{Tr}_{10}$ and $\mathrm{Tr}_{11}$ form the frequency
Fig. 5. Circuit diagram of the receiver section of the transceiver. Selectivity is determined by the bandwidth of the 10.7 MHz crystal filter in the i.f. circuit.

Correction: First frequency multiplier stage requires a 1000 pF decoupling capacitor between top of tuned circuit and earth.


multiplication circuit needed to convert the 22 MHz signal to the required 134 MHz local oscillator frequency. $\mathrm{Tr}_{11}$ is a Class $B$ doubler, bringing the frequency up to 44 MHz , and $\mathrm{Tr}_{10}$ is a Class C tripler, which converts this to approximately 134 MHz .
In terms of sensitivity, noise figures, dynamic range and selectivity, the performance of the receiver is excellent; indeed it is very much better than some of the commercial transceivers available to the radio amateur.

## The transmitter

As stated previously, the transmitter is based on a Mullard design, which was developed some years ago. It has been slightly modified to ease alignment problems associated with transmitter frequency multiplication. The circuit diagram for the transmitter multipliers and power amplifiers is shown in Fig. 6.
$\mathrm{Tr}_{12}$ acts as a Class B grounded-base frequency doubler for bringing the 24 MHz synthesizer frequency to 48 MHz . $\mathrm{Tr}_{13}$, with its collector tuned to the 145 MHz band, acts as a Class C frequency tripler.
$\mathrm{Tr}_{14}$ is a 145 MHz amplifier which has a little forward bias applied in order to minimize the amount of r.f. drive required. $\mathrm{Tr}_{15}$ and $\mathrm{Tr}_{16}$ amplify the

Fig. 6. Circuit diagram of the transmitter multipliers and power amplifiers.

145 MHz signal, present at the $\mathrm{Tr}_{14}$ collector, to a level of 2.5 W ; again Class $C$ is used in the power stages to maintain a high level of efficiency.

If the transmitter is to be used from a good portable location, it is advisable to use a band-pass filter to reduce any harmonics to an acceptable level.
Fig. 8 shows the microphone compressor and deviation control circuits of the transmitter.
Frequency modulation is applied to the varicap diode via the preset potentiometer $\mathrm{R}_{82}$, which adjusts the level of deviation. The SL622, $\mathrm{IC}_{13}$, is an audio compressor. This ensures that the level of deviation remains constant over the wide range of microphone inputs.

## Construction

As the synthesizer is the heart of the transceiver, it was decided to build this unit first, thereby enabling it to drive

Fig. 7. A suitable regulated 15 V power supply circuit for the transceiver. It requires an unregulated 12 V power supply.

the transmitter and receiver at a later date.
The synthesizer logic was built on a piece of Vero-Board measuring $33 / 4 \times$ 4 in , and hard wired between i.c. pins using sub-miniature wire. This method of construction was chosen as it allows easy modification and also enables the board to be made smaller than a conventional p.c.b.
The v.c.o. associated with the synthesizer was built on a separate double-sided p.c.b., one side of the board being used for a ground plane the other side being used for interconnections. Using the varicap shown, the v.c.o. operates from 18 to 36 MHz for a control voltage of 0 to 12 V respectively. However, other types of varicap diode may be suitable, providing the v.c.o. is capable of operating over the range of 20 to 25 MHz .
The transistors used in the v.c.o. are general purpose, types 2N3707. Other types such as the BC108 and the BC109, are equally suitable.
The power supply for the synthesizer logic must provide a well-regulated and noise-free 15 volts. This is to ensure that the 4059 is sufficiently fast to cope with the 6 MHz clock. A suitable power supply is shown in Fig. 7. This supply is designed to provide 15 V at 100 mA over a voltage input range from 9 to 18 V . Note that the 74LS74 requires a 5 V rail, not 15 V as with the c.m.o.s. devices. To interface the 5 V logic of the 74LS74, a pull up resistor, $\mathrm{R}_{7}$, is used.
The receiver is built on a double-sided p.c.b. and contains all the circuitry shown in Fig. 5. Again, one side of the p.c.b. is used as ground plain, this is to simplify board design and minimize the possibility of instability occurring.
When building the r.f. and mixer stages it is important to keep lead lengths as short as possible in order to minimize lead inductance, which can be appreciable at v.h.f. frequencies.

The 10.7 MHz stages, consisting of the SL612 and HAll37, must be logically laid out. This means keeping inputs away from outputs. Also, as there is a considerable amount of gain at 10.7 MHz , attention should be paid to the screening of the receiver, otherwise there might be i.f. breakthrough.

The i.f. filter probably represents the most expensive item in the receiver and could cost as much as $£ 20$. However, at the time of writing there were many ITT and STC surplus crystal filters on the market at very competitive prices. As the filter gives the receiver all its selectivity, care must be taken to ensure that the i.f. signal cannot leak around it, otherwise the receiver may be blocked off by a strong local station on another channel.

The layout of the transmitter multiplier and p.a. stages is not unduly critical, providing all leads which carry r.f. current are kept short, that is, less than $1 / 8 i n$, (this includes the $\ln F$ disc capacitors associated with the supply decoupling). Clip-on heatsinks should be fitted to the driver and p.a. stages to keep the collector temperature below $70^{\circ} \mathrm{C}$.
The actual method of assembling the four boards into a complete transceiver is left to the discretion of the constructor, but it is a good idea to fit the synthesizer logic into a screened box with leads for power in, 24 MHz in, control wires in, and error voltage out only. This is to reduce the possibility of any noise generated by the synthesizer logic interfering with the receiver.

The transmit/receive switching has also been left to the discretion of the constructor, although in the prototype, electronic transmit/receive switching was used, with the exception of the aerial changeover, which was a sub-miniature relay.

## Alignment

The first unit to be adjusted is the v.c.o. (see Fig. 4). This is accomplished by connecting the control wire of the varicap diode to earth, then adjusting the slug in $L_{1}$ for a frequency of approximately 20 MHz . After this operation, connect the control wire to $V_{\text {cc }}$ and check that the frequency rises to above 25 MHz , then tune $\mathrm{L}_{2}$ for maximum output at 24 MHz .
When the v.c.o. is aligned, connect it to the logic in the synthesizer and set the thumbwheels to channel 00 and the mode switch to normal transmit. If all is well the control voltage should settle to a value between 2 and 8 V and the output frequency should be 24.16666 MHz . If the frequency is slightly out, then $\mathrm{C}_{2}$ requires adjustment to set the reference accurately at 1.0146666 kHz . Switching the thumbwheels to 01 should cause the frequency to increase by 4.1666 kHz at 24 MHz , ( 25 kHz at 2 metres).

To check the receiver i.f. and repeater shifts, it is only a matter of switching


Fig 8. Microphone compressor and deviation control circuit for the transceiver.
from normal transmit on channel 00 to normal receive and checking that the output shifts from 24.1666 to 22.38333 MHz , (a 10.7 MHz shift down in frequency at 2 metres).

Likewise, to check the transmitter 600 MHz repeater shift, switch from normal transmit to repeater transmit and check that the frequency shifts from 24.16666 to 24.06666 MHz , that is, a 600 kHz shift down in frequency at 2 metres. Again, when testing the inverse-repeater receive, check that the frequency shifts from 22.3833 to 22.2833 MHz , that is, another 600 kHz shift down in frequency at 2 metres.

If all appears well with the synthesizer, then it can be connected to the transmitter. Set the mode switch to normal transmit on channel 20 and adjust $\mathrm{L}_{9}$ (Fig. 6) for maximum voltage across $R_{72}$ (about IV), ensure that the drive to $\mathrm{Tr}_{13}$ is on 48 MHz ( $\operatorname{not} 24 \mathrm{MHz}$ ), then adjust $\mathrm{C}_{68}$ for maximum voltage across $R_{75}$ (above 0.1 V ) and check that the collector of $\mathrm{Tr}_{13}$ is tuned to 145 MHz (not 96 MHz ). It is only necessary to tune $\mathrm{C}_{69} \mathrm{C}_{79} \mathrm{C}_{71}$ and $\mathrm{C}_{73}$ in turn to produce maximum output on 2 metres.
The transmitter r.f. stages are now complete, and it only remains to apply audio to the varicap diode to produce a f.m. transmission on 2 metres. Adjusting $\mathrm{R}_{82}$ (Fig. 8) sets the level of deviation, and should be adjusted by monitoring the transmission on a receiver of suitable bandwidth.

To align the receiver, set the synthesizer to normal receive on channel 20, and then adjust $\mathrm{L}_{7}$ (Fig. 5) for maximum voltage across $\mathrm{R}_{3 \mathrm{~g}}$ Inject a signal on 145.5 MHz at the aerial input and adjust $\mathrm{C}_{64}, \mathrm{C}_{65}$, and $\mathrm{C}_{67}$ in turn for maximum sensitivity, reducing the level of the signal as the receiver is tuned up.
Finally, to achieve the best noise figure, adjust the coupling between $\mathrm{C}_{27}$ and $L_{8}$ for maximum sensitivity. The receiver alignment is now complete and should require less than $1 \mu \mathrm{~V}$ for full
quietening, and the mute should lift on signals down to $0.5 \mu \mathrm{~V}$. After connecting an aerial to $\mathrm{L}_{3}$ and selecting a popular channel, stations should be heard. It may be necessary to trim $\mathrm{C}_{64}$ slightly to match the aerial correctly. It may also be necessary to adjust the value of $\mathrm{R}_{65}$ slightly, to remove any crossove distortion present in the output stage.

The transceiver is now completel: ready for use on the air.

## Conclusion

Through the extensive use of integrate circuits in the synthesizer, and receive i.f. section, it is hoped that alignmen problems associated with multichannt transceivers have been lessened considerably. That is, it is only necessary to adjust the reference crystal to 1.000 MHz and the transceiver will be exactly on frequency, no matter which channel or mode is selected.

It is also possible to build the synthesizer separately and to use it todrive commercial rigs, thus avoiding many expensive crystals which could cost about $£ 5$ per channel (a 22 -channel transceiver would have $£ 110$ worth of crystals inside it). By comparison, a synthesizer of the type described could be built for as little as $£ 30$ and it would also produce all 40 channels in addition to offering repeater and inverse repeater operation - obviously a great saving.
Correction notes. The $220 \mathrm{k} \Omega$ resistors $\mathrm{R}_{8}$ to $\mathrm{R}_{25}$ (see Fig. 3) on the 4560 inputs (pins 15,1 , $3,5,14,2,4$ and 6) are pull-down resistors which should be between the inputs and earth, and not as shown. Connections should also be made direct between the inputs and the thumbwheels and mode lines; 15 to $\mathrm{a}, 1$ to b, etc. The right-hand pin on IC 4 shown as pin 3, should read 'pin 8'. Further details in next issue.

## Printed circuit boards

A set of p.c.bs comprising two double sided and one single sided is available for E1. 1 inclusive from M. R. Sagin at 23 Keyes Road, London NW2. The boards accommodate components for the receiver, transmitter, synthesizer and v.c.o.


## Synthesized communications receiver

A communications receiver, designated the PR2250, is claimed by the makers, Plessey Avionics \& Communications Ltd, to be the first receiver unit - as distinct from system (see p.85, Nov. 76 issue) - to take most of the tedious manual labour out of surveillance and monitoring operations. The receiver, which covers l.f., m.f. and h.f. bands from 10 kHz to 30 MHz , is a solid-state high-stability synthesized instrument capable of being digitally controlled from either a keypad on the front panel or from an external serial data stream. An optional microprocessor module, claimed to be unique in this type of equipment, may be programmed to allow the receiver to interface with many different control systems. Tuning is by this keypad or a photodiode, and l.e.d. dial scanning-system

(as described in pp.65-6, Sept. 77 issue), enabling fast and accurate searching to be carried out.
The tuning dial has two tuning rates, one for fine tuning and one for searching, both providing frequency steps of 10 Hz . The keypad enables a new frequency to be selected quickly by simply punching in the desired frequency in kHz . A non-flicker I.e.d. display indicates the receive frequency in MHz. Push button controls also provide selection of a.m., f.m., c.w., u.s.b., 1.s.b. and i.s.b. modes, bandwidth filters, a.g.c. and a.f.c. All of these selections, and the frequency, can be stored in a built-in 16 -channel memory so that they can be recalled almost instantly.

The synthesizer, a patented Plessey design, allows tuning from one end of the frequency range to the other in

10ms (typical). Specifications include a sensitivity on s.s.b. of 15 dB signal-to-noise-plus-noise for a $1 \mu \mathrm{~V}$ (e.m.f.) input and a typical noise figure of 10 dB from 100 kHz to 30 MHz . Intermediate frequency and spurious signal rejection is greater than 80 dB down.

Although this product represents astep forward in British-made professional communications equipment, at a price of about $£ 6,500$ - which doesn't include the microprocessor - it remains to be seen whether it can survive the competition if the Japanese, who are proving to be very proficient at producing this type of equipment at low cost, succeed in entering the professional communications equipment market. Plessey Avionics \& Communications Ltd, Ifford, Essex.
WW 301


## Digital printer

A microprocessor controller is used in the SP302 five-by-seven impact dotmatrix printer to enable it to perform functions such as double-width printing and double and triple spacing, which, the makers claim, are not available on competitive units. Tab functions are fitted as standard and may be externally controlled by simple software. The standard input baud rate is 110 , but other rates can be internally set. The unit prints at a rate of 50 characters per second. It will print multiple copies and is also capable of using ordinary $37 / 8 \mathrm{in}$ adding-machine paper. The SP302 measures $244 \times 127 \times 305 \mathrm{~mm}$ and has a power requirement of 25 W from 115 to 230 V at 50 or 60 Hz . Syntest, 169 Millham Street, Marlboro, Mass. 01752, U.S.A.
WW 302

## Low-cost DMM

A calculator-sized $31 / 2$-digit multimeter, the PDM35 introduced by Sinclair Radionics Ltd, is available in the UK at $£ 29.95$ plus $8 \%$ v.a.t. The meter measures $155 \times 75 \times 30 \mathrm{~mm}$ and weighs only 175 g . Its design is claimed to be based on the results of an international survey which tried to determine the features required from a multimeter by the majority of users. As a result, the meter does not have an alternating-current range. The PDM35 has four direct-voltage ranges, from 1 mV to 1 kV , and six direct-current ranges, from $\ln A$ to 200 mA , with a $10 \mathrm{~m} \Omega$ input impedance on the voltage ranges, and a maximum resolution of 0.1 nA on the current ranges. Accuracy in each case is $1 \% \pm 1$ digit of the reading. Its alternating-voltage range is suitable for frequencies from 40 Hz to 5 kHz from 1 to 500 V r.m.s. with an accuracy of $1 \% \pm 2$ digits. The meter also has five resistance ranges from $1 \Omega$ to $20 \mathrm{M} \Omega$ with an accuracy of $1.5 \% \pm 1$ digit, and five junction-test ranges. Sinclair Radionics Ltd, London Road, St. lves, Huntingdon, Cambs PE 17 4HJ.

## WW 303

## Sound level meter

The db-306 Metrologger is a pocketsized sound-level meter incorporating a minicomputer which measures and displays A-weighted sound levels over a selected 64 dB range. It also indicates the equivalent continuous sound level, the maximum sound level sampled and the time duration of the measuring period. The instrument, which utilizes a microprocessor, is normally supplied with a $1 / 4 \mathrm{in}$ ceramic microphone that can either be mounted on the Metrologger or be positioned up to 100 ft away. Sound levels are displayed in 1 dB incre-
ments on a digital readout which is updated four times per second. An inexpensive 9 V battery having an operating life of about 72 h is used to supply the device. Dawe Instruments Ltd, Concord Road, Western Avenue, London W3 0SD.
WW 304

## R.f. analyser

All the elements to form a complete transmission and reflection measuring instrument are contained in the Wiltron 640 r.f. analyser. The unit comprises a swept r.f. generator covering 1 to $1500 \mathrm{MHz}, \log$ arithmic amplifier with detectors, one of which is for reflection measurements, and a display (built by Tektronix). The display is in the mainframe of the instrument, the other elements of the system being in the form of plug-in units. System noise level is around -60 dBm and the r.f. output is calibrated to +10 dBm , so that a 70 dB dynamic -range is obtained. Several marker systems, for level and frequency, are provided; a nice refinement is the 'tilt' facility for frequency marker pips to enhance visibility on the skirts of responses. The output level is held to within 0.2 dB over the 1500 MHz range, and over the often-used 400 500 MHz sections it is constant to within 0.1 dB . Wiltron, 825 E Middlefield Road, Mountain View, CA 94043, U.S.A. WW 305


WW 302


WW 303

## R.f. transistor

A r.f. transistor, the BF199 manufactured by AEG-Telefunken, is suitable for video i.f. amplifier stages. It is a silicon n-p-n epitaxial planar transistor having a feedback capacitance of only 0.32 pF . Absolute maximum ratings include a $V_{\text {CEO }}$ of $25 \mathrm{~V}, \mathrm{~V}_{\text {CBO }}$ of $40 \mathrm{~V}, \mathrm{~V}_{\text {EBO }}$ of $4 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}$ of 25 mA and $\mathrm{I}_{\mathrm{B}}$ of 2 mA . Total power dissipation is 300 mW and the junction temperature is $150^{\circ} \mathrm{C}$. Other characteristics include a typical gain bandwidth product of 500 MHz and a typical $h_{\mathrm{fe}}$ of 88 . The BF199 is packaged in a T092Z case. Norbain Semiconductor Division, Norbain House, Arkwright Road, Reading, Berkshire RG2 0LT.
WW 306

## A-to-d converter

The LD 130 three-digit analogue-todigital converter i.c., from Siliconix, has been reduced in price by $45 \%$ from $£ 6.36$ to $£ 3.45$ (for quantities of 100 or more). This follows the company's improvements in high-volume manufacturing techniques. Sampling rates for this device vary from 1 to 60 per second and accuracy is quoted as $0.1 \% \pm 1$ digit, with a 1 mV resolution. Siliconix Ltd, 30A High Street, Thatcham, Newbury, Berks RG13 4JG.
WW 307


WW 304

## Microcomputer development system

A microcomputer system known as the muPro- 80 comprises a complete 8080A microcomputer, a control and display panel, an in-circuit emulator, a dual floppy-disc system and a software package. The software package includes a multi-purpose real-time executive and a high-level relocatable output assembler, having a number of sub-routines with Algol-type statements such as 'if', 'then begin', 'end' and 'else begin'. This package also includes a disc operating system and a text editor. User programmes can be operated in a time-share mode. The control and display panel enables memory or register contents to be examined or changed, break points to be set and the last 64 instructions to be executed and traced. All of the functions are provided for without the use of any of the system memory locations, such that all the 64 K memory locations, 255 i/o ports and interrupt lines are available to the user. Microsystem Services, Duke Street, High Wycombe, Bucks.
WW E98

## TV aerial isolators

Three aerial-isolation assemblies, types L1910, L1911 and L2114, have been designed for use in the $75 \Omega$ coaxial inputs of television receivers. The assemblies accept both 9.5 and 9 mm diameter plugs and are suitable for v.h.f. and u.h.f. receivers. Type L1910 has a single input and single output, type L1911 has a single input into a diplexer board giving two outputs, one v.h.f. and one u.h.f., and type L2114 has a single input and output but includes a built-in high-pass filter. Radio interference immunity in the frequency band from 470 to 775 MHz is greater than 25 dB , and from 775 to 860 MHz is greater than 20 dB . Signal attenuation figures at 10 MHz range from 0.5 dB for the L1910 to 25 dB for the L2114. The assemblies are accepted by many national safety authorities in Europe, Scandinavia and South Africa. Belling \& Lee Ltd, Great Cambridge Road, Enfield Middlesex. WW 309

## D.m.o.s. f.e.ts

A range of d.m.o.s. f.e.ts, available from Mullard, is comprised of siliconinsulation gate f.e.ts of n-channel enhancement mode. The devices are manufactured by a double-diffused process which gives them frequency performances up to 2 GHz . Characteristics claimed include high gains, low noise levels, good linearites, low intermodulation distortion, and low feedback capacitances. Types available include TO72 devices for general purpose r.f. amplifiers up to u.h.f. (types SD200 to

SD203), high speed f.e.ts having low 'on' resistances (SD210 to SD215), and dualgate f.e.ts for v.h.f. and u.h.f. mixer and amplifier applications (types SD300, SD303 to SD308). Also available is the eight-pin d.i.l. type SD6000 which consists of two dual-gate devices in a single encapsulation. It is intended for use in varactor and conventional tuners for v.h.f. f.m. circuits. One device is characterised as a mixer, and the other as a r.f. amplifier. Mullard Ltd, Mullard House, Torrington Place, London WClE 7HD.
WW 310

## High-current r.f. inductors

Two encapsulated r.f. inductors, introduced by Plessey Windings, are designed to carry higher-than-normal direct currents without degrading the inductance value. Type 11207, which has an inductance range from $50 \mu \mathrm{H}$ to 10 mH and a typical $\ddot{\mathrm{Q}}$-factor greater than 100 , measures only 7 mm in height and 19 mm in diameter. Its superimposed d.c. value (the direct current which will cause the inductance to reduce to $95 \%$ of the zero d.c. value) is from 85 mA to 1.2 A , depending upon the inductance value. Type 11206, measuring only $61 / 2 \mathrm{~mm}$ inheight and 14.5 mm in diameter, has an inductance range from 1 mH to 100 mH , Q-factors from 75 to 200 , and its superimposed d.c. ranges from 7 to 60 mA . Plessey Windings, The Plessey Company Ltd, Ilford, Essex.
WW 311

## Locking dials

Two precision locking dials, types LK25 and LK50, have been made by Argo Electronic Components Limited. The LK25 is a miniature dial (measuring 22 $\times 25 \mathrm{~mm}$ ) having a photoanodized face with graduations of $0-10$ over an arc of 270 degrees and $0-100$ over an arc of 300 degrees. The LK50, which incorporates a dial measuring approximately 2 in
square, has standard graduations of $0-10$ over 270 degrees and $0-100$ over 300 degrees. However, for both dials, graduations can be supplied to customer specification, and may be used over 360 degrees. Argo Electronic Components Limited, Stiron House, Electric Avenue, Westcliff-on-Sea, Essex, SS0. WW 312

## Crystal oscillators

A high-power v.h.f. crystal oscillator, the model CO-284W, provides a stable fixed-frequency output of $1 \mathrm{~W}(+30 \mathrm{dBm}$ into $50 \Omega$ ) at any frequency from 5 to 300 MHz , and $0.5 \mathrm{~W}(+27 \mathrm{dBm}$ into $50 \Omega)$ in the range 300 to 500 MHz . The standard stability is $\pm 0.0025 \%$ from 0 to $70^{\circ} \mathrm{C}$, with optional stabilities of $\pm 0.0003 \%$ from 0 to $50^{\circ} \mathrm{C}$, or -55 to $125^{\circ} \mathrm{C}$. Although the oscillator is factory set to within 10p.p.m. of the specified frequency, adjustment to within lp.p.m. is available as an option. For pha-se-locking applications, electronic tuning (v.c.x.o.) is also optional. Other models in this v.h.f. family are available with lower-level sinewave (5 to 500 MHz ) or e.c.l.-compatible (5 to 200 MHz ) outputs. Lyons Instruments Limited, Hoddesdon, Herts.
WW 313


WW 309


WW 308

## Push-fit terminals

Push-fit terminals in the Terminette range include both stand-off and lead-through types which are supplied in matched sets of pins and p.t.f.e. insulators. The insulators are supplied in a variety of colours and are chamfered on the lower edge to enable an easy push-fit into a prepared hole. Subsequent insertion of a pin will rigidly lock an insulator into a chassis. Terminals are available for chassis hole sizes from 0.062 to 0.156 in and in metric sizes. All types meet the requirements of DEF5334B and some of these have NATO references and can be released to MOD05-29. H \& T Components Limited, Crowdy's Hill Estate, Kembery Street, Swindon, Wiltshire.
WW 314

## DMM kit

The Model 2000 easy-to-build kit assembles into a bench/portable $31 / 2$ digit multimeter having an accuracy, when correctly calibrated, of $0.1 \% \pm 1$ digit, and an input impedance of $10 \mathrm{M} \Omega$. It has five functions, giving 28 ranges, and is provided with $100 \%$ overrangeand overload protection. The alternatingand direct-current and resistance functions each have six ranges, enabling resistance measurements from $1 \Omega$ to $20 \mathrm{M} \Omega$ and current measurements from 100 nA to 2 A to be made. The direct- and alternating-voltage functions each have 5 ranges providing voltage measurements from $100 \mu \mathrm{~V}$ to 1 kV . Overrange load indication, polarity and zeroing are all automatic. Price is less than $£ 70$ including tax. Sabtronics (UK) Ltd, 50 Galton Road, Westcliff-on-Sea, Essex. WW 315

## Silicon rectifiers

Silicon-controlled rectifiers in the MCR100 series, from Motorola, are sensitive to gate-trigger currents up to a maximum of $200 \mu \mathrm{~A}$. These devices, which will pass a forward r.m.s. current of up to 800 mA , are supplied in peak reverse- and forward-blocking-voltage ratings from 100 to 600 V in 100 V steps. The reverse- and forward-blocking currents are no more than $100 \mu \mathrm{~A}$ at $125^{\circ} \mathrm{C}$ and the maximum holding current is only 5 mA . Motorola Ltd, Semiconductor Products Division, York House, Empire Way, Wembley, Middx HA9 0PR.
WW 316

## Miniature trimming resistor

A high-resolution ten-turn trimming resistor, from Lemo (UK) Ltd, measures
9.7 mm (max. height) by 4 mm diameter It is available in maximum values from $100 \mu$ to $100 \mathrm{k} \mu$, with the lowest adjustable values being about $25 \%$ of the quoted values. Power ratings are 370 mW at $40^{\circ} \mathrm{C}$, tolerances are $\pm 30 \%$, and the temperature coefficient of resistance values vary from $1 \times 10^{-3}$ per degree Kelvin, for up to $1 \mathrm{k} \Omega$, to $2.5 \times$ $10^{-3}$ per degree Kelvin for up to $100 \mathrm{k} \Omega$. Lemo (U.K.) Ltd, 6 South Street, Worthing, W. Sussex BNll 3AE.
WW 317

## Temperature-operated switch

A temperature-operated switch, from Lee Green Precision Industries Ltd, operates in temperature ranges between 50 and $80^{\circ} \mathrm{C}$. The Thermotrigger, as it is called, is made from vanadium pentoxide, the resistance of which changes abruptly from a high value at a low temperature to a low value at a high temperature. Temperature coefficient of resistance changes are $-5 \%$ in the pre-transition region, $-8 \%$ in the transition region, and $-20 \%$ in the posttransition region. Lee Green Precision Industries Ltd, Grotes Place, Blackheath, London SE3 0RA.
WW 318

## Tweezer pliers

Stirex k-40 tweezer-pliers are moulded in glass-filled propylene and have adjustable locks and self-opening handles. The locking device can be moved up or down the handles so that the jaws can grip and hold objects up to 7 mm in diameter. They may also be used without the locks. The pliers, which have finely serrated jaws with tip widths of 4 mm , are 165 mm long overall. They are non-hygroscopic, non-magnetic, resistant to most acids, and they weigh only 14 g . Tele-Production Tools Limited, Stiron House, Electric Avenue, Westcliff-on-Sea, Essex SS0 9NW.
WW 319

## High-current relays

Relays in the Ideco range have up to four change-over contacts and are available in ratings up to 10A. Most of the relays are plug-in types having five, eight or eleven-pin bases.

Contact materials include gold-plated pure silver, silver-cadmium oxide or silver-palladium. The springs are phosphor bronze, with mechanical lives of about 50 million operations, and coil voltages range from 6 to 110 V direct or 6 to 240 V alternating. A. B. Relays Ltd, Orgreave Crescent, Sheffield Sl3 9NQ. WW 320


WW 317


## M.I.s.-Mine's lots safer

As Arnold Postlethwaite once said, "It's not t'fall as 'urts thee, lad - it's t'sudden stop at t'bottom." Arnold was speaking with difficulty at the time, having just fallen downstairs, hitting every tread on the way down and was, one felt, hardly in a position to come on with the homespun philosophy.

But a little philosophy, homespun or not, would seem to be appropriate to those deliberating on the microwave landing systems of the future - electronic assistance in avoiding "t'sudden stop at t'bottom." Amid clamorous protestations of thinking only of the good of passengers, all the parties interested in the final choice of British, American or German systems are intent on making sure that they finish up on the gravy train, morally as well as financially (I use the word 'morally' in the sense of 'moral victory' and in no other manner). Whichever system is adopted by ICAO, the manufacturers who 'lose' stand to make very nearly the same profit as they would if their own system were chosen, since those who propose the Doppler method can make scanning equipment equally well, and vice versa.

So, it appears that the choice of a system is being held up for nebulous reasons of national pride, rather than for commercial and technical considerations - Doppler and scanning-beam both seem to work well in most situations. Computers don't seem to be able to come up with a clear-cut choice, particularly when told that airports have suddenly sprouted imaginary buildings, and it seems that aircraft have got to go and actually land at various 'troublesome' airports and find out the hard way. Let's hope they get on with it and reach a decision soon; one based on the facts and not political manoeuvrings, as has happened so often before. Let us also hope that, when automatic landing is eventually a common procedure, the reliability of the equipment is not open to question. The alternatives of landing on the runway or twenty feet under it must not hang on the life expectancy of a $100 \mathrm{k} \Omega$ resistor or the cleanliness of a plug and socket.

## Table talk

While desultorily casting around for something revolutionary to invent during the afternoon, my colleague froze, the peas from his knife rolling in a steady stream across the table, ununheeded. "Why not?" he said, as our ravioli and chips grew cold, "Why not design a computer graphics thing for the telly?" We stared at him, as he caught the peas with his fork, temporarily at a loss for words, and then we all started talking at once. "But,"
"Whatdya mean . .?". .. "You can't . .."
. and similar meaningful human communication.

As the last chip disappeared and chocolate sponge spoons were wearily taken up, we had decided that to do the job properly you would need a mainframe computer about the size of our office, and our management people are a bit funny about that sort of thing. But the idea was a good one. For example, you could rearrange all the furniture and view it from different angles, in perspective. That cabinet for all the hifi equipment can be seen from all viewpoints and you may then see that it would completely block Grandpa's view of the tv when the turntable lid was up. Exhibition designers would find it helpful and architects could completely remodel Bruddersfax town hall or the Humber bridge without spending a penny on concrete until it looked about right.

Not that it's a new idea, of course but, using a microcomputer and a domestic tv as display unit, it could be a lot more useful than playing ludo. Personally, I never believed all that about NAAFI tea, but I'm beginning to wonder about our canteen's ravioli.

## Watt voltage?

Many years ago in the dear, dead days of my youth and long before I learned not to argue with 'experts', I had to show one of these experts who was also an M.P. one of the new signal generators made by the company for which I worked. Its attenuator was calibrated in decibels relative to $l_{\mu} V$ across $50 \Omega$ and, because he felt that he had to say something intelligent, the chap said "Ah, but are they voltage or power decibels?" Well, that took me aback a little, but I came back, quick as a flash, and said that it didn't matter because the calibration was the same for voltage or power. The ensuing argument, which was thickly be-spattered with expressions to do with going back to school and with the possession of infinite $Z$ between the ears did not reach an amicable conclusion, but I was saved from the Star Chamber by the fact that someone's power supply exploded and diverted his attention.

The fact is, of course, that so long as the impedances do not change, decibels can be either voltage or power. The
ratio of the gain of an amplifier at 1 kHz to its gain at 20 kHz may be 3 dB and, if you work it out, you can see that this can mean either voltage or power assuming that the output impedance is still the same. It does mean that the power output is halved and the voltage output is $1 / \sqrt{ } 2$ that at 1 kHz , but this is -3 dB in each case.

Perhaps I can now stop the argument that started twenty years ago and refer people to this piece in the future.

## Would you care to re-phrase that?

Far too headstrong, that's the basic problem with colonials, particularly with those in America. Give them their head and heaven only knows what they'll get up to. I dare say that, after a little time to reflect on the working of this Declaration of Independence thing, they're already beginning to regret the whole nasty business. We've been good to them - some would say we've been too generous by half. They've come over here and we've been so glad to see them back that we've let them make their motor-cars here and pretended to marvel at their talking pictures and no one's ever said an unkind word. Well, not many, anyhow.

They're good chaps, taken as a whole, although perhaps a little slow on the uptake when they're wanted, but they have had this attitude of late that has denoted a certain, well, greediness. They have turned up here and bought things. Things like England, for instance. And it's got to stop - they'll be wanting to take Yorkshire back to San Francisco next and reassemble it stone by stone.

As a warning not to be so selfish, then, several British companies have already decided that the best and wisest way to deal with these youthful high spirits is to do the same sort of thing back to them. Racal, for example, has taken over not only Milgo, but Dana Electronics as well. I expect they'll give them back in a little while, but it's the example that's important. EMI has had companies over there for some time now and lots of our firms have colonial outposts. Goodness knows how they manage it, what with Indians and the weather, but they all come from good, pioneering stock.

It's all very well to be independent all young people need to break away once in a while - but freedom has to be used properly. Let us hope that they have learnt their lesson now. After all, we've done our best to keep them out of trouble. Geordie George from up in Washington all the way down to young Robert (Hope, that is) have actually gone there and kept an eye on them and still they keep saying they are independent now. They even managed to convince poor old George. Well, they can't say they weren't warned.

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## SOLID MAHOGANY CABINET

## A high-quality push-button

 FM Varicap Stereo Tuner combined with a 24 W r.m.s. per channel Stereo

## Amplifier.

Brief Spec. Amplifier Low field Toroidal transformer, Mag, input, Tape In/Out facility (for noise reduction unit, etc.), THD less than $0.1 \%$ at 20 W into 8 ohms. Power on/off FET transient protection. All sockets, fuses, etc., are PC mounted for ease of assembly. Tuner section uses 3302 FET module requiring no RF alignment, ceramic IF, INTERSTATION MUTE, and phase-locked IC stereo decoder. LED tuning and stereo indicators. Tuning range $88-104 \mathrm{MHz}$. 30dB mono S/N @ 1.2 NV . THD $0.3 \%$. Pre-decoder 'birdy' filter

PRICE: £58.95 + VAT

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A very high performance tuner with dual gate MOSFET RF and Mixer front end, triple gang varicap tuning, and dual ceramic filter/dual IC IF amp.


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Sens. 30dB S/N mono@ $1.2 \mu \mathrm{~V}$ THD typically $0.3 \%$ Tuning range $88-104 \mathrm{MHz}$
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2.30-3.10 Interface
3.10- 3.50 Applications
4.10-5.00 Question Time

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QUALITY: All components are brand new first grade full specification guaranteed dewices All resistors except where stated as metal oxide) are low nolse carbon film types All printed circuit boards are fibreglass. drilled roller tinned and supplied with circuit diagrams and construction layouts

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Pack Price Fraquency meier meter drive components． Threglass printed circ uil board ．．．$£ 10.35$ PTimary： 0.117 V 234 V ．secondary 15 SV E4．90 Set of capacitors．rectifiers．volizge regulator for power supply ．．．．．．．．．．．．．．．．．E2．10 Set of miscellaneous parts．incleding sockels． luse holder．fuses．inter－cannacting wire．atc
Set al matafwark parts including silk scres Set of metafwark parts includiang silk screan
prinled tascia panel．acrylic silk scresm printed prinled tascia panel．acrylic silk scresen printed fixing parts etc． Construction mote
16．Teak catinal $18.3^{\prime \prime} \times 12.7^{\prime \prime} \times 3.1{ }^{\prime \prime}$
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APPLICATIONS: $H_{1 .}$ F - Mıxers .- Disco - Guitar and Organ ... Public address
APPLICATIONS: H
SPECIFICATIONS
INPUTS Magnetic Pick-up 3 mV Ceramic Pick-up 30 mV Tuner 100 mV Microphone 10 mV OUTPUTS Tape 100 mV Man output 500 mV R M S
lACTIVE TONE CONTROLS Treble - 12 dB at 10 kHz Bass + at 100 Hz
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APPLICATIONS: Updating audio equipment - Guitar practice amplifier -- Test amplifier -- Audio APPLICATIONS: Updating audio equipment - Guitar practice amplifier - Test amplifier -- Audio SPECIFICATIONS:
OUTPUT POWER 15 W RM S Into 8:? DISTORTION $01 \%$ at 15 W
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APPLICATIONS: HI-FI -- High quality disco - Public address - Monitor amplifier -. Gutar and SPECIFICATIONS:
INPUT SENSITIVITY 500 mV
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FEATURES: !harmal shudown - Very Iow disiortion - Loadine protection - - Imegral Henisink No external components
APPLICATIONS: HI-FI - Disco - Monitor - Power Slave -- Industrial - Public address SPECIFICATIONS INPUT SENSITIVITY 500 mV
OUTPUT POWER 1 2OW RMS Into 8:) LOAD IMPEDANCE 4-16!? DISTORTION $005 \%$ al 100 W at SIGNAI NOISE RAIIO 96 dB FREQUENCY RESPONSE $10 \mathrm{~Hz}-45 \mathrm{kHz}$ - 3 dB SUPPLY VOLTAGE SIZE $114 \times 100 \times 85 \mathrm{~mm}$

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SPECIFICATIONS
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[^6]

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Ref} \& <br>
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12 v
$$ \& 24v \& \& <br>
\hline 07* \& 20 \& 4.40 \& 79 \& 111 \& 0.5 \& 0.25 \& 2.20 \& <br>
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\hline \multicolumn{4}{|l|}{*115 or 240 sec only} \& \multicolumn{5}{|c|}{30 VOLT RANGE} <br>
\hline \multicolumn{4}{|c|}{50 VOLTRANGE} \& \multicolumn{5}{|c|}{\multirow[t]{2}{*}{Primary 220-240v
SEC TAPS 0.12-15-20.24.30}} <br>
\hline \multicolumn{4}{|c|}{\multirow[b]{2}{*}{SEC TAPS 0-20-25-33-40-50V}} \& \& \& \& \& <br>
\hline \& \& \& \& \multicolumn{5}{|l|}{} <br>
\hline 57 \& voltages available \& AVAILABLE \& \multirow[t]{2}{*}{4050 V} \& \multicolumn{5}{|l|}{} <br>
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112} \& \multicolumn{2}{|l|}{Amps} \& \multicolumn{2}{|l|}{£} <br>
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1250 MHz Sine wave and pulse a m Signal Geneato TF867 15KH200．00 ／p $0.4 \mu \mathrm{~V}-4 \mathrm{~V}$ ．Int．\＆Ext $5 \mathrm{KHz}-30 \mathrm{MHz}$ with Terminating unit ．．．．$£ 185,00$ Solid State Generator 6058B．Freq．range 8.12 .5 GHz ．Int．\＆Ext．mod freq Stab $0.003 \% .50 \cap$ impedance $£ 530.00$ A．M．Signal Generator T
Military version $10-485 \mathrm{MHz}$
Milary $\begin{array}{r}\text { £450．00－£800．00 }\end{array}$ R．C．Oscillator TF1370A $10 \mathrm{~Hz}-10 \mathrm{MHz}$
Square Wave up to 100 KHz Square Wave up to 100 KHz High outputs
up to 31.6 V
$£ 225.00$ Phase／AM．Signal Generator TF 2003 O．4－12MH2 Signal Generator $\quad \mathrm{M} 150.00$ F．M．／A．M．Signal Generator TF

$995 A / 3 \mathrm{~S}$ ．Ministry type No．CT 402 $1.5 \mathrm{MHz}-220 \mathrm{MHz}$ ，R．F．$o / \mathrm{p} 2 \mu \mathrm{~V}-200 \mathrm{mV}$ Internal \＆External Mod Facilities $V$ good | condition |
| :--- |
| A．M．Signal Generator TF8010／1．Freq． |
| 385.00 | A．M．Signal Genarator TF8010／1．Freq．

range $10-470 \mathrm{MHz}$ R F output 0 － 1 －IV range $10-470 \mathrm{MHz}$ R．F．output $0-1 \mu-1 \mathrm{~V}$
Piston attenuator 500 hms impedance A M．Signal Generator TF BO $1 \mathrm{~B} / 3 \mathrm{~S}$
$12.485 \mathrm{MHz} .0 .1 \mu \mathrm{~V} .1 \mathrm{~V}$
E Signal Generator TF144H／4．Later models in super condition
MF／HF Signal $\mathbf{E 5 0 0 . 0 0}$ to $\mathbf{£ 6 5 0 . 0 0}$ MF／HFSignal Generator TF2002 $10 \mathrm{KHz}-72 \mathrm{MHz}_{2} 100 \%$ A．M．depth int A．M．Variable from 20 Hz to 20 KHz R．F．
$0 / \mathrm{p}, 0.1 \mu \mathrm{~V}$ to 2 V Solid State E 675.00 AM／FM Signal Generator TF995B／2

## MARCONI－SANDERS

Microwave Sweep Generator type 6600A £2．500．00 MUIRHEAD L．F．Decade Oscillato D880A．2－phase $0.01 \mathrm{~Hz} \cdot 11.2 \mathrm{KHz}$ Decade Oscillator D890D $1 \mathrm{~Hz}-11.2 \mathrm{KHz}$ PHILIPS
PM5501 Colour bar generator．Extremely light and compact instrument for mobile maintenance， 5 different test patterns for and service．R．F．output signal switchable． VHF ，Band III and UHF Band IV．IKHz tone for sound performance checks（sine
wave）
$£ 165.00$ wave）$\quad £ 165.00$

50 MHz Pulse Generator PM5712 | Pulse Generator PM5775 ．．$\quad \mathbf{£ 8 9 0 0 . 0 0}$ |
| :--- |
| 8900 | Pulse Generator PM5776 $\quad \mathbf{£ 9 0 0 . 0 0}$ L．F．Generator PM5 105 10Hz－100KHz

Sine $\&$ Square Wave $2 \mathrm{~V}(\mathrm{R} . \mathrm{M}$ S．） Stabilised o／p．Low Distortion：＜0．8\％ $(10 \mathrm{~Hz}-100 \mathrm{KHz}) \ldots \mathrm{E}$ ） 156.00

## DIGITAL VOLTMETERS AND MULTIMETERS

Avo

 Model 7x $\quad$ £40．00 Heavy Duty Mk 5 （with case）$\quad £ 40.00$
 AVO Model 9 or Test Set No $1 \quad \mathbf{£ 5 5 . 0 0}$ DYNAMCO
Digital Voltmeter DM $2023 \mathrm{c} / \mathrm{w}$ DC ranging unit C1．Scale $999990.001 \%$

## HEWLETT PACKARD

DVM type 3430 A 3 digit 5 ranges 100 mV to 100 V ．FS input resistances 10 Mohms Digital Multimeter 34702 A with Display 34740 A． 4 digit display． 4 ranges both $A C$ \＆DC plus 6 ranges of ohms $A C$ function covers 45 Hz to 100 KHz ．Ohms ranges are 1000 hms to 10 Mohms FS LED display．New condition．A much sought－ G400．00
PHILIPS


Electronic Analogue Multimeter PM 2503 $D C \& A C$ Volts， 100 mV .1 KV f．s．d． Resistance 100 ohms－10M Ohms．DC 81
AC Current $1 \mu \mathrm{~A}$－ A is．d．$\quad$ E 90.00 SIGN／ROGERS
A．F．Voltmeter
A．C．Converter LM 1219 ． $30 \mathrm{mV}-300 \mathrm{~V}$ mean reading．Freq．range $10 \mathrm{~Hz}-10 \mathrm{KH}$ D．C Digital Voltmeter LM1420．2 $2.5 \mu V-1 \mathrm{Kv}$ in 6 ranges $\pm 0.05 \%$ DC
E235．00 accuracy
D．V．M．Type LM1420．2Ba．©235．00
DC，true R．M．S．and mean A．C sensing．Accurate measurement irrespective of harmonic distortion accuracy $\pm 0.25 \%$ Freq．
$20 \mathrm{~Hz}-20 \mathrm{KHz}$ DVM Type LM $1440210 \mu \mathrm{~V}-2 K \vee D C 5$ ranges Oven controlled zenerdiode．
Accuracy $\pm 0.033 \% \quad$ FSD
$\pm 0.005 \%$ Accuracy $\pm 0.033 \% \quad$ FSD $\pm 0.005 \%$
reading D．V．M．LM 14803 Autoranging version of LM 1440.3 Max reading 39999 $5 \mu \mathrm{~V}-2 \mathrm{KV}$ DC．Full spec．on request $\quad$ P．O．A． D．M．LM 1604 DC only $1 \mu \mathrm{~V}$ sensitivity． $0.01 \%$ accuracy．Max reading Autoranging 110 dB series mode reject $\begin{array}{ll}\text { On．No common Mode error } & \text { P．O．A．} \\ \text { D．M．M．} 7050 \text {（Autoranging）} & \text { E245．00 }\end{array}$

## OSCILLOSCOPES



Dual Trace Scope 4000． 50 MHz 7 nsec Rise Time $5 \mathrm{mV} / \mathrm{cm}$ sensitivity．Calibrated sweep delay．Gated trigger．X－Y display 8 $x 0_{\mu} \mathrm{V} / \mathrm{cm}$ ．Scope $130 \mathrm{C} \quad 595.00$
500 KHz bandwidth．Identical $X$ and $Y$ amps．$X 2$ to MARCONI INSTRUMENTS
MARCONI INSTRUMENTS
40MHZ TF 2200 series supplied with 3 plug ins． 6455 （single trace）

TM 6456 （dual trace）．
TM 6457 （TV diff）．
Full specs．on request． 6 MONTH
WARRANTY
PHILIPS
PM6507 Transistor Curve Tracer．Solid State CRT $-10 \times 12 \mathrm{~cm}$ ．Full spec．on
request

## probes

$\times 1$ Part No 90 ．．．．．．．．．．．．．． $\mathbf{£ 7 . 0 0}$
$\times 10$ Part No． 91
$\times 18 \times 10$（switchable）Part No $£ 9.00$
£11．00
SOLARTRON
CD1740 50 MHz Scope System c／w CX1741 \＆CX1744．Dual Trace，DC－ $50 \mathrm{MHz} 10 \times 8 \mathrm{~cm}$ display．Sensitivity Solid State $\quad \mathbf{4 8 5 . 0 0}$ Portable Scope DC－6MHz Double Beam
CT 436

## TEKTRONIX

DC3OMH2 Oscilloscope 545A c／w CA \＆L
Plug－ins
f 445.00
Plug－ins


Type 485350 MHz Portable．Dual Trace 5 mV ／div． $1 \mathrm{nsec} / \mathrm{div}$ sweep rate．Delayed sweep Auto focus，variable trigger hold off 50 ohms internal input protection
$\mathbf{£ 3 . 2 5 0 . 0 0}$ Type 549 （Mainframe） $\begin{aligned} & \mathbf{£ 3 , 2 5 0 . 0 0} \\ & \mathrm{DC}-30 \mathrm{MHz}\end{aligned}$ Bistable split screen storage．Automatic Erase． $5 \mathrm{~cm} / \mu \mathrm{S}$ writing speed．Calibrated sweep delay Various plug－in units $\begin{array}{lll}\text { avalable } \\ \text { Type } & 551 & \text { DC－27MHz Main frame } \\ \mathbf{7 7 5 0}\end{array}$ power supply Various plug－In units Type 5648 （Mainframe）Storage Oscillo scope Various plug－in units available
$\mathbf{£ 7 5 0 . 0 0}$

## TELEQUIPMENT

Rack Mounting Scope S54AR Fitted with P7 long persistence CRT．Single trace
DC－ $10 \mathrm{MHz} \quad 10 \mathrm{mV} / \mathrm{cm}$ Unused condition $10 \mathrm{mV} / \mathrm{cm}$ E205
$\square$
 $\begin{array}{ll}\text { 49－53 Pancras Road } & \text { ADD 8\％vat } \\ \text { London NW1 2QB } & \text { TO ALL PRICES }\end{array}$ Tel：01－837 7781．Telex： 298694

## Brokers Itd 49-53 Pancras Road, Lon̄don NW12QB Tel: Ol-837 7781 <br>  <br> New Catalogue just out. Send for your copy now - POST FREE

## SCOPE TEST EQUIPMENT

Time Mark Geneatior $184 \quad$ £275.00


Snsce Pulse Generatior Model 21010 c/w |  | loads and connectors |
| :--- | :--- | :--- | TRANSMISSION TEST EQUIPMENT

AIRMEC/RACAL
Wave Analyser 248A. $\begin{aligned} & 5.300 \mathrm{MHz} \\ & \text { E } 250.00-£ 300.00\end{aligned}$ Wave Analyser 248 Freq range 5 MHz $\begin{array}{ll}\text { 300MHz } \\ \text { Modulation Meter } 409 & \begin{array}{c}\text { E145.00 } \\ E 295.00\end{array}\end{array}$ Type 210 A Modulation Meeter 2.5.
300MH AM Range $0.100 \%$ FM Range $010 \pm 100 \mathrm{KHz}$ in 4 ranges
general radio
Type 1900A Wave Analyser c/w Graphic
Spec: $\quad 1900 \mathrm{~A} \quad 20 \mathrm{~Hz}-50 \mathrm{KHz}$
bandwidths 3.10 and 50 Hz Tracking averages 30 mV .300 V F.S.D. Inpu impedance 1 M ohm 3 meter speeds Spec: $15218 \quad 4.5 \mathrm{~Hz}-200 \mathrm{KHz} 1$ sensitivity. Linear d8 plot of r.m.s ac
voltage tevel 20,40 or 80 dB range £2,000.00 HEWLETT PACKARD
Sweeping Local Oscillator 3595A Plug-in for use with 3590A Wave Analyser Freq MARCONI INSTRUMENTS
Distotion Factor Meter $\begin{aligned} & \text { Fundamental Freq. Range } 100 \mathrm{~Hz}-8 \mathrm{KHz}\end{aligned}$ Dist measuring ranges $0-5 \% \quad 0-50 \%$ Measures all spurious components
30 KHz
$\mathbf{£ 6 0 . 0 0 - £ 8 0 . 0 0}$ RADIOMETER
Wave Analyser FRA 2 T3 Special version of FRA 2 with facilities for Intermodulation measurements of frequency responses Freq. range 30 Hz to 16 KHz incremental freq. $0 \mathrm{~Hz}_{\mathrm{z}}$ to $\pm 60 \mathrm{~Hz}$. Selectivity 3 curves with following 1 dB points $\pm 1.25 \mathrm{~Hz}$
$\pm 12.5 \mathrm{~Hz} \pm 63 \mathrm{~Hz}$ Voltage range $100 \mu \mathrm{~V}$ 1 KV . Auxiliary Oscillator Range OHz to
1.6 KHz and 1.5 to 1.6 KHz o $/ \mathrm{p}-10 \mathrm{~V}$ (EMF) continuously variable impedance 1

## BRIDGES

AVO/BPL
(ype Cza $/ 5$ Componen Comparator E245.00 MARCONI INSTRUMENTS FT245 CCT Magn Meter C Wscillato WAYNE KERR
COMPONENT BRIDGE 8521 (CT375) Resistance 10 ranges from 1 M ohm to om 50 kuF to 500 pF . Inductance 10 ranges from $1 \mu \mathrm{~F}$ to 500 KH . Capable of measuring components in situ $\mathbf{£ 1 0 5 . 0 0}$ Universal Bridge 8221A (CT530) 0.1\% Accuracy. Measures R, G £275.00 operated

## FREQUENCY COUNTERS

ADVANCE

- $\mathbf{E 1 1 0 . 0 0}$ Timer Counter TC14 9 digit. Display
storage. $\mathrm{DC}-250 \mathrm{MHz}$ Time limits $\begin{array}{ll}\text { storage. } \\ \text { selectable } 0.1, ~ & \text { s-100s. Multiple period } \\ \text { average } & 10.10 \quad \text { Sensitivity } \\ 10 \mathrm{mV}\end{array}$ $100 \mathrm{mV}, 500 \mathrm{mV}$. Overload protected
carriage and packing charge extra on all items unless otherwise stated

| Timer Counter |
| :--- |
| and plug-in capability. DC | Spec. similar to TC14 14 E585.00 Plug-In Unit TC15 P1. $1 \mathrm{MHz}-500 \mathrm{MHz}$. 10 mV -IV. Full 500 MHz display with 1 Hz

resolution in only 2 secs
$\mathbf{E 2 0 0 . 0 0}$ resolution in only 2 secs $\quad \mathbf{E 2 0 0 . 0 0}$ 80MHz Gate times $10 \mu$ s to steps. Sensitivity
wave Overload protected ( $\mathrm{m} . \mathrm{s}$.) sine
E290.00 Frequency DC - 100 MHz 6 digit. Time. period. period average, count, totalise,
pulse width, ratio. 300.00 Type TC18 Time Counter Freq. measurement 10 Hz - 512 MHz 6 digit LED
display UNUSED CONDITION E 275.00 display
FLUKE FLUKE
Industrial Counter Industrial Counter Totaliser $1 \mathrm{g4} 1 \mathrm{~A} .5 \mathrm{~Hz}$ -
40 MHz 40 mV sensitivity R.P. measurement sensitivity R1PM. VENNER
3MHz Freq counter TSA $6674 \quad £ 80.00$ RACAL
9520 Pry Period Meter 5 Hz .10 MHz E110.00 Universal Counter Timer 9838 Measuring functions:- Frequency. Single and multi period. Ratio and
Multiple ratio. Time interval - single line Multiple ratio. Time interval - single line
and double line totalising. 10 Hz to 100 MHz Frequency. 10 Hz to 5 MHz Period.

## VOLTMETERS

## BOONTON

R. F. Voltmeter 91 C . Measurement range 1 mV to 3 V . Frequency range. 20 KHz to
1200 MHz (with ' $T$ ' Adaptor supplied). 1200 MHz ( $\mathbf{W}$ ith R Adaptor supplied)

Supplies also with R.F probe and tip and | Supplies also with R.F probe and tip and |
| :--- |
| $50 \Omega$ termination. Weight 12 lbs. |
| 455 | BRUEL E KJOER

measurements of voltage, frequency and modulation factor AM and FM High impedance FET probe $50 \Omega$ termination and 60 d 8 attenuator included. Sensitivity $50 \Omega \mathrm{~V}-50 \mathrm{~V}$ FSD. 100 KHz to 230
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Electronic Voltmeter 2409 True R.M.S. Average and Peak 2 Hz to 200 KHz . FLUKE
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Resolution 1 ppm of range
E395 Resolution 1 ppm of rang
GENERAL RADIO
Electronic Voltmeter 1806A AC DC 9


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R.F. Voltmeter $3406 \mathrm{~A} 20 \mu \mathrm{~V}$ sensitivity £485 RHODE \& SCHWARZ

## $1521 \quad 10 \mathrm{KHz}-30 \mathrm{MHz} \quad 0.2 \mathrm{~V}-\mathrm{IV}$

## MISCELLANEOUS

ADVANCE
Digital Panel Meters. DPM. 102. 103, 343 Price and specs. on application AVANTEK
Unit Amplifier Type UF 101 Gain control modules designed torms. Weight $1 / 20$ amplifier or other systems. Weight $1 / 202$
$50 \Omega$ impedance Frequency response 10 $500 \mathrm{MHz} \mathrm{f150}$ Unit Amplifier Type UA 103. Frequency
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 Response Nominal gain $13 \mathrm{~dB} \quad £ 100$ Full specs
on request BIRD
Coaxial Resistor 8053 10W RF coaxial load resistor
E20.00 Wartmeter Termaline 68353 ranges 0 -
$120 / 0.600 / 0.1200 \mathrm{~W} 30-500 \mathrm{MHz}$ $120 / 0.600 / 0-1200 \mathrm{~W} 30-500 \mathrm{MHz}$

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GENERAL RADIO Standard Frequency Multiplier 1112 A.
Price \& specs. on application Standard Frequency Multiplier 11128 HEATHKIT \& specs. on application MARCONIINSTRUMENTS $\epsilon 75.00$ $\begin{array}{ll}\text { R.F. Power Meter TF1152/1 } & \text { £75.00 } \\ \text { R.F. Power Meter TF1 } 152 A / 1 & \text { E80.00 }\end{array}$ Coqr Gain and Delay Test Set TF2904 625 line 5505.00 R.F. Power Meter
ranges $D C-1 \mathrm{GHz}$ F. Extension Unit. TM6448 for use with
$\mathbf{~} \mathbf{2 0 0 . 0 0}$


VHF Field Strength Meter HFV 25
300 MHz in 1 band Measurement range $100 \mathrm{~dB}(\mu \mathrm{~V}) 50 \mathrm{ohm}$ impedance $£ 1,750$ Standard Stereodecoder MSDC BN4 193 Polyscop 1 $\quad \mathbf{£ 8 5 0 . 0 0}$ Selektomat USWV E8N... E800.00 requency Indicator FKN © $\mathbf{E 4 7 5 . 0 0}$ $30 \mathrm{~Hz}-15 \mathrm{KHz}$. $\mathbf{E 8 5 0 . 0 0}$ Type MSC Stereocoder BN4192/2 $\mathbf{£ 1 , 2 5 0}$ RECORD

| Chart Recorder - $500 \mu \mathrm{~A}$ Movement 1 in |
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| E 70.00 | $\& 6$ in. per hou

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## END OF YEAR SPECIAL OFFERS

Quantities of these equipments at bargain 'st December, 1977.
OSCILLOSCOPES


MARCON TEKTRONIX
531A c/w CA Plug-In DC-13MHz. Dual 533 A c/w CA Plug-In DC-13MHz. Dual race $£ 185.00$ $535 A \mathrm{c} / \mathrm{w}$ CA Plug-In DC-13MHz. Dual
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$\mathbf{£ 2 0 0 . 0 0}$
$545 \mathrm{c} /$ wCA Plug-In DC-24MHz £225 OSCILLATORS
ADVANCE
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| A1065 | 1.25 | E | 0.35 | Pr83 | 50 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| AR | 0.80 | EL86 | 0.50 | PY88 | 0. |
| ARP3 | 0.60 | EL90 | 0.50 | PY5004 | 1.35 |
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| B12H | 3.00 | EL95 | 0.70 |  |  |
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| E8 10 F | 6.00 | EZ40 | 0.60 | 026 | 5 |
| EA50 | 0.45 | EZ41 | 0.75 | 427 | 1.00 |
| EA 76 | 2.00 | EZ80 | 0.30 | U191 | 5 |
| EABC 80 | 0.40 | EZ81 | 0.35 | U80 |  |
| EAF42 | 0.70 | GY501 | 0.80 | UABC80 | 0.50 |
| EB91 | 0.30 | Gz32 | 0.65 | UAF42 | 5 |
| EBC33 | 1.00 | Gz33 | 2.50 | UBC4 1 | 0.60 |
| EBC4 1 | 0.75 | G237 | 2.00 | UBF80 | 0 |
| ¢8F80 | 0.45 | KT66 | 4.00 | UBF89 | 0.50 |
| EBF 83 | 0. | K188 | . 00 | UBL1 | 0 |
| EBF89 | 0.40 | MH4 | 1.00 | UBL21 | 0.75 |
| EC52 | 0.40 | ML6 | 1.00 | UCC85 | 0 |
| ECC81 | 0.45 | A2 | 0.45 | UCF80 | 0.80 |
| eccer | 0.40 | 082 | 0.45 | UCH42 |  |
| ¢CC83 | 0.40 | PABC | 0.40 | UCH8 | 0 |
| ECC84 | 0.35 | PC86 | 0.65 | UCL8 | 5 |
| ECC8 5 | 0.45 | PC88 | 0.65 | UCL8 | \% |
| C86 | 1.25 | $\mathrm{PCS}^{\text {P }}$ | 0.65 | UF41 | 5 |
| ECC88 | 0.55 | PCC84 | 0.45 | UF80 | 0.40 |
| ECC 189 | 0.80 | PCC85 | 0.50 | UF85 | 0.50 |
| ECF 80 | 0.45 | PCC89 | 0.55 | UF89 | 0.50 |
| ECF8 2 | 0.45 | PCC189 | 0.65 | UL41 | 5 |
| ECF801 | 0.75 | PCF82 | 0.40 | UL84 | 0.50 |
| ECH42 | 0.85 | PCF84 | 0.85 | UY41 | . 55 |
| ECF81 | 0.45 | PCF86 | 0.65 | UY85 | \% |
| ECH84 | 0.50 | PCF201 | 0.90 | /30 |  |
| ECL80 | 0.60 | PCF801 | 0.55 | VR150/30 |  |
| ECL82 | 0.40 | PCF802 | 1.55 |  |  |
| ECL83 | 1.20 | PCF 805 | 1.10 |  | 0.50 1.50 |
| ECL86 | 0.55 | PCF88 | 1.85 | +66 | 1.75 |
| EF 36 | 0.75 | PCH200 | 0.80 | z800u | 3.00 |
| EF40 | 0.70 | PCL81 | 0.60 | 2801u | 3.50 |
| EF4 4 | 0.75 | PCL82 | 0.45 | 2900 | 1.50 |
| EF80 | 0.35 | PCL83 | 0.70 | 143 | . 80 |
| EF83 | 1.50 | PCL84 | 0.50 | $1{ }^{164}$ | 0.30 |
| EF85 | 0.45 | PCL86 | 0.60 | 185 | 0.55 |
| EF86 | 0.4 | 5 | 85 | 154 | 0.40 |
| EFP89 | 0.35 |  | 0.60 | 155 | 0.40 |
| EF9 1 | 0.85 | PFi200 | 0.70 | 114 | 0.40 |
| EF92 | 0.75 | PL36 | 0.60 | $1 \times 28$ | 0.80 |
| 95 | 0.45 | PL8 1 | 0.55 | $2 \times 2$ | 0.80 |
| Efis 3 | 0.40 |  | 0.50 | 2021 | 0.55 |
| EF184 | 0.40 | PL83 | 0.50 | $2 \mathrm{2k25}$ | 9.00 |
| EF804 | 2.00 | PL84 | 0.50 | 3 A 4 | ${ }^{0.60}$ |
| EFL20 | 0.75 | PL504 | 0.85 | 3 E 29 | 5.50 |
| H90 | 0.60 | PL508 | 0.5 | 306 | 0.40 |
| EL32 | 0.80 | PL509 | 2.00 | 354 | 0.50 |
| EL34 | 1.20 | PL802 | 2.50 | 3 V 4 | 0.85 |
| EL37 | 3.00 | PY33 |  | 58/254M 550 |  |
| EL4 1 | 0.80 | Pr80 0.60 |  | 58/25SM 5.50$58 / 258 \mathrm{M} 5.50$ |  |
| E181 | 0.60 | PY81/80 | 0.45 |  |  |
| EL. 82 | 0.60 |  | 0.45 | 5B/258M 5.50 |  |

PLUMBICON TUBES TYPE XQ.1020R

| Mullard-£150 |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 5046 | 0.60 | 6SA7 | 0.55 | 30PL 1 | 1.00 |
| 5 V 4 G | 0.65 | 65G7 | 0.60 | 3 PLL 13 |  |
| ${ }_{5 \times 3 \mathrm{GT}}$ | 0.65 | 6557 | 0.60 | ${ }^{30 \mathrm{OLL}} 14$ | 1.10 |
| 523 | 1.00 | 653791 | 0.50 | ${ }^{3516 G T}$ | 0.80 |
| ${ }^{524 \mathrm{G}}$ | 0.70 | 6SK7 | 0.60 | ${ }^{35154}$ | 0.60 |
| 524 Gr | 0.75 | 6SLITG | 0.55 | ${ }^{357461}$ | 0.70 |
| $64{ }^{6} 7$ | 0.60 | 6SN7GT | 0.55 | 50 C 5 | 0.70 |
| $6 \mathrm{AC7}$ | 0.60 | 6507 | 0.65 | ${ }^{50 C 066}$ | 1.20 |
| бан6 | 0.75 | 6 V 6 Gr | 0.60 | 75 | 1.00 |
| 6ak5 | 0.45 | 6×4 | 0.45 | ${ }_{7501}$ |  |
| 6akb | 0.40 | $6 \times 56$ | 0.45 | 76 | 0.80 |
| 6AL5 | 0.30 | 6x5GT | 0.55 | ${ }^{78}$ | 0.75 |
| 6alsw | 0.65 | ${ }_{6} \mathrm{Y} 6 \mathrm{G}$ | 0.95 |  | 0.75 |
| 6AM5 | 1.60 | 674 | 0.65 | ${ }^{854} 2$ | 0.75 |
| 6amb | 0.65 | 6.30L2 | 0.90 | $723 \mathrm{~A} / \mathrm{B}$ | 9.00 |
| 6AN8 | 0.85 | 787 | 0.80 | 803 |  |
| 6405 | 0.50 | $7{ }^{7} 4$ | 0.80 |  | 18.00 |
| 64asw | 0.85 | 9 D 2 | 0.60 | ${ }^{807}$ | 1.00 |
| ${ }^{6456}$ | 0.80 | 906 | 0.75 | ${ }^{813}$ | 6.50 |
| 6at6 | 0.65 | 1163 | 11.00 | ${ }^{8298}$ | 5.50 |
| ${ }^{6446}$ | 0.40 | 12 Ab | 0.60 | ${ }_{8}^{832 A}$ | 4.50 |
| 6av6 | 0.50 | $12 \mathrm{AL5}$ | 0.70 | ${ }^{8664}$ | 2.80 |
| ${ }^{64 \times 4 G I}$ | 0.80 | ${ }^{2} 2 \mathrm{ar6}$ | 0.45 |  |  |
| 6 6x5GT | 1.00 | $12 \mathrm{Al7}$ | 0.45 |  | 0.50 |
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It's skilled work, calling for sound practical experience of radio and electronics theory, ranging from audio to microwave and including the use of advanced test equipment for fault diagnosis. Training in this field will be given to suitable, less experienced engineers.

The Company offers excellent salaries and benefits together with first-class working conditions in well-equipped workshops. This Unit is conveniently situated in pleasant surroundings within easy reach of the $\mathrm{A}_{\mathrm{I}}$ and Mi.


A GEC-Marconi Electronics Company

If the job sounds interesting and you'd like to put us to the test, write with details of experience to: Mrs. E. Wagg, Marconi-Elliott Avionic Systems Ltd., 22-26 Dalston Gardens, Stanmore, Middlesex HA7 IBZ. Tel: oI-204 3322.

## JOIN THE DIGITAL REVOLUTION

Do you have several years experience in the design of high speed digital signal and/or data processing equipment? If so why not contact Pye TVT Limited, where new appointments are to be made at Senior Development Engineer level?
The work will take place in our modern Studio Engineering Laboratory and the successful candidates will be involved in all aspects of the project assigned to them.
A degree or equivalent qualification is required for these positions together with proven digital design experience. Software design experience is an advantage and experience in the broadcast TV equipment field is highly desirable. Occasional travel overseas will be necessary.
Benefits are all those normally associated with a progressive company and include in approved cases relocation expenses to this pleasant part of East Anglia.
Please write or telephone : Dave Barnicoat, Personnel Officer,
Pye TVT Limited, PO Box 41, Coldhams Lane, Cambridge CB1 3JU.
Telephone Cambridge 45115

## SENIOR ENGINEER Links Outside Broadcast Unit Hanworth

The above vacancy exists at the Hanworth Division of Thames Television. Duties involve the maintenance and operation of microwave links and communications equipment, planning and surveying microwave paths.

Candidates should have extensive engineering knowledge together with the appropriate qualifications. Salary for this position will be not less than £6,000 per annum.

For further details and an application form please contact
Mr. I. D. McGuinness, Staff Relations Officer, Thames Television Limited, 306-316 Euston Road, London NW1 3BB.
Telephone 01-3879494
extension 338.


## MARINE ELECTRONICS TECHNICAL OFFICER

An electronics Technical Officer is required to assume duty as soon as possible after 31 st
January 1978 , in the Marine Geoscience January, 1978, in the Marine Geoscience Group of the Department of Geology. Duties of marine geophysical equipment, both in the laboratory and at sea. Some experience in this field would be a recommendation. The successful applicant will spend several 2.3 week cruises per year aboard the University's research vessel "Thomas B Davie."
In addition to a salary on the scale R4040 x $180-5100 \times 240-5580 / / \times 240-$ $6300 / / \times 360-6660$ per annum plus a
pensionable allowance of $10 \%$ of salary the University Offers a veriety of excellent fringe benefits.

Further detalls may be obtained from the Professor of Marıne Geoscience, Department of Geology, University of Cape Town. Rondebosch, 7700 South Africa

Applications giving names of two referees must reach The Registrar, University of Cape Town. Private Bag, Rondebosch 7700, by 31 st January. 1978.

# DESIGN/DEVELOPMENT 

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## Come and make headlines with us.

Headlines like these are only possible when you're acknowledged internationally as one of the world's leaders in avionics. To keep us at the forefront we need highly motivated design/development engineers keen to make their mark. And at Ferranti there's plenty of opportunity to do just that. On projects like the Tornado, Sea Harrier, Jaguar and Lynx.

And headlines like these also mean expansion. Which explains why we're looking for more graduate mechanical and electronic engineers to join our airborne radar and inertial navigation teams. They must have the design/development experience to spearhead the progress of equipment from drawing board through to production.

We are particularly interested in talking to engineers with backgrounds in the design of:-

Digital/analogue circuitry.
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For an application form, write to John McPhee at the address below:

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## Appointments <br> Printed CircuitDesign Engineers

To meet the challenge presented by future telecommunications systems, S.T.C. one of the leaders in the field of electronic telephone switching equipment, require Printed Circuit Design Engineers to work in the Equipment Design Department.

Using logic information supplied, the job is to design and check printed circuit layouts to meet circuit parameters and manufacturing constraints, and to digitise and edit these layouts. To assist their work, the Designers will have access to the most advanced interactive graphic design aids.

The required qualifications are a minimum of ONC Electrical or Mechanical Engineering or equivalent, with previous experience in Printed Circuit Design and Mechanical Drawing Office practises. We are offering competitive benefits and very good prospects to the right men or women.

The positions are being offered at the New Southgate location of S.T.C. which is on the outskirts of North London. The location is well established and pleasantly situated with all the extra amenities and facilities that a large Company can provide.

For further information please 'phone or write to:
Mike Randal (Department 32210),
Switching Main Exchange Products Division,
Standard Telephones and Cables Limited, Oakleigh Road South, New Southgate, London N11 1HB. 01-368 1200 Ext. 3066.

## Standard Telephones and Cables Limited

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## IMPERIAL COLLEGE TECHNICIAN GRADE 5

required to assist in the maintenance and operation of the S.R C sponsored AEI EM 7 1 MEV high voltage electron microscope. ghe success ful candidate will work under the
guidance of a Grade 7 Technician to maintain and improve a wide range of electromechanical, electronic and vacuum systems, so that a knowledge of electronics and electron optic instruments would be an advantage. The nature of the work is not routine and candidates must be prepared to adapt to changes in work requirements The duties of the successful canddate will also
include involvement in cine film and video include involve
tape editing.
Salary range £3377.£3761 including London weighting
Applications giving details of qualifications and experience together with the names of two referees should be sent before 30
November 1977 to Mr.G J Green. DeNovember of Metallurgy and Materials Science, Imperial College of Science and Technology. Prince Consort Road, London SW7 2BP.
(7695)

UNIVERSITY COLLEGE HOSPITAL MEDICAL SCHOOL

## requires an

## AUDIO-VISUAL TECHNICIAN

to take charge of the Audio-Visual Aids Department. Salary within University or London VA Technicians scale E2929 to Superannuation benefits
Applications, giving the names of two referees, to the Secretary. University College Hospital Medical School (WW). University Street. London. WC1E 6JJ. from whom further particulars may be obtained

## ELECTRONIC ENGINEERS

Required to join a progressive research team as Research Engineers. Applicants should be conversant with electronic çircuit design, both analogue and digital. A knowledge of stepping motors and servo systems an advantage.
This is a challenging position and will appeal to those engineers who enjoy combining both their theoretical and practical abilities. A B.Sc. or equivalent is the required qualification.
Please write with full personal and career details or telephone for an application form 01-205 7050.

## Personnel Officer

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## FIELD \& BENCH ENGINEER

required for work on radio telephone equipment
(7609)



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Is required in the School of Film and Television to assist in the daily operation and maintenance of a colour television studic equipped to broadcast standards. A sound knowledge of colour television systems is essential and some experience
with studio equipment would be an advantage. Candidates should hold C \& G Part II Certificate or equivalent although Part I Centificate holders may be consid. ered.
The salary will be in the range £3376. £3855 according to qualifications and experience. 4 weeks holiday. Pensionable appointment.
Interested applicanis should write giving fult details of previous ex-
perience, etc.. to: Mr. H. Denyer, porience, orc.: of Mr. K. Kensington Gore, SW7 2EU.
(7678)

CHELSEA COLLEGE
University of London

## ELECTRONICS TECHNICIAN

Grade 3 required as soon as possible by the Department of Pharmacy. The job invotves the repair and maintenance of scientific equipment with some construction and design work. Experience and/or electronic qualifications are necessary. Salary in the range $£ 2930-£ 3276$ per annum (inclusive of London Allowance and Supplements). Application forms from the Manager of Technical Services. Department of Pharma6LX. Closing date: 30th November 1977.

## PUBLIC ADDRESS ENGINEER

## Applications are invited for persons with

 knowledge of public address work at conferences and An intelliAn intelligent practical person with limited
experience and willing to learn would be considered.
A good salary with opportunity for overtime will be offered to successful applicants. Please write giving age and previous experience to.
GRIFFITHS HANSEN (RECORDINGS) LIMITED
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## THE POLYTECHNIC OF CENTRAL LONDON <br> School of Engineering and Science

## TECHNICIAN GRADE 3

for an expanding group working in communication and computer fields. Experience in electronics and workshop practice necessary. OND or ONC or 2 A levels or Ord. required $£ 2790-£ 3120+$ suppl. max. £208. Apply to the Establishment Officer, PCL. 309 Regent Street. London, W1R 8AL. 01-580 2020. Ext 212.


The Plessey Development Laboratory at Havant, Hampshire, is sub-contractor for the most advanced VHF communications system ever to be developed for the British Army. This system - known as "Single Channel Radio Access" - allows mobile subscribers to use the Ptarmigan trunk telephone net work for both voice and data messages.
We are now proceeding with the second phase of development, creating new career opportunities for Technician Engineers who wish to advance their knowledge.
What jobs are on offer?
We are looking for 'Technician Engineers with a minimum of 5 years' relevant experience in industry or H.M. Services to work in the following fields.

## VHF Radio Equipment Development and Evaluation

Successful candidates will be responsible for getting development models of transmitters and receivers working and characterising their electrical and environmental performance under a wide varietyof conditions.

## Development of Special Purpose Test Equipment

This new development of automatic test equipment for use in the Army's Electronic Repair Vehicles will interest candidates with a knowledge of ATE and associated programming techniques.

## Development and Evaluation of Digital Logic Modules

Candidates with a special interest in digital circuits and systems will find opportunities to work under the guidance of experienced senior engineers on the most up-to-date techniques, including microprocessors.
What qualifications?
The type of work we do needs people with plenty of solid practical experience of transistorised equipments, a common sense approach and a willingness to work with others towards a common goal. However, if you have the experience we are looking for, it is likely that you will also possess a City \& Guilds Full Tech. Cert., ONC or HNC.
Salaries and cureer prospects
We operate a separate career structure for Technician Engineers which offers plenty of opportunity for promotion. You could become a Principal Technician Engineer in charge of a small section, while the exceptional younger person would be encouraged to qualify to transfer into the Professional Engineering grades. Because our plans for business expansion are soundly based on a full order book for a wide range of both government sponsored and private venture products, we can offer you both job stability and the up-to-date experience which is essential to our future growth.
Technician Engineers are recognised as important members of our teams and are rewarded accordingly. These jobs will carry salaries up to $£ 4,000$ p.a.
Situated in a semi-rural environment near Portsmouth, Chichester, the South Downs and several seaside resorts, we are well placed for housing, educational and recreational amenities. Relocation assistance will be given and there is a comprehensive range of large company benefits.
Please write with brief career details or telephone for an application form. L. Wise, Recruitment Manager, The Plessey Company Limited, Martin Road, West Leigh, Havant, Hants. Tel : (07012) 6391. Applications are invited from either sex.
©plessey

## Instrumentation Technologist <br> Salary c. £4,500

Redland Technology provide Research and Development services for manufacturing divisions of the Redland Group who manufacture materials and building components for the construction industry.
The post available is for an electronics engineer or physicist to work in the instrumentation section of the research centre, set in pleasant countryside at Graylands, Horsham, Sussex.
The successful candidate will be responsible for designing and constructing specialised instrumentation systems and for advice and consultation services on suitable techniques for process control and materials testing. He/she will be provided with technical support staff.
The work involves the measurement of a wide range of parameters, but particularly load, vibration, noise, temperature. A microcomputer is available to analyse data recorded on FM tape. Projects often result in the development of novel solutions which, where appropriate, will be patented.
Minimum qualifications are a degree or HNC in an appropriate subject plus at least two years relevant experience. Car ownership would be an advantage as some travelling will be necessary.
Please write in the first instance to
Dr. R. A. Hazelwood, Redland Technology Limited
Technology and Product Development Centre, Graylands, Horsham, Sussex. Tel : Horsham 2351 Ext 281

THE UNIVERSITY OF LIVERPOOL

DEPARTMENT OF INORGANIC, PHYSICAL AND INDUSTRIAL CHEMISTRY

## TECHNICIAN

The successtul applicant witt work on the maintenance, repair and onstruction of a wide range of scientific insirumentation, involving amplification of low level signals (including lock-in amplification) R.F. echniques and digital and analogue techniques. Salary in a range up to E3367 per annum (under review)
Application forms may be obtained from the Registrar. The University Quote ref RV/590/WW

(7679)

## Immediate opportunity in Amman, Jordan, for <br> ELECTRICIAN/ ELECTRONICS TECHNICIAN

to join staff of new Army museum Will have sole charge of sophisticated British designed electrical system ncluding electronically controlled audio-visual installation. Appointment for approx one year, during which time applicant will train Jordanian national. An interesting and responsible post. Salary by arrangement. All Jordanian expenses paid.
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30 Windmill Sireet, Landon. W1 01-637 5551

## Salies MANAGER for Mainline Electronics

The Mainline division of Crellon Electronics has been formed to supply electronic components to the amateur market.
The division requires a Manager (male or female) with entrepreneurial flair and drive to take over sales and marketing
Applicants, who must have considerable experience in this field will be fully responsible for the total promotion of the division's products - and directly concerned with its profitability.
Salary around $£ 5,000$ plus a generous profit sharing scheme Which should produce over $£ 7,500$ p.a.

## WILTSHIRE <br> AREA HEALTH AUTHORITY

 Area Physics Service
## Medical Physics Technician <br> (GRADE IV)

Required for this new department which provides an Area physics service (centred on the City of Bath) and serving ultimately three health districts. The person appointed will be assisting with the provision of physics service chiefly to the clinical measurement activities in the Area. An interest in electronic techniques and instrumentation desirable
Salary: £2346-£3267 per annum plus supplements.
Job description and application forms are available from the Area Personal Officer, Wilishire Area Health Authority, Rowden Hill House, Chippenham, Wilts SN 15 2AN. Tel. 0249-51251 ext. 236.
Closing date: 12 th December, 1977

MARINE ELECTRONICS FIELD EN GINEER experienced install \& ser vice SSB VHF radar, autopilots electronics. Able to work on own nitiative. Home and abroad. Must tive in or near London. The right person could advance to manage ment level. Telesconic Marine Lid 243 Euston Road, NW1. (7680

RADIO - TELEPHONE ENGINEERS Experienced in V.H.F. mobile equipment. Top salaries for top ability. We are a young progressive company currently the busiest. and fastest expanding radio-telephone firm in London. Ring London Communications on 01-328 5344 ask for Mike Rawlings or
Clarke.
$(7356$

Write or telephone
Alan Thompson Mainline Division Crellon Electronics Ltd 380 Bath Road, Slough. Berks. Tel: Burnham (06286) 4434

## H.M.G.C.C.

has vacancies for

# ELECTRONIC ENGINERS 

to work in fields of:
a. VHF/UHF communications equipment design.
b. General circuit design analogue and digital.

## Qualifications

Candidates should have one of the following academic qualifications
i. Degree in Science or Engineering
ii. Degree standard membership of a Professional Institution
iii. HNC or HND in a scientific or engineering subject
or equivalent qualifications.

## Experience

For the grade of Higher Scientific Officer the following post-qualification is also required, 2 years for candidates with 1 st or 2 nd Class Honours degrees and 5 years for other candidates.

## Salaries

Scientific Officer (under age 27)
£2462-£3840
Higher Scientific Officer £3567-£4767

A pay supplement of $£ 313.20$ per annum is included in the above salary scales. An additional supplement of $5 \%$ of total earnings subject to a minimum of $£ 130.50$ per annum and a maximum of $£ 208.80$ per annum is also payable.

Application forms may be obtained from:


The Administrative Officer
HM Government Communications Centre
Hanslope Park
Hanslope
Milton Keynes MK19 7BH

## Metropolitan Police Office

# Tape Recording Specialist 

to work in the Tape Laboratory, Camberwell, London, on copying and processing tapes, and preparing tapes for specialist tape recorders. Duties also include giving evidence in Court about work carried out on tapes, and occasionally analysing various phenomena of tape recordings using specialist analytical equipment and then acting as expert witness in Court. The successful candidate will be responsible for own case work (initial training given) and will work in close collaboration with Police Officers and with the various Constabularies which will necessitate travel anywhere in the country.

Candidates (aged at least 21.) must have ONC in Engineering (with a pass in Electrical Engineering " $A$ '") or C\&G Radio. TV and Electronics Technicians Cert. No. 272, or an equivalent or higher qualification. In addition, they should have a thorough understanding of tape recorders and recording techniques; have experience of work in professional broadcast studios on audio and video tape recorders; and be fully conversant with checking tapes for quality and defects.

Salary starting between $£ 3490$ and $£ 4460$ (according to age) and rising to $£ 4765$. Promotion prospects. Non-contributory pension scheme.

For further details and an application form (to be returned by 8 th December, 1977), write to Civil Service Commission, Alencon Link, Basingstoke, Hants RG21 1JB, or telephone Basingstoke (0256) 68551 (answering service operates outside office hours). Please quote $\mathbf{T} / \mathbf{9 6 2 5}$.

## ELECTRONICS ENGINEER <br> (LIGHTING CONTROLSYSTEMS)

We are a leading company in the field of thyristor controlled lighting and associated equipment, including standby power sources. power distribution and industrial control. A high percentage of our production is exported

We require an Electronics Engineer to carry out design and development on both existing and now products The work encompasses all aspects of design from initial concept and prototypes to final production, and may include some project work.

Applicants should have a minimum qualificiation of HNC together with some industrial design experience involving analogue and digital techniques. The ability to work with minimum supervision is essentia

## Contact: P. J. Harrison, Technical Director


oaron Controls Limited 60/62 Greenhill Crescent Holywell Industrial Estate Watford, Herts WD1 8RL Tel. Watford (0923) 37144 Telex 922080

## IMPERIAL COLLEGE OF SCIENCE AND TECHNOLOGY

## TECHNICAL STAFF GRADE 5

Vacancy for electronics technician to construct and develop specialised electronic and electrical equipment, and to assist research groups with electronic instrumentation in the field of control, measurement and data-handling.
Salary range $£ 3377-£ 3856$ per annum inclusive of London weighting and permitted supplement. Four weeks' holiday plus additional days at Christmas and Easter. Superannuation scheme. Sports and social facilities. 37 and half hour, 5 day week.
Apply to Mr. G. J. Green, Department of Metallurgy and Materials Science, Imperial College of Science and Technology, South Kensington, London SW7 2BP.

## CATHODIC PROTECTION TECHNICIAN

Wisbech

up to $£ 3957$
Applications are invited from suitably qualified persons for the above post based in Wisbech, Cambridgeshire.
The successful applicant will be required to carry out resistivity surveys, potential surveys, locations and Pearson surveys, continuity and insulating flange tests and all patterns of interference tests. Responsibilities also include negotiating with Electricity, Water, Post Office and all other Authorities concerned with Cathodic Protection.
Candidates should be experienced in all aspects of Cathodic Protection including installation and maintenance of impressed and sacrificial schemes, ground bed installations, transformer rectifiers, test posts, and all general duties associated with Cathodic Protection.
Salary within the range $£ 2769-£ 3456$ plus Phases I and II pay policy supplements. Excellent conditions of service including sick pay, holiday and pension schemes. The region operates a car mileage scheme.
Apply to Mr. J. G. Hagger, Area Personnel Officer, Eastern Gas, Newmarket Road, Cambridge CB5 8JE. Tel. Cambridge 65341, as soon as possible.

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AVERAGE SALARY $£ 77$ for $\mathbf{4 0}$ hours per week on a 3 shift 5 cycle week pattern or $£ 69$ for 40 hours per week for 1 -week-end in 4 day stagger pattern.

Full details will be supplied to craftsmen writing for the standard application form and quoting reference 67/IND174/77 to the Manager, Hunterston 'A' \& 'B' Nuclear Power Station, West Kilbride, Ayrshire LA23 90.J.

The above vacancies are open to male and female applicants.

## SATELLITE COMMUNICATIONS ENGINEERING

Within the International Telecommunicatıons Satellite Organisation (INTELSAT) two types of satellite earth station are used -
Standards ' A ' and ' B '. Cable and Wireless is the world's largest operator of both types of INTELSAT earth station. Our involvement encompasses ownership, operation, maintenance, consultancy and the lease of earth stations to clients. The Satellite Division plans and coordinates the procurement and commissioning of earth stations, including our own design of Standard 'B' station, and is involved in systems studies for our own network and for projected domestic satellite schemes. Rapid growth in this high-technology field means outstanding opportunities are availab.e to men and women in the following key posts

## Satellite Systems Engineers

You will be involved with the study of satellite systems, and in the preparation of specifications and procurement of earth station sub-systems. You should be numerate and hold a degree or equivalent in electrical engineering, electronics or a related discipline. Experience in microwave techniques or
radio communications is essential, and experience in satellite communications systems engineering would be advantageous. Candidates with specialised experience in related fields (such as digital radio, antenna theory or microwave propagation) will also be considered.

## Satellite Earth Station Planning Engineers

You will be concerned with the planning of satellite systems and the preparation of specifications, tender evaluations, procurement and installation of earth station sub-systems. You should hold an HNC or equivalent in electrical engineering, electronics or a related discipline, and membership of the Institution of Electrical Engineers or the Institution of Electronic and Radio

All posts are London-based, but as most of our business is overseas. you will make short trips abroad.
Starting salaries (currently under review) will be around $£ 5,000$ rising to £6,000 in annual increments. Subsequent promotion. leading to salaries of $£ 7.600$, is possible. Benefits are those to be expected of an

Engineers would be an added advantage. Experience in microwave techniques or radio communications is essential, and experience in satellite communications systems engineering would be an advantage. Candidates with field operations experience but without planning experience will also be considered.
international company, and includes relocation assistance where necessary.
Please telephone or write with full CV to:
Recruitment Manager,
Dept. A907/749,
Cable \& Wireless Limited, Mercury House, Theobalds Road, London WC1X 8 RX .
Tel: 01-242 4433 ext. 4059.


## Team Leaders

The Directorate of Radio Technology, Central London, provides the technical expertise and engineering support for forming and implementing management policy, and is concerned with all aspects of spectrum engineering. It is also involved in the technical preparations for the 1979 World Administrative Radio Conference.

The successful candidates will each lead a section dealing with one of the following activities: the forward planning. management and regulation of civil frequency bands; radio propagation over the whole frequency spectrum; specifications and equipment type approval for fixed and mobile services, including microwave links; the application of computer techniques to frequency management problems; the nationwide radio interference service; the provision of technical advice on radio services' licensing; and the operation of an international radio monitoring service.

## up to $\mathbb{£ 6 , 9 1 5}$

Candidates must have qualified for corporate membership of IEE or IERE, and in addition have several years professional experience. They must have at least two years' experience in a relevant field, and should have a broad outlook enabling them to appreciate the general principles of frequency spectrum engineering.

Starting salary between $£ 5705$ and $£ 6915$, depending on qualifications and experience. Non-contributory pension scheme. Promotion prospects.

For further details and an application form (to be returned by 9 December 1977) write to Civil Service Commission. Alencon Link, Basingstoke, Hants, RG21 1JB, or telephone Basingstoke (0256) 68551 (answering service operates outside office hours). Please quote $T(D) 85 / 1$.

## Engineering Opportunities

Pye Business Communications Limited, a member of the Pye Group of Companies - part of the international Philips organisation - markets, installs, commissions and services a wide range of communications equipment
The Engineers appointed to the following positions will enjoy the excellent employment conditions associated with a major cumpany
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 ENGINEER- Southern Area, resident North of the Thames, for Private Telephone systems, Intercom and Public Address systems and Paging Equipment A Final C. \& G. Telecommunications Certificate, ONC or equivalent is essential for the above vacancies, together with a clean driving licence. Company cars will be provided.
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Please apply to
Mrs. Ann Macnab, Personnel Manager. Pye Business Communications Limited, Cromwell Road, Cambridge CB1 3HE Telephone Cambridge 45191

## Pye Business Communications Ltd

## ChELSEA COLLEGE University of London <br> ELECTRONICS TECHNICIAN

Grade 3 required for the construction and maintenance of electronic equipment in the Electronics Undergraduate Teaching Labo ratory, and also to assist with laboratory
classes and Audio Visual Aids requirements

Day release for an approved course of study could be arranged. Relevant experience and ONC or equivalent required. Salary $£ 2930$ £3276 inclusive. Further information and application forms from Mr, M. E. Cane
(3ET), Department of Electronics, Chelsea College. Pulton Place. London. SW6 5PR

THE UNIVERSITY OF LEEDS. Electronics Technician (Grade 5) required, in School of Chemistry thectronks Workshop. to work on of electronic equipment. The appliof electronic equipment. The applicant must be conversant with dircuits printed circuit techniques and electromechanical equipment Preference will be given to the applicants who have substantial knowledge of, and service experience on, multiohannel analysers. Qualification - HNC or equivalent and candidates must have a mini mum of 7 years appropriate exper ience. Salary on a scale $£ 2,889$ g3,367 p.a. Applications in writing, qualifications and state of health to Mr. G Spink School of Chemistry The University, Leeds LS2 9JT.

VERY EXPERIENCED Electronic Engineer for electronic, keyboard and amplification services. Salary negotiable. Phone Maurice Placy. quet 01-749 3232 .

## Chelsea college ELECTRONICS TECHNICIANS GRADE 5

Vacancies exist in our Electronics Workshop and Electronics Research Laboratory. Interesting prototype design and development plus servicing or experimental work. Relevant experience essential. Salary in the range $£ 3377-£ 3856$ per annum (inclusive of London Allowance and Supplements) Application forms and further details frm Mr. M. E, Cane (5E), Department of Electronics, Chelsea College, Pulton Place, London SW6 5PR.
SENIOR ELECTRONICS ENGINER for search. programme of Space Research, programme of satellite and related experiments, in parvears experience of designing ana logue and digital circuitry re. quired, familiarity with high re liability applications, computer interfacing techniques, computer interfacing techniques and micromrocessors ars advtantage. HNC Salary scale £2889-£3367 p.a. (under review) apply Assistant Secretary Personnel Office, University of Birmingham, P.O. Box 363 , Birmingham B15 2TT Ref $105 / \mathrm{C} / 287$.
(7678
ELECTRONICS TECHNICIAN (IGrade 5) required in Department of Psychology, University of Reading. Final C.G. Electronic Servicing or equivalent qualification desirable. Salary in scale £2889-£3367 pa (under review). Apply with full details and names of 2 referees. quoting Ref. T.W.W. 46A to Assistant Bursar (Personnel), University of Reading. Whiteknights, Reading,
Berks, RG6 2AH.

## Research Officer

Ideal opportunity occurs for a suitably qualified person to undertake interesting and varied projects and investigatory work in the field of electromagnetic interference
The ideal applicant, male or female, will be aged $22-40$ with graduate or HNC qualification, experience of Radio frequency measuring techniques - measurement and calibration
ERA is an independent engineering organisation specialising in the profitable application of electrotechnology within industry, commerce and the public sector.
Competitive conditions of employment amid pleasant rural surroundings offer attractive career prospects
Contact the Personnel Manager ERA Ltd., Cleeve Road. Leatherhead Surrey. Leatherhead 74151

7662


ELECTRONICS TECHNICIAN (Grade 6) required by Physiology Dept for 6) required by Physiology Dept for the design and construction of used in muscle research. Experience in analog, digital and computer interfacing circuitry desirable. This is a grant supported post. Salary in range $£ 3.802$. ${ }^{4} 4,435$ inc. of London Weighting. Application form from Personnel Officer (Technical Staff FF15) University Corlege London, Gower
St., London WiciE 6BT.

ELECTRONIC TECHNICIAN Grade 5 required in the Chemical and Biofor the electronic workshap knowled ge of fault-finding and seor vicing standard electronic instruments required, together with the ability to work on prototype circuits. Salary in range $\mathbf{~} 3,377$ to £3,856 including London Weighting. Application form and further details from Personnel officer (Tech nical Staff EB8). University College London. Gower Street, WC1E 6BT (7716


## ELECTRONIC TECHNICIANS

Opportunities for the experienced and sometimes inexperienced in St. Albans and Luton.
Work situations range from fault finding on PCB's and components, to batch product testing of equipment that utilise very advanced techniques including microprocessors and the repair/ calibration of all manner and types of test instruments.
Attractive salaries and, where appropriate, relocation are offered for the right candidates.
Further information may be obtained in confidence from John Prodger
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Longacres, St. Albans, Herts. tel: St. Albans, 59292

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