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COMMENT 3

Equality?

5 NEWS

- New material could give blind people sight
- Welsh firm trials mains comms
- Low-cost bendy displays come a step nearer reality
- LED for torches and bike lights
- Electroluminescent display adds a new dimension to a car's dashboard
- Most powerful 2GHz transistor
- Breakthrough in superconductors
- Micromachined fans

12 **BENDY BATTERIES ENABLE SMART** SOLUTIONS



Ultra-thin battery technologies look set to add major new functionality to travel tickets, ID tags and greetings cards. Andrew Emmerson reports.

14 THE INVENTION OF THE SUPERHET

Ian Poole has been investigating how today's most popular receiver topology the superheterodyne - made its debut.

18 **GANG YOUR OWN** POTS

Off-the-shelf, ganged potentiometers are rare and can be very expensive. Cyril Bateman has devised a method of ganging your own dual, triple and even quad pots.

MEASURING 21 CAPACITANCE

Michael Slifkin and Shai Kriegman have been investigating three ways of

measuring capacitance - one using phase shift, one using the error signal from a PLL and the third looking at a frequency change due to the unknown capacitor.

26 **DIRECTION FINDER USING VHF**

Ian Hickman's final article about a simple direction-finding system describes the RSSI demultiplexer and other circuitry not covered in the two earlier articles.

30 **CIRCUIT IDEAS**

- Electronic lock with extra security features
- Constant-current diode look-alike
- Power-failure safety interlock
- Battery voltage low/high indicator
- Programmable staircase waveforms
- Stepper motor driver with half stepping

39 **NEW PRODUCTS**

The month's top new products, selected and edited by Richard Wilson

48 **DETECTING VIBRATIONS** AND TREMORS

In Joe Carr's explanation of vibration detectors and seismographs, he details an easy-to-build instrument that's sensitive enough to detect Earth tremors.

USB OSCILLOSCOPE 54 FOR THE PC

Eddy Insam shows how easy it is to make an oscilloscope that plugs into your PC's USB port. Its sampling rate may not



be very exciting, but it can be perfectly acceptable for audio and teaching work.

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WEB DIRECTIONS 66

Useful web addresses for electronics engineers.

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A new ceramic material from NASA may one day be used in artificial retinas to help the blind to see. For this and the month's other top news stories, turn to page 5.



Exclusive reader offer - save 20% on a 16-channel stand-alone digital i/o controller – page 64.



Ian Poole looks at the invention of one of the biggest breakthroughs in wireless - the superhet. Turn to page 14.



On page 18, Cyril Bateman describes how you can roll your own dual, triple and even guadganged potentiometers using offthe-shelf belts and pulleys.

59

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Equality?

was listening to Woman's Hour on Radio 4 during the Christmas break and heard an item featuring a woman who was training as an AA technician. This interested me, because in a 25+ year career in manufacturing industry, I have only ever worked with one female graduate design engineer. I have, though, worked with several in Europe, while working for multinational companies, so there is clearly something unique about women and engineering in the UK.

I decided to place a posting on the Woman's Hour message board, commenting on the item and I asked the specific question as to the reasons why women don't want to read engineering at university. I was quite prepared to be informed that it was too difficult, boring, low remuneration etc. However, I wasn't prepared for what actually did happen: not one reply. My posting was removed a week later, because no one had responded.

When I did inspect the messages on either side of mine, they comprised a discussion about pornography, which appeared to contain an infinite number of postings – none of which detailed anything new – together with postings concerning an item about the wisdom of bribing children. It was abundantly clear, though, that a sizeable proportion of these comments were placed by university educated – in arts subjects no doubt – middle-class individuals, working in the service or public sector, who clearly wouldn't know what a shop floor or export was if they fell over it.

I spent two years prior to the economic downturn attempting to recruit senior electronics design engineers, with a good background in analogue electronics and electromganetics. I had to give up in the end, which resulted in two major projects being cancelled and the profits of my company suffered accordingly. The balance of payments deficit isn't an illusion: it is very real.

But did this worrying experience gain any exposure in any news item or newspaper letter (I wrote to the *Independent*)? Of course not, because the engineering profession doesn't have any powerful lobby group to represent it. And, of course, engineering isn't regarded as 'sexy'.

I contrast this with the medical profession. Do *Electronics World* readers remember the outcry when Laura Spence was rejected to read medicine at Oxford? I don't believe this matter would have been reported at all if she'd been rejected to read engineering. Prior to the election, the BMA threatened a mass resignation of GPs, one of the reasons being because of the alleged shortage of



Clare Roberts has been named as the Young Woman Engineer of the Year for 2001. Roberts, a design engineer for Yorkshire Electricity, was awarded £1000 and a silver rose bowl.

GPs. And what was the result of this bully boy approach? Why a whopping inflation-busting pay rise, taking the average salary of a GP to £84.5k. Remember the vilification that Arthur Scargill received when he adopted these tactics in the seventies in an attempt to save the mines?

It seems ironic that Dr Smith and I must both have spent many long hours gaining our engineering Ph.Ds, though we are often treated as pretend doctors; the GP who uses 'Dr' as a courtesy title is regarded as the real thing – and paid as such.

I bought my first copy of *EW* when I was twelve years old because, like the young James Clerk Maxwell, I always wanted to know: 'what makes it a goer.' Thinking and acquiring knowledge were my major interests in life, irrespective of whether it was through an intellectual or practical route.

While I doubt whether any politicians ever read Electronics World, I think I'm perfectly justified to ask them what they really do mean by the 'knowledge economy', because it's clear from my experience that it certainly doesn't exist in the UK any more.

Lawrence Jones

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UPDATE

New material could give blind people sight

A new ceramic material from NASA may one day be used in artificial retinas to help the blind to see.

Artificial retinas have been experimented with before. Most of them have used photo-sensitive silicon detectors.

Unfortunately, silicon needs to be coated as it, and body fluids, mutually deteriorate on contact.

The photo-sensitive ceramic material has been designed from the outset to be non-reactive in the body.

"We are conducting preliminary tests on the ceramic detectors for biocompatibility, and they appear to be totally stable," said NASA scientist Professor Alex Ignatiev.

The epitaxially-grown oxide

ceramic detectors are to be implanted as a hexagonal array of $100\,000$ individual devices each 5μ m across. Avoiding one large device allows nutrients, which enter the eye from the back, to reach the rest of the eye.

To make them easy to handle, the devices are attached to a soluble polymer film for insertion.

"An incision is made in the white portion of the eye and the retina is elevated by injecting fluid underneath," said team surgeon Dr Charles Garcia. "Within that little blister, we place the artificial retina." The first human trials of such detectors will begin this year, said NASA.



New neural network chip has 1024 Risc processors

Aberdeen-based parallel-processor developer Axeon has unveiled its design for a neural network processor.

Called VindAX, a name derived loosely from the Latin for delivering, the design comprises up to 1024 Risc processing elements forming a single instruction, multiple data array.

Axeon calls VindAX the Learning Processor. "Our system learns by example," said Dr Neil Lightowler, Axeon's chief technology officer.

"The architecture was designed to suit a neural-network algorithm. Each of the processing elements – a true processor with memory and ALU – is optimised for this algorithm."

The firm is aiming the processor at complex non-linear systems, especially those for which it would be hard to develop a traditional algorithm. Handwriting recognition software that could be used in hand-held PCs has already been developed internally by Axeon.

Neural networks were hot topics in the eighties, but never lived up to their potential. Lightowler puts this down to silicon not being fast or small enough to implement a decent sized net.

In a 0.18µm process, a 1024 VindAX will occupy 67mm², and run at 200MHz. At this speed it could carry out 50 000 classifications a second, consuming 0.1W of power.

A silicon version of VindAX is available for prototyping and small volume systems. It has 256 elements – enough for applications such as handwriting recognition.

Welsh firm trials mains comms

A firm from South Wales is the latest to demonstrate communications over mains power lines. LANergy has shown 14Mbit/s links over the standard 230V system.

The Newport-based firm is aiming its technology at home and office networking, linking laptops, PCs, DVD players and other equipment without installing any extra wiring.

"It means a low-cost, easy to

install network which will transform the way that small firms and homes are managed," said Brian Lasslett, chairman of LANergy.

The firm has developed hardware to interface equipment with the mains, and software to manage all the nodes on a network. Encryption is used to provide security – which is critical for a mains-based system. A Welsh venture fund has backed the firm to the tune of £1.35m. The first commercial use of the Axeon neural network might just be in automotive, if the firm's first external collaboration is successful. The firm has teamed up with Lotus Engineering, which is looking at hydraulic operation of valves in engines.

"There's a lot of interest in getting away from fixed mechanical actuation," said John Moore, commercial manager for research and technology at Lotus.

The firm started looking at hydraulics over ten years ago, but it was too complex and the electronics was too slow and expensive. Now the hydraulic actuators and electronics can be made fast enough and at the right price, said Moore.

However, the control problem is non-linear and extremely complex. Axeon's neural network seems an ideal candidate. "Their technology looks like it's able to cope with this very complex control problem," Moore said.



On the left is John Moore of Lotus Engineering and with him is is David Gorshkov, vice president of sales at Axeon.

Low-cost bendy displays come a step nearer reality

Philips Research is claiming "A significant step towards low-cost flexible displays," for this polymerdispersed LCD which it demonstrated recently.

It is a reflective 64 by 64 pixel 256 grey level display with a 5.1cm diagonal and paper-like contrast, which can be refreshed at video speed, said the company.

The active matrix is based on a polymer created from a

polythienylenevinylene pre-cursor. It is separated from the liquid crystal by spin-coated polyvinylphenol.

The display fill is polymerdispersed liquid crystal that can be switched between light-scattering and transparent states by applying a voltage. In the transparent state, the reflective back electrode is exposed. No polariser or alignment layer is needed in the structure.



LED for torches and bike lights



This led assembly from Agilent-Philips joint venture LumiLeds is aimed specifically at torches and bicycle lights.

Called the LuxeonStar/O, the large-die device uses a supplementary lens and state-of-theart light extraction techniques to squeeze as much useful light as possible into a beam.

Common LED colours are available.

Satellites linked by optical beam

The first optical data link between two satellites using a laser beam has been tested by the European Space Agency. Images were sent at 50Mbit/s from the French space agency's earth observation satellite SPOT 4 to its processing centre in Toulouse, via the ESA's Artemis satellite.

This is a significant achievement, when one considers that the two satellites are moving at some 7km/s. Extreme accuracy is needed to establish such a link, keeping the partner satellite illuminated.

The accuracy of the pointing system must be around 1 μ rad. Even the time taken for light to travel between the satellites has to be accounted for.

The relay satellite, Artemis, is in a parking orbit with an altitude of 31000km, while SPOT 4 is much closer to Earth, orbiting just 832km The white version consumes around a watt and is slightly more efficient than a conventional halogen bulb. Maximum operating current is 350mA when the device drops around 3.42V.

A thermal plane is included in the substrate to keep die temperature down.

Other Luxeon devices including un-lensed arrays of various forms are available. www.luxeon.com

up. The laser relay allows SPOT4 to transmit data to its base for much longer periods of time.

Artemis will eventually reach a geostationary orbit, allowing much longer contact periods with for SPOT 4, up to half its orbit time.

Both satellites used on-board SILEX systems built by Astrium. The firm said its equipment can support image data rates of up to 250Mbit/s.

3-D chips... This structure can be made "using the standard materials, equipment, and processes already present and very well understood in the world's semiconductor manufacturing foundries", said Matrix Semiconductor, revealing this three-dimensional structure. The one pictured was made at a TSMC foundry for Matrix. The company is going into production with devices made using the same technique, the first of which will be Matrix 3-D Memory - a write-once memory developed as a consumable, much like traditional camera film. Intended markets include archival storage for digital cameras, audio players, portable games, PDAs and cell phones. These 3-D memory cards will be interchangeable with standard flash cards.



Electroluminescent display adds a new dimension to a car's dashboard

Planar Systems has developed a new type of instrument cluster display for cars.

The display, developed with automotive suppliers Sagem and Johnson Controls, is transparent and appears to float, allowing other displays to be viewed through it. In the version shown, a speedometer, gear selection and fuel level information are displayed on the new display that uses electroluminescent (EL) technology.

Johnson's industrial designers

initially came up with a concept that could not be realised with displays readily available on the market. Sagem and Planar came to the rescue with the EL device.

"The prospect of adding a depth dimension to the already crowded instrument cluster really appeals to the transportation industry," said Erick Petersen, director of business development at Planar. "Planar is currently investigating a number of other applications for this technology."

www.planar.com

Head-up display works in full daylight

Microvision has begun shipping its Nomad personal head-up display.

Nomad has VGA resolution and is said to be readable in full daylight.

The company is aiming at applications in four markets industrial, aerospace, medical, and military.

Several companies are already signed up to evaluate the display.

Eurocontrol, the European agency for the safety of air navigation, is to evaluate Nomad to reduce air traffic controller workload.

Medical firm Stryker Leibinger will couple Nomad with a surgical workstation and Telesensory is working on a device to help



partially sighted people read. www.mvis.com



Most powerful 2GHz transistor will transmit to 500W

NEC has made a single chip 113W 2GHz transistor from a nitride semiconductor and claims it to be the most powerful yet.

Two features contribute to the transistor's power output: a GaN-AlGaN heterojunction allows it to operate with a 40V rail and its thinned (50µm) sapphire substrate triples thermal conductivity compared with earlier devices.

Combining four nitride transistor chips in one package will make possible a transmission power output of 400 to 500W for mobile phone base stations.

Commercialisation is predicted within two to three years. 113W is delivered at 10% duty cycle.

Breakthrough in room temperature superconductors

New findings may bring roomtemperature superconductors closer.

North American scientists, based at the Universities of Maryland and Toronto, claim to have the first clear evidence that electrons in so-called high-temperature superconductors behave differently relative to electrons in any other material.

In a research paper, the team show that electrons in a copper oxide (hightemperature) superconductor violate a fundamental theory of modern physics known as the Fermi-liquid theory. This theory forms the foundation for scientists' understanding of metals. One tenet of the Fermi-liquid theory is that electrons in a metal can be treated as 'quasi-particles' whose ability to transport heat is strictly determined by their ability to transport charge.

However, the new findings show that, in at least one type of superconductor, the electron system can conduct heat in a way that is largely unrelated to the way it carries charge.

"In spite of intense research by condensed matter theorists and experimentalists all over the world, nobody yet understands the mechanism by which hightemperature superconductivity can occur," said Professor Richard Greene of Maryland's department of physics. "Our experiment shows that the very low temperature, normal metal state of so-called hightemperature superconducting materials is unlike any other material known. These findings represent a significant fundamental scientific result. And although they are not of practical significance at present, they may be a step toward someday finding a room-temperature superconductor."



OLSON IS THE MARKET LEADER FOR HIGH QUALITY MAINS DISTRIBUTION PANELS



Compressing compiler claimed to reduce embedded code by up to 40%

A microprocessor developer from Ireland has developed hardware and compiler techniques that it claims can reduce the size of embedded code by up to 40 per cent.

Silaria, from Dublin, calls the technique Crush. It finds frequently used sequences of instructions in the code, and replaces them with a single custom instruction.

"Code density is key because it has a direct impact on cost," said John Hickey, Silaria's president and CEO.

Crush is designed to be used with the firm's Proteus3 processor core, which has a fine-grained instruction set architecture, with variable instruction lengths of 8, 16, 24 and 32-bits.

Rather than encode all instructions as 32 bits, Proteus3 allows commonly used instructions, such as Add or Return, to use perhaps 16 bits. This is similar to other code compression techniques such as ARM's Thumb.

Proteus3 alone, Hickey claimed, can be five to ten per cent better than ARM7 Thumb on code density, as some instructions, such as a return, can be 8 bits, whereas Thumb is 16 bits.

However, this replacement of certain 32-bit instructions with smaller versions is based on a general analysis of all programs. Crush can replace commonly used 32-bit



instructions in an individual program. "This allows individual programs to be compressed by crushing the most used 32-bit instructions," said Hickey.

More significantly, it can also spot commonly occurring sequences of instructions and replace them with a single custom instruction. This has no effect on execution time, Hickey said.

A table of the custom instructions is loaded into the processor design, making the whole process transparent to the user.

The architecture has over a

thousand custom instruction slots,

but Hickey reckons 64 to 128 is enough to get a 40 per cent code density improvement.

Crushed instructions need extra on-chip hardware, and for between 64 and 128 custom instructions this would mean around 25000 extra gates, doubling the size of a 32-bit, 32-register core.

If the final chip design uses a cache, then the denser code could lead to faster operation by avoiding cache misses. Alternatively, the cache could be made smaller.

Crush will form part of the company's tool set, and will be released early next year.



A thin film substrate for stacking multiple die in a package has been developed by Swedish firm Strand Interconnect. In order to avoid thermal expansion problems, the interposer material is made from silicon.

Tiny fans wiggle to cause a draught

Wiggling fans made using micromachining could cool future generations of consumer electronics and semiconductor ICs, if scientists from Purdue University have their way.

The researchers create a micromachine that waves a small blade back and forth, much like a Chinese fan. Piezoelectric material is used to move the blades of the fan, which therefore use little power – around 2mW.

Importantly, the fans do not need magnets, which removes any undesirable electromagnetic interference.



The Australia Telescope can now receive frequencies of up to 100GHz, following an upgrade with indium phosphide chips. Velocium, part of US electronics firm TRW, supplied the amplifiers and digital receivers. "The extremely high frequencies allow astronomers to take pictures of new phenomena with greater detail than ever before." said Dwight Streit, boss of Velocium. The image shows one of the upgraded receivers.

As transistor densities increase on chips, the need to remove heat close to the source becomes more important, both to increase reliability and improve performance.

"Even if it's just a bit overheated, its performance and reliability goes down," said Suresh Garimella, an associate professor of mechanical engineering at Purdue. "Another reason for cooling is to improve performance as you go to smaller and smaller devices."

Small fans for use on a chip would have a length of around $100\mu m$, while a fan inside a laptop PC or mobile phone could have blades somewhat longer, perhaps an inch long.

While the idea of a piezo fan is not new, the Purdue team claim its design is quiet and efficient. A mathematical process for determining the best design for a specific application has also been developed.

"What we bring to the table is a knowledge of the modelling of these fans," Garimella said. "[we have determined] how to analyse the design, to figure out how large a patch should be for how long a blade, how thick the patch should be and what happens if you modify all these quantities – in short, how to optimise the performance of these fans."

Electronic nose firm sniffs out US market

UK electronic 'nose' firm Osmetech has started clinical trials in the US of its bacterial vaginosis (BV) sensor device.

Previously the firm had received regulatory approval from the US Food and Drug Administration (FDA) for its urinary tract infection (UTI) sensor.

"This announcement is a landmark achievement for Osmetech and a first for the e-nose industry," said James White, Osmetech's chief executive. "We are delighted to have reached the milestone of FDA approval and to have done so in the time scale we set at the start of the FDA submission process."

Researchers seek to improve 3D-sound experience

Individually-tailored surround-sound systems could come out of research at the University of California, Davis.

It has long been known that adding certain acoustic artefacts to stereo audio signals can make sounds appear to come from different directions to the listener.

The artefacts mimic various filtering effects that occur to sound waves as they are diffracted, refracted and absorbed by the body, head and ears of the listener on the way to the listener's inner ears.

Several commercial 3D sound systems exist, but all are based on a single, averaged head and ears model. This model is called an HRTF, or headrelated transfer function.

Listeners who closely resemble the model get the best 3D effect, which worsens as the subject differs further from the average.

The researchers, Richard Duda and Ralph Algazi, have measured the 3D hearing of 45 humans and also measured their torsos, heads and ears in Trials of the BV sensor will take place at three hospitals, including St Georges Hospital in London. Data from the trials will be used to apply for FDA approval to market the product, expected by the summer.

According to the firm, a study at John Hopkins Hospital in the US showed the sensor gave diagnostic readings that were both quicker and more objective than current sensing methods.

In October 2000, a large study in London showed the device matched the existing 'gold standard' methods, Osmetech said.

an attempt to find some form of correlation between physical shape and 3D sound perception.

The eventual aim is to somehow scan listeners with a video camera, then automatically adjust the sound characteristics to suit them.

"I believe that this customisation of systems to individual characteristics represents an important and achievable goal for computer technology. Our current work, with colleagues at the University of Maryland and Duke University, is taking the next step toward this goal by using computer vision techniques and highperformance computing to obtain personalised HRTFs," said Duda.

To allow others to have a go at finding correlation between physical and acoustic characteristics, Duda and Algazi have put their measurements up on the Web at

interface.cipic.ucdavis.edu/. Click on HRTF data. The US National Science Foundation funded Duda and Algazi's work.

Disney's Wurlitzer gets a face lift

Devon-based Musicom has updated Disney's mighty Wurlitzer to bring its reliability into the 21st century – and add some new features at the same time.

"The old system had about 50 miles of cable and 25000 different sets of contacts," said Musicom's founder and MD Tony Koorlander. The contacts tend to wear out, making maintenance a headache and "the Wurlitzer [console] comes out of the floor on a lift. The original bundle of cables didn't like all the flexing and was a nightmare", said Koorlander.

Musicom's system replaces everything except the key contacts and solenoids with electronics.

A proprietary nine-wire bus in a flexible cable takes data from a computer in the console to the various organ pipes.

PIC microcontrollers running Musicom software de-multiplex the bus signals and operate solenoids through Musicom interface cards.

Opto-isolation is used extensively to beat electrical noise, which is considerable according to Koorlander. Heavy ground is used for the same reason.

The console computer is a TP300



Wurlitzer console rear view with standard Musicom hardware and the TP300 rack at the back.

Pentium-level machine from DSP Design. It runs Musicom software over a QNX operating system. This set-up replaced the in-house Z80 system Musicom had used before, but which proved inadequate for the Wurlitzer.

www.musicom-ltd.com



TI combines DSP and Risc cores

Standard chips combining advanced signal processing and Risc cores are now available, following Texas Instruments' launch of its C5470/1.

The devices combine a C54x class DSP with an ARM7 processor and a host of peripheral circuitry.

"On one device we tried to get the best each core can offer," said Arnaud Duclap, European business development manager at TI. A 100MHz C54x DSP with 72kwords of RAM shares 8kwords of RAM with a 47 5MHz ARM7, which has its own 16kbyte of memory

The chips are aimed at applications with both signal processing and control requirements. For example, in a text-to-speech system, the DSP performs speech synthesis, while the ARM7 carries out linguistic processing functions. But why not just use two separate processors? By combining the cores on a single die, "we reduce the system size, we reduce the cost and the power by 40 per cent", said Duclap.

Peripherals common to both chips include two multichannel buffered serial ports, two UARTs, serial port, I²C, and general purpose I/O. A keypad, LCD, USB and Bluetooth links can be directly control.

Exploding ICs... Chemists at the University of California, San Diego have discovered that silicon wafers can be easily made into tiny explosives that might be used one day to chemically analyze samples in the field or serve as power sources for tiny electronic sensors the size of a speck of dust.

"Most people are familiar with silicon as the material that's used in computer chips for circuits," says Michael J. Sailor, a professor of chemistry and biochemistry who headed the research project. "This is the same material, but we're making it into a very finely divided form of silicon – a nanocrystal – that has such a high surface area that it burns very quickly. The faster the burn, the bigger the bang."



Bendy batteries enable smart solutions



Ultra-thin battery technologies look set to add major new functionality to travel tickets, ID tags and greetings cards. They're making smart cards smarter too, as Andrew Emmerson reports. B atteries add bulk, not to mention cost. But if you had access to low-cost, paper-thin batteries, the possibilities would be endless. You could embed them in credit and identity cards, in baggage labels, in RF-enabled price and product tags, in contactless carpark and travel tickets, in novelty greetings cards and in all manner of toys and games.

Cheaper, lighter batteries would benefit existing products as well as these more novel applications. Thinner batteries could bring down the cost of the disposable cellphone as well as reduce the size and weight of all manner of portable and mobile devices, such as notebook computers and personal digital assistants.

All these possibilities are just around the corner, according to the Power Paper company of Israel, whose product of the same name works exactly like a traditional battery but is nearly as thin as a piece of paper. Power Paper cells make an ideal replacement for watch or calculator batteries but unlike existing button cells, Power Paper is just half a millimetre thick.

The cell is made up of five ink-like layers of material – a collector and cathode layer on one side, a collector and anode layer on the other and an electrolyte core. This produces a dry battery that's not only flexible but also has no need for a hermetically sealed metal case. It's 'green' too; all ingredients are non-toxic and safe, permitting disposal without endangering the environment.

Output is 1.5 volts and cells can be used in multiples to provide more power. A lin^2 (25mm²) printed cell will provide this voltage with a capacity of 15mAh and a shelf-life of more than two years. They can be used in multiple combinations for greater voltage requirements in any shape or size.

A rechargeable version is planned, although the first production of Power Paper batteries will be targeted at disposable products, such as travel cards, car park season tickets, greeting cards, toys and industrial tags.

Standard silk-screen printing presses are used to print the batteries onto paper and other substrates, giving amazing versatility and allowing them to be manufactured into any shape or size. They can also be integrated with printed circuits, RFID patch antennas and microchips, enabling them to perform functions such as controlling prescription drug injections, monitoring smart tags and labels or transmitting radio frequency identification label information over long distances.

Another promising niche market is in medical applications. These include bracelets that monitor temperature and provide health readings, prescription labels that read directions aloud to patients and remind them to take their medication, powered transdermal patches that deliver medication and combined



microsensors and readouts on disposable strips for performing complex diagnosis at home.

Unlike other batteries, Power Paper's energy cell is open and requires no casing to hold chemicals. It can be printed, pasted, or laminated onto paper, plastic, and other media. The actual power source is a zinc and manganese-dioxide (MnO_2) based cathode and anode. Both are fabricated from proprietary inks. As these are dry, the metal casing needed in conventional batteries to contain active chemicals is unnecessary. No special production equipment is required, nor does Power Paper require to be made under clean-room conditions, lowering manufacturing costs.

Power Paper expects to form a broad range of joint ventures and licensing agreements with companies wishing to integrate the technology into their manufacturing process and the first such deal was announced in July 2001. Graphic Solutions, Inc., of Burr Ridge, Illinois (USA) will use the technology to enhance its existing business making printed circuitry, electroluminescent products, antennas for RF tags, labels, name plates and panels.

Another ultra-thin battery technology about to hit the market is the dry-polymer battery supplied by Leading Edge Technologies, Lakeland, Florida. It is slender enough to fit between the laminated outer layers of a credit card and this is the prime application currently. Battery life is stated to be three to five years, depending on use, and contacts on the card allow it to be recharged. The company is aiming for a price of 10 cents or less per unit once production volumes enter the millions.

First user of this technology is a smart electronic credit card with a built-in battery and microprocessor that's ready to be trialled by 200000 users in the USA. The card manufacturer is called PrivaSys and their CEO, Joan Ziegler, states that tests will begin with these battery-powered cards shortly with several US credit and debit card issuers. The PIN-activated card protects card-holders' credit information through the use of secure card numbers that are created dynamically for each purchase. When consumers need to pay, they tap their personal identification number into the keypad.

A microprocessor chip on the card generates a random number that is visible on the liquid-crystal display and is also transmitted to the card's magnetic stripe. The one-time code is converted into the consumer's actual card number at the card issuer's mainframe computer after it has been swiped through the point-of-sale terminal and sent up line. This, says PrivaSys, entirely eliminates unauthorised cloning or 'skimming', one of the key methods of counterfeiting credit and debit cards. The number display allows card-holders to use the cards to make purchases by telephone or over the Internet.

New battery materials don't stop here either. Another thin and flexible – but intrinsically more expensive – battery technology uses lithium polymer and offers more power. One model, developed by a research team of the Korea Institute of Science and Technology, uses polyacrylonitrile as an electrolyte and is aimed at use in portable computers.

In the USA researchers at The Johns Hopkins University have developed a polymer-based all-plastic battery that is rechargeable and environmentally friendly. This has military and space applications but would also be cost-effective in small consumer devices, such as hearing aids and wristwatches.

In the longer term batteries may be commercialised using 'cages' of the pure carbon variant known as buckminsterfullerene ('buckyball') to contain lithium and fluorine or methanol. For these we may have to wait rather longer, however.

Contacts: http://www.powerpaper.com http://www.privasys.com/Prod_SecurSys.htm



Flat batteries allow smarter cards to be made that entirely eliminate unauthorised cloning or 'skimming' – one of the key methods of counterfeiting credit and debit cards.

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of the superhet Ian Poole has been investigating how today's most popular receiver topology - the superheterodyne – made its debut.

The invention

rom the very earliest days of radio, the two aspects of radio performance - sensitivity and selectivity - have been of paramount importance. Shortly after the beginning of the twentieth century when coherers and spark transmitters were in use, the lack of receiver sensitivity seriously limited the range over which transmissions could be made.

The limited selectivity also hampered performance. Indeed, after the Titanic sank, the number of stations transmitting and aiming to help caused congestion on the airwayes and actually impaired the rescue.

To overcome these problems, receiver performance needed to be greatly improved. This resulted in the invention of the superheterodyne or superhet receiver. The name is derived from the words supersonic - i.e. above the audible range - and heterodyne from the fact that two signals are mixed together.

The basic concept was invented during the First World War, and many basic broadcast sets today are little more complicated than the first superhet built, although most sets today are somewhat more sophisticated and



1934 Marconiphone



1936 Murphy A30

HISTORY



certainly have a far superior performance. Even so the basic concept is still the same.

Superhet principle

The superhet principle involves the use of a local oscillator to convert the incoming signals down to a fixed frequency, an intermediate frequency amplifier and a filter. Tuning the receiver is effected by changing the frequency of the local oscillator so that signals on different incoming frequencies are able to pass through the filter, Fig. 1.

The advantage of this technique is that a highperformance filter can be designed for a fixed frequency. Designing variable-frequency filters would be far more difficult and expensive and the level of performance would be far inferior.

Early ideas

The story of the superhet starts with some early experiments that were performed by an American Engineer named R. A. Fessenden. He thought that the sensitivity of receivers could be improved if the transmitter were made to send out a signal that would produce the desired signal in the receiver.

Fessenden proposed that one way this could be done was to transmit two signals close in frequency to one another. Once received and detected at the receiver, audible beat notes would be heard. He patented the idea on 28 September 1901. It is particularly important because this was the first recorded instance of using the beat-frequency phenomenon for wireless communication, Fig. 2.

Having formulated his first idea, Fessenden started to refine it. Transmitting two high-power signals to beat with one another in the receiver wasted power and required two transmitters. His next idea used a locally generated signal from the receiver to interact with the incoming signal to form the audible beat note.

Apart from being considerably more efficient this second method had the further advantage that it enabled the receiver operator to vary the frequency of the beat note at the receiver by simply changing the frequency of the locally generated signal, Fig. 3.

The idea was successfully used with a distance of over 3000 miles (5560km) achieved using the system. However





its efficiency was low as a result of the very low sensitivity of the non-polarised telephone receivers that had to be used. As a result the idea remained dormant for several years.

Renewed interest

The next time the idea of beats was renewed was in 1910 after some tests between two American cruisers. During the course of these experiments, one of the wireless operators noticed several unusual effects in the receiver when the transmitter on the ship was started up.

The beat note would have been at around 20kHz, well above the frequency response of any transducers and hence difficult to detect. Nevertheless, the resulting observations lead to a more detailed investigation of local oscillators and the effects of signals beating with one another.

The result of this investigation was a far more sensitive receiver. A rectifier and normal moving-coil telephone earpiece replaced the original earpieces and the design of the circuitry was optimised. Tuning was accomplished by varying the frequency of the local oscillator, which in these early receivers used a high-frequency alternator, Fig. 4.

In 1913, the heterodyne receiver was used in some more naval tests, but this time between Arlington Virginia and the warship *Salem*. Here the new receiver performed well, enabling communication to be established over distances up to 6400 miles.

Not only was the set more sensitive, but it also gave superior performance under conditions of interference, proving itself against magnetic detector and ticker receivers.



Valves

One of the major enabling factors in the development of the superhet was the invention and development of the valve, or tube.

Initially, the invention of a diode detector was made by Ambrose Fleming, professor of electrical engineering at University College London in 1904. This formed the platform for de Forest to develop the triode valve in the USA two years later. This was initially used only as a detector, but later it was discovered the device could amplify signals.

Naturally the performance of these early valves was very poor. Gain was limited and the frequency response restricted their use to a few hundred kilohertz.

Valves were used in many types of receiver, including direct-conversion receivers. Here they made the generation of the local-oscillator signal much easier. The first instance of a valve being used as an oscillator in this way was by Arco and Meissner of the Telefunken company in Germany in 1913.

Although valves were very expensive around this time they were less expensive than a high-frequency alternator and they were also much more compact. As a result a considerable amount of interest in their use was stimulated by their use in this application. A number of people including H. J. Round, an employee of the Marconi Company, experimented with their use as a source of a heterodyne signal for use in receivers.

Wartime impetus

The next major impetus for the development of radio technology arose from the hostilities in Europe during the First World War.

The advantages of wireless had been recognised by the military authorities on both sides of the divide and as a result many of the leaders in wireless development were looking to methods by which they could improve the technology. On the side of the Allies H. J. Round, M. Latour and later E. H. Armstrong were some of the leading lights in the developments being made, and for the Germans, there was W. Schottky.

All these scientists had a common goal – that of giving higher degrees of amplification and better selectivity. This proved to be very difficult between frequencies of 500 and 3000 kilocycles. These were considered to be very high frequencies.

Many of the problems arose from the limitations in the development of the valves of the day. Here inter-electrode capacitances meant that frequencies above about 500 kilocycles presented considerable difficulties when using valves. They lost gain and became very unstable. This set people thinking how these problems could be overcome.

One method was naturally to look at the root cause and investigate how the performance of valves could be improved. H. J. Round undertook much valuable work in this area. He developed a new valve type known as the V24 that had low levels of inter-electrode capacitance. He also introduced a number of other measures to improve receiver performance, and made some significant advances, but they did not provide the complete solution.

Accordingly others looked to means whereby greater improvements in performance could be gained.

Lucien Levy

The next major development was made by a French engineer named Lucien Levy. He was mainly interested in reducing the effects of interference and he developed an idea that he claimed would completely eliminate the effects of "parasites and ordinary interference". His method of reception used the superheterodyne principle and the patent covering his proposal was filed on 4 August 1917.

Levy's receiver had a first mixer where the signals were converted down in frequency. The output, or beats, were said to be 'ultra-acoustical' but sufficiently low in frequency that they could be easily tuned.

A second heterodyne converted the keyed carriers that were to be received into audible beat notes. By using this principle, Levy believed that the ultra-acoustical beats would be so far separated in frequency from the atmospheric disturbances and interfering stations that the wanted signal could be easily separated and interference would be completely eliminated, Fig. 5.

Although the receiver shown only has two valves in the signal path, Levy intended that further stages of amplification would be added in the intermediate frequency stages to boost the signal level. This would be tuned if necessary. The main difference between Levy's superhet and the principle used today is that the intermediate frequency stages were variable in frequency.

Armstrong's idea

When the American forces arrived in Europe to join the other forces in the struggle against Germany, they found that the receivers they possessed were not suitable for the high frequency work required.

Edwin Armstrong spent some time researching the problem and came up with a similar solution to that proposed by Levy. However, he proposed that a receiver would have a fixed-frequency intermediate stage instead of a variable-frequency one. One of the main advantages of this approach at the time was that it overcame the poor performance of valves at high frequencies. By using a low-frequency IF stage, the required level of gain could be achieved without the problems of instability that would be encountered at higher frequencies. Armstrong stated that incoming signals along with their modulation would be converted down to the lower frequency where they would be amplified and tuned and finally rectified in the normal way, Fig. 6.

Armstrong filed the patent for his idea on 30 December 1918 and it included the possibility of multiple conversions. It was over six months behind one filed in Germany by Schottky. Armstrong though had built and tested an eight-valve superhet. It consisted of a first detector or mixer, heterodyne oscillator, three stages of intermediate frequency amplification second detector and two stages of low frequency amplification.

Schottky had not built and tested his idea so Armstrong is generally credited with the invention of the superhet radio.

In abeyance

With the cessation of hostilities the need for the superhet wireless set reduced. Valves were very expensive and few wireless stations were active. Accordingly little was heard of the idea for a number of years.

Soon, many developments were made. Valves became cheaper. Indirectly-heated valves were introduced so that a common heated supply could be used and individual bias batteries were not required, making valves much cheaper to use.

More components such as dual-gang capacitors were developed. All of these combined with the increasing number of stations broadcasting meant that higher levels of performance were required. As a result the superhet started to be used for broadcast sets. This occurred during the late 1920s in the US and early 1930s in Europe where the growth in broadcasting was slower.

Now the superhet radio is used almost universally for broadcast and most other types of receiver, and it looks to remain in its position of dominance for very many years to come.

More information about radio and electronics history as well as today's technology can be found at: www.radio-electronics.com.

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Gang your own pots

Off-the-shelf, ganged potentiometers are rare and can be very expensive. Cyril Bateman has devised a method of ganging your own pots in multiples of two, three or even four gangs. uality double-ganged potentiometers needed to build a low-distortion test oscillator can prove difficult to source. Triple and quadruple ganged devices needed for a distortion analyser are almost impossible to locate anywhere.

One major supplier of precision potentiometers -B.I.Technologies – catalogues a variety of styles having a small diameter rear extension spindle. Perhaps this could be used to gang together the required number of controls.

The company's Web page provides a stock-finding facility which accesses both distributor and factory stocks. Unfortunately, despite repeated searches using this facility, I failed to find a single usable item listed as being in stock anywhere – world-wide.

For many years, the Alps factory has provided goodquality carbon-track linear and logarithmic controls, in 27mm and 40mm sizes. These are manufactured as single, double and quadruple-ganged assemblies. These used to be available, both motorised and non-motorised, from hi-fi kit specialists such as Hart Electronics.

Today these double-ganged devices can still be obtained from stockists, but usually only in a limited range and with logarithmic tapers. Double-ganged linear styles proved difficult to source and regardless of value or taper, I could not find any quadruple-ganged stocks.



Fig. 1. This semi-overhead view of my dual gang version, illustrates both types of outrigger bearing. Clearly shown is the possibility of reducing the depth needed for all three assemblies. Unwanted spindle length will be removed at final installation. For this assembly, the potentiometer spindles were spaced on 40.5mm centres. This spacing provided easy connections to my circuit board.

CONTROL & INSTRUMENTATION

Occasionally, good quality carbon track dual controls intended for audio amplifiers can be obtained These are matched to 1dB. But ganged multi-turn precision wirewound controls simply do not exist – unless you want to order a very large quantity.

The tracks of potentiometers matched to 1dB can vary considerably relative to each other as the control is rotated. While this tolerance is adequate for many audio purposes, better than 1dB matching is needed when building low-distortion oscillators and low-distortion notch filters.

Having wasted much time attempting to source the ganged components that I needed to build a new distortion analyser,¹ I decided to explore other means to provide suitable variable controls.

Ganging methods

I decided to mechanically gang together the required number of conventional multi-turn precision wirewound controls. These are standard distributor stock items and are available in many values. Ganging these together would also allow using mixed values as needed for, say, a twin-tee filter, as well as making identical multiples.

Like John Linsley-Hood,¹ I too recalled the special dial drive pulleys once used with spring tensioned wire belts as reduction tuning drives for receivers. However I was unable to find a supplier. On reflection, I don't think that they would be all that useful for ganging together multi-turn potentiometers.

Initial attempts to 'gear' together two controls using pulleys or gear wheels failed, so I sought another method. It had proved almost impossible to source suitable light-duty small pulleys, chains or gear wheels for shaft diameters larger than 3mm. Most suitable potentiometers come with either 6mm or 0.25in shafts.

After many catalogue searches I decided to use the smallest and lightest toothed belt and matching pulley drive wheels I could find. Such drives are noiseless at low speed. Being backlash free, it looked as though they would provide a possible, if slightly heavy duty, solution.

Implementing the idea

I mounted two ten-turn wire-wound potentiometers onto an experimental slotted aluminium face plate. Initial experiments ganging these together using 14-tooth pulleys and a 60XL025 belt obtained from RS worked well, but the idea needed refining. The potentiometer spindle reached only into the middle of this pulley wheel so some means to mount a control knob was needed.

Initially I tried simply extending one spindle by super gluing a short length into the pulley boss. This worked quite well, but still needed improving. The main problem now was that the slack in the potentiometer



Fig. 2. This triple gang version required care when marking out the drill centres for each potentiometer. Because of the 120° belt wrap-around, my original calculations resulted in too much tension in the belt. I found best results when the spindles were equi-spaced by 35.5mm, at the corners of an equilateral triangle.

spindle bearings did not ensure that both spindles remained parallel.* This allowed some 'run-out' of the belt on the pulley wheels. With the spindles glued or pinned into the pulley, brute force would be needed in order to replace a control.

Outrigger bearings would ensure the necessary spindle alignment. At some increase in the depth required for an assembly, commercial spindle extensions would then allow the potentiometers to be

*There's a chance that using Loctite as opposed Superglue might improve this situation as it tends to have a slight centring effect as it expands. If you try it, let the Loctite cure with the spindle vertical. If your drilling isn't too accurate, try drilling the holes half a millimetre or so further apart than they should be then file them out with a rat's tail until the belt's a snug fit. If you tend to get three-cornered holes when you drill sheet metal, try putting a small (25mm or so) square of rag over the position of the hole you're about to make so that the drill grabs the rag just before it starts to drill the hole. Any theories as to how this works? Ed.

Materials used

Pulleys Two gang belt Triple gang belt Quadruple gang belt Extension spindles type 56 Quality outrigger bearing Low cost outrigger bearing 14 tooth Martin 14XL037 obtained from RS as part number 350-8052. 60XL025 obtained from RS as part number 338-7524. 70XL025 obtained from RS as part number 338-7530. 80XL025 obtained from RS as part number 338-7546. obtained from Farnell as part number 101-603. obtained from Farnell as part number 321-345. obtained from Electrovalue as part number 12MMB.



Fig. 3. Needing little more space than for the triple gang version, this quad version with its spindles spaced by 33mm, is free from backlash yet rotates freely, with little effort. It could be used for the two-stage Linsley-Hood distortion meter design, or to make a tracking spectrum analyser/tracking generator assembly.

adjusted to synchronise their values, or be easily replaced when necessary.

To carry these outrigger bearings, a second identical mounting plate would be needed. Both plates would then be mechanically secured together using low cost 40mm long plastic spacers.

For this two-gang version, I used two identical mounting plates measuring 80mm by 25mm. The two potentiometer mounting holes were spaced at 40.5mm centres.

Ten-turn wirewounds are relatively expensive so I preferred to not shorten their spindles. These were all left standard for the photographs. However when ready for final installation, each assembly could be slightly foreshortened by cutting these spindles as required. This possibility is clearly visible in the photograph of my two gang assembly, Fig. 1.

While all three versions used the same 14-tooth pulley wheels, to minimise size, each needs a different length toothed belt. Being of US origin, these belts are specified by their circumference in tenths of one inch and each tooth is spaced by 0.2in. Hence the belt 60XL025 I used for the two-gang design comprises thirty teeth and has a width of 0.25in. Since each pulley contains 14 teeth, this is the shortest usable belt with these two pulleys.

As you can in this photograph, I demonstrate two

different outrigger bearings. The deeper one behind the knob is a very low cost, ± 0.07 die-cast moulding, suitable for intermittent use.

The second bearing, nearest the camera, is much more expensive, listing at ± 1.26 . Made by Radiatron it is a quality brass turned part, so more suited to continuous rotations.

Triple and quad gangs

Mounting plate blanks measuring 80mm by 50mm were used for both the triple and quadruple ganged versions. For the triple-ganged design I used a Gates 35-tooth belt, 70XL025. With three 14-tooth pulleys, the potentiometers were mounted on a pitch-circle diameter of 41mm. This results in the spindles being spaced by 35.5mm at the corners of an equilateral triangle. This triple-gang version was assembled using two 20k Ω potentiometers and one of 10k Ω for use in a twin-tee notch filter, Fig. 2.

The quadruple-ganged version is easier to produce than is the triple gang and takes up little extra space. Using a 40-tooth 80XL025 belt with four 14-tooth pulleys, the spindles were equally spaced at the corners of a 33mm square Fig. 3.

Pulleys with fewer teeth and shorter toothed belts are listed, but at the time I built my units, all were on very extended lead times.

Having determined the spacing and materials needed for these ganged pot assemblies has provided me with a very low-cost and instant solution for what had previously appeared to be an insoluble problem.

Reference

John Linsley-Hood, 'Measure thd to 0.001%', *Electronics World*, Feb.1998, pp.104-108.

Michael Slifkin and Shai Kriegman have been investigating three ways of measuring capacitance – one using phase shift, one using the error signal from a PLL and the third looking at a frequency change caused by the unknown capacitor.

Measuring capacitance

Reactance – i.e. capacitance and inductance – can be measured in various ways. Any change in the way that a circuit functions due to the addition of a reactance can be measured and used to determine the reactance.

It is preferable that such measurements have high resolution, good linearity and low noise. Linearity is not an absolute requirement; it can often be compensated for by adjusting readings. However it certainly makes life easier if measurements are linear.

There are many devices now on the market whose action is controlled or modified by reactance. Our aim here is to take some of these devices and see whether they can be used to measure capacitance.

One of us has already described a high-performance

inductance meter using modern integrated circuitry¹. This is certainly cheap compared to comparable commercially available meters, but not that cheap in absolute terms. Here we describe three methods of measuring capacitance. All use minimal chip count and are very cheap.

The first method consists of adding the capacitance to an LC resonant circuit and measuring the phase shift using a phase detector. The second consists of altering the free running frequency of an analogue phase lock loop and measuring the error signal between this oscillation and that of a fixed frequency input signal. In the third method, the capacitance is used to shift the frequency of an oscillator i.e. to obtain frequency modulation and the modulation is measured with an FM demodulator.

Michael and Shai are with the Jerusalem College of Technology



TEST AND MEASUREMENT



has a maximum at some frequency so that the signal of that frequency will pass through with minimum attenuation and zero change of phase. Adding a small reactance changes the maximum of the filter and so causes some attenuation of the original signal and more importantly a change of

Data sheet sources

A data sheet for the MC1496 can be downloaded from:

http://www.onsemi.com/pub/Collateral/MC1496-D.PDF=20

The web address for the NE564 is:

http://xanadu.ece.ucsb.edu/~long/ece145b/NE564.pdf=20

A high-quality fixed-frequency oscillator is needed for the first two of these measurements. You could use a laboratory signal generator, but we built a crystal-controlled 16MHz oscillator for used in two of the methods.

Figure 1 shows the circuit diagram of that oscillator. Its frequency has been chosen as a compromise between high frequency for high accuracy and simplicity of construction. A higher frequency oscillator would have made construction more complicated. Additionally we had the crystal to hand, which was probably the most pressing reason for choosing it.

Resistor R_1 in the circuit limits the amplitude it can be adjusted to give more or less output but in any event one should not allow a current greater than about 1mA to flow.

Method 1

The phase shift method works as follows. A simple LC filter will have a maximum at some frequency so that the signal of that frequency will pass through with minimum attenuation and zero change of phase. Adding a small reactance changes

the maximum of the filter and so causes some attenuation of the original signal and more importantly a change of phase. This is illustrated in Fig. 2.

For small phase changes, the attenuation will be very small as the top of the response curve of this simple filter is almost flat. Hence an accurate measurement of the phase gives a measure of the capacitance.

It is possible to show mathematically what ought to happen. The resonant frequency f_0 is given by:

$$f_0 = \frac{1}{2\pi\sqrt{LC}}$$

A change of capacitance dC gives a change of frequency:

$$f = \mathrm{d}C \times \frac{J_0}{2C}$$

and the change in phase $d\phi$ is given by;

dφ $= -\omega^2 L Q$ dC

Here, ω is $2\pi f$ and Q is the Q-factor of the coil $\omega L \div r$. Variable r represents the internal resistance of the coil.

The phase change is linear with C for small changes in phase and increase in amplitude with increasing frequency and increasing Q-factor. This is why we picked 16MHz for our test frequency. Interestingly enough most commercial instruments use a lower frequency of 1MHz.

We proceeded to measure the phase using a simple phase detector. This is an analogue multiplier or mixer followed by a low-pass filter. Figure 3 shows the block diagram. If there are two signals:

$$V = A \times \cos(\omega t + d)$$

and:

$$V_2 = A \times \sin(\omega t + \Phi_2)$$

then the product:

$$V_1 \times V_2 = \frac{A^2}{4} \times \cos(2\omega t + \Phi_1 + \Phi_2) + \frac{A^2}{4} \times \sin(\Phi_1 \pm \Phi_2)$$

(Apologies, the \pm in the above equation should be a –). The first term is at twice the frequency of the test signal and can be removed with a low-pass filter. The second term is DC and proportional to the sine of the phase shift. For small angles the sine equals the angle in radians. So that we now have a voltage from the multiplier proportional to the phase shift and hence to the capacitance but only for small phase shifts.

The circuit diagram is shown in Fig. 4. C_9 in the LC circuit gives the 90° phase shift. Capacitor C_{11} is a trimmer that's adjusted to give the resonant condition. The phase detector



TEST AND MEASUREMENT





TEST AND MEASUREMENT

doing this but as we already have the PLL, we can use this as the demodulator. In this case we have a fixed capacitor tuning the PLL to the frequency of the oscillator. When the signal frequency is altered by the sample capacitance there is an error signal from the PLL. This error signal should be proportional to the shift in frequency and, if the frequency shift is small, linear with capacitance.

Figure 10 shows the circuit. We have provided it with a power supply. Actually it is the same one we used with the 16MHz crystal oscillator. You could of course use batteries if you don't have the components readily to hand.

There is a buffer after the oscillator to prevent pulling. The demodulator section is just the PLL with a fixed capacitor to make the free running frequency equal to that of the oscillator.

As Fig. 11 shows, the output is quite linear. It is possible to encompass a greater range with the PLL in this mode than in method 2. Bear in mind though that method 2 uses fewer components and chips. Any form of FM demodulator could be used here, but the one shown is probably the simplest.

In summary

We have barely scratched the surface of this topic. There's a plethora of ICs performing all sorts of functions that are reactance dependent. In addition, method 3 lends itself to all kinds of possibilities. You could of course simply measure the new frequency directly and use a look up table.

A method that we are currently developing and hope to publish soon consists of two identical oscillator circuits. One is detuned with the test capacitance. We measure the beat frequency between the two and get the capacitance directly from this. This is particularly suitable for very low values of capacitance.

Reference

 Michael Slifkin and Alexander Gornstein, 'A novel inductance meter', *Electronics World* May 2000, p. 424-428.





Direction finder using VHF

Ian Hickman's final article about a simple direction-finding system describes the RSSI demultiplexer and other circuitry not covered in the two earlier articles. He also provides notes on putting the system together.

his is the third and final article in a set that describes a direction finding system. The system locates the orientation of VHF broadcast transmitters relative to where you are, as a means of determining your position. Here I will be discussing the commutation oscillator, the demultiplexer switch, the leaky integrators and the audio section of the direction finder.

Figure 1 shows the complete circuit, with the section relevant to this article highlighted in yellow. The audio department is very straightforward. It simply accepts the audio output from pin 6 of the CA3089 IF subsystem IC via a 50μ s de-emphasis time constant produced by a *CR* network.

The two previous articles...

My first article on this topic, in the January issue, introduced the direction-finding concept and discussed the antenna and splitter block. The second article dealt with the sigma blocks, the multiplexer, the local oscillator, the mixer, the IF strip and the quadrature coil.

The audio signal feeds an an LM380 audio amplifier IC via a volume control. From there, it passes to a a 35Ω loudspeaker via a 100μ F electrolytic capacitor. Note that in place of the CA3089, used as it was in stock, the more recent pin-compatible CA3189 should prove entirely suitable, offering improved performance.

RSSI processing

At the heart of the system is the processing of the received signal-strength indicator, or RSSI, output at pin 13 of the CA3089. This is used to extract information as to the bearing of the station being received.

Information extraction is performed under the control of the multiplexer clock generator, which uses two of the four sections of a TL084 quad op-amp, running on $\pm 15V$ stabilised supplies. Consequently, when the output of the right hand op-amp is at $\pm 15V$, the voltage at its noninverting input is $\pm 5V$.

Via the $180k\Omega$ resistor, the 100nF capacitor at the inverting input charges towards +5V. As it reaches this level, the output starts to fall and positive feedback to the non-inverting terminal will drive the output to -15V. The $180k\Omega$ resistor now charges the capacitor towards -5V, the





output switching back to +15V as soon as the noninverting input reaches -5V, and the cycle repeats.

As the $\pm 15V$ square wave at the output is larger than needed, another op-amp section, connected as a high input impedance unity gain buffer, picks off the ±5V square wave at the non-inverting input.

The 1N4148 diode blocks the negative-going phase, resulting in a clock waveform swinging between OV and +5V. This is applied to the DG201 quad CMOS switch. The clock drive at pin 8 of the DG201 produces an antiphase clock at pin16. Thus the RSSI output is connected to each of the two leaky integrators in turn via switches on alternate half cycles of the unity mark:space ratio multiplexer clock. Switch pins 2 and 15 pass the alternating X and Y signals respectively to the leaky integrator.

As I explained earlier, the amplitude of the RSSI voltage will vary at the clock rate of the multiplexer clock generator, unless of course the signal arrives at each of the two antennas in exactly the same phase. If the two are in phase, then the output voltages of the two leaky integrators would sit at the same negative voltage, were it not for the $470k\Omega$ resistor to +5V at the inverting input of the lower integrator

The $470k\Omega$ resistor results in a -0.5V offset at its output, relative to the output of the upper leaky integrator. Hence the 1mA full-scale-deflection meter reads half scale when the RSSI output is the same on both phases of the multiplexer clock. This means that a normal 1mA FSD moving coil meter can be used; a centre-zero meter is not required.

The time constant of the leaky integrators is long compared to the period of the multiplexer clock, so that the outputs are ripple free and a steady meter reading results. If however the signal being received arrives from an offcentre direction, the phases at the two antennas will differ. This is converted to an amplitude difference, as described earlier. Current through the meter will therefore be less than or more than 0.5mA, and the pointer will move to left or right accordingly.

Implementing the direction finder

The two antennas I used were electrically-short monopoles, about 15cm long. This means that they were around a twentieth of a wavelength at the operating frequency in the VHF FM 88-108MHz broadcast band, as against the 75cm length of a resonant quarter-wave whip.

Each antenna was mounted above a 30cm square sheet of

single-sided copper-clad FGRP board. The system was constructed from four such sheets, as shown in Fig. 2. Three were secured, side by side, to two wooden battens. The fourth board was halved, and used as feet, one at each end

Each foot was sweated to the copper side of one of the outer boards; the solder fillet is detailed in the inset of Fig. 2. Two tin-plate gusset pieces, similarly sweated in place, were used for bracing. The edges of the copper on the centremost sheet were connected to the adjacent sheets with soldered wire links every 3cm, providing an effective ground plane of 30 × 90cm.

Two 1mm sockets were mounted on the centreline, separated by 45cm, or 60%, of a quarter of a wavelength. The 18SWG TCu antennas were mounted on 1mm-pin plugs, making the antennas demountable for transport.

The sockets carry the signal through to the copper-faced underside of the ground plane, where all the circuitry is mounted. The circuitry of each input stage, see Figure 1 on page 64 of the January issue, was mounted adjacent to its antenna socket.

Components with one lead grounded, such as decoupling capacitors, resistors, etc., were soldered directly to the ground plane, and their other ends used as mounting points for the other components; so-called 'fresh air' construction.

Mounting remaining circuitry

The remaining circuit blocks were built up as separate entities and tested, before being mounted on the copperfaced underside of the ground plane.

Non-RF sections, like the multiplexer clock oscillator, were constructed by groups of students on 0.1in-matrix copper-strip board, other such units being the audio output stage and the leaky integrators plus RSSI demultiplexing switches.

Another solution is to use fresh air construction to build the RF sections, the local oscillator and its associated output amplifier IC on a 5cm square piece of copper-clad. When tested and working, this can be secured to the centre of the ground plane with multiple wire straps round the edge.

The MAX4313 RF multiplexer, with the mixer and its output amplifier formed a unit. This unit's 51Ω terminated inputs were fed via miniature 50Ω coax leads from the outputs of the two Σ hybrids at the antenna circuits. The IF filter together with the CA3089 and its associated components formed a third RF unit, likewise mounted with multiple ground straps.

The meter was mounted in the position shown in Fig. 3, and the tuning and volume controls on a small subpanel below the meter on the underside of the ground plane.

Setting up

The tuning range of the local oscillator should be checked to ensure that it covers the VHF FM 88-108MHz broadcast band. If a spectrum analyser is not available, an FM receiver can be used.

The local oscillator, with a few centimetres of wire tacked onto the output of its amplifier, should provide marked quieting of background noise of a nearby receiver when tuned in.

As the oscillator is required to run 10.7MHz higher than a station being received, this should apply for receiver settings of 98.7MHz to the top of the band, with some further range on the $100k\Omega$ tuning potentiometer still in hand. Adjust the tuning slug in the 6.9mm diameter former of four turn oscillator coil to achieve this.

Assuming all the other units have been verified

beforehand, there just remains the final alignment. Tune a strong local station, and adjust each of the antenna trimmers for maximum readings at the outputs of the leaky integrators.

Finally, the system should be aligned with the major axis of the ground plane at right angles to the known position of the station, the latter being on the side of the kit away from the meter.

Note that this setting up must be done in a large open space; a park or sports field, preferably not surrounded by high buildings, should be chosen. If it isn't, multipath propagation via reflections may vitiate the proceedings. A slight adjustment to one of the antenna circuit trimmers should now result in the meter pointer sitting centre scale.

The equipment should now be rotated 45° to the left. If the pointer moves to the right, i.e. tends to remain pointing at the source of the signal, all is well. If it moves to the left, interchange the X and Y outputs to the leaky integrators.

An alternative procedure, which may be more convenient, is to provide your own 'local station', using a signal generator, although a clear open space is still needed. Set the signal generator output to maximum at, say, 100MHz. Lay it on its back, fit a quarter wavelength – 0.75m – of stiff wire sticking up from the output socket, and position it about three metres away from the directionfinding equipment. Then proceed as above.

Using the equipment

To find the bearing of a transmission that's being received, the equipment should be rotated in the



Fig. 3. The completed direction finder viewed from above, showing the two antennas as dots in the centre of the two outer panels, and the meter.

horizontal plane, until the meter reads centre scale. On rotating the kit somewhat to the left or right, the pointer should move to the right or left respectively. If it moves the other way, the source is behind you, so an about face is called for.

When the pointer is centre scale it indicates the bearing of the source, which can be plotted on a map as a line through your map position. Another bearing, taken from a different place, should give you a cross bearing, indicating the approximate map coordinates of the source.

Naturally, if the bearings differ but little, the result will not be very reliable. A second site giving a bearing at right angles to the first provides the greatest accuracy. But always bear in mind that reflections from large buildings, etc., can influence your readings.

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Electronic lock with extra security features

T wo push-buttons enhance the security and simplify construction of this electronic lock. One determines the bit value, 0 or 1, while a second generates one bounce free clock pulse per push. This clock pulse advances the code stream in a dual, four-bit register.

An identical match with the program switches will drive the output positive to V_{DD} for three seconds. After that, a reset occurs. The supply voltage may range from 5V to 15V.

Exclusive-OR gates compare the word

entered against bit-level switch settings. A mismatch means that one or more gate outputs will be high, causing current to be drawn to ground, via 4047 pin 14 and Tr_1 's base. Thus Tr_1 holds off Tr_2 , the output driver.

All bits the same demands no base current and Tr_2 is free to turn on via the 18k Ω sink path. Jumper 'A' alone activates the first four bits. Jumper 'B' activates the last four. Both jumpers in place require all eight to match. Remove the jumpers to force a continuous output. This bypasses the lock.

Transistor Tr_2 can easily supply 10mA for a load compatible with the chosen supply voltage; a reed relay or opto coupler would give power amplification as required by a solenoid actuated mechanical latch.

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Constant-current diode look-alike

C onstant-current sources are widely used in electronics. Most are of the diode/transistor or transistor/transistor three-terminal variety.

Constant-current diodes are two-terminal devices, ideal drop-in substitutes for resistors in many instances. However, they suffer from relatively poor compliance, limited power dissipation – 300mW typical – small range of currents available (15mA max.), and only moderate voltage withstand (100V max.) when compared with the less convenient three-terminal current sources constructed from discrete components.

The two-terminal circuit shown here is composed of a pair of complementary constant-current sources, where each provides a constant current for the voltage reference of the other, thereby improving performance. Compliance is excellent and the current can be set to almost any value.

Power dissipation and voltage withstand are limited only by the transistors chosen. The constant current can be calculated from the forward voltage of the LEDs, V_{F} ; V_{BE} for Tr_1 and Tr_2 ; and the resistor values $R=R_1=R_2$. In the example:

 $I_{CONST} = 2\frac{V_F - V_{BE}}{240} = 2\frac{1.6 - 0.6}{240} = 8.33mA$

Power-failure safety interlock

M ost of the costly analytical instruments used in laboratories, like mass spectrometers and gas chromatography instruments, are designed to be run continuously. This is because of their requirement for extreme hardware stabilisation and sustaining a high vacuum by constant operation of ion pumps.

Once shut off, such instruments take days to regain full operating efficiency. If restarted immediately following shut off, serious damage their sensitive systems can result.

Commercial modules are available for incorporation between a interruptible power supply unit and such instruments. Their function is to disconnect the instrument from the supply in case of power failure. After restoration of mains supply the instrument is held disconnected until an operator resets the module whenever it is considered that the conditions suitable for the particular instrument.

The circuit here presented consists of a relay and a push switch. The rating of the selected relay and switch depends on the power consumption of the instrument to be protected. When the power is present, pressing the push switch will energise the relay, and the instrument is powered up. When the power fails and then restores, the relay remains de-energised until the operator

The circuit can be made up on a tiny piece of stripboard using the layout shown, and makes an excellent replacement for current setting resistors in many audio power amplifiers.

David N J White Bearsden Glasgow





Two-terminal constant-current diode equivalent.

pushes the button again.

This simple idea is a useful and cheap alternative to costly commercial modules performing the same function. **Ejaz Rehman Islamabad Pakistan** F94



Battery voltage low/high indicator

n the simple battery discharge/overcharge indicator shown, when $V_{b.} > V_n$, red LED D_3 flashes because of the high voltage level on the output of IC_{1b} and pulses on output of IC_{1a} . When $V_b = V_n$, green LED D_2 lights because of the high voltage level on IC_{1a} output and low voltage level on IC_{1b} output. When $V_{b.} < V_n$ green LED D_2 flashes because of the low voltage level on the output IC_{1b} and pulses on output IC_{1a} .

For my application V_n (normal) was defined as from 11V to 12.5V, with nearly 4.5 V on IC_{1a} input (when the voltage on IC_{1b} output is low) and nearly 5.9V on IC_{1b} input.

If an audible discharge-overcharge alarm is needed, an RC-oscillator driven by the pulses from IC_{1c} output may be used, as in the figure. **Vasiliy D Borodai** Zaporozhje

Ukraine



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ABC Mini 'Hotchip' Board



Currently learning about microcontrollers? Need to do something more than flash a LED or sound a buzzer? The ABC Mini 'Hotchip' Board is based on Atmel's AVR 8535 RISC technology and will interest both the beginner and expert alike. Beginners will find that they can write and test a simple program, using the BASIC programming language, within an hour or two of

connecting it up. Experts will like the power and flexibility of the Atmel microcontroller, as well as the east with which the little Hot Chip board can be "designed-in" to a project. The ABC Mini Board 'Starter Pack' includes just about everything you need to get up and experimenting right away. On the hardware side, there's a pre-assembled micro controller PC board with both parallel and serial cables for connection to your PC. Windows software included on CD-ROM features an Assembler, BASIC compiler and in-system programmer The pre-assembled boards only are also available separately.

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Serial Port Isolated I/O Controller

Unit provides eight relay outputs and four optically isolated digital inputs. Can be used in a variety of control and sensing applications including load switching, external switch input sensing, contact closure and external voltage sensing. Programmed via a computer



serial port, it is compatible with ANY computer & operating system. After programming, PC can be disconnected. Serial cable can be up to 35m long, allowing 'remote' control. User can easily write batch file programs to control the kit using simple text commands. NO special software required - uses any terminal emulator program (built into Windows). Screw terminal block connections. All components provided including a plastic case with pre-punched and silk screened front/rear panels to give a professional and attractive finish (see photo).

Order Ref	Description	nc. VAT ea
3108KT	Seriai Port Isolated I/O Controller Kit	£54.95
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Full details of these items and over 200 other projects can be found at www.QuasarElectronics.com



Programmable staircase waveforms

aboratory experimentation often needs staircase like waveforms in addition to the standard signals like sine wave, step, pulse, square wave, triangular and saw tooth waveforms, which are normally available from standard signal generators. This idea produces staircase waveforms with adjustable steps and frequency.

Figure 1 shows a circuit that provides n-1 steps in the up direction, where n is an external input whose value is between 2 and 15 for the 4-bit up counter. When the state reaches the given data of 'n' the counter resets producing the 0000 state.

The D-to-A converter produces the analogue equivalent of the counter data, giving the staircase signal. A variant using a down counter with feedback to the preset rather than the clear input produces a downward staircase signal. When the counter reaches the data of 'n' the counter presets producing the 1111_2 state.

A triangular-like staircase signal producing 15 up steps and down steps is generated by the circuit shown in Fig. 2. An up/down counter is used. Its up and down controls are generated from a bistable device, where PR and CLR asynchronous inputs are derived from the bottom (0000) and top (1111_2) states. The triangular staircase is available at the D-to-A converter output. Initially, when the state is 0000, OR gate output is '0' and NAND gate output is '1', providing an UP control input to the up/down counter. Subsequently both the PR and CLR are '1' until the count reaches the 1111₂ state. When it reaches 1111₂, the CLR input to the bistable device is '0' making the 'down' control input active for the counter. Output from the D-to-A converter is a triangular-shaped staircase wave.

Figure 3 shows the circuit for adjusting the number of up steps and down steps. The four most-significant bits of two analogue to digital converters control the maximum and the minimum states of the up/down counter. This is achieved through manual potentiometer adjustment; changing the analogue inputs to the Ato-D converters controls the limits.

The frequency may also be adjusted by feeding the clock through a programmable divider, which can be controlled by a potentiometer and D-to-A converter arrangement, similar to that shown for controlling the number of steps. *K* Balasubramanian

Mersin Turkey

F54


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Stepper-motor drive sequencer with half stepping

n Fig. 1, the stepper motor half-step drive uses an astable multivibrator made from a 555. Output from the astable drives two shift registers and, via an inverter, a decade counter. Outputs from the decade counter drive a decoder. Switch R is a common reset for shift registers, decade counter and astable.

When clocked, shift registers 1 and 2 produce a wave-drive sequence for a stepper motor as shown in the table in Fig. 1. Shift register 2 outputs go to four two-input NOR gates in cyclic order, as in Fig. 2.

shown in the table in Fig. 2. The first shift register and NOR outputs of Fig. 2 with decoder outputs are connected to inverters, AND and OR gates as in Fig. 3. Signals P1, P2, P3, P4 are the half step drive sequence as shown in the table in Fig. 3.

Fig. 4. Thus the half step drive sequence which is the hybrid of

At the count of 9 of the decade counter it is reset automatically and the sequence starts afresh. Figure 5 shows the motor's windings. V Gopalakrishnan



India F75

Q'A Q"A Q'B Outputs from the NOR gates are the two phase drive sequence as Q'B Q"B Q'C Wave Two phase drive drive Each P output drives one of four power amplifiers as shown in Q'C Q"C wave drive and two phase drive is achieved. Q'D Switch SW selects the direction of rotation of the stepper motor. Fig. 2. Two phase drive. Q'D Q"D +5 Q'A (F75a) (F75b) 100k 7432 , 7402 555 ₹100k Α Q'B Q'C Q'D SI Q'A 7404 Clk 74194 S .01µ 100µ (Shift 1) +5V OCI B A BCD ≶1k Fig. 1. 0 0 Wave drive SW circuit for stepper 7432 motor. 7402 Q'A Q'B Q'C Q'D S. MR MS Cłk CPo 74194 S 7490 (Shift 2) Decade counter OCIr CP. ABCDL R DC B A Wave drive sequence: QD QC QB QA 0 0 0 1 0 0 1 0 D' C' B' A' 0 0 1 0 0 1 0 0 7442 1 of 10 decoder 08 02 01 °3°4°5 °6 07 To Fig.3

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NEWPRODUCTS Please quote Electronics World when seeking further information

2.0mm pitch connector has small footprint

A range of 2.0mm pitch Minitek connectors from FCI is claimed to take 38 per cent less board space than 2.54mm alternatives, said the supplier. Shrouded headers with up to 2 x 25 positions have been added, and feature a four-wall construction that is designed to avoid receptacle mis-mating. Top entry surface mount receptacles with up to 50 positions have also been



introduced, and provide a choice of low (2.3mm) and standard (4.5mm) profiles. For a cleaner solder process, headers and the SMT low profile receptacles have become selectively plated, with either 0.76μ m gold or gold flash on contacts and 2.6μ m tin/lead on tails. The PCB receptacles have phosphor bronze contacts. Headers and receptacles range in size from 2 x 2 to 2 x 25 positions. FCl

Tel: 0033 139 492 082 www.fciconnect.com

12-bit ADC for extended temperature applications

The 12-bit WM2152 analogueto-digital converter from Wolfson Microelectronics delivers 30Msample/s conversion performance over an extended temperature range. The device has a typical power consumption of 160mW. Onchip features include internal voltage reference to minimise external circuitry, a programmable gain amplifier (PGA), and out of range indicator flag. An external voltage reference can also be selected, for applications where a common system reference or precision reference is required. The differential sample and hold input enables high common mode noise immunity and low distortion, and the PGA supports 1V and 2V maximum differential input modes plus a third mode for single-ended input signals such as composite video. The PGA modes are selected via two external mode selection pins. Conversion is controlled by a single clock input, and an output enable pin is provided allowing several devices to share a common bus. Wolfson Microelectronics Tel: 0131 667 9386 www.wolfson.co.uk

3W DC-to-DC in small package

HiTek Power has introduced a range of miniature high voltage DC/DC converters to supply in the kilovolt range. These devices can be directly mounted on the PCB, and are potted to provide a unit that can be used in equipment with a 12V supply, whether fixed or battery operated. The units are priced at under £50. The series A range comprises five models that operate from a nominal 12V input, producing output voltages of 1, 1.5, 2, 3, or 4kV. They accept input voltages ranging from 5 to 14V, with an output proportional to the input voltage. Regulation (half load to full load) is 5 per cent, and output ripple is less than 0.5 per cent. Low noise models are available, which reduce ripple by a further 50 per cent. The devices are rated at 3W and are housed in a package measuring 38 x 66 x

Microcontroller with integrated 433MHz radio

Microchip's rfPIC microcontroller family includes the 18-pin rfPIC12C5O9AG which features an integrated 315/433MHz ASK transmitter, and the 20-pin rfPIC12C5O9AF which features an integrated 315/433MHz FSK/ASK transmitter. They are the first in a range of devices aimed at RF interface design in embedded control applications, such as remote sensing, remote control, toys, security and access control. The onchip 315/433MHz transmitter enables designs to conform to US FCC Part 15 regulations and European ERC 70-03E and EN 300 220-1 requirements. It features a VCO phase-locked to a quartz crystal reference, which allows narrow receiver bandwidth to maximise range and interference immunity. Both devices contain 1024 words of program memory with 41 bytes of user RAM. They offer six I/O pins with on-chip clock oscillator, 33 singleword instructions, full-speed lus instruction cycle at 4MHz, seven special function hardware registers, two-level deep hardware stack, 8-bit real time clock/counter with 8-bit programmable

prescaler, watchdog timer and direct LED drive. They feature in-circuit serial programming which allows the devices to be programmed after being placed in a circuit board. The rfPIC devices are supported by the standard range of PICmicro development systems including the MPLAB ICE 2000 universal in-circuit emulator. Microchip Tel: 0118 921 5858 www.microchip.com



NEWPRODUCTS

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19mm. They are designed to be operated from a 12V source of either polarity. Input/output isolation of 2kV allows the user to have an output voltage of either polarity as well as being able to ground the output voltage at a remote point with the lower voltage converters. The units are protected against overvoltage, reversed input polarities and shorts or opens across the output terminals. *HiTek Power*

Tel: 01243 841888 www.hitekp.com

TSOP-6 packaged dual Mosfet

International Rectifier has introduced the IRF5810 dual Mosfet in a TSOP-6 package which combines two P-channel HEXFET power Mosfets into a single 1.3 x 2.9mm footprint. Typical applications include load and battery management circuits in cell phones, PDAs, MP3 players, notebook computers and PCMCIA cards. The device has



40 per cent lower device onresistance than competing dual Mosfets in the TSOP-6 package, claims the company. Each Mosfet within the package performs at $90m\Omega R_{DS(on)}$ each. An extended lead frame maximises the size of each die in the TSOP-6 outline. The lead frame increases current capability and on-state efficiency, yet fits in the standard TSOP-6 footprint, said the supplier. A single IRF5810 can replace two TSOP-6 devices in load management circuits, or a much larger TSSOP-8 packaged dual Mosfet in battery management circuits International Rectifier Tel: 020 8645 8003 www.irf.com

Power PC 7400/G4 optimised DSP library

Transtech DSP has released a Clibrary optimised for the PowerPC 7400/G4 processor and fully compliant with the VSIPL (Vector Signal and Image Processing Library) openstandard. The library APIs have been defined by the VSIPL forum, a group of cross-industry users working to define an open standard and portable library. VSIPL is suitable for applications in both the defence and commercial arena. The library is based on the 'core' VSIPL profile and includes over 500 functions, such as vector & scalar arithmetic, Fast Fourier Transforms (FFTs), window

functions, filters, matrix operations, system solvers, logical operations, and OR decomposition. The PowerPC AltiVec processors support multi-media and signal processing through the use of a SIMD vector unit. The VSIPL library has been optimised to tap into this to accelerate these applications. Designed to work with the VO750 and VOG4 quad Power VME cards, the library has also been optimised to use the PowerPC processor's cache benefits such as transparently decomposing the algorithms to run in L1 cache. Transtech DSP Tel: 01494 464432 www.transtech-dsp.com

MPEG-2 core for FPGA implements video decode

Amphion Semiconductor has launched what it claims to be the first application-specific core for MPEG-2 video decode in a single high-density programmable logic device. The Amphion CS6651 MPEG-2 video decoder is an intellectualproperty (IP) core for decompression of MPEG-2 main profile at main level and MPEGl encoded video streams for implementation in a single FPGA device. The CS6651 incorporates memory controller and display DMA, and maps into single devices in the Altera APEX20K SRAM PLD family

and the Xilinx Virtex series FPGA family. Amphion Semiconductor Tel: 028 9050 4040 www.amphion.com



1.3MHz switching regulator gets design tools

National Semiconductor has announced a switching regulator with its own design tools. The LM2698 uses current-mode control (without the need for a sense resistor) and pulse-width modulation (PWM). There is a 0.2Ω 1.3A (guaranteed) internal switch and 1.3MHz clock. Features include 2.2V to 12V input voltage range, user selectable 0.6 or 1.3MHz clock and filtered output. National Semiconductor Tel: 0870 240 2171 www.national.com

Miniature power relay has Cd-free contacts

Featuring environmentally-friendly cadmium-free contacts. Schrack PB miniature power relays from Tyco Electronics target applications in white goods, small home appliances and heating temperature controllers. The relays have been designed to cater for similar applications in the widely used "sugar cube" type format. They need a PCB area of 225mm², compared to the area requirements of the "sugar cube" type relays of approximately 370mm² being 15×15 mm square and 20mm tall. Configured with a single 1 C/O (Normally Closed or Open) or 1 N/O (Normally Open) contact, these relays feature creepage and clearance to VDB 0435 and VDE 0700. Contact ratings are 10A N/O and 3A N/C, with a 15A inrush capability. Maximum switching voltage is 250VA or 100V DC while maximum breaking AC capacity comes in at 2500VA. Coil versions available are 6V, 12V and 24V DC. Normal coil power is 360mW. A Class F coil is also available. Tyco Electronics Tel: 020 8954 2356

www.tycoelectronics.com



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Bluetooth qualification tool

Nohau UK has been made UK distributor for Telelogic's PreOual software, designed for Bluetooth qualification test suites to be executed by the developer. Qualification requires Bluetooth products to pass a series of tests defined by the **Bluetooth Qualification** Program. Manufacturers demonstrate interoperability and compliance to the Bluetooth specification as required by the Adopter's agreement. Telelogic's PreOual includes the executable test suites required for Bluetooth certification. Nohau UK Tel: 01962 733140

www.nohau.co.uk

3W surface-mount wirewound resistor

A moulded wirewound resistor from Vishay Intertechnology offers a 3W power rating at 70°C, and derates linearly to 275°C. The WSC6927 extends the derating temperature by as much as 77 per cent compared to competing devices currently on the market, which results in higher power operation at elevated temperatures, said the company. The resistor is available in a resistance range of 0.1Ω to 2.6k Ω , and features tolerance options of ±0.5 per cent, ± 1 per cent, and ± 5 per cent. The resistor measures 17.53 × 6.98mm, with a 7.11mm height profile. As a surface mount device, it can be used to

replace 3W axial leaded components, enabling automatic, highspeed assembly. The resistor also features compliant terminations that eliminate the risk of solder fillet cracking. Vishay Intertechnology Tel: 001 610 251 5287 www.vishay.com

Elevated interface for 25mm board spacing

Samtec is offering an expanded range of micro pitch interfaces for matched impedance and high speed applications that require elevated designs with board-toboard spacings greater than the 16mm usually required for clearing common heat sink profiles. Samtec's Q-UP high speed interfaces are available with 19mm, 22mm, 25mm and 30mm board spacings. These are used where increased ventilation and air circulation is required. Full surface mount designs are available on 0.8mm pitch (QTE Series) and 0.5mm pitch (OTH Series), while mixed technology design on 0.635mm pitch (MIT Series) with through-hole ground plane and surface mount signal pins is available with 18.75mm and 22mm stack heights. Samtec

Tel: 01236 739292 www.samtec.com

GPRS mobile parametric test set in one box

Agilent Technologies is targeting GPRS appliance design with a one box test system for para-



600V MOSFET relays

Optical isolation provides a rated dielectric strength of 5kV RMS between I/O terminals for Mosfet relays from Omron. With 50mA LED forward current at the input, the G3VM-601xY caters for 600V loads with output loss of 454mW. Designed for switching analogue signals, the six-terminal relay provides 100mA current for AC loads and up to 200mA for DC loads. Standard resistance with the output turned on ranges from 22Ω with AC connection, down to 8.5Ω with DC connection, while leakage current is 10µA when the relay is closed. Turn-on time is 1.5ms maximum, 0.5ms typical, while turn-off occurs in less than 1ms. The device has a single-pole, single-throw, normally-open switching format. Omron Tel: 020 8450 4646

www.omron.co.uk

metric and signalling design validation The E6701A GPRS option for its 8960 series 10 (E5515C) wireless communication test set retains the measurement and call processing capability of the El964A GPRS mobile test application. The test set's protocol logging capabilities facilitate testing and troubleshooting of new signalling features and data channels, so engineers can extract better information about their design. Users of the E6701A can set timing advance and cell parameters, which allows for testing and troubleshooting issues related to timing and base station configuration. The company has also introduced ESG vector signal generator for testing 2.5G, 3G and broadband wireless communications systems and components. It is available with measurement frequency ranges of 250kHz to 1, 2, 3, 4, or 6GHz. Agilent Technologies Tel: 07004 666 666 www.agilent.com

Integrated T1/E1 socket offers protection

Pulse's Pulsejack family of RJ-45 connectors combines isolation transformers and line side protection into a T1/E1 single socket, offering integrated



magnetics and telephone network voltages (TNV) circuit protection. The connectors require no separate circuit transceiver, leading to improved electrical performance and space saving. Contech Research has independently verified the RJ-45 family meets all relevant surge immunity and electrical safety standards, including Bellcore GR-1089, FCC Part 68 and UL 1949/1950. Improved electrical performance can be achieved by minimising the electrical trace length between the RJ-45 connector and the integrated circuit transceiver. Pulse Tel: 01483 401 700 www.pulseeng.com

Fast 12 and 16-bit ADCs in MSOP package

Linear Technology has introduced the LTC1860 and LTC1864, serial 12-bit and 16bit, 250ksample/s analogue-todigital converters (ADCs). Both parts come in 8-lead MSOP packages. The LTC1864 has high resolution and both can sample up to 250ksample/s drawing 850µA at full speed and 2µA at 1ksample/s. A selfclocking conversion enables the accuracy of the ADC to be

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independent of the shift clock frequency. This allows the devices to operate with low MCU clock speeds as in optoisolation applications or with high clock speeds to save processor overhead. They are pin compatible. They are packaged in either MSOP-8 or SO-8. Both are available in the commercial and industrial temperature ranges. *Linear Technology*

Tel: 01276 677676 www.linear-tech.com

Resettable fuse with a range of footprints

Wickmann is offering six surface mount devices in its Polyfuse family of resettable fuses. These polymer-based devices provide overcurrent protection in products such as computer motherboards, USB hubs and ports, CD/DVD drives, PDA's, digital cameras and battery packs. There are six surface mount footprints available. The SMD0805 has the smallest footprint. Measuring 2.2 x 1.5mm, it features four hold current ratings from 100mA to 500mA with a current interruption capability of 40A at rated voltage. Both the SMD1206 and SMD1210 series are optimised for protection of computer peripherals, PC Cards and various I/O port types. Wickmann Tel: 01527 518841

www.wickman.co.uk

LCD controller design kit

Digital View's latest LCD flat panel display interfacing kit contains all the components necessary to support LCD applications. Consisting of a preprogrammed ACL-1024 RGB controller board, panel connector



TFT panels from 2.5 to 24 in.

Diamond Electronics is offering a series of TFT LCD displays that are available in a range of sizes from 2.5in (diagonal measurement) to 24in - corresponding to pixel counts of 480 x 234, up to 1900 x 1200. The displays can be supplied as stand-alone panels; with a range of controller cards; as kits with all major components for specific application areas; or as complete packaged designs. The supplier will also undertake custom designs. Controller cards support applications such as acceleration for Microsoft Windows displays; 3-D graphics acceleration; and conversion and display of RGB

signal streams. For users who are incorporating the pa

incorporating the panels into applications such as industrial monitoring and control displays, a kit is available that includes the TFT panel together with a PC controller card with a full industrial specification, that will easily handle data acquisition, control and display functions. Fully configured kits can also be supplied for RGB conversion applications. Options are also available that upgrade the viewability of the displays with high brightness or high contrast enhancements. **Diamond Electronics** Tel: 01477 500450 www.diamondelec.co.uk



cable, inverter, inverter power cable, rotary on screen display, and complete documentation, the Digital View ACL-1024 LCD design kit provides the components necessary to run a range of LCDs from manufacturers such as LG. Mitsubishi, Sharp and Toshiba. The pre-configured board provides connection to TFT LCD panels with a resolution of 640 x 480, 800 x 600 and 1024 x 768. It also provides smooth full screen image expansion for XGA modes, with an optional daughter board offering connectivity to TDMS and LVDS TFT panels. Measuring 179×114.3 mm the controller features a user programmable on-screen display of functions, integral power/signal protection and on-board circuitry for handling power on/off. The product is VESA DPMS power management compatible. Each kit is also available with an optional DVB-1000 NTSC/PAL

add on video board. This new board supports composite, Svideo and component video inputs. Digital View Tel: 020 8236 1112 www.digitalview.com

PCI bus scope card with 14-bit resolution at 100MS/s

The Gage CompuScope 14100 is available from TTi (Thurlby Thandar Instruments). It is a single slot, PCI bus card capable of performing 14-bit analogue/digital conversion at sampling rates of up to 100Msample/s in single-channel mode and 50Msample/s in dualchannel simultaneous mode. The



unit features a 50MHz analogue bandwidth with over 70dB dynamic range and 63dB signal/noise ratio. One billion samples of on-board memory allows very long data recording sessions for applications including DSL testing, wireless signals and antenna research. The fast data throughput offered by the PCI bus allows for bus mastering, data can be transferred from the on-board memory to the PC's memory at rates of up to 100Mbyte/s. This is typically 100 times faster than using the ISA or VXI bus and more than 1000 times faster than GPIB rates, said the supplier. This fast data throughput allows use in applications such as radar or imaging applications requiring trigger frequencies in excess of 10kHz. With the software, the unit can act as a digital oscilloscope, an FFT analyser, a transient recorder with time and date-stamp capabilities or a waveform analyser. TTi

Tel: 01480 412451 www.tti-test.com



NEWPRODUCTS

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Automotive inductors are QS9000 qualified

Cooper Electronic Technologies has introduced a range of inductors designed to address the OS9000 certification standards. This series of surface mount electronic power inductors has passed the requirements for shock, temperature, vibration, solderability, flammability, dielectric voltage requirements, and wash process tests, thus making the components suitable for automotive designs. The Coiltronics brand of Uni-Pac inductors are suitable for traditional 12V automotive distribution systems or for 42V systems of the future. Typical uses within 12V systems include boost converters and buck converters. If the engineer is developing around the future

42V requirements, the inductors can again be used in boost converters, buck converters, filtering applications plus isolation controls. All of the inductors have been optimised to operate in ambient temperatures of -40 to +125°C, with storage temperatures of -55 to 150°C. *Cooper ET Tel: 00561 752 5019 www.cooperet.com*

Bluetooth version 1.1 qualified chip set and stack

CSR (Cambridge Silicon Radio) and Open Interface of the US have announced a Bluetooth version 1.1 qualified combined design using CSR's BlueCore Bluetooth silicon and Open Interface's BlueMagic protocol stack. The two firms are already



shipping a combined hardware and software design for the earlier version of the Bluetooth specification. *Cambridge Silicon Radio Tel: 01223 692689 www.csr.com*

Large flash-based FPGAs for industrial

Actel is shipping its largest flash-based field-programmable gate array (FPGA) devices qualified to industrial specifications. The ProASIC A500K180 and A500K270 industrial devices offer 370,000 and 475,000 system gates, respectively. The industrial grade FPCAs are specified to operate at ambient temperatures ranging from -40 to +85°C. The devices are suitable for applications such as remotely deployed networking and



telecommunications systems, civilian aircraft avionics and industrial control. These latest devices join the firm's ProASIC A500K050 and -130, which were qualified earlier this year. It offers industrial-qualified devices from 2K to 475K system gates. The A500K270, with 475000 system gates, offers nearly 27000 registers and 63Kbits of RAM in 28 blocks of 256 x 9 bits. The 370000-gate A500K180 offers more than 18000 registers with 54Kbits of RAM in 24 blocks. Actel Tel: 01276 401450 www.actel.com

Plug-in SIM card adapter for test

Orga has announced a plug-in SIM card adapter for testing mobile telephones. The adaptor fits into the mobile's internal SIM connector. Through a thin, flexible connector, the mobile can be completely closed whilst connecting to the testing equipment, said the supplier. There are four versions of the adaptor available. The device also has the same parameters as a full sized paddle, so no adjustment or calibration is necessary. According to the company, the information stored in the SIM is usually passed on to the mobile equipment for processing. External intervention is impossible, and therefore special commands cannot be sent

Fans withstand a splashing

EAO has expanded its offering of Sanyo Denki cooling fans with the addition of a range of San Ace splash proof fans. The IPX7 protection rated fan features a silicone-sealed motor housing, which is capable of withstanding the 1000 hour salt-water spray test. The range which comes in four different frame sizes is available in 12V, 24V or 48V DC. Other options include low, medium and high-speed versions and the inclusion of either a pulse sensor to monitor the operating speed of the fan or a lock sensor to determine whether the fan motor is running or not. High airflow and high static pressure are strong features of the range with 1.06m³/min typically attained with the 80 by 20mm version (static pressure 39 pascals), 1.38m³/min with the 92 by 25mm version (static pressure 45 pascals), 3.0m³/min with the 120 by 38mm (static pressure 70 pascals) version and 5.9m³/min with the 140 by 51mm version (static pressure 142 pascals). These fans, with UL, TUV and CSA approval, have an LIO life expectancy of 100000 hours measured at an ambient temperature of 600°C. Operating temperatures range from -10 to +60°C and noise levels from 22 to 52dB. EAO

Tel: 01444 236000 swww.eao.com ARBITRARY WAVEFORM GENERATOR-STORAGE OSCILLOSCOPE-SPECTRUM ANALYZER-MULTIMETER-TRANSIENT RECORDER-

The HS801: the first 100 Mega samples per second measuring instrument that consists of a MOST (Multimeter, Oscilloscope, Spectrum analyzer and Transient recorder) and an AWG (Arbitrary Waveform Generator). This new MOST portable and compact measuring instrument can solve almost every measurement problem. With the integrated AWG you can generate every signal you want.

• The versatile software has a user-defined toolbar with which over 50 instrument settings quick and easy can be accessed. An intelligent auto setup allows the inexperienced user to perform measurements immediately. Through the use of a setting file, the user has the possibility to save an instrument setup and recall it at a later moment. The setup time of the instrument is hereby reduced to a minimum.

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- When a quick indication of the input signal is required, a simple click on the auto setup button will immediately give a good overview of the signal. The auto setup function ensures a proper setup of the time base, the trigger levels and the input sensitivities.
- The sophisticated cursor read outs have 21 possible read outs. Besides the usual read outs, like voltage and time, also quantities like rise time and frequency are displayed.
- Measured signals and instrument settings can be saved on disk. This enables the creation of a library of measured signals. Text balloons can be added to a signal, for special comments.

- The (colour) print outs can be supplied with three common text lines (e.g. company info) and three lines with measurement specific information.
- The HS801 has an 8 bit resolution and a maximum sampling speed of 100 MHz. The input range is 0.1 volt full scale to 80 volt full scale. The record length is 32K/64K samples. The AWG has a 10 bit resolution and a sample speed of 25 MHz.The HS801 is connected to the parallel printer port of a computer.
- The minimum system requirement is a PC with a 486 processor and 8 Mbyte RAM available. The software runs in Windows 3.xx / 95 / 98 or Windows NT / 2000 / XP and DOS 3.3 or higher.
- TiePie engineering (UK), 28 Stephenson Road, Industrial Estate, St. Ives, Cambridgeshire, PE17 3WJ, UK Tel: 01480-460028; Fax: 01480-460340

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in order to identify and locate errors. The firm's SIM simulator monitors and tests the reaction of the mobile as it communicates with the card allowing testing at electrical, protocol and application level. This SIM adaptor is designed to simplify the connection so mobiles do not need to be modified and the need to solder connections is eliminated. Orga Tel: 0118 377 6000

www.orga.com

SCSI PMC modules claim Ultra 160 lead

BVM has extended its range of SCSI PMC modules with the addition of an Ultra160 SCSI controller. Believed to be the first of its kind, this module designated the PMC-SCU160, allows designers to use current ranges of Ultra160 peripherals that are available. Unlike earlier SCSI controllers, which degrade to match the slowest peripheral on the SCSI bus, this module has domain validation allowing it to talk to each peripheral at its optimum speed. *BVM*

Tel: 01489 780144 www.bvmltd.co.uk

1.8V op-amps take 100μA and drive 2kΩ.

Maxim Integrated Products has introduced the MAX4292 dual op amp with rail-to-rail inputs and output in a UCSP package measuring 1.5 by 1.5mm. It consumes 100 μ A of supply current per channel, yet drives a 2k Ω load and achieves an openloop gain of 120dB. Operating from a single +1.8V to +5.5V supply, it delivers 20,000 hours of operation from two alkaline AA cells. Maxim Tel: 0118 930 3388 www.maxim.com

Arbitrary waveform generator

It's possible to define, create and modify waveforms for specific applications using Tecstar's new function generator. This is a "fully-featured" programmablesweep function generator integrated with an arbitrary waveform generator. Designated the FGA2030, the generator delivers from 0.2Hz to 20MHz and its arbitrary facility works from 0.4MS/s to 30MS/s with 12-bit resolution. The unit's 256K non-volatile memory is



configurable as 1 by 256Kwords or 8 by 32Kwords for storing waveforms. A selectable 10MHz Bessel filter and internal/external triggering are standard features. The instrument is supplied with Tecstar's 'Wavegen' Windows software for creating waveforms on the PC. Waveshapes can be drawn freehand or calculated. Tecstar Electronics Tel: 01480 399499 www.tecstar.co.uk

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- 5. Dialogue For Three, Flute, Oboe and Clarinet, 1913
- 6. The Toymaker's Dream, Foxtrot, vocal, B.A. Rolfe and his orchestra, 1929
- 7 As I Sat Upon My Dear Old Mother's Knee, Will Oakland, 1913
- 8 Light As A Feather, Bells solo, Charles Daab with orchestra, 1912
- 9 On Her Pic-Pic-Piccolo, Billy Williams, 1913
- 10 Polka Des English's, Artist unknown, 1900
- 11 Somebody's Coming To My House, Walter Van Brunt, 1913
- 12 Bonny Scotland Medley, Xylophone solo, Charles Daab with orchestra, 1914
- 13 Doin' the Raccoon, Billy Murray, 1929
- 14 Luce Mia! Francesco Daddi, 1913
- 15 The Olio Minstrel, 2nd part, 1913
- 16 Peg 0' My Heart, Walter Van Brunt, 1913
- 17 Auf Dem Mississippi, Johann Strauss orchestra, 1913
- 18 I'm Looking For A Sweetheart And I Think You'll Do, Ada Jones & Billy Murray, 1913
- 19 Intermezzo, Violin solo, Stroud Haxton, 1910
- 20 A Juanita, Abrego and Picazo, 1913
- 21 All Alone, Ada Jones, 1911

Total playing time 72.09

21 tracks – 72 mlnutes of music. Published by *Electronics World*. All recordings reproduced by Joe Pengelly.

Detecting vibrations and tremors



Picture courtesy US Geological Survey: http://neic.usgs.gov/

number of scientific and engineering investigations involve vibration signals. For example, engineers can often characterise a metal plate or beam by placing a vibration sensor on it, and then subjecting it to an impact.

I've seen one case where strain gauge sensors were placed on a metal beam whose other end was then whacked with a 'dead-blow' hammer, as in Fig. 1. A dead-blow hammer is a hammer whose blow reaction is absorbed by some means. The idea behind using a deadblow hammer is to prevent bouncing of the hammer from creating more than one blow.

In another case, sensors were placed at critical points on a bridge, and then a small explosive charge was ignited at the other end. The charge was of the order of a cherry bomb, so caused no damage. In both cases, the idea was to cause an impact, and then record the vibrations in the structure that result from it. If you saw the movie Jurassic Park, then you saw

Impact point Sensor Beam

Fig. 1. It is possible to obtain information about a beam by placing vibration sensors on one end of it and hitting the other with a 'dead-blow' hammer.

In Joe Carr's explanation of vibration detectors and seismographs, he details an easy-to-build instrument that's sensitive enough to detect Earth tremors.

another use of vibration sensors. A shotgun shell blank was held in a rig against the ground and exploded. The seismic waves were recorded on a lap-top computer and processed to show the all-too-realistic image of a Raptor skeleton.

Oil exploration is done the same way. An explosion is set off at a site, and a cluster of vibration sensors around a perimeter are used to sense the vibrations. From their data the geologists can construct an image of the underlying structure, and from it predict where oil might be found.

The idea behind any sensor is to find some transducible property that responds to the event being measured. A number of things can be used. For example, the strain gauge consists of a thin resistive wire stretched across a membrane or diaphragm.

When the wire is deformed, its dimensions change, so its resistance will also change. This phenomenon is called piezoresistivity. When the strain gauge resistor is used in a Wheatstone bridge, then a sensitive measure of the deformation caused by vibration can be obtained.

Inductive sensors

Another transducible property for vibrations is the inductor shown in Fig. 2a). Inductance of the coil of wire is determined by the number of turns, diameter of the coil, the length of the coil and the nature of the core.

If a ferrite or powdered iron core is used, then a large increase of inductance occurs when the core is slipped into the coil form. 'Slug-tuned' inductors are used in radio circuits. A threaded core is adjusted to be more or less inside the coil, depending on the value of inductance required.

Vibration sensors can be made by placing the core on a spring, pendulum or some other means of translating the motion caused by vibration into motion of the core – and therefore a change of inductance. In Fig. 2b) a pendulum is used to move the core in and out of the coil form.

There is a class of motion sensors that depends on the fact that a magnetic field and coil in motion relative to

each other causes a current to be induced into the coil. It doesn't matter whether the coil moves or the magnet moves, so long as there is relative motion.

Figure 3 shows a crude form of permanent magnet moving coil (PMMC) vibration sensor. A horseshoe magnet is positioned such that an inductor can move inside of its field. The coil, which may have many, many turns, is connected to a spring. When vibration is sensed, the coil moves up and down against the spring, causing a current to be induced because of the magnetic field.

A more familiar example is something that you might not at first see as a vibration sensor. The ordinary radio or television loudspeaker, Fig. 4, will do this neat trick. The PMMC loudspeaker consists of a fixed permanent magnet – usually made of Alnico – and a lightweight moving coil that is attached to the paper speaker cone.

When an electrical current from the radio output stage flows in the coil it produces a magnetic field that either attracts or repels the permanent magnet's field, depending on the polarity of the audio signal. Thus, as the polarity of the audio signal switches back and forth the coil and cone move in and out.

The same loudspeaker that can be used as a radio output device can also be used as a microphone. Intercom sets that allow talk-back use this phenomenon. When sound vibrations impinge on the cone, they move the coil relative to the magnet, causing a current to be induced into the coil. This tiny current can be amplified and used as an audio signal.

Note that word in the previous paragraph: 'vibration'. If the vibrations being measured can be coupled to the speaker cone, then the speaker will act as a vibration sensor. I've seen speakers placed flat against metal plates for the purpose of recording vibrations.

One science fair student cemented a plastic drinking straw against the bottom of the speaker cone, and used it to couple to the vibration source. Essentially, the kid made a large-scale 'spike microphone'.

Linear differential voltage transformers

Figure 5 shows a special form of vibration or displacement sensor called a linear differential voltage transformer, or LDVT. It consists of three inductors, L_1 through L_3 .

Inductor L_1 is excited by an AC signal, which is magnetically coupled to secondary coils L_2 and L_3 . When the core is exactly midway between L_2 and L_3 the currents flowing in them will be identical.

The dots indicate the phasing of the two secondary coils. Because of the connection of L_2 and L_3 , the currents are in series opposing. Thus, when the core is midway, the currents are equal and opposite, so null to zero. But when the core is offset in one direction or another, then one of the coil currents will predominate.

The amplitude of the output signal – expressed as a voltage – gives an indication of the magnitude of the core shift, while the polarity indicates the direction. When the core is coupled to something like a pendulum or diaphragm, then the vibrations received will move the core in an out of the coil-pair, causing an AC output signal to appear.

Differential capacitor sensors

Capacitance exists whenever two metallic objects are in close proximity to each other, and are separated by an insulating material -i.e. a 'dielectric'. Such a device is called a capacitor.

Capacitance is a measure of the capacitor's ability to



Fig. 2. Inductive vibration sensing. In a) is the basis of such a sensor. If the coil's ferrite core is disturbed, the inductance of the coil changes. In b), the coil and core are attached to a pendulum which transfers movement from the pendulum's base plate to the coil's core.

store an electrical charge in an electrical field set up between the plates. The value of capacitance is proportional to the area of the metal surfaces and a property of the dielectric called the dielectric constant. For dry air, the dielectric constant ε is 1. Capacitance is inversely proportional to the distance between the metal surfaces – in other words, the closer together they are, the higher the capacitance.

Capacitors are used in a variety of electronic applications. But in this particular case we are interested in a class of capacitors that are variable, and can be made to vary under the influence of vibration.

Differential capacitors

This class of variable capacitor actually consists of two variable capacitors actuated by the same shaft or other mechanical device. They are configured such that one capacitance increases its capacitor while the other decreases its capacitance in the same manner.





If you were to measure the total capacitance across the two capacitors, then you would find that the net capacitance does not change even though the values of the two capacitor sections do.

Figure 6 shows a vibration sensor that utilises the differential-capacitance phenomenon. The two capacitors are formed by two metal cylinders $-C_1$ and C_2 – separated by a small dielectric gap of air or other material. These cylinders share a metal plunger that is inside and axially concentric with them.

An insulating sleeve prevents the plunger – the 'common plate' of the differential capacitor – from shorting out against either cylinder. When the plunger is equally inside both cylinders, favouring neither, then their respective capacitances are equal. But if the actuating arm moves, then the plunger will move more deeply into one cylinder and out of the other. As a result, the ratio of the two capacitances changes.

The inset in Fig. 6 shows the equivalent circuit schematic for the differential capacitor vibration/displacement sensor.

A circuit for using a differential capacitance sensor is shown in Fig. 7. The differential capacitor – comprising C_{1A} and C_{1B} – is connected as two arms of a Wheatstone bridge. The remaining two arms are resistors R_1 and R_2 . It is the nature of this type of bridge circuit that output voltage V_0 will be zero when the ratios of the capacitive reactances and resistances are equal:

$$\frac{X_{C1A}}{X_{C1B}} = \frac{R_1}{R_2} \text{ for } V_0 = 0 \tag{1}$$

If R_1 equals R_2 , the output voltage will be zero when the differential capacitor is balanced, i.e. C_{1A} is equal to C_{1B} . When vibration or other motion displaces the plunger, however, C_{1A} is no longer equal to C_{1B} so the bridge is unbalanced and V_0 is non-zero. The amplitude of V_0 depends on the amplitude of the mechanical displacement of the plunger.

Other forms of circuit can be used with a differential capacitive sensor. For example, the two capacitors C_{1A} and C_{1B} can be used to control the frequency of radio frequency oscillators. When C_{1A} is equal to C_{1B} , then the two frequencies would be equal, but a change in that equality will force the frequencies apart. One frequency will rise, while the other will fall.

If the two frequencies are combined in a nonlinear mixer, then the resultant heterodyne beat note will be proportional to the deflection of the plunger. A frequencyto-voltage converter or other form of circuit can be used to produce the analogue waveform.



Another approach is shown in Fig. 8. It is derived from a sensor called the Shackleford-Gunderson seismometer. In this type of sensor circuit the common plate of the capacitor is excited by a 4 to 6MHz RF signal, and the other two are used as 'receive antennas'. When the detected and integrated outputs of these receive antennas are combined the resultant signal is proportional to the vibration.

Still another approach is to charge the capacitors through a high value resistor with a DC source, and then use an electrometer to measure the voltage across the capacitors. The voltage will be proportional to the charge, which in turn is set by the capacitance. The result is that the two DC voltages will vary according to the position of the plunger.

Electrometers are amplifiers with extremely high input impedances, so can be used to measure the charge developed across small value capacitors.

Another form of differential capacitor vibration sensor uses a pendulum to move a metal disc between two sections of a capacitor. The sensor's stator plates, call them C_{1A} and C_{1B} , are made with bits of sheet metal, or blank unetched printed circuit board stock – which might be easier to work.

Two sets of plates are used for each capacitor, and they are connected together. As the pendulum bob swings back and forth it will 'shade' more or less of each set of stator plates depending on the amplitude and direction of the swing. Similar circuit strategies as above will also work for this sensor.

Lehman-type seismometer project

The Lehman seismometer was designed by Dr James D. Lehman of the Department of Physics at James Madison University in Harrisonburg, Virginia. It was the subject of the 'Amateur Scientist' column written by Jearl Walker in *Scientific American* (July 1979, p. 152ff)*. This seismometer is capable of detecting earthquakes of 4.8 or more on the Richter scale in the country where it's used, and 6 or more in other areas of the world.

The plan view is shown in Fig. 9, while a side view is shown in Fig. 10. A pendulum boom of 75cm or more in length is made of $\frac{5}{16}$ -inch (8mm) stock. It is suspended from an upright section, Fig. 11, such that its 'knife-edge' butt end rests against a strike plate on the upright section.

The last 20cm or so of the boom is threaded to accept a $5_{1/6}$ -20 or finer hex nut. When side-to-side vibrations characteristic of an earthquake are present the pendulum arm will swing back and forth in the horizontal plane. A pair of stops, Fig. 12, are used to prevent over-travel of the pendulum boom arm.

The components of the seismometer are mounted on a flat base plate. This plate can be made of aluminium, wood or plastic, but it must be level and stable. Do not use material thinner than 1.5cm for the base.

The upright segment is made from copper plumbing pipe, right angle-joints, and a pair of universal flanges for mounting to the base plate. The overall height is about 46cm. A lower cross piece holds the strike plate, while the upper cross piece holds a special wire attachment point



fixture - about which, more shortly.

In Fig. 11 you will see the side view. The wire suspending the boom is either piano wire or #26 AWG Nichrome wire. A small turnbuckle is used to make the wire taught and level the boom.

The upper end of the suspension wire is tied off to the upper cross piece, Fig. 13. The Walker article used an oil burner furnace nozzle to precisely position the suspension wire

Detail in plan view of the lower cross piece is shown in Fig. 14. The cross piece can be machined from a bar of aluminium or brass, although some have made it out of plastic - not recommended, I am told. The ends of the cross piece are bevelled to allow it to be mounted to the vertical upright members.

In the centre of the cross piece is the strike plate assembly. Detail for the strike plate itself is shown inset. A ¹/₄in (6mm) carriage bolt or other flathead machine bolt is used to make the strike plate. It is cut off from the threaded segment, leaving only the head and the unthreaded portion of the shank. The head is machined flat and polished. This makes a nice, low friction surface for the knife edge end of the pendulum boom.

A hole is drilled through the cross piece just slightly larger than the strike plate. A 1/4-20 (or finer) hexagonal nut is soldered or glued to the backside of the cross piece such that its hole is centred over the hole drilled in the cross piece. A matching hex head bolt is threaded into the



In a) is a plan view while b) shows a side view. Here, a magnetic field moves relative to a coil of wire to induce a current.

nut, so that it sits against the back of the strike plate. When this bolt is adjusted, it will adjust the position of the strike plate

A sensor assembly is mounted on the boom arm, refer again to Figs. 10 and 11. It is used to detect motion of the boom and translate it into an electrical signal that can be recorded on paper or stored in a computer. Also on the boom arm is a lead weight to make the boom act like a pendulum, and a damping assembly. The damper is needed to damp out oscillations of the pendulum in order to spread out the frequency response. Without the damping assembly the vibrations closest to the pendulum's natural period will cause the greatest response. The frequency response is thus peaked at a specific frequency, rather than being broader.

Detail for the sensor assembly is shown in Fig. 15. A

was a radiation detector made out of aluminium foil and a tin can on page 190 of the September 1979 issue. Excellent magazine. Ed.

*For interest, there



plan view is shown in Fig. 15a), and a side view is shown in Fig. 15b). The sensor's 'transducible event' results from the fact that a magnetic field moving relative to a coil of wire will induce a current in that wire.

In Fig. 15a) you will see a strong horseshoe magnet mounted on the boom arm of the pendulum. The magnet is fixed with a wing nut connector of the sort used to fasten chemistry and optical apparatus.

Inside the curve of the horseshoe magnet is a coil of wire. The coil should be several centimetres in diameter, and consist of about 8000 to 12000 turns of #36 AWG (or so) enamelled wire. The former for the coil can be constructed of wood or plastic. A slow speed drill is used to scramble wind the wire onto the form.

Winding the coil can be a dangerous operation, and you take your own responsibility if you attempt it. Make sure the drill has a very slow speed (most variable speed drills will do this). Fix the drill and the form in a vice, or other fixture. The wire should be on a dowel mounted offset from the drill and form, but positioned so that it can be unwound without kinking. Wear protective safety goggles when winding the coil. Injury to your eyes could occur if the wire breaks, or if you do something wrong.

The mounting of the coil and magnet are shown in side view in Fig. 15b). The mounting assembly can be made from either wood or plastic, or any convenient material.

The damping assembly is shown in both plan and side views in Fig. 16. The lead weight is made of plumber's lead, and is fixed to the threaded end of the boom with a pair of hex nuts. The period of the pendulum can be adjusted by the position of the weight, if needed.

The damping action is created by swinging a metal plate through the magnetic fields of opposing horseshoe magnets. When the pendulum moves eddy currents are set up in the aluminum plate, and these in turn create a magnetic field that is opposite to the polarity of the field that created the current. This is a magnetic version of 'rust on the door hinges' to keep it from swinging for a long time after excited by vibration.

The aluminium plate is fixed to the pendulum by a wooden or plastic dowel. One end of the dowel is threaded to fit the thread on the 8mm boom end. A wood-screw can be used to fasten the aluminium plate to the other end of the dowel. Seismometers are usually buried below grade. Some people place them in a simple box on a flat rock outcropping, but that is not terribly good practice. Others build a concrete pedestal and box to house the seismometer.

Thermal insulation surrounding the inner chamber where the seismometer is located is used to keep the temperature stable. It is essential that the seismometer be perfectly level, and not tilted. It may take several days to get the seismometer adjusted properly.

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USB oscilloscope for the PC

Eddy Insom shows how easy it is to make an oscilloscope that plugs into your PC's USB port. Its sampling rate may not be very exciting for RF people, but it can be perfectly acceptable for audio and teaching work.

The author

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USB interfacing module

This off-the-shelf USB module was the subject of last month's article. It offers an easy route to adding USB to your project. Using it also reduces the PC software to the lowest level of difficulty.

The USB module in question is the FT8U245AM, manufactured by FTDI, (*www.ftdichip.com*). To work, the chip only requires a 6MHz crystal, some passives and the USB socket. It interfaces to an external microprocessor via an 8-bit parallel port and four read/write and control lines.

From the micro's point of view, the FT245 chip looks like a 384-byte first-in-first-out buffer, or FIFO, for transmit, and a 128 byte FIFO buffer on receive. The device works in the USB full speed mode and can transfer data at around a megabyte per second.

covered the basics of USB interfacing using an off-the-shelf module in an article presented in last month's issue. Here I develop the concept by describing a simple virtual instrument that allows you to look at waveforms on your PC.

Figure 1 shows the basic concept. I used a Texas Instruments TLC5510 flash a-to-d converter. You could also use the faster TLC5540 version. These converters are cheap, fast CMOS devices that take very little current. They also require the minimum of interfacing as you will see. As all the chips are surface mount the whole circuit is small enough to fit into a hand held probetype enclosure.

How the circuit works

The 74HC132 schmitt trigger chain forms a simple relaxation oscillator gated by the TXE 'buffer data available' signal from the USB module. This simple arrangement produces write clock pulses for the module and in slightly delayed form, for the a-to-d converter. The gating ensures clocks are only generated when the USB module is ready to accept them.

When testing, note that the clock will only run if there is a program at the PC end 'reading' the data, otherwise the transmit first-in-first-out buffer fills up and the module just stops taking data. The oscillator frequency sets the a-to-d sampling rate. Because of USB transfer limits, the maximum rate will be about 1MHz. However, the application program in your PC may not be able to accept this data continuously, especially if you have other fast USB peripherals connected such as sound cards or video grabbers.

Figure 2 shows how a two-trace version of the scope could be implemented. I have used two a-to-d converters. It is easier – and possibly cheaper – to do it this way rather than try to arrange for an analogue switch on the input.

As it is now necessary to take two readings per sample, the overall sampling rate will be halved, reducing the effective bandwidth to 250kHz.

PC INTERFACING

This is still good enough for audio work.

The principle can be extended to four or more traces by simply adding more D-type flip-flops. One disadvantage of this method is that the PC does not know which sample corresponds to which input. I cheated by adding a button on the software to switch the traces around.

A similar arrangement can be used to read from higher resolution 12, 14 or 16 bit a-to-d converters, by selecting between the most significant and least significant bytes of a single, wide converter. If the converter has fewer than 16 bits, a spare bit can be used as an external trigger input.

Alternatively, the unused bits can be set to a fixed data pattern. This may enable the PC software to synchronise to the correct byte order by analysing the repetition patterns in the byte pairs received.

Both the a-to-d and the FTDI USB module can operate at up to 10MHz in burst mode. It is therefore possible to run the scope ten times faster by filling the transmit FIFO buffer at higher clock rates, and dumping to the PC at the normal 1MHz transfer rate.

The 384 byte first-in-first-out buffer may not be large enough for some applications. However, it is quite easy to add an external FIFO RAM – such as a 256K TV framegrabber FIFO. The buffer connects between the a-to-d and the USB. Clocking and gating arrangements are relatively straightforward. This would allow the scope to run at up to 5MHz bandwidth in burst mode.

Such a set-up could also form the basis of a simple USB video frame grabber.

Input circuit

All configurations share the same input circuitry. This is detailed in Fig. 3. The TLC5510 has an adjustable analogue input span that is determined by the externally applied voltages at pins REFT and REFB.

The chip includes a number of internal resistors that allow designers to choose a set of gap options without adding external circuitry. The chip was designed mainly for video conversion, and the two main range options are 0.0 to 2.3 volts, and 0.6 to 2.6 volts.

A small problem arises here. The USB cable provides a variable supply voltage between 4.2 and 5.25 volts. As a result, the simple resistor divider inside the chip will not give an accurate reference. It also draws about 10mA quiescent current. As an alternative, I used an external 2.5V reference diode, dispensing with the internal resistors.

The analogue input to the a-to-d converter is internally connected to various capacitor switches, and ideally requires a low-impedance source. Some means of shifting the voltage levels is needed so that the probe input can measure negative voltages. Also, some means of trimming the overall gain and zero points is necessary.

I used two op-amps in the simple configuration shown. You may prefer to use some other arrangement. That doesn't matter as long as it eventually provides an output of 0 to 2.5 volts at low impedance into the a-to-d converter.

The op-amp should be a high-impedance, high-frequency type, enough to give a gain of at least ten at 10MHz. Be aware that some op-amps have limited top saturation voltages. With a positive supply of 5 volts they may clip below the 2.5 volts needed. I used an LF353 for no other reason than I happened to have one available at the time. It seemed to work very well.

All power is derived from the USB wire. The USB interface module, the a-to-d converter and the op-amp all require very little current and can happily work off these voltages at all times, even when the USB interface is inactive.

However, in order to generate the negative supply for the op-amp, I needed to include a DC-to-DC converter, Fig. 4,



Fig. 1. Simple USB oscilloscope: A high speed a-to-d converter drives the USB module directly. Maximum clock rate is about 1MHz, resulting in a bandwidth limit of about 500kHz. As shown, the input range is from 0 to 2V. Extra analogue input circuitry will be required for voltage level shifting and gain adjustment. Circuit layout is critical as the converter is highly susceptible to spikes and noise. Apart from a PC, the only other thing that you need except for this circuit, is a USB cable.





PC INTERFACING

Fig. 3. A possible analogue input signal conditioner. The input range is -10 to +10V with an input impedance of about $1M\Omega$. The second op-amp performs a bit of level shifting and gain to provide the 0-2.5V required by the a-to-d converter from the probe's input range of -10 to +10 volts. The two potentiometers are used to trim zero level and final gain.





transistor is used to switch power only when the USB module is active. This is derived from the TXE signal.

which robbed nearly 15mA from the 5-volt supply. I found myself adding a simple circuit using a p-n-p transistor to remove power to the DC converter and to the op-amp when the USB is inactive.

The USB module does not provide a specific 'power down' output, so I crudely derived this from the TXE signal. If you use a more efficient low-power DC-to-DC converter, you may not need this part of the circuit. It may be possible to derive the negative supply from the clock. I assume it should work as the op-amp takes little power. I have not tried this.

The TLC5510 chip is incredibly sensitive to noise and supply glitches. This shows as bumps and spikes on the displayed waveforms, especially during 0111111_2 to 100000_2 type transitions. The application notes suggest using separate connections for digital and analogue supplies. You *do* need these if you want a clean output.

Obtaining the parts

The Texas a-to-d converter is available from Farnell at about £4 each in small quantities. The USB module is available from FTDI in the UK for £23.50, which includes VAT and shipping. The company has local agents in the US and Australia. Check the company's web site *www.ftdichip.co.uk* for more up to date details on pricing and availability.

An alternative device FT232, provides a serial output instead of a parallel one. This could be used as a direct replacement for systems using legacy RS232 interfaces. All information is available from the FTDI web site.

Vcc 10k 10k 25k (20k+10k var) 0V .. +2.5V reft 13 0V reft Vref 2.5V Vref 2.5V

> If you are creating your own PCB layout, make sure you download the data sheets from the Texas web site including all application notes. They contain very useful recommendations on noise suppression.

For my prototype, I used air-to-air wiring for all pin connections on the Texas chip. This at least guaranteed that no PCB cross-talk noise would be present.

I might have spent ten minutes getting the USB module to work but I am not saying how much time I spent trying to minimise analogue noise problems.

PC application software

On the PC, you only need to install the FTDI comms emulation driver (VCP). All the relevant drivers are downloadable from the manufacturer's web site. These are available for all relevant versions of Windows, Mac and Unix/Linux.

Detailed installation instructions are also available from the web site. They are very simple, and you may not want to read them – especially if you, like me, are one of those people who only read the documentation when things don't work.

After installation, you will notice that an extra COM port has been added to the existing list in your control panel. Note that this COM port is only enabled when the USB module is active. So, if you want to change the number for example, ensure the USB module is plugged in.

Writing software for the PC is reasonably simple. Many of you will be conversant with software programming, whether in Basic, Borland or Microsoft tools. If you have written software for the serial port, read no further! The only thing you will need to know is that the USB module looks in all respects like a COM port. The only relevant difference is that speed parameters are ignored. The chip runs at full speed.

For those of you using Basic, there are a number of plugin ActiveX and DLL component modules you can get for driving the serial port. I have not tried any of these, but considering the high data rates involved, I would not be surprised if some of these are not able to keep up with the incoming stream from the USB module.

However, there is an alternative. The USB module's manufacturer, FTDI, also supplies a set of drivers that will allow you faster and more direct access to the chip resources. Known as D2XXX, these drivers are provided in the form of separate DLL files that present a relatively simple interface to VB, VC++, C+ Builder, Delphi etc.,

Listing.	Software in C f	or communicating with the USB oscilloscope.
1100	Hann Patron and	
int	com Init (in	its: it port, int bsize);
int	com_Exit ().	
//**	Local variable	, user does not need to access these
int	com_handle= NU:	L; // comms port handle // flag to order thread to stop
int	com_bmask= 0;	// max buffer size mask, i.e. 0x3FFF
int	com_bsize= 0;	<pre>// circular buffer size = bmask+1</pre>
int	com_bindx= 0;	// circular index to last written data
char	com zerror [2!	(6]; // stores error messages
//** Th	e main running 1	hread. This is called by the Init function, and
// made	to stop by the	Exit function. User does not call this.
static	DWORD WINAPI CON	mode with simple sleep delay for simplicity Thr(LPVOID p)
{ CO	MSTAT comstat; I	WORD errmask;
//** In	itialise and val	idate
	m_thrstop=0; m_bindx== 0;	
//** Do	the forever loo	p.(till "stop" flag is set externally)
wh	ile ((com_thrst	op==0) && (com_handle!=NULL) &&(pDlg!=NULL))
{	//** Add any ext	ra operations here,
	//* Get nr of by	tes in rx queue to read
	ClearCommError (om_handle,&errmask,&comstat);
	int ntr=comstat.	cbInQue; // this gives the Q size
	{ //* align for	wraparound in circular buffer
	if ((ntr + co	<pre>m_bindx)>com_bsize) ntr=com_bsize-com_bindx;</pre>
	PBYTE ptr = co	m_bdata + com_bindx;
	DWORD prd:	ytes to our buffer
	int ne=ReadFil	e(com_handle,ptr,ntr,&nrd,NULL);
	//* Handle er	ors, if any
	if (ne==0)	Error(com handle.&errmask.&comstat):
	//pDlg->0	ommErrorCB(errmask); //could call user error CB
	}	
//* if 1	RX data is valid	, call user call-back with buffer data
	{ // Any relev	ant low level signal processing could go here
100	11	
// Call	a function in t	he user application. Pass: buffer start, index
// pDlg	is a pointer to	the user application Object (assuming c++)
// Alte:	rnatively, just	call a normal user function without reference.
	int ee=pDlg-	<pre>>CommReadCB(com_bdata,com_bindx,nrd); m_thrstop=1; //If <>O_user wants to short</pre>
	com bindx +=	nrd;
	com_bindx &=	<pre>com_bmask; //align index on circular buffer</pre>
	}	
//* If 1	, nothing read fro	m line, block thread to allow buffer to fill.
	else	
	(Sleep(10);	<pre>// could add extra user operations here</pre>
1	5	
//** End	d of thread	
CO	m_thrstop=0;	
}	curn o,	
//*****	*****	***************************************
//** In:	itialise. Call t	his once at the start of your program d port number, e.g. 3
// bsi:	ze' is the wante	d buffer size, must be a power of 2, e.g. 32768
int cor	m_Init(int port,	int bufsize)
{ int	t e; lidate	
if	(com handle!=0)	return 0; // If already open, ignore call
CO	m_bsize=bufsize;	<pre>// save buffersize, must be 2^n</pre>
CO	<pre>m_bmask=bufsize- m_zerror[0]=0;</pre>	l; // set mask e.g. 0x7FFF
//** Op	en the comms por	t
cha	ar zp[8]; sprint	f(zp,"COM%d",port);
CO	m_handle=CreateF	ile(zp, // "COMn"
// no sl	hare (only optic	n)
		0,
// Secur	rity attribs	
// must	be this for com	ms:
		OPEN_EXISTING,
// non I	FLAG_OVERLAPPED	
// must	be NULL for com	u, ms
	101 001	NULL

<pre>// cannot open com port? if (com_handle==INVALID_HANDLE_VALUE)</pre>	
if (com_handle==INVALID_HANDLE_VALUE)	
<pre>(int ee=GetLastError();</pre>	
sprint: (com_zerror, "COM%d error %d", port,ee);	
com_nandle=Noll;	
//** Allocate a dynamic byte buffer for storing incoming data	
com_bdata= new BYTE[com_bsize];	
// error, cannot allocate memory	
<pre>if(com_bdata==NULL) { com_Exit(); return -1; }</pre>	
//** Allocate internal buffer sizes within the comms driver	
e=SetupComm (com_handle, com_bsize, 1024);	
DCB Dcb: // Holds a dcb structure	
// fills DCB with current settings 0=fail	
e=GetCommState(com handle, &Dcb);	
Dcb.DCBlength= sizeof(DCB);	
Dcb.BaudRate= 115200; // Ignored by FT driver	
Dcb.fAbortOnError= FALSE; // Stop tx rx if error r	cx
Dcb.fBinary= TRUE; // Binary mode, noEOF ch	lec
// l=enable CTS for outgoing flow control	
Dcb.fOutxCtsFlow= 0;	
// 1=enable DSR for outgoing flow control	
Dcb.fOutxDsrFlow= 0;	
// set DTR to on (DTR_CONTROL_HANDSHAKE)	
Dcb.fDtrControl = DTR_CONTROL_ENABLE;	
// 11 crue, enable rx when DSR=1	
// if TRUE, XON/XOFF enabled for outgoing	
Dcb.fOutX= 0;	
// if TRUE, XON/XOFF enabled for ingoing	
Dcb.fInX= 0;	
// Error char replacement when parity detected	
// if TRUE, discard NULL bytes on ry	
Dcb.fNull= 0;	
// leave rts on always (RTS_CONTROL_HANDSHAKE)	
Dcb.fRtsControl= RTS_CONTROL_ENABLE;	
Dcb.fAbortOnError= 0;	
Dcb.ByteSize= 8; // nr of bytes	
// 0.1.2: 1.1.5.2 ONESTOPRIT:	
Dcb.StopBits= ONESTOPBIT;	
Dcb.ErrorChar= 0x00;	
e=SetCommState(com_handle,&m_Dcb); // set DCB 0=fail	
//** Set Timeouts	
COMMTIMEOUTS Cto; // Comms Timeout structure	
Cto.ReadTotalTimeoutMultiplier= 0:	
Cto.ReadTotalTimeoutConstant= 0;	
Cto.WriteTotalTimeoutMultiplier= 0;	
Cto.WriteTotalTimeoutConstant= 0;	
e=SetCommTimeouts(com_handle,&Cto);	
//** Open thread now	
//** Open thread now DWORD tid; = HANDLE	
<pre>//** Open thread now DWORD tid; * HANDLE hThr=CreateThread(0,0, (LPTHREAD START ROUTINE)com Thr,0,0.6tid);</pre>	
<pre>//** Open thread now DWORD tid; * HANDLE hThr=CreateThread(0,0,(LPTHREAD_START_ROUTINE)com_Thr,0,0,6tid); if(hThr==NULL) { int ee=GetLastError();</pre>	
<pre>//** Open thread now DWORD tid; HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,6tid); if(hThr==NULL) { int ee=GetLastError();</pre>	
<pre>//** Open thread now DWORD tid; HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,6tid); if(hThr==NULL) { int ee=GetLastError();</pre>	
<pre>//** Open thread now DWORD tid; • HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,6tid); if(hThr==NULL) { int ee=GetLastError(); sprintf (com_zerror, "Cannot create thread"); com_Exit(); return -1;</pre>	
<pre>//** Open thread now DWORD tid; • HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,6tid); if(hThr==NULL) { int ee=GetLastError(); sprintf (com_zerror, "Cannot create thread"); com_Exit(); return -1; } //=</pre>	
<pre>//** Open thread now DWORD tid; • HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,6tid); if (hThr==NULL) { int ee=GetLastError(); sprintf (com_zerror, "Cannot create thread"); com_Exit(); return -1; } //=</pre>	
<pre>//** Open thread now DWORD tid; • HANDLE hThr=CreateThread(0,0, (LPTHREAD_START_ROUTINE) com_Thr,0,0,5tid); if (hThr==NULL) { int ee=GetLastError(); sprintf (com_zerror, "Cannot create thread"); com_Exit(); return -1; } //=</pre>	
<pre>//** Open thread now</pre>	e
<pre>//** Open thread now</pre>	.e
<pre>//** Open thread now</pre>	e
<pre>//** Open thread now</pre>	.e
<pre>//** Open thread now</pre>	e

PC INTERFACING



Fig. 5. Screen dump produced using a simple oscilloscope PC display program. The bottom line shows a real-time FFT display of the top trace.

Need more information

Tony Wong 'Understanding USB' Electronics World, November 1999. A very good article describing the inner workings of USB. http://www.usb.org – the web site of the USB organisation.

http://www.ftdichip.com – for more information on the USB chip and module.

http://www.Farnell.co.uk

http://focus.ti.com/docs/analog/analoghomepage.jhtml Texas Instruments data sheets on a-to-d converters and application notes

J Hyde 'USB Design by Example' Wiley, NY 1999. http://www.usb-byexample.com

J Axelson 'USB Complete' Lakeview Research 1999. http://www.lvr.com

The last two references are good reference books on USB. They contain many examples and background information.

You just link it to your software project.

For those of you using C or C++, you can use any of the many Windows API functions. The Listing shows some very simple plain vanilla code that you can include in your application when using the FTDI serial emulator interface. The only two functions required are *com_Init()* and *com_Exit()*. These are used to initialise and terminate the comms service. These should be called once at the beginning of the program and once at the end.

The initialisation program starts a 'thread'. This is a section of code that runs transparently in parallel with the main user program. The thread keeps a watch on incoming serial-port bytes, and calls a function in the main user program whenever there is a quantity of bytes to transfer. This is known as a 'call-back' function. In this way, the main program does not need to spend time reading the COM port – usually a cause for hangs and clumsy behaviour in some Windows programs.

I have made the code in the thread very simple, and have not resorted to advanced methods such as events or overlapped functions. I wanted to make it easily readable and understood by those not fully conversant with advanced programming topics.

The thread consists of an infinite loop, which is only

terminated when an error occurs or when a global flag is set. This follows the normal practice that threads should, terminate themselves.

First, the loop within the thread calls a comms API to find out how many bytes there are in the comms buffer. This has the seemingly inappropriate name of *ClearCommError()*. In our case, this function will nominally return either 4K or zero. The 4K is the default transfer size for the FT driver as specified in the INF file.

If there are bytes to read, the *ReadFile()* function is called to move these into a circular buffer, and then call a function in the user program. I have called this function *CommReadCB()*.

If there are no bytes to transfer, I put the thread to sleep for 10ms. The *Sleep()* API Function is a simple and efficient way of blocking a thread for a fixed time, as it manages the multi-tasking using very few CPU cycles. This blocking ensures the thread does not run continuously and hog large chunks of the CPU time.

In my version of the program, I have extra code within the thread for interpolation of arrays, and for detecting a 'trigger' point to emulate a triggered scope. These are written around the 'Read' and 'Sleep' function to maximise usage of spare time. I have not shown these as they may reduce readability of the relevant sections of code.

The call-back function in the main program is used to process the incoming array. For example, rearranging data arrays for display bitmaps, performing filtering and processing, etc.

One important point for software developers: you must avoid calling certain Windows API functions – especially graphics functions – from within the thread or the user callback. If you do call them, you may cause very odd effects including crashes. Windows provides the *PostMessage* function for this.

The FTDI driver defaults to 4Kbyte transfers. There can be up to 250 calls to the call-back routine in the main program every second. Some PCs can handle this rate quite happily.

With my simple demo program, my 300MHz PC had enough time to display each array received on oscilloscopelike graphics, and also compute and display the same data as a 1024 point FFT – including integer to float interpolation.

I am not much of an artist when it comes to designing front panels; Fig. 5. shows my crude attempts at displaying the scope output on a PC. I am sure you will be able to come up with much more attractive designs.

Taking it further

If you intend to commercialise your product using USB you must register your own VID and PID with the USB organisation (www.usb.org). Becoming a member of this Forum costs \$2500 a year, although you can obtain an ID number without joining the organisation for \$1500. Check their web site for more details.

Alternatively, you could use the default VID and PID of the FT245 device. This has the disadvantage that connecting multiple FT devices to the same computer may confuse the drivers. However, FTDI maintains a book of unique PIDs that can be uniquely allocated to you at no cost. Please email *fred.dart@ftdchip.com* for more details.

The only downside of going this route is that you cannot apply for USB certification for your products, or use the new trademarked USB logo on your packaging. To do so, you must apply for your own VID.

You may be wondering how the PID is stored within the FT chip. The FTDI device contains its own hard-wired VID (0403_{16}) and PID codes (0601_{16}) . When adding your own, you store these in a separate 93C46 EEPROM chip that connects directly to the FT245 chip. This is the little box shown in Fig. 3 of last month's article.

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Are you using high level programming	g languages?		Yes	No			
Do you use a PC at work?			Yes	No			
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Do you buy test gear?	Yes	No		
If you buy test gear, do you buy it largely for home	or for work? Home	Work	Both	
If you buy test gear, approximately how much a ye	ar do you spen £500	d? £500-1000	£1000-10000	>£10000
Is the majority of test gear you buy new, or second	I hand? Mostly new	Bit of both	Mostly secor	nd hand
What earnings bracket are you in? Note that we w accordance with the law.	ill only use this Up to £12k	information for £12-22k	statistical purp	oses, and strictly in
,				
Are there other magazines in the same field that y	ou enjoy/value Yes	more than Elec No	tronics World?	
If so, what's your favourite?				

60

If you don't subscribe, to Electronics World, what stands in the way of you subscribing?

What	is y	ou f	avourite	section	in the	magazine?	' (i.e.	circuit ideas,	news,	new	products,	RF	design)
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What additional topics, if any, would you like to see in the magazine?

Which three suppliers do you use most, regardless of whether they are component or equipment suppliers?

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2	
3	

Which single supplier do you spend most with?

If you work in electronics and buy components and equipment, which three prodcut areas do you spend most on? (e.g. T&M, passives, digital ICs)

What is your job title?

PRIZE DRAW

Many thanks for spending the time to fill in our survey. Please mail it back (no stamp needed) to *Electronics World* FREEPOST SEA11435 Sutton SM3 8BR

or fax it on 01792 878233.

Senders of all entries returned by 11 March 02 will be eligible to win one of five, one-year subscriptions to *Electronics World*. This applies to overseas readers too. Winners will be drawn from a hat and notified by post as soon as possible after the closing date. The prize is not negotiable, and no correspondence will be entered into regarding it. Please provide your details in the coupon on the right.

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Real-time digital filter

RTDF – a unique, integrated hardware and software system for designing, downloading and running very high performance filters in real time. Available only from *Electronics World*.

The RTDF 1.5 is a unique real-time audio-bandwidth digital filter with infinitely adjustable characteristics – all available at the click of a button. Filter design and execution is accomplished in two easy steps. In fact, you can have a no-compromise filter up and running within seconds.

If you want to change the filter completely – low-pass, bandpass, high-pass or arbitrary – just repeat the two steps.

he RTDF filter system includes hardware based on an advanced digital signal processor, low-level firmware that implements the filtering operations, and a high-level PC-based software interface that designs the filter according to your requirements. Once a filter is designed, the software interface is used to download the filter to the hardware system via a serial link,

where it is executed on demand. You don't need to know about digital signal processing theory or the mathematics associated with digital-filter design. But if you're a filter expert, you won't find yourself restricted by

RTDF - key features

- Runs under Windows 95, 98 or ME
- Generates FIR filters with a maximum of 1024 coefficients.
- Multiple pass, stop or arbitrary filters.
- Lower –3dB frequency 3.7Hz at 48kHz sample rate and 1.2Hz at 12kHz sample rate.
- Filter operates in single or dual channel modes.
- Import mode ASCII import of any frequency response.
- Hardware module holds up to 16 filters, instantly selectable with one mouse click.
- Zero-phase distortion in the pass, transition and stop bands, ignoring input and output coupling.
- Choice of rectangular, Bartlett, Hamming, Hanning, Blackman or Kaiser Windows.
- Virtual control panel allowing run-time changes to filter gain and sampling rate.
- Includes frequency and time domain plots of filter performance.
- Frequency response plotted as linear, dB, square, root, real, imaginary or phase.
- Impulse, frequency and phase response exportable in a variety of formats (dB, power etc) as ASCII files for incorporation into standard spreadsheets.
- 18-bit resolution in single, 16-bit in dual-channel mode.
- Normal or turbo speed, software selectable.
- User selectable sample rates of 48kHz, 24kHz, 16kHz, 12kHz, 9.6kHz, 8kHz, 6kHz, 4.8kHz, 4kHz, 3.2kHz or 3kHz.
- Maximum input and output level 4V pk-pk



Step 1 – Enter the characteristics of your filter, such as cut-off points and sharpness, in the Filter Design Interface Window on the left of the display.



Step 2 – Click on the Update button to view your result in graphical form, download it to the hardware module and run the filter from the hardware control panel – done!



Filter hardware: a DSP56002 board with RAM and 16/18bit oversampling analogue converters for stereo i/o at up to 48kHz sampling.

RTDF Version 1.5

the easy-to-use interface. If you want to do it the hard way, you can even design your filter in long-hand then download the filter's frequency response as an ASCII file to the RTDF's control program!

The RTDF is a total filter solution. Due to its flexibility, it is particularly well suited to processing audio signals in real time. High-quality analogue signal conditioning and a dual-channel 16/18-bit resolution analogue-to-digital converter and digital-toanalogue converter provide a resolution sufficient for the most demanding applications.

In short, the RTDF brings the power of digital signal processing to any audio-bandwidth domain that requires highperformance electronic signal filtering. Applications include sensor linearisation, audio signal processing, signal analysis, vibration analysis, education and research in electrical, electronic and other physical sciences.

Low-pass, high-pass, multiple band-stop / band-pass filters may be combined to produce very complex filters for frequencies up to 24kHz.

The software can accept measured responses to define a filter template. This can be used for measurement equalisation or to search out signal signatures in noisy environments.

Since the filters are implemented using a symmetrical finite impulse response (FIR) method, no phase distortion occurs in the filtered signal – no matter how sharp the filter is. Because the processing module is so fast, it is possible to design filters with responses far beyond what is possible with traditional analogue techniques.

Windows software

The control program runs under Windows and provides a user-friendly filter-design tool that de-mystifies the process of specifying the filter. The filter design process simply becomes one of describing the desired frequency response.

The design package indicates the response that will be produced and any deviations from that specified. User designs may be stored for re-use and actual responses may be entered from measurements for simulation or equalisation purposes. Once designed, filters are calculated and downloaded to the hardware within seconds.

The software designs the filter according to the user's specifications. The filter is expressed as a set of FIR, zero-phase distortion coefficients or taps. Collectively, the coefficients of a filter are known as the *impulse response*.

The system's gain and sampling rate can be adjusted while the filter is running and you can switch instantaneously between a filtered or non-filtered signal.

Most importantly, the software includes a flexible filter design section. A window displays the frequency or impulse response of the realised filter in various formats. A hardware control section downloads a filter and provides for general communication with the filter module.

The DSP module

The hardware DSP module connects to the PC via a standard COM port using the cable supplied.

The 16/18-bit over-sampling dual-channel sigma-delta a-to-d and d-to-a conversion system can easily be set to any one of eleven sample rates, ranging from 48kHz down to 3kHz.

So how fast can the filter operate, and how many filter coefficients can it practically employ? The maximum number

of taps at the highest frequency range of 24kHz, in single

Minimum system requirements

100 MHz PC running Windows' 95, 98 orME SVGA display (800X600 pixels) or higher CD ROM drive 10 Mbytes of free hard disc space Serial port, capable of 19200 bit/s channel mode, is 527. At this range, the system is sampling at 48kHz. This represents a very sharp filter indeed.

Using a frequency range of 12kHz – sample rate of 24kHz – in single-channel mode, the system can operate a filter with a maximum of 937 taps. At any range below this, it can operate a filter with a maximum of 1024



taps. The performance of a 1024-tap filter is so extremely sharp that it is quite unlikely that you would ever need to use it.

In dual-channel mode, the maximum number of taps permissable at the highest frequency range of 24kHz is 191. With a frequency range of 12kHz, it is 397. Full details concerning tap numbers, frequency ranges and operating modes are provided with the on-line documentation.

System includes:

- Filter DSP board
- Windows filter design software on CD plus demonstration-filters
- Fully-worked help files featuring tutorial
- Installation instructions
- Analogue i/o cables
- RS232 COM port download cable
- Power supply

Use this coupon to order your RTDF filter

Please send me RTDF filter(s) at £243 excluding £7 shipping and UK special delivery and VAT (£293.75 fully inclusive: e-mail electronics.world@ntlworld.com for quantity discounts and o/s postage rates). Note: normal price £499!

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Please mail this coupon to *Electronics World*, together with payment. Alternatively fax credit card details with order on **020 8722 6098**

Address orders and all correspondence relating to this order to RTDF Offer, Electronics World, Anne Boleyn House, 9-13 Ewell Road, Cheam, Surrey, SM3 8BZ email j.lowe@cumulusmedia.co.uk Make cheques payable to Electronics World. Reader offer - buy the VIOM for £33.60 excluding VAT and save 20%.

VIOM control module

For a limited period, Phaedrus is offering its new VIOM i/o control module to Electronics World readers at a special offer price of £33.60+VAT (£39.48 total). Carriage is free for the duration of this special offer. Its normal ex-VAT price is £42 excluding VAT.

VIOM is a versatile input/output control module. Each of its 16 outputs may be controlled locally by its own inputs, timers and counters or remotely via the module's RS232 serial port.

In local mode the outputs can be controlled by an AND/OR combination of any of the inputs. In addition delays can be inserted before the output becomes active and then on and off periods can be defined to allow for cyclic operation.

Active high or active low operation can be individually selected on each output. Outputs may be controlled from a host via the serial port either individually or collectively. The host may also interrogate the module for the current status of the outputs.

Timer/counter registers can be controlled from the inputs or the serial port. Thresholds can be defined which can be used to trigger outputs. Values of the registers can be read via the serial port.

Inputs can be interfaced with clean contacts or CMOS/TTL outputs and can be defined as active high or active low. They can be programmed to report to a host via the serial port either periodically or on a change of state. Alternatively the host can interrogate the module and request the status of all inputs.

Optional dedicated 8 way display and relay modules can be connected directly to the module via ribbon cable.

Software

Any PC application that can be used to generate ASCII characters and send them out through the RS232 COM port can be used to program the module. Hyperterminal, supplied free with Windows, is among these applications.

There are 95 ASCII-based commands for controlling the module.

PC based applications can be downloaded from the web address given here, free of charge. These include a tutorial and a data logger plus applications for monitoring and controlling I/O pins together with the eight counter/timers. More applications will follow during 2002.

Several projects for using VIOM in stand alone mode are supplied with kit. These include a combination lock, a quiz box, chasing lights and a logic demonstrator.

How do I order?

The offer price includes the i/o module plus its manual in PDF form, and the example projects mentioned above. Offer ends 31 May 2002 and is limited to one item per reader. A special web address has been set up exclusively for EW readers wanting to order by credit card. Visit:

www.phaedrusltd.co.uk/pages/html/ewoffer.html

for details. Alternatively, send a cheque or postal order to Phaedrus Ltd, Unit 1 Darwin Enterprise Centre, Railway Road. Darwen BB3 3EH.

Please make cheques payable to Phaedrus Ltd. Phaedrus can be reached on 01254 772622.

Applications

- Education
- Low-volume productionPrototype
- Domestic control
- Digital I/O
- Data links
- Security systems



Specification

- Number of Inputs: 16
- Number of outputs: 16
- Inputs: CMOS, 10kΩ loading
- Output type: open collector
- Max output current: 100mA
- Number of internal (virtual) I/O ports: 16
- Number of timer/counters: 8
- Size of timer/counters: 32 bit
- Time resolution: 10ms
- RS232 serial port
- 95 ASCII based commands
- Supply voltage: 9-16V DC
- Supply current: 40mA
- Dedicated display and relay modules available

Programming the module

The following VIOM code could be sent to VIOM as a text file via its RS232 port. The lines starting with '<' are comments. The code makes output 2 oscillate at a rate of 1 second on and 1 second off when inputs 1 and 2 are both active. The first line, 'FDL', tells the VIOM module that the following file is a download:

FDL <AND inputs on output 2 W02J0 <Set On time for output 2 to 1 second W02D2 <Set Off time for output 2 to 1 second W02E10 W02F2 <Allocate Inputs 1 and 2 to Output 2 W02L1 W02L2

www.phaedrusltd.co.uk

Connector types

- Power: screw terminals
- Inputs: screw terminals and IDC 10-way male connectors
- Outputs: screw terminals and IDC 10-way male connectors
- Dimensions: 90mm×107mm (3.4in×4.25in)

SMALL SELECTION ONLY LISTED - EXPORT TRADE AND QUANTITY DISCOUNTS - RING US FOR YOUR REQUIREMENTS WHICH MAY BE IN STOCK

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UK MAILING LIST GROUP

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Following on from the newsgroup discussion last month there is a UK Email group for TV technicians where you can send an Email to everyone in the group. There's just over 30 people in the group at present. For more details and how to register look at the egroup home page. Just a general comment though you do have to be careful who you give your Email address to so that you can avoid "spamming" - that is getting lots of unwanted Email about dubious Russian site (amongst others).

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Specifications

Switch position 1 Bandwidth Input resistance Input capacitance Working voltage

Switch position 2 Bandwidth Rise time Input resistance 1MΩ Input capacitance Compensation range Working voltage DC to 10MHz $1M\Omega$ – i.e. oscilloscope i/p 40pF+oscilloscope capacitance 600V DC or pk-pk AC

DC to 150MHz 2.4ns $10M\Omega \pm 1\%$ if oscilloscope i/p is

12pF if oscilloscope i/p is 20pF 10-60pF 600V DC or pk-pk AC

Switch position 'Ref' Probe tip grounded via 9MΩ, scope i/p grounded

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3P221

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ADVERTISERS' INDEX

BETA	. 43
CONFORD ELECTRONICS	. 31
CRICKLEWOOD ELECTRONICS	. 35
CROWNHILL	IBC
E. A. SOWTER	. 31
J. K. MICROSYSTEMS	. 37
J&N FACTORS	. 70
JOHNS RADIO	. 65
JPG ELECTRONICS	. 35
LABCENTER ELECTRONICS	BC
LAPLACE INSTRUMENTS	. 37
MILFORD INSTRUMENTS	. 35

NUMBER ONE SYSTEMS	. 29
OLSON ELECTRONICS	8
PICO	. 31
QUASAR ELECTRONICS	. 33
QUICKROUTE	2
RD RESEARCH	4
SPACE ELECTRONICS	. 43
STEWART OF READING	. 43
TELNET	IFC
TEMWELL CORPORATION	. 53
TIE PIE	. 45

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