

BUILD YOUR OWN 16 bit, 64K RAM colour computer

Standard features –

- High speed 24K byte extended basic interpreter
- Powerful TMS9995 16 bit microcprocessor
- 48 bit floating point gives 11 digit accuracy
- High resolution (256 x 192) colour graphics
- Screen memory does not use up user memory space
- 16 colours available on the screen together in graphic mode
- Fast line drawing and point plotting basic commands
- High speed colour shape manipulation from basic
- Full textual error messages
- String and Array size limited only by memory size
- Real time clock included in basic
- Interval timing with 10mS resolution via TIC function
- Named load and save of basic or machine code programs
- Auto-run available for any program
- Powerful machine code monitor
- Assembler and Disassembler included as standard
- Auto line numbering facility
- Full renumber command
- Simple but powerful line editor
- Buffered i/o allows you to continue executing the program while still printing
- Flexible CALL statement allows linkage to machine code routines with up to 12 parameters
- Basic programs may contain spaces between key words to make programs readable without using more memory
- Over 34K bytes available for basic programs
- Extended basic includes IF-THEN-ELSE
- Supports up to 16 output devices: Screen and cassette interfaces included as standard
- Supports bit manipulation of variables from basic
- Error trapping to a basic routine included
- Basic supports Hexadecimal numbers
- Separate 16K video RAM for graphics

With this powerful machine (featured in Electronics Today International as a constructional project) you have access to highly advanced systems and software developed specially by MPE Ltd for the CORTEX. For business, education, R & D – or simply increasing your knowledge and understanding of computers – it beats comparably priced off-the-shelf machines hands down!

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STATEMENTS PRINT TIME RENUM MAG MWD (1) INT POS IF WAIT BOOT TOF BASE (1) LOG COL NON LOAD TEXT DIM RUN SOR MOD GOTO 1 UNIT MOTOR PLOT EF SIZE FNA-FNZ TIC KEY GOSUB BAUD ESCAPE UNPLOT DEF SIZE FNA-FNZ TIC KEY GOSUB BAUD ESCAPE UNPLOT DEF CONMANDS ADR SGN OPERATORS POP CALL NOESC COLOUR NEW MON ADR BUT OR REM DATA RANDOM CHAR END DELIMITERS SASC CRB LOR NEXT RESTOR LIST SHAPE CRB TO SIN MEM AND NEXT RESTOR LIST SHAPE CRF TAB COS MWD NOT INPUT STOP NUMBER SGET <
Self assembly kit £295 Ready built £395 All prices exclusive of VAT. Carriage paid.
Optional extrasRS232C interface kit£9.20Floppy disc interface£65.50Pair of 5¼" disc drives£300.00Hardware kit & connectors£49.50CORTEX B – Basic machine+ RS232CCORTEX C – as above + disc drives £895.00Full assembly instructions and 216 page user's manual.
POWERTRAN CYBERNETICS, Portway Industrial Estate, Andover, Hants SP10 3NM.
Please send me
Name

PRACTICAL ECTRONICS

VOLUME 19

No. 12

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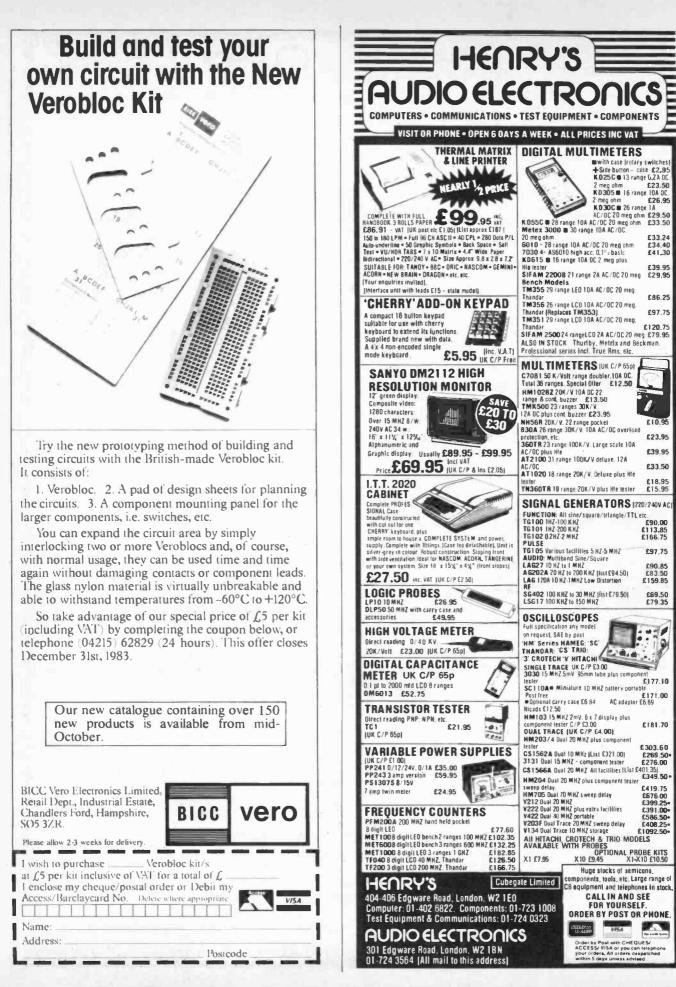
DUE TO LACK OF SPACE PART TWO OF SIMPLE SPEECH AND MICRO-FILE HAVE BEEN HELD OVER TILL NEXT MONTH

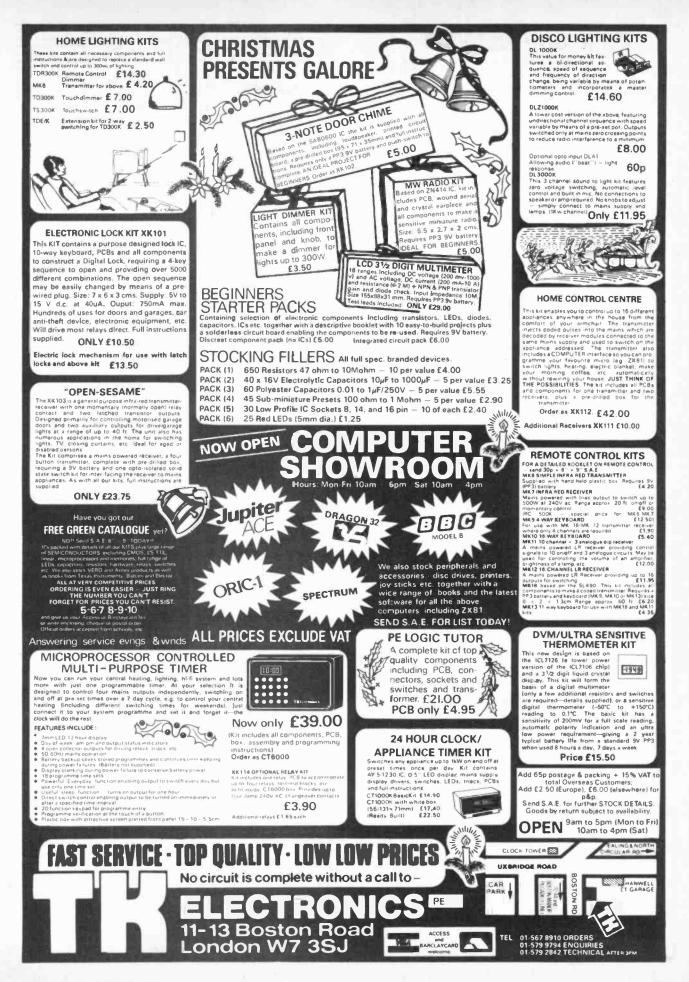
OUR JANUARY ISSUE WILL BE ON SALE FRIDAY, DECEMBER 2nd, 1983 (for details of contents see page 65)

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8Ω, 0 3W, 2"; 2·25", 2·5", OPTO C	S LIQUID	VOLTAGE REGULATOR	5	SOCKETS	S profile w	Wire wrap	S	SPEC			ORT	Н
3" 80p LEDS price includes Clips 0 0.3W, 2-5" 40(1; 64(1 or TIL209 Red 3mm 10 3	HSPLAYS digit 495	+ve -ve 5V 7805 40p 7905		8 pin 14 pin	8p 2 10p 3	25p 35p				UNI		
80Ω 80p TiL211 Green 3mm 14 4 TiL212 Yellow 14 6	digit 530 digit 625	12V 7612 40p 7912 15V 7815 40p 7915	45p	16 pln 18 pin	10p 4	42p			0		IN RO	
DIODES BRIDGE TIL220 2" Red 12	PX25 195	18V 7818 40p 7918		20 pin 22 pin	20p 6	60p						for Cen-
AA129 20 (plastic case) Rectangular LEDs with B	PW21 295 PX65 270	100mA T092 Plastic Casing	c =0.	24 pin	25p 7	70p 1						on 16K
BA100 15 1A/100V 20 Rectangl. Stackable	L74 65	5V 78L05 30p 79L0 6V 78L62 30p	5 50 p	28 pin 40 pin			or 48K	Specti	rum.			
BAX13 20 1A/400V 26 LEDS 18 II BY100 24 1A/600V 34 Triangular LEDS R&G 18 II Triangular LEDS R&G 18 II	LO74 115 LO74 220	8V 78L82 30p 12V 78L12 30p 79L1		ZIF DIL S	OCKET	_	M				ATUR	ES
BY126 12 2A/50V 30 0-2 Hashing LED Hed 56 IL BY127 14 2A/200V 40 0-2 Bi colour LEDs	LCT6 Darlington solator 135	15V 78L15 30p 79L1	5 50 p	24 way	ę	565p				£59.	00 I details	
CR033 250 2A/400V 46 Red/Green 65 T CR033 40 2A/400V 65 Green/Yellow 80 C	TIL111 70 DCP71 120	ICL7660 248 LM317K 78H05 5V/5A 550 LM317P	250 99	28 way 40 way		750p 799p				eed block		1
0A47 12 6A/100V 83 02" Tri colour LEDs 0A70 12 6A/600V 125 Red/Green/Yellow 85 2	0RP12 78	78H12 12V/5A 640 LM323K 78HG +5 to LM337T	500 175	DIL PLI	UGS (Heade	prs)			PCB Male	e Fer	nale	Female
OA79 15 10A/200V 215 02" Red High Bright 59 4	N33 135 in diode 610	+24V 5A 599 LM723	30		Solder IDC 38p 95	C	2 rows			Ingle So		Card-Edge Connector
OA85 15 25A/200V 240 10271 lotra Bed (amit) 46	Schmitt	-24V 5A 685 RC4194	375	16 24	42p 10	00p 38p	10 way	,	90p			120p
OA91 8 BY164 56 TIL32 Infra Red (emit) 52	Receiver 610	LM309K 120 RC4195	160	28	14	10p	16 way 20 way	, •	130p 1	50p 110	Op	195p 240p
OA200 8 TIL 78 (detector) 55 S	SWITCH	SWITCHES				18p	26 way 34 way	, ,	175p 2	00p 15	Op	320p 340p
OA202 8 ZENERS TIL81 82; TIL100 90 T	Reflective		2A 250V	RIBBON	(price per		40 way 50 way	1	220p 2	50p 19	Dp ·	420p 470p
1N916 5 39V 400mW 1N4001/2 5 8p arch 7 Segment Displays	o RS 186	1A DPDT 14 SPST 1A DPDT C/OFF 15 DPDT	35 48	10	Grey Co 15p 28	olour 8p		-	235p 2	70p 20		
1N4003 6 Range 3V3 to TL 321 5"C.An 120 A	×23×2" 85	A DP on/on/on 40 4 pole on SUB-MIN		16 20	25p 40 30p 50	Op		ECTORS		FEMALE	S PLI	JGS
1N4006/7 7 33V 3W DL704 3"C.Cth 125 4	×21×21" 103 ×4×21" 120	Spring loaded SP change	2 amp	26	40p 65 60p 85	5p	DIN 41	ashed cor 617 31 w	ay	170p -	Angle Str	1750
1N5401 15 FND357 or 500 130 5	×4×2" 105	Latching or SPST on	off 54	40	70p 90		DIN 410 DIN 410	612 2×32 612 2-3×3	2 way 32 way	275p 3	320p 22	Op 285p
1N5406 17 MVAM2 165 ±1 3" Red or Green 150 5	×21×11 90 ×21×21 130	SPDT c/over 110 SPDT Bla	sed 105				DIN 41	612 3×32	? way	360p 3	385p 26	Op 395p
1544 9 BB105B 40 Bargraph NSM3914 500 5	x4x1]" 99 x4x2]" 120	DPDT C/C	DFF 88	'O' CONNI Pins	ECTORS: 9 15	25	27 3	-0-3V. 6-0	0-6V 100m	nA. 9.0-9V		
6A/100V 40 FERRIC CHLORIDE	x4x2" 120 x4x3" 150	Non Locking DPDT Bia			vay way	way	way 6	VA: 2×6	V- 5A; 2x	-0-15V 75r 9V-4A; 2x		98p
6A/800V 65 3A/100V 48 80E 1 500 - 80 8	x5x3" 180 x6x3" 210	Push to make 15p 3 pole c/ Push break 25p 3 pole c/	over 205		80p 110p	160p		2VA: 2x		2x6V-12	A; 2x12V-	250p 5A;
3A/400V 56 1959 + 50p paip 1 3A/800V 85 DALO FTCH DECIST	0×41×3" 240 0×7×3" 275			Angle 15	50p 210p	250p	355p 2:	x15V-4A			325p	(35p p&p)
SCR's 8A/100V 60 DALOEICH RESIST 1 8A/400V 69 Pen plus spare tip 100p 1	2×5×3° 260 2×8×3° 295	ROTARY: (Adjustable Stop 1 1 pole/2 to 12 way, 2p/2 to 6 wa	y. 3 pole/	FEMALE			1			8A 15-8A	; 20V- 6A	(60p p&p)
Thyristors 84/800V 115 0 8A-100V 32 12A/100V 78 COPPER CLAO BOARDS		2 to 4 way, 4 pole/2 to 3 way	48p	Solder 10 Angle 16		290p	338p 5	OVA: 2×			; 2×12V-2 ; 2×30V-0	A; 2×15V-
5A/300V 38 12A/400V 82 Fibre Single Double- 5A/400V 40 12A/800V 135 Glass sided sided	SRBP 95"×85"	ROTARY: Mains 250V AC, 4 Am				300p	420p				465p	(60p p&p)
5A/600V 48 16A/100V 103 6"×6" 100p 125p	110p	OIP SWITCHES: (SPST) 4 way 6 way 80p; 8 way 87p; 10 way		COVERS 8	80p 75p	75p				A; 2×15 /-1 25A; 2		
8A/600V 95 16A/800V 220 VEROBOAROS 0 1"		(SPDT) 4 way 190p.		IDC 25 way	y Pig. 385p. 9	Skt. 450		Lineac -		0166		(600 <u>p8(p)</u>
12A/100V 78 25A/400V 185 12A/400V 95 25A/800V 295 21×31" 85p — 'DIP Bo	ard 374	AMPHENOL PLUG					D	IL Plug It	Headers)		ble Assem	DIA
12A/800V 188 25A/1000V BT106 150 480 21×5" 100p - Vero St 31×37 100p -	rip 144	24 way IEEE 475p	Solder 450p		EOGE CO	NNECT	UNS		14pin	16pin	24 pin	40 pin
B1116 180 30A/400V 525 33×5" 115p 95p PROTO C106D 38 T2800D 120 33×17" 390p 275p Verobio		36 way Centronics 525p	450p 485p	SIL	2 x 18 way	180p	2	4" louble En	145p	165p	240p	325p
TIC44 24 TIC45 29 SOLDERCON Pit of 100 pips 55p Europe	350 adboard 520	ASTEC UHF MOOULATORS		0.1" 20 way	2×22 way 2×23 way	199p 170p		6"	185p 198p	205p 215p	300p 315p	465p 490p
TIC47 35 PINS Spot Face Cutter 150p Bimboa	rd 1 695	6MHz Standard 325p 8MHz Wideband 450p		65p 32 way	2×25 way 2×28 way	225p 210p	20	4"	210p 230p	235p 250p	345p 375p	540p 595p
2N4444 130 500 370p Pin Insertion Tool Superst	110 JJZ 113			95p	2×30 way 2×36 way	245p 295p					umper Lea	
OLAC VERO WIRING PEN and Spool	350p Combs 6p ea.				2×40 way 2×43 way	315p 395p			20pin 160p	26pin 200p	34pin 260p	40pin 300p
ST2 25 Spare Wire (Spool) 75p; Wire Wrapping Stakes 100	250p	AUCTAL	1		2×75 way	550p						525p
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		HER 8300 DAISEY	CRYS			005	-					JEDP
COMPUTER CORNER	BROT	HER 8300 DAISEY HEEL PRINTER/	CRYS 32-768KH 100KHz	z 100 235			MIC	CRO	CON	NPU'	TER	
COMPUTER CORNER • SEIKOSHA GP100A – Unihammer Printer, no	BROT W	HER 8300 DAISEY	32-768KH 100KHz 200KHz	2 100 235 268	Model A	£299;	MIC Mode	CRO el B £3	CON 199 (in		TER). We st	tock the
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COMPUTER CORNER • SEIKOSHA GP100A – Unihammer Printer, normal & double width characters, dot resoluting raphics 10" Tractor feed, parallel interface stadard. FREE 500 Sheets	on an- 75 Provid type ir	THER 8300 DAISEY HEEL PRINTER/ TYPEWRITER les very high quality	32.768KH. 100KHz 200KHz 455KHz 1MHz 1.008M 1.28MHz	2 100 235 268 370 275 275 392	Model A full range ware like, shi), Disk	£299; e of Bl , Disc tettes,	MIC Mode BC Mic Drives Printe	CRO el B £3 cro pe s (Top rs, Pril	CON 199 (in riphera quality nter P	MPU Incl. VAT als, Har y Cuma aper, Ir	TER). We sindware ana & I interface	tock the & Soft- Vitsubi- Cable,
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COMPUTER CORNER SEIKOSHA GP100A – Unihammer Printer, no mal & double width characters, dot resolutio graphics 10" Tractor feed, parallel interface sta dard. FREE 500 Sheets £17 SEIKOSHA GP 250X Printer £20 SEIKOSHA GP 2700 The 7 colour graphic primt at the price of a standard Dot matrix printer. A uniq 4 hammer method enables text & high res graphi to be drawn in 7 basic colours or 30 shades. 7 × matrix. Up to 106 char. per line at 50 CPS. Variat line spacing to 1/120". Tractor or Friction feed. Ce tronix interface standard. £37 KAGA – RGB 12 inch medium resolution colo monitors £27 Connecting lead for KAGA monitors £27 MICROVITEC 14" colour monitor. RGB inp Lead incl. £27 TEX EPROM ERASER. Erases up to 32 ICs in 1 30 min. £27 SOLID STATE 30 minute Electronic TIME for above UV Erasors £1 Spare 'UV lamp bulbs £2 MULTIRAL PSU KIT. Output: +5V/5A; +12 +25V; -5V; -12V @ 1A. £37	BROT Winter Brown	THER 8300 DAISEY HEEL PRINTER/ TYPEWRITER les very high quality n any six interchange- tyles including Italics, and 4 conventional nees. Ideal for busi- use. Connects directly BC Micro via standard mics interface or can ed as a stand alone writer. As typewriter, built in timing func- built in timing func- busit in timing func- b	32 768/H 100/KH2 455/KH2 455/KH2 100/KH2 455/KH2 100/KH2 100/KH2 100/KH2 100/KH2 100/KH2 100/KH2 100/KH2 100/KH2 200/K	2 100 2 208 370 370 370 395 395 205 200 225 200 225 200 225 200 225 200 225 200 225 200 225 200 200	Model A f full range ware like, shi), Disk Dust Cove Connectoi Locational A send SAE DIS CS50 DIS CD50 S/S, 4 CS50 S/S, 4 CS50 S/S, 4 CS50 S/S, 4 SING own 1 BBC) Single Twin 1 (5 yeaal 10 Ver 10 Ver	£239, £239, a of Bid , Disc tertes, tertes, a of Bid , Disc tertes, a of Bid a of Bid tertes, a of Bid a DFS, Applica f on or a DFS, Applica f on or a OFS, Applica f on or a OFS, A OF	MIC MIC MIC Mode BC Min Printe assette assette work WORR WORR WORR WORR WORR WORR WORR CAS Cable Cable Cable Cable Disk	CRO al B £3 cro pe s (Top rs, Prilie Reco ade Ca FROM Construction Constructi	CON 199 (in riphera quality riphera phera bles, F bles, F Bolles, F Bolles sel, BC e leafle call	APU ccl. VAT als, Har y Cuma aper, III a Casse Plugs & ammele ird, EF Plugs & tarmmele ird, EF CALC, DOKS, of t. BCN b own sed witt ed witt ed witt n line	TER). We si dware ana & I hterface Socket Socket Socket Socket Socket Socket PROM ated W Softwain etc. etc. Powe th own h own h own h own case (400k Case (800k	tock the & Soft- Vitsubi- Cable, onitors, s), Plot- en Kit, Eraser, atford'ss r Sup- £180 PSU, £350 PSU, £250 d with £275 d with £275 d with £275 d with £212 f with
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SEMICONDUCTORS FROM ROUND THE WORLD	VALUE PACKS Pak No. Ory description Price VP28 10 Rectangular 2" Green LED'S £1.00 VP29 30 Ass. Zener Diodes 250mW - 2W Mixed Vits. Code £1.00 VP30 10 Ass. 10W Zener Diodes Mixed Vits. Code £1.00 VP30 10 Ass. 10W Zener Diodes Mixed Vits. Code £1.00 VP31 10 5 Amp SCR's T0-66 50-400v Coded £1.00 VP32 20 3 Amp SCR's T0-66 Up To 400v Uncode £1.00 VP33 200 Sil. Diodes Switching Like IN4148 D0-35 £1.00 VP34 200 Sil. Diodes Gen. Purpose Like 0A200/ BAX1316 £1.00 VP35 50 1 Amp IN4000 Series Sil Diodes Uncoded Ail Good £1.00 VP35 50 1 Amp IN4000 Series Sil Diodes Uncoded Ail Good £1.00 VP35 50 1 Amp IN4000 Series Sil Diodes Uncoded Ail Good £1.00 VP35 50 1 Amp IN4000 Series Sil Diodes Uncoded Ail Good £1.00 VP36 8 Bridge Rects. 4 x 1 Amp 4 x 2 Amp £1.00	BI-PAK SOLDER - DESOLDER KIT Kit comprises: ORDER NO. VP80 1 High Quality 25 ward General PurposeLightweightSolderingProm 240v mains incl 3716* (4.7mm) bit. 1 Quality Desoldering Purpe High Suctions with automatic ejection Knurled anti-corrosive casing and Teflon nozzle 1.5 metres of De-Soldering braid on pastic dispenser 2 yds (1.83m) Resin Cored Solder on Card T Heat Shunt tool tweezer Type Total Retail Value over £12.00 DUR SPECIAL KIT PRICE £9.95	BI-PAK PCB ETCHANT AND DRILL KIT Complete PCB Kr comprises I Expo Mini Drill 10,000 PPM 12V 0C Incl 3 collets & 3 x Twist Bits I Sheet PCB Transfers 210mm x Bom I Etch Resist Pen I Bich Resist Pen I Bich Resist Pen S aneets copper dad board 2 sheets Fibreglass copper clad board Full instructions for making your own PCB boards Retail Value over £15.00 OUR BI-PAK SPECIAL KIT PRICE £9.95 ORDER NO. VP81
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HYBRID LED COLOUR DISPLAYS Red, Green, Yellow – .3/.5/.6 inch Mixed types and colours NUMERIC & OVER- FLOW Common Anode/Cathode. GaAsP/GaP. Brand New, Full Data incl.	IC BARGAINS VP40 30 Assorted 74 Series TL 1C:s Gates, Flip-Flops & MS1:s + Data Book All New, Normal Retail Value Over £6:00. Our Price £2:50 VP41 30 Assorted CM0S 1C:s CD4000 Series, Pack Includes 00/09/12/14/18/21/23/2 52/28/03/54/4/58 AV/AE Types Public Type Data Sheet Value Over £8:00. Normal retail. £2:50 V041 30 Assorted TAS VP41 + Type number required. £2:50 RATCHET SCREWDRIVER KIT Comprises 2 standard screwdriver blades 5 & 7mm size, 2 cross point size 4 & 6. 1 Ratchet handle. 5-in-1 Kit £1.45 each. 0/No 329B	INTRUSION ALARM The DOOR BIRD DB 2000 alerts you before your door is opened. Just hang on the inside door knob – alarm is activated as soon as the outside door knob is touched. ONLY £3.95	OPTO 7 – Segment Displays Brand new 1st Quality LITRONIX DL 707R 14-pin Red 0.3" Common Anode Display 0-9 with right hand decimal point TTL compatible by DC Supply. Data supplied IN PACKS OF 5 pieces £3 10 pieces £5 0 pieces £5 100 pieces £30 100 pieces £30 100 pieces £30 30 peach THE MORE YOU BUY – THE LESS YOU PAY
Presented with a Pre- Quality Electronic d ALL at realistic information so Just solid facts we have availa STILL DO. We hold with is design and th telept	NEW CATALOGUE ofessional Approach and Appeal to ALL who require Components, Semiconductors and other Accessories prices. There are no wasted pages of useless often included in Catalogues published nowadays. i.e. price, description and individual features of what table. But remember, BI-PAK's policy has always been y components at competitive prices and THAT WE wast stocks "in stock" for fast immediate delivery, all our Catalogue are available ex stock. The Catalogue e Visa/Access credit cards, which we accept over the tone. eceive your NEW 1983 BI-PAK Catalogue, send 75p JS 25p p&p to:: Send your orders to Dept. PE12 BI-PAK PE SHOP AT 3 BALDOCK ST, WARE, HERTS TEMS: CASH WITH ODDER. SAME DAV		VP38 100 Sificon NPN Transistors All Perfect Coded Mixed, Types With Data And Eqvt, Sheet No Rejects Ranas- tic Value 63.00 VP39 100 Silicon PNP Transistors All Perfect, coded Mixed, Types With Data And Eqvt, Sheet No Rejects Real Value Value 23.00 2N3055 The best known Power Transistor in the word - 2N3055 NPN 115w. Out Bi-Park Special Offer Price 00 off 10.0 Mt 50.04 100 off B0312 COMPLIMENTARY PNP POWER TRANSISTORS TO 2N3055. Equivalent MJ2955-

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T.V. SOUND TUNER **BUILT AND TESTED** In the cut-throat world of

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WITH CASE £24.95 + £2.00p&p

f 24.95 + 2.00TV companies do their best to transmit the highest quality sound. Given this background a compact and Independent TV tuner that connects direct to your HI-Fi is a must for quality reproduction. The unit is mains-operated.

This TV SOUND TUNER offers full UHF coverage with 5 pre-selected tuning controls. It can also be used in conjunction with your video recorder. Oimensions: 11%"x 8%"x 3%" E.T.I. kit version of above without chassis, case and hardware. £12.95 plus £1.50 p&p

PRACTICAL ELECTRONICS SPECIAL OFFERI **STEREO CASSETTE** RECORDER KIT COMPLETE WITH CASE ONLY £31.00 plus £2.75 p&p

 NOISE REDUCTION SYSTEM. • AUTO STOP. • TAPE COUNTER. • SWITCHABLE E.O. • INOEPENDENT LEVEL CONTROLS. • TWIN V.U. METER. • WOW & FLUTTER 0.1% • RECORO/PLAYBACK I.C. WITH ELECTRONIC SWITCHING. • FULLY VARIABLE BECORDUNC BLASE FOR VARIABLE RECORDING BLAS FOR ACCURATE MATCHING OF ALL TYPES

Kit Includes tape transport mechanism, ready punched and back printed quality circuit board and all electronic parts. i.e. semiconductors,

resistors, capacitors, hardware, top cover, printed scale and mains transformer. You only supply solder & hook-up wire. Featured in April P.E. reprint 50p. Free with kit

STEREO TUNER KIT **STEREO CASSETTE** DECK This easy to

build 3 band stereo AM/ FM tuner kit is designed in conjunction

with P.E.(July

(31). For ease of construction and alignment it incorporates three Mullard modules and an I.C. IF System.

FEATURES: VHF, MW, LW Bands, Interstation muting and AFC on VHF. Tuning meter. Two back printed PCB's. Ready made chassis and scale. Aeriat: AM-ferrite rod, FM-75 or 300 ohms. Stabillsed power suppty with 'C' core mains transformer. All components supplied are to P.E. strict specification. Front scale size 10%"x 2%" approx. Complete with diagram and instructions

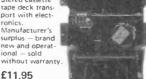
SPECIAL OFFER! £13.95 +£2.50 p&p.

125W HIGH POWER AMP MODULES

The power amp kit is a module for high power applications - disco units, guitar amplifi points opplicators class during, gallar anphi ers, public address systems, The unit is protected against short circuiting of the load and is safe In an open circuit condition. A large safety margin exists by use of generously rated components, result, a high powered rugged unit. The PC board is back printed, etched and ready to drill for ease of construction and the aluminium chassis is preformed and ready to use. Supplied with all parts, circuit diagrams and Instructions.

ACCESSORIES: Stereo/mono mains power sikit with transformer: £10.50 plus £2.00 p&p.





plus £2,50 p&p.

Stereo cassette

lust requires mains transform er and input/ output sockets and a volume control to com

plete. Supplied with full connection details



SPECIFICATIONS:

KIT £10.50

Max. output power (RMS): 125 W. Operating voltage (DC): 50 - 80 max. Loads: 4 - 16 ohm. Frequency response measured @ 100 watts: 25Hz - 20KHz. Sensitivity for 100w: 400mV @ 47K. Typical T.H.D. @ 50 watts, 4 ohms; 0.1%. Dimensions: 205x90 and 190x36mm

> BUILT £14.25 +£1.15 p&p



8" bass/mid range and 3%" tweeter. Complete with screws, wire, crossover components and cabinet. All wood pre cut – no cutting required. Finish - chipboard covered wood simulate, size 14%"x 8%"x 4". PAIR for ONLY

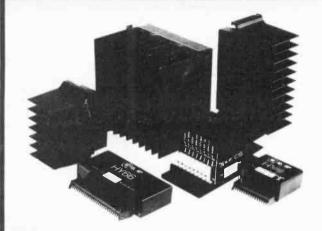
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Over the last few years we have received feedback via the general public and industry that our products are from Taiwan, Singapore, Japan, etc... ILP are one of the few 'All British' electronics Companies manufacturing their own products in the United Kingdom. We have proved that we can compete in the world market during the past 12 years and currently export in excess of 60% of our production to over twenty different countries - including USA, Australia and Hong Kong. At the same time we are able to invest in research and development for the future. assuring security for the personnel, directly and indirectly, employed within the UK. We feel very proud of all this and hope you can reap some of our success.

I.L.Potts - Chairman

WE'RE INSTRUMENTAL **IN MAKING A LOT OF POWER**

In keeping with ILP's tradition of entirely self-contained modules featuring, integral heatsinks, no external components and only 5 connections required, the range has been optimized for efficiency, flexibility, reliability, easy usage, outstanding performance, value for money.

With over 10 years experience in audio amplifier technology ILP are recognised as world leaders.



Module	Output	Load		ORTION	Supply	Size	WT	Price
Number	Power Watts rms	Impédance	T.H.D. Typ at 1KHz	1.M.D. 60Hz/ 7KHz 4:1	Voltage Typ	mm	9ms	VAT
0EYH	15	4-8	0.015%	<0.006%	2 18	76 × 68 × 40	240	£8.40
HY60	30	4.8	0.015%	< 0.006%	± 25	76 x 68 x 40	240	£9.55
HY6060	30 + 30	4.8	0.015%	< 0.006%	± 25	120 x 78 x 40	420	£18.69
HY124	60	4	0.01%	< 0.006%	± 26	120 x 78 x 40	410	£20.75
HY128	60	8	0.01%	< 0.006%	± 35	120 x 78 × 40	410	£20.75
HY244	120	4	0.01%	< 0.006%	± 35	120 x 78 × 50	520	£25.47
HY248	120	8	0.01%	< 0.006%	± 50	120 x 78 x 50	520	£25.47
HY364	180	4	0.01%	<0.006%	± 45	120 x 78 x 100	1030	£38.41
HY368	180	6	0.01%	<0.006%	± 60	120 x 78 x 100	1030	£38.41

Protection: Full load line, Slew Rate: 15v/µs, Risetime: 5µs, \$/N ratio: 100db. Frequency response (-3d8) 15Hz -50KHz, Input sensitivity: 500mV rms, Input Impedance: 100K Ω , Oamping factor: 100Hz >400,

PRE-AMP SYSTEMS

Module Number	Module	Functions	Current Required	Price inc. VAT
HY6	Mono pre amp	Mic/Mag. Cartridge/Tuner/Tape/ Aux + Vol/Bass/Treble	10mA	€ 7.60
HY66	Stereo pre amb	Mic/Mag. Cartridge/Tuner/Tape/ Aux + Vol/Bass/Trable/Balance	20mA	£14,32
H¥73	Guitar pre amp	Two Guitar (Bass Lead) and Mic + separate Volume Bass Treble + Mix	20m A	£15.36
HY 78	Stereo pre amp	As HV66 less tone controls	20mA	£14.20

Most pre-amp modules can be driven by the PSU driving the main power amp A separate PSU 30 is available purely for pre-amp modules if required for £5,47 (inc, VAT). Pre-amp and mixing modules in 18 different variations.

ease send for details.

Mounting Boards

For ease of construction we recommend the B6 for modules HY6--HY13 £1.05 (inc. VAT) and the B66 for modules HY66--HY78 £1.29 (inc. VAT).

MOSFET MODULES Supply Voltage Typ Size Price inc. VAT Module Output Load DISTORTION WT Powe 1.M.D. 60Hz/ Numl T.H.D. Watts Typ at 1KHz 7KHz 4:1 rms 4-8 4-8 <0.005% <0.005% <0.006% ± 45 20 × 78 × 40 MOS 248 ± 55 ± 55 20 × 78 × 80 39.8 MOS 364 180 <0.005% <0.0069 20 × 78 × 100 Protection: Able to cope with complex loads without the need for very special

rotection is to tope with Dompies loads without the need for very spec protection circuity (fuese will suffice) Slew rate: 20x/µx, Rise time 3µs, S/N ratio: 100db Frequency response 1–3480: 1542 – 100KHz, Input sensitivity: 500mV rms Input impedance: 100K Ω Damping factor: 100Hz ≥ 400,

'NEW to ILP' In Car Entertainments

C15

Mono Power Booster Amplifier to increase the output of your existing car radio or cassetie player to a nominal 15 watts rms.

Very easy to use. Robust construction

Mounts any where in car.

Automatic switch on.

Dutput power maximum 22w peak into 4Ω. Frequency response (~3d8) 15Hz to 30KHz, T.H.D. 0.1% at 10w 1KHz S/N ratio (DIN AUDIO) 80dB, Load Imperance 3Ω. Input Sensitivity and Impedance Selectable) 700mV rms into 15K Ω 3V rms into 8 Ω. Size 95 x 48 x 50mm, Weight 256 gms.

C1515 Stereo version of C15.

£17 19 (inc. VAT)

£9.14 (inc. VAT)

Size 95 x 40 x 80, Weight 410 gms.

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Model Number	For Use With	Price inc. VAT	Model Number	For Use With	Price inc. VAT	Model Number	For Use With	Price inc. VAT
PSU 21X	1 or 2 HY 30	£11.93	PSU 52X	2 x HY 124	£17.07	PSU 72X	2 x HV248	122.54
PSU 41X	1 or 2 HY60, 1 x HY6060, 1 x HY124	£13.83	PSU 53X	2 x MOS128	£17.86	PSU 73X	1 x HV364	122.54
PSU 42X	1 x HY128 1 x MOS128	£15.90	PSU 54X	1 x HY248	€17,86	PSU 74X	1 x HY368	124,20
PSU 43X	1 x MOS128	£16.70	PSU 55X	1 x MOS248	€19.52	PSU 75X	2 # MQS248, 1 × MQS368	624,20
PSU 51X	2 x HY128, 1 x HY244	£17.07	PSU 71X	2 x HY244	£21.75			

The toroidal transformer is now accepted as the standard in industry. overtaking the obsolete laminated type. Industry has been quick to recognise the advantages toroidals offer in size, weight, lower radiated field and, thanks to I.L.P., PRICE.

Our large standard range is complemented by our SPECIAL DESIGN section which can offer a prototype service within 7 DAYS together with a short lead time on quantity orders which can be programmed to your requirements with no price penalty.

50 VA

6+6 9+9

9+9 12+12 15+15 18+18 22+22 25+25 30+30 110 220 240

80 VA

Regulation 12%

6+6

9+9 12-12 15-15 18-18 22-22 25+25 30+30 110 220 240

11.67 13.64 14.87

19 30 89

22 67

1Kg

90 x 30mm

80 x 35mm 0.1 Regulation 13%

2x010 2x011 2x012 2x013 2x013 2x014

2x015 2x016

2×017 2×028

2-010

3×011 3×012

3x013

3x013 3x014 3x015 3x016 3x017 3x028 3x029 3x030

Size

5 6 7

VA

15 V A

SERIES SECONDARY RMS

Volts

6+6 9+9 12+12 15+15 18+18 22+22 25+25 30+30

(encased in ABS plastic)

Regulation 18%

6 • 6

9+9 12+12 15+15 18+18 22+22 25+25 30+30

Prices Including P&P and VAT

6.79

8.60

10.51

30 VA 0.45Kg

62 x 34mm 0.3 Regulation 19%

0:010

0+01

0x012 0x013 0x014 0x015 0x016

0x010

1+010

1+011

1x013 1x014 1x015 1x016

1-012

VA 15 Size £

50 80

120

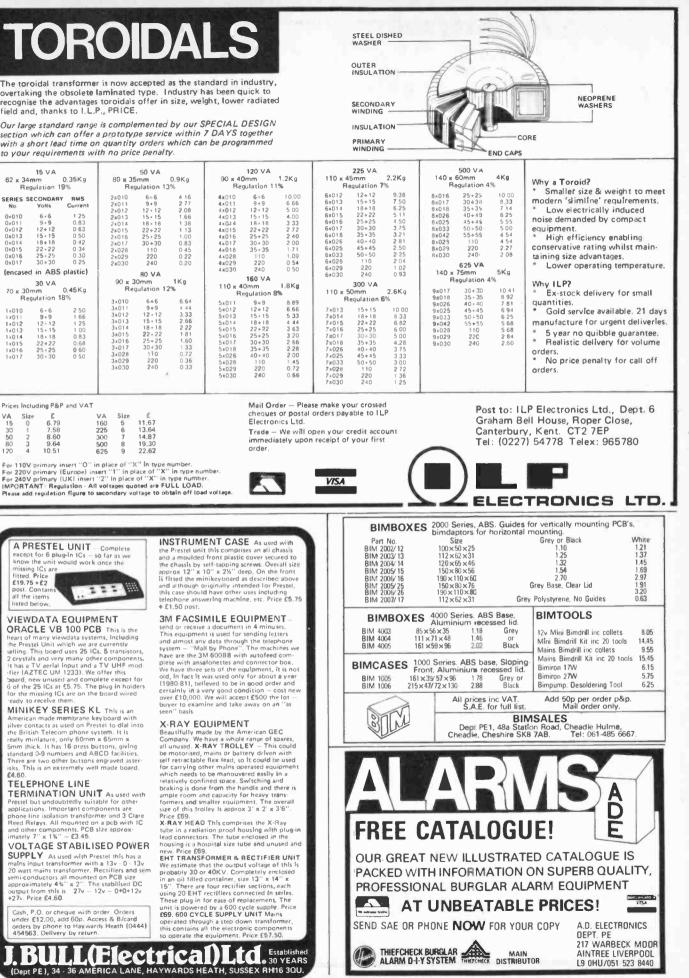
70 x 30mm

0.35Kg

Current

1.25

0.83 0.63 0.50 0.42 0.34 0.30 0.25



A PRESTEL UNIT - Complete except for 6 plug-in ICs - so far as a issing ICs ar fitted. Price £19.75 +£2 111. ost. Contain If the items isted below

VIEWDATA EQUIPMENT

VIEWDATA EQUIPMENT ORACLE VB 100 PCB This is the heart of many viewdata systems, Including the Prestel Unit which we are currently selling. This board uses 25 ICs, 5 transistors, 2 crystals and very many other components, 1 thas a TV aerial input and a TV UHF mod-tiler (AZTEC UM 1233). We offer this board, new unused and complete except for 6 of the 25 ICs at 55.75. The plug in holders for the missing ICs are on the board wired ready to receive them.

MINIKEY SERIES KL This MININET JOINED NE THIS is an American made membrane keyboard with silver contacts as used on Prestel to dial into the British Telecom phone system. It Is really miniature, only 60mm x 65mm x 5mm thick. It has 16 press buttons, giving standard 0-9 numbers and ABCD facilities. There are two other buttons engraved aster-isks. This is an extremely well made board. # 60 £4 60

TELEPHONE LINE

TERMINATION UNIT As used with Preset but undoubtedly suitable for other applications. Important components are phone line isolation transformer and 3 Clare Reed Relays. All mounted on a pcb with IC and other components. PCB size approx-imately 7" x 1%" – £3.45.

VOLTAGE STABILISED POWER $\begin{array}{c} \textbf{SUPPLIVE} \quad As used with Prestet this has a mains input transformer with a 13x \cdot 0 \cdot 13y \\ 20 watt mains transformer. Rectiliers and as semiconductors all mounted on PCB size approximately <math display="inline">4k^{\prime\prime}$ x $2^{\prime\prime}$. The stabilised DC output from this is 27v-12v-0+0+12v +27v. Price £4.60.

Cash, P.O. or cheque with order. Orders under £12.00, add 60p. Access & B/card orders by phone to Haywards Heath (0444) 454563, Delivery by return.

INSTRUMENT CASE As used with the Prestel unit this comprises an all chassis and a moulded front plastic cover secured to the chassis by self-tapping screws. Overall size approx 12" x 10" x 23" deep. On the front is fitted the minikeyboard as described above and although originally intended lor Prestel, this case should have other uses including telephone answering machine, etc. Price £5.75 + £1.50 not: telephone ans + E1.50 post.

3M FACSIMILE EQUIPMENT

3M FACSIMILE EQUIPMENT – send or receive a document in 4 minuses, This equipment is used for sending letters and atmost any data through the telephone system – "Mall by Phone". The machines we have are the 3M 60088 with autofeed com-plete with ansafonettes and connector box. We have three sets of the equipment, it is not id, in fact it was used only for about a year (1980-81), believed to be in good order and certainly in a very good condition – cost new over £10,000. We will accept £500 the lot – buyer to examine and take away on an "as seen" basis.

X-RAY EQUIPMENT

X-RAY EQUIPMENT Beautifully made by the American GEC Company. We have a whole range of spares, all unused. X-RAY TROLLEY – This could be motorised, mains or battery driven with self retractable flex tead, so it could be used for carrying other mains operated equipment which needs to be manouvered easily in a relatively confined space. Switching and braking is done from the handle and there is ample room and capacity for heavy trans-formers and smaller equipment. The overall size of this trolley is approx 3' x 2' x 3'6''. Price E69.

EHT TRANSFORMER & HEC (TITER OW). We estimate that the output voltage of this is probably 30 or 40KV. Completely enclosed in an oil filled container, size 13" x 14" x 15". There are four rectifier sections, each using 20 EHT rectifiers sconected in series. These plug in for ease of replacement. The unit is powered by a 600 cycle supply. Price unit is powered by a 600 cycle supply. P £69, 600 CYCLE SUPPLY UNIT Mains operated through a step down transform this contains all the electronic componen to operate the equipment. Price £57.50.



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and DOING







VOLUME 19 No. 12 DECEMBER 1983

POP SOFTWARE

THE personal computer has now been with us for five years and in that time has moved from a strictly specialist hobby item to a mass interest consumer product. The problem is that the real reasons for the development of the microprocessor and ultimately the personal microcomputer, for problem solving, accounting, design and development, filing and similar tasks, have been overshadowed—in the eyes of most consumers—by the ability of the micro to play exotic klingon-zapping games.

The sales of software cassettes for games are rivalling pop records and record companies are being forced into software sales to assure their futures. Don't get us wrong, we enjoy frog hopping and flying war planes (fictional ones of course) as much as the next person. We also believe the fun side of computing, which has led to the massive sale in the UK of home computers, can ultimately be blamed for providing the basis of an exciting new development in home computing: the use of micros for "serious" applications.

As regular readers will know, PE has shown the way in both microcomputer

development, with publication of a number of basic designs in the early days, and in developing systems for control purposes. Our Into The Real World series proved very popular with those wishing to interface their computers to the outside world. With electrical control systems for Aquarius and Electron being announced recently the manufacturers are also moving in this direction. It is noticeable that much of the new software is for applications like word processing, education, data bases and personal accounting; no doubt the software houses realise that game playing will eventually wane.

Where does PE come in? We will continue to provide designs to stretch microcomputers, to interface them with other items and to develop other uses. We will also provide designs using dedicated micros in control situations.

HOW THEY WORK

With so many children learning programming it is unlikely that there will be many jobs available in that area in years to come, but those with an additional understanding of electronics will no doubt be better placed to secure employment. Not so many months ago parents encouraged their children to learn electronics—the technology of the future—now they seem to feel computing is more important, let's not forget we need to know how the damn things work. For those that are interested our *Introduction to Digital Electronics* course is essential reading.

Incidentally we find it frustrating to buy some software in a microcomputer shop and have to wait while the assistant laboriously writes out a sales docket with full name and address and then separates the copies before taking the money. With all that high technology around surely just one micro could be switched from games to perform a sensible task of stock control and mailing list recording. Or is that just too much to expect?

We would be interested to hear of other areas where mundane tasks are still performed "by hand" while the staff are surrounded by high technology.

Mike Kener

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We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in PE. All letters requiring a reply should be accompanied by a stamped, self addressed envelope, or addressed envelope and international reply coupons, and each letter should relate to one **published project only**.

Components and p.c.b.s are usually available from advertisers; where we anticipate difficulties a source will be suggested.

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Copies of most of our recent issues are available from: Post Sales Department (Practical Electronics), IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SE1 OPF, at £1 each including Inland/Overseas p&p. Please state month and year of issue required.

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Copies of PE are available by post, inland or overseas, for £13.00 per 12 issues, from: Practical Electronics, Subscription Department, IPC Magazines Ltd., Room 2816, King's Reach Tower, Stamford Street, London SE1 9LS. Cheques, postal orders and international money orders should be friade payable to IPC Magazines Limited. Payment can also be made using any credit card and orders placed via Teledata. Tel. 01-200 0200. Items mentioned are available through normal retail outlets, unless otherwise specified. Prices correct at time of going to press.

BUBH Company

ELAN'S ENTERPRISE

The home-micro market is without doubt an expensive and difficult place in which to compete for business. By the spring Elan Computers will add their machine to the list, which by that time is likely to exceed 30 options under £250. Elan feel they will not be missing out on the Christmas sales bonanza by launching the Enterprise 64K in the spring. 'It's a product of the future' they say, and are confident that it will remain when its contemporaries bite the dust.

The Enterprise has 69 contoured full travel keys with an extra 8 being user-definable. On the right of the keyboard is an integral joystick that can be used for games or text manipulation. Two memory options are available, the 64K and the 128K, both are ultimately expandable to 4 Megabytes with add-on units.

The machine has a built-in word processor and can handle 56 lines of text at any one time, each with 84 columns. Two cassette recorder connections enable the user to read text or data from one tape machine while writing on the other, an indicator ensuring correct volume level setting when loading programs.

Another unique feature is stereo sound, via connection to a hi-fi or to headphones.

The graphics and sound are both controlled from customised chips. The colour of each pixel is controllable and therefore high-resolution graphics are a reality rather than a claim. Four sound sources are available each with full volume control on stereo output. Tone generation is from 30Hz to 125kHz giving a musical range of a full 8 octaves. Multiple combinations of white noise generation, low/high pass filtering and ring modulation are possible.

The peripherals are modular and designed to stack together in an effort to make interconnecting spaghetti-junctions redundant. The machine is connected to the stack 'base unit' via a flexible ducting which contains all the necessary wiring. The base unit has an adaptor which supplies all the peripherals in the stack with their needs.

Separate 64K ROM cartridges can be plugged into the machine, with games and education dominating this aspect. Languages include FORTH and LISP with BASIC as the norm.



ULTRASONIC MOVEMENT DETECTOR

Riscomp Limited have just launched the US 5063 which is an ultrasonic movement detection module employing digital techniques for processing the received signal. Such an approach not only provides a superior performance when compared to the conventional analogue circuitry, but also allows a choice of three levels of discrimination against false alarms. An exit delay and fixed alarm time have been incorporated, together with a selectable entrance delay. The transmitter section of the module is crystal controlled which allows several units to be used without inter-action problems where large areas are to be

covered. A 'hold' facility, together with a built-in I.e.d. indicator, is provided for setting-up purposes. Priced at £13.95, the module is supplied with a comprehensive data sheet.

Riscomp Ltd., 21 Duke Street, Princes Risborough, Bucks HP17 OAT (084 44 6326).

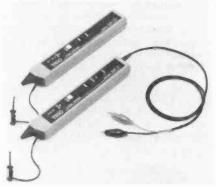


LOGIC PROBES

Two brand new logic probes from Trio are now available from House of Instruments, DP-71 for TTL and CMOS up to 50MHz and the DP-70 for TTL up to 30MHz.

Logic probes enable the quick determination of the operational state of logic circuits, providing the user with a powerful troubleshooting and development tool.

These new probes feature response to input frequencies up to 50MHz and pulse widths down to 20ns, achieved by an extremely short ground lead, inherent in the clever design. A light compact plastic case that can be held neatly in one hand, houses the probe and l.e.d.s, while switches have been grouped for easy viewing and use on one side only.



The operational status of logic circuits is clearly indicated by three l.e.d.s, one for logic high (1), one for logic low (0) and one to indicate the existence of a continuous pulse. By placing the probe to memory mode, single pulses down to 20ns in width, leading or trailing edges, can be detected with a reset switch to return to normal pulse mode.

Inputs are protected up to $\pm 100V$ d.c. continuous and $\pm 250V$ d.c. or 175V r.m.s. up to 15 seconds. DP-70 with a 50kohm input impedance, operates from internal batteries, while DP-71 has a 1Mohm impedance and may be powered from a 4.5 to 18V d.c. source.

The DP-70 is priced at £24.00 plus p&p and VAT and the DP-71 is priced at £49.00 plus p&p and VAT.

Quiswood Ltd. 30 Lancaster Road, St. Albans, Herts AL1 4ET (0799 24922).

POINTS ARISING...

INGENUITY UNLIMITED Pseudo Telephone Ringer Sept. 83 IC2 should be a 7490 decade counter---not a 555 timer as printed.

MAREE PLACE

ThermionicDIntegrated CircuitsFGAdva

A valve renaissance? Thermionic valve versions of integrated circuits, for reall No need to check, this is not the April issue, nor are integrated thermionic circuits (i.t.c.s) the work of Ireland's Ballygobackwards. They are, according to a report in Electronic Times, the joint creation of Arizona University and Los Alamos Scientific Laboratory, Circuits are delineated using conventional i.c. photolithographic techniques. Everything one would expect is sputtered onto an insulating sapphire substrate-grids, cathodes, anodes and heaters (the latter in the form of metalisation films).

But why? Because the i.t.c. will run at 600 deg. C compared with silicon's ceiling of 150 deg. C. And i.t.c.s, with as many as 1000 switches per square inch, are radiation resistant. Los Alamos, which had to develop a high temperature ceramic package for the device, has produced i.t.c.s which could be used commercially right now, although, as yet, there are no takers.

Cool Chips

An ingenious and cost-effective system for keeping i.c.s cool is being marketed by Welwyn Electric after experimental work in conjunction with British Telecom's engineers at Martlesham. The chip carriers are mounted on a flexible p.c.b. which has holes punched through it. Each i.c. is sited over one of these holes, holes which accommodate steps, or pillars, in an alloy backplate type heatsink. The flexible p.c.b. ensures that each i.c. is tensioned against its metal pillar to give good thermal contact, and to evenly distribute mechanical stress. Thermally conductive grease is used to improve heat transfer from the chip carriers to the heat sink



During production of an assembly, the fact that the flexible polyimide circuit has holes in it makes it easier to clean off flux. Once assembled, another plate is clipped to the top of the module to create a sandwich of only a few centimetres thick, which may be stacked one upon another.

DISC SYSTEM FOR MICRO-B

Advanced Memory Systems have produced the smallest possible disc system for the BBC Microcomputer using the new Hitachi 3" drives.

The drives are cased in rigid steel and cost £225 and £399 for the single and double versions. The system comes complete with cables, manuals, utilities on disc and EPROM as well as free discs.

The Hitachi drive has a brushless direct motor and when cased fits neatly on top of the BBC Micro. The casing has been textured and painted to match the computer.



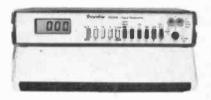
Each side holds 100K of storage. The small light on the drive casing reminds you which side you are using.

At present the product is only available by mail order from Advanced Memory Services Ltd., Woodside Technology Centre, Green Lane, Appleton, Warrington. (0925 62682).

BATTERY DMM

The TM356 is a battery-operated $3\frac{1}{2}$ digit multimeter with a large 0.5" liquid crystal display.

It has a full measurement capability of d.c. and a.c. volts, d.c. and a.c. current, resistance and diode check in 29 ranges permitting measurement of voltages from $100\mu V$ to 1000V (750V a.c.), current from 100nA to 10A, and resistance from $100m\Omega$ to $20M\Omega$.



Battery life from alkaline 'C' cells is typically in excess of 3000 hours and a low battery indicator shows when approximately 10% of life remains.

The instrument is housed in a tough ABS moulded case with carrying handle/stand making it suitable for portable applications as well as bench work.

The TM356 is priced at £85.00 plus VAT. Thandor Electronics Limited, London Road, St. Ives, Huntingdon, Cembs. PE17 4HJ (0480 64646).



Bulletins announcing new semiconductor devices arrive at PE daily, so it is possible only to describe them briefly. Details of how to obtain further information are included, however.

Motorola Darlingtons added to 25kVA line are complements to MJ10042/45/47. The MJ10041 handles 25A @ 850V. MJ10047 handles 100A @ 250V. MJ10044 handles 50A @ 450V.

▶ Formerly available 350-600V (150W) nchannel TMOS power MOSFETs now available in TO-218AC package.

♦ 48 complementary transistors added to bipolar line (TO-220 package) are direct replacements for GE devices, available as

D44C, D45C, D44H & D45VH series. MOSFET "Energy Management" series raises power by 250%. Typical device:

raises power by 250%. Typical device: $100A/60V @ 20m\Omega \text{ on-resistance.}$

• MC10HO16 high speed binary counter joins MECL 10KH line. Has improved propagation delay over Fairchild F10016.

TL431 is a 3-terminal adjustable shunt. regulator: $2 \cdot 5 - 36V$ with low temp. coef. 1 - 100mA range, & dynamic impedance of 220m Ω . ♦ MC3424 is a dual channel supervisory chip for power supply protection. Every function on board to sense overvoltage transients, regulator failures, input line loss etc.

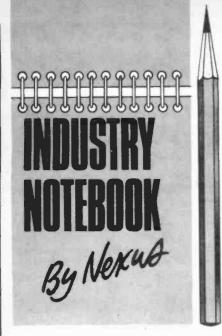
Slotted opto-couplers/interrupters are replacements for GE H21 & H22 series. Infrared diode faces silicon n.p.n. photo-transistor in plastic housing. Slot allows beam to be interrupted. Called MOC7811-23. Motorola European Literature Centre, Motorola Semiconductors, 88 Tanners Drive, Blakelands, Milton Keynes.

Litronix/Siemens Intelligent alpha-numeric displays called DL1814. 8 × 17 seg. 0.112" chars incorporating CMOS memory, decode & drive etc.

• 35 dot intelligent display is DLO 7135 series. In red, yellow or green, produces full ASCII range chars. On board memory, decode & drive simplifies row/address signalling from μP .

"World's smallest" production reflective switch is the SFH900 (match head size). Up to 5mm position sensing and has ambient light filtering, and excellent light/dark ratio.

10kV r.m.s. opto-isolator HIL10. Synchro Services, High St., Harrold, Bedford.



NEW LOOK

The most heartening event for industry and the general economic climate this year was the Trades Union Congress. It followed, of course, the general election which revealed that fewer than 40 per cent of union members supported the Labour Party while the majority voted either Conservative or Liberal/SDP Alliance.

Congress had few doubts on the survival of trade unions but many on the credibility of their traditional ally—the Labour Party, which they had themselves created and supported financially.

Thus, at the 115th Annual Congress, questions which would have been unthinkable only a few years ago were now being openly asked. Should Congress now consider distancing itself from Labour if not actually parting company completely? Would not the membership benefit more by co-operation with the Government in union reform rather than resisting? Is it not time for union leaders to recognise that they are servants of their members rather than their bosses? Was it not just conceivable that the union leadership had got it all wrong, in policies, in union management, in leadership itself?

Of course there were cries of betrayal, invocation of memories of the Tolpuddle Martyrs, the tradition of working class solidarity. But all this sounded hollow in 1983 when the majority of members are property-owning consumer-oriented men and women inclined to consider themselves middle rather than working class.

The truth is that the membership has changed while the leadership has been left behind. The cloth cap and choker, traditional badge of the union member, is in decline, the business suit and briefcase in the ascendancy. Service industry membership now exceeds that of the industrial shop floor.

Yes, Congress would re-consider general

policy, would talk to the Conservatives, would participate in the 'little Neddy' working parties. The old hard-liners still had their way through block votes on withdrawal from the Common Market, on anti-nuclear policy but not convincingly.

The new-look unions which could emerge in the year ahead might well conclude that the interests of their members are best served through generating new wealth by co-operation rather than by obstruction and old-fashioned confrontation. Much of the old rhetoric was still mouthed at Congress but one detects a positive shift to moderation.

ACORNS INTO OAKS

Those who complain of the power of the big corporations and multi-nationals often conveniently forget that they all started as virtually one-man bands and this includes even mighty IBM. And the opportunities of the small business to grow are as great today as ever they were. Anyone can join at any time in getting a start. Success, however, is dependent on entrepreneurial flair, the right product or service offered, hard work, sound financial management and a modicum of luck.

The Racal Group started off with two partners and £100 capital. In the last six years of general economic recession Racal's peripheral small companies, the odds and ends making accessories and components, are now recording over £120 million in sales, equal to the whole Group turnover as recently as 1977. So growth is possible even in recessionary periods.

The Racal founders worked at Plessey before deciding to break out on their own. History repeats itself with two Racal employees breaking out in 1978 to form their own company, Telemetrix. Roy Cole and John Westover offered their expertise first as consultants, later becoming manufacturers as well, mainly through a subsidiary company, Westward Micro Systems, building graphic terminals. Telemetrix, after five years, are turning over £5 million a year with profits of £1.5 million and have made their debut on the Stock Exchange.

So the list of new-generation electronics millionaires and potential millionaires grows. Cole and Westover had good jobs in Racal. Now they have better jobs and a fortune in prospect. Any hard feelings? Not at all. Racal is a valued customer of Telemetrix, as are GEC, Plessey and Ferranti.

Another newcomer to the Stock Exchange is Oxford Instruments. When I first came across this company it was a tiny outfit working closely with the Clarendon Laboratory, Oxford University, on their programme of cryomagnetic research which involved the use of powerful magnets operating at cryogenic temperatures. The company's founder and deputy chairman, Martin F. Wood, is a mechanical and mining engineer who joined the Clarendon in 1955 and designed all their giant magnets, later leaving to start Oxford Instruments but with the Clarendon still a major customer.

Today Oxford Instruments is still in

magnets which are used in nuclear magnetic resonance body scanners, the latest wonder in medical diagnosis. Martin Wood has a 30 per cent stake in a company which is now putting up a new £5 million factory and is expected to improve profits to over £8 million next year from increased capacity. His present success owes much to the Clarendon Laboratory, to Sir Nicholas Kurti who headed the cryomagnetic research programme and, incidentally, to Mullard Ltd who generously endowed the Laboratory with a sum which covered most of the costs of a new building to house the experiments and was formally inaugurated on 30th June, 1965.

PRICE WAR

Meantime, the violence characteristic of so many computer games is now matched, if not exceeded, by the violence of price competition between personal computer manufacturers. Some have already been forced out of business, others are subject to panic 'rescue' measures.

The long hot summer carries some of the blame as demand slumped when millions took to the beaches and forgot about shopping for electronic novelties. But the autumn is the build-up period for stocks in preparation for the Christmas rush and many companies hadn't the cash-flow nor the financial backing in support. Such are the hazards of the consumer market.

Viewing the current scene and recalling what happened to the pocket calculator and the digital watch it is understandable that investors are jittery. As prices tumble so margins are squeezed. This must mean that more of the hardware will be made in low-cost areas in the Far East, leaving most of the profit, if any, in software.

If the expected Christmas boom turns out to be a bust, look out for real give-away prices in the New Year.

OUTLOOK

Economists remain in stubborn disagreement on the general economic outlook although the optimists still appear to outnumber the pessimists. The August unemployment figures were down for the first time in four years but still depressingly high and forecast to rise again. But three million unemployed needs to be set against 23.5 million who are employed and record car sales, video recorders and other consumer durables don't exactly give a picture of a nation on its knees. Nor do the record crowds at Test matches and other sporting events, not to mention the level of overseas holidays and simultaneous record personal savings.

Of course the distribution of wealth is unequal both geographically and financially in the UK, it always has been. Attempts by governments over the past 30 years at levelling have been universally unsuccessful. But with information technology and light industry (e.g. electronics), the distance from the centre is becoming less critical and herein lies the hope that manufacturing and prosperity can be more equally spread.

The Proto-Board

Now circuit designing is as easy as pushing a lead into a hole ... No soldering No de-soldering No heat-spoilt components No manual labour No wasted time

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only £13.95 + V.A.T. ULTRASONIC MODULE



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INFRA-RED SYSTEM **IR 1470**



Consisting of separate transmitter and receive both of which are housed in attractive moulded dases, the system provides an invisible modulated beam over distances of up to 50th operating a relay when the beam is broken Intended for use in security systems, but also ideal for photographic and measurement the only £25.61 + V.A.T. Size: 80 × 50 ×

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SIREN MODULE SL 157

Produces aloud penetrating sliding tone which, when coupled to a suitable horn speaker, produces S.P.L.*a of 110dbs at 2 metres. Operating from 9-15V, the module contains an inhibit facility for use in 'break to activate' circuits, Pile 22,95 + VA.F.

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This weather-proof horn speaker provides extremely high sound pressure levels (110dbs at 2 metres) when used with the CA 1250, PS 1865 or SL 157 Price £4.95 + V.A.F.

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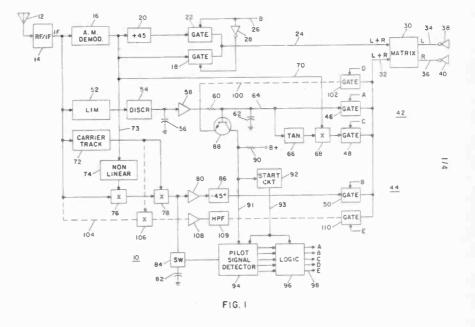
BROADCAST BATTLE IN USA

In the USA, there's currently a pitched battle on the open market over AM stereo broadcasting. There are five different systems of transmitting mono-compatible stereo on AM and the Federal Communications Commission originally intended to choose a single system as the US standard. But now the FCC has adopted the free market approach. Record radio stations across America can use any system they like. In theory, the broadcasters, press, electronics trade and public are supposed to choose the best system. But of course in practice it isn't working that way. Receiver manufacturers won't invest in any one system until it is a standard. Nor will the public. So almost no-one receives the broacast transmissions in stereo.

In an effort to break the deadlock Leonard Kahn, inventor of one of the five systems, is patenting a multi system receiver. His British patent application 210555B gives a useful run down on the five systems proposed. One uses a mixture of amplitude and frequency modulation; another uses phase modulation instead of frequency modulation; a third uses compatible quadrature amplitude modulation; the fourth is a modification of the third, compatible phase multiplex; and the fifth is an independent side band system which modulates the carrier so that the left channel is on the lower side band and the right channel is on the upper side band. Although they are different, all five systems have one feature in common. They all rely on a pilot tone to switch the receiver into stereo mode. Kahn observes that the pilot frequencies are different for each system. For AM/FM it's 20Hz, for AM/PM it's 5Hz, for CQUAM it's 25Hz, for CPM it's 55-96Hz and for ISB it's 15Hz. What Kahn proposes is a multi-system AM stereo receiver which has tone sensing circuitry and can decode two or more of the five systems.

The circuit in Fig. 1 can cope with ISB, AM/PM and CQUAM signals.

Logic circuit 96 determines which system is being received from the frequency of the pilot tone. It then switches gates 46, 4B and 50 for appropriate decoding; gates 102, 110 are used where the decoder can handle other systems. Of course, some of the circuitry for one system can be used for another system, but it is still an inelegant approach which underlines the dangers of a free market choice, philosophy.



INSTANT DIGITAL DISC

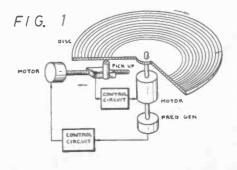
Although it was Philips who invented the Compact Disc system, Sony contributed valuable improvements to the digital coding and error correction. The company is now pushing ahead with further improvements. One is described in British patent application 2106698. The aim is to offer broadcasters a Compact Disc player with instant start.

When a Compact Disc player is in the stand-by mode, the disc is spinning and the laser is trading one turn of the pit spiral over and over again. The time taken to scan one full turn varies from 0.126 seconds, at the inside of the disc, to 0.292 seconds at the outside of the disc. So when the player is switched from standby to play mode (like lowering the stylus of a conventional gramophone), there will be a delay of up to 0.292 seconds before the laser can read from the beginning of the turn and start to reproduce sound. Although inconsequential for domestic use, this brief delay can be a problem for broadcasters. The Sony broadcast player will have a RAM which can store sufficient bits from the disc to buffer the delay and make a genuinely instant start possible. In standby mode the player stores the buffer bits and reads out these

bits from its RAM before changing over to direct readout from the disc. The RAM needs to store over 0.5 megabits of information to cope with the delay of up to 0.292 seconds, because the data stream coming off the disc is running at over 4 megabits a second.

Fig. 1 is a cut-away view of a typical disc player to which this system may be applied.

The broadcast player also incorporates forward and reverse and slow playback modes. These allow a broadcaster to "spot" a chosen passage of a recording down to the split second for instant start. In these modes the laser keeps skipping back and forth over the same tracks to expand the music in time.



W ITH "white goods" manufacturers at last coming to terms with microelectronics control, we are fast moving away from the world of springs and contacts towards an era in which flexibility, convenience and efficient energy management will be taken for granted. The latter is of particular importance now that fuel is no longer cheap domestic gas consumers are still smarting from swingeing increases—and yet the average gas central heating system wastes this precious fuel in two ways.

As for those springs and contacts, they will not disappear overnight, and many of us (author included) still depend upon them in equipments of an earlier generation. In a typical gas central heating system, the boiler is fired when the gas valve is opened, this valve being energised by a simple thermo-mechanical switch. This presettable thermomechanical switch, known to the user as the "Water Temperature" thermostat, is linked to the nearby boiler jacket. Heat from the jacket travels slowly along a heat-conducting copper wire to the thermostat, causing it to cut off the gas valve again when the required temperature is reached. Alas, this is a sluggish process in which the termostat is expensively out of step with the boiler. Also, an ideal gas controller would sense the temperature of the hot water storage tank in addition to the boiler, but presumably the *average-priced* system could not be stretched to include this feature certainly a heat-conductive copper wire could not be *stretched* as far as the tank, usually a "cool" distance away from the boiler/control unit. Even were this extra sensor at the tank to be included, what would the thermostat do with *two* inputs? If, on a cost basis, the manufacturer of the day had ruled out electronics, whilst electronics was the only technology capable of the logic necessary in a two-input thermostat, then we may hazard a guess at why gas central heating storage tanks are generally minus a temperature sensor.

CONVENTIONAL GAS SYSTEMS

Gas central heating systems vary in detail, Gas Saver being designed to improve the efficiency of the author's particular system, this being an "Ideal Standard Concord". Gas Saver will almost certainly make any comparable system run much more economically. The type this applies to is described in Fig. 1, in which the circulation pump is energised only whilst radiator heating is required. It can also be seen that only the boiler temperature is sensed, and that the thermostat is at the opposite end of a heat-conducting copper wire—a linkage that is painfully slow to respond, and which takes no account of the temperature of the hot water storage tank. Both of these shortcomings cost the consumer money. Fig. 2 shows the gas ignition pattern and temperature curves of the conventional CH system outlined.

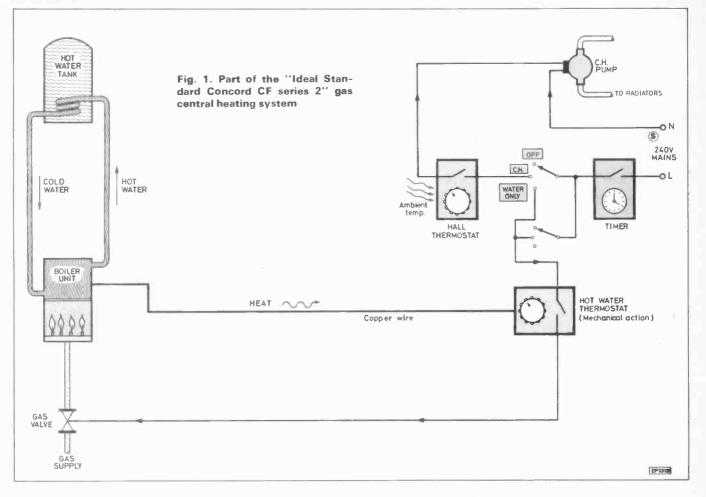
GAS SAVER

If nothing else, Gas Saver senses the boiler water temperature electronically, and therefore responds *instantaneously* to changes in heat level. But more importantly than this "secondary" economising mode, it senses the temperature of the hot water tank, in order to compute more logically the control signal to the gas burner. What this means, in short, is that with the radiators off, once the hot water tank has reached its preset temperature the system will shut down until hot water is next drawn off. And with

Mike Abbott

Dynamite will prevent one type of gas wastage, but if you want to save unnecessary gas in your central heating system, you'll need some electronics. Read on ...

HOME PROJECT



the radiators on, Gas Saver will ignore the tank water temperature but at least regulate the gas burner duty cycle more precisely than the conventional system. Fig. 4 shows how Gas Saver behaves with the radiators turned off-its "primary" economising mode.

The entire project was designed to be as cheap as possible to construct, without compromising reliability and safety-essential requirements in such a utility.

Gas Saver, when installed, renders the original water thermostat redundant, water temperature from then on being set

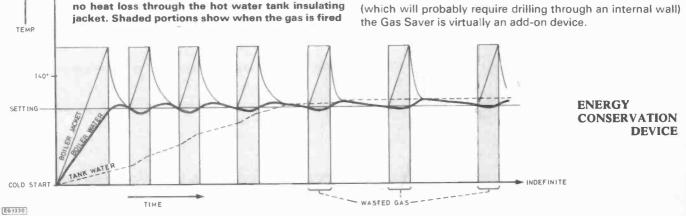
Fig. 2. Temperature patterns of a conventional gas

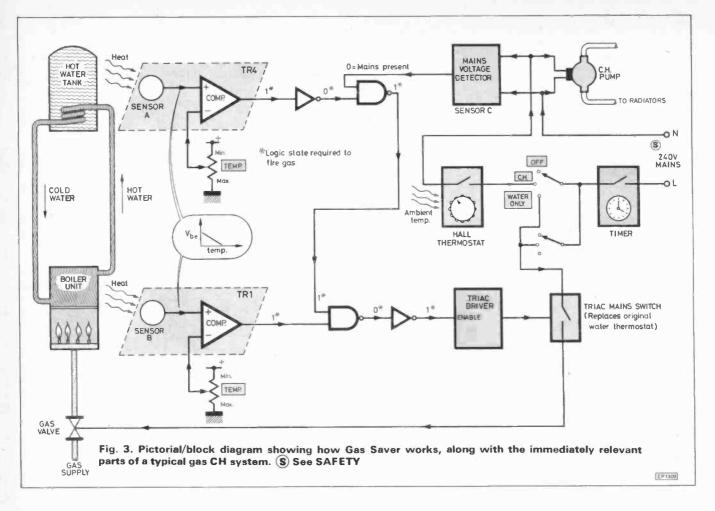
central heating system starting from cold, and with

no demand. The graph is simplified, and assumes

by potentiometer VR1, and controlled via a triac mains switch that replaces the electrical contacts of the old thermostat (see Fig. 3). The original thermostat may be left in place, so that should a fault develop in Gas Saver, it is possible to convert quite quickly back to the original system. The plans given in this article adhere to a policy of plug-andsocket interconnection compatibility with the central heating's own distribution panel. If this policy is complied with, the original system, albeit less efficient, is always only a few seconds away from standby operation. Gas Saver can take its mains supply from the timer switch, and its only other claim on the central heating unit's wiring is the direct in-out rerouting of the circulation pump wires.

Once the heat sensor at the hot water tank is installed (which will probably require drilling through an internal wall)

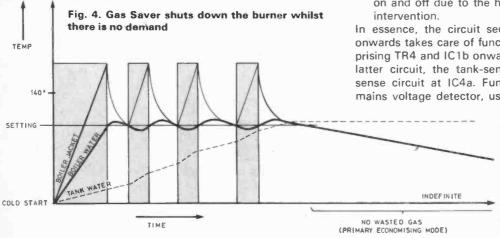




CIRCUIT DESCRIPTION

The circuit (Fig. 5) is easier to understand once the three criteria of the design are appreciated. The three functions are:

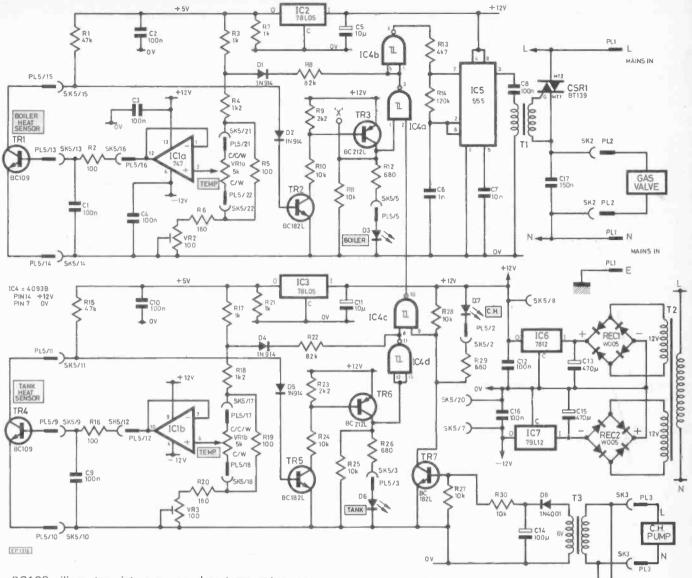
1) To take an instruction from the user to establish the water temperature required, this being achieved via the "Temperature" control VR1, and then regulate the gas burner duty cycle to average that temperature. The *boiler* temperature sensor, TR1, is the controlling agent for this function. As with a conventional controller, the temperature selected applies to both the radiators *and*/or the hot water storage tank.



- To temperature-sense the hot water storage tank (TR4), and shut down the system when the water in the tank has reached the preselected temperature (this is the primary economising mode) and is achieved by inhibiting function 1.
- 3) To sense whether or not the radiator circulation pump is energised, and if so, *release* function 1 from the override capability of function 2. In other words, if the system is shut down because the water tank is up to the required temperature, switching on the radiator heating will again allow the gas to ignite, irrespective of the hot water tank condition. By sensing the pump itself, the system will respond, whether the radiators are turning on and off due to the hall thermostat, or due to manual intervention.

In essence, the circuit section comprising TR1 and IC1a onwards takes care of function 1. The identical circuit comprising TR4 and IC1b onwards takes care of function 2. This latter circuit, the tank-sense circuit, overrides the boilersense circuit at IC4a. Function 3 is realised by a simple mains voltage detector, using T3 for step-down and isola-

> tion. This is followed by a PSU style circuit which drives TR7 into conduction whenever the pump is energised. Therefore, whilst the radiator heating is on, the tank circuit's ability to inhibit the boiler control section is removed (at IC4c). There is, in effect, **a** cascaded override function.



BC109, silicon transistors are used as temperature sensors. Although each circuit could have monitored the voltage generated across a silicon junction diode, transistors were chosen because in a three-terminal mode they offer gain at the sensors themselves.

Taking the boiler half of the circuit as an example, what happens is this: the chosen temperature setting manifests itself as a voltage on the wiper of VR1a, and ignoring the voltage follower IC1a for a moment, this voltage is delivered to the base of the sensing transistor TR1. When this transistor is cold, the threshold, or "knee" voltage between its base and emitter is higher than the potentiometer voltage, and so the transistor is switched off. It follows then, that TR2 will be switched fully on, and likewise TR3. When illuminated, D3 shows that the boiler is below the preset temperature. To IC4a, the conduction of TR3 represents a logic 1 and so, unless overridden by the second sense circuit, IC4a outputs a 0. This output is tapped via R8 to provide positive feedback, modifying the original VR1 potentiometer voltage by something equivalent to a few degrees C. Thus the conditions which hold TR1 off are enhanced. This hysteresis is necessary for stable operation.

With the circuit in this state IC4b output is at logic 1, which enables the 555 oscillator, IC5. The pulse transformer is pulsed, the triac driven into conduction, and the gas valve

Fig. 5. Circuit diagram of Gas Saver. Output "X" is explained under "OTHER CH SYSTEMS", but can otherwise be ignored

opened. With the gas fired, the temperature of the boiler begins to rise, and with it the temperature of TR1.

At the preset temperature, TR1 will begin to conduct. This is because its base-emitter "knee" voltage will have fallen to, or below, the voltage generated by VR1. As TR1 moves into conduction, TR2 and TR3 are forced out of conduction, producing a logic 0 at pin 1 of IC4a.

Any temptation for the circuit to "hover" in a halfswitched state is eliminated by the positive feedback via R8 and D1. Because now R8 is no longer stealing current from VR1's potentiometer network, the voltage to the base of TR1 will rise, tipping that transistor further into conduction. IC4 is a *Schmitt trigger* NAND, adding to the "snap" action of this bistable circuit.

The presets VR2 and VR3 are included in the potentiometer network because, like human beings, no two transistors are identical. It is necessary to be able to modify VR1's voltage swing by an amount capable of offsetting the Vbe production spread of BC109s. TR1 and TR4 should at least switch at the *same* temperature even though VR1 is not calibrated in deg. C at its dial, and the procedure for setting up presets for this is described later.

Returning to IC1; this is merely an impedance converter, or voltage follower, necessary because the silicon junction is a current device and will switch more rapidly if driven from a low impedance source. Impedance conversion would not be needed if dual-ganged 100 Ohm potentiometers were readily available to the hobbyist. Such potentiometers as appear "off the shelf" begin at 4k7 or 5k, and so one of these was selected, and its characteristics modified by a cheap op. amp. Resistor R2 protects the transistor from overdrive, whilst C1 acts as a short-circuit to r.f. interference picked up along the cable to the sensor. Ideally, this capacitor should be positioned at the sensing transistor end of the cable, but this may be mechanically inconvenient. It works perfectly well within the main unit on the prototype system, even though this effectively puts it in series with the lead inductance as far as the transistor is concerned.

Independent regulation for the two identical circuit sections is provided (by IC2 and IC3) so that neither is affected by the other's switching transients.

The circulation pump-sense override is now described. Obviously, if the central heating radiators are turned on, then the fact that the hot water tank has reached its required temperature should no longer be allowed to inhibit the gas burner. It is therefore necessary to detect whether or not the radiator circulation pump is running, and if so, to lock out the hot-tank signal. The pump detector is a simple PSU type circuit, taking its mains input when the pump does. This PSU simply inhibits the hot-tank signal by driving TR7 into conduction.

The illumination of D7 indicates that the radiators are being driven by the circulation pump, and it will thus light up and go out again repeatedly, in step with the hall thermostat (air temp.). Actually, although D7 lights up immediately as the pump energises, there is a delay of about four seconds before going out, due to the time constant of C14 and R30. This is of no consequence. The transformer, T3, may seem a clumsy way of detecting the state of the pump, but it is quite the cheapest, and the idea of this project is, after all, to avoid wasting money.

DRIVING THE GAS VALVE

CONTROL BOX

The gas valve in the author's central heating system was found to be highly inductive. As a result, the pulse frequency from IC5 needed to be quite high; and more importantly, capacitor C17 had to be included for power factor correction. In plain English, this last point means that because current builds up slowly in an inductor, and because a triac is a current avalanche device it will fail to fire early enough for a full 360 degree conduction angle. Capacitor C17 provides some "lead" current for the triac to latch on to, If the gas valve makes an audible buzz, C17 may not be large enough (assuming nothing else is wrong). If the gas valve makes any kind of noise, switch off the system until the fault is rectified. Do not try to achieve a smooth triac action by connecting a capacitor/resistor snubber network across the triac itself, as this will probably leak enough current to prevent the gas valve dropping out again, once energised.

HEAT SENSORS

Transistors make economic, and conveniently linear temperature sensors—as long as the operating parameters are within certain limits. Silicon devices, including specifically designed silicon temperature sensors, will work up to 150 deg. C, a figure which gives a 50% safety margin in the application of sensing water temperature to boiling point at atmospheric pressure. So, unless an explosion is imminent, the two metal cannistered BC109s are not likely to run away thermally. If they were to, they would shut down the gas burner anyway!

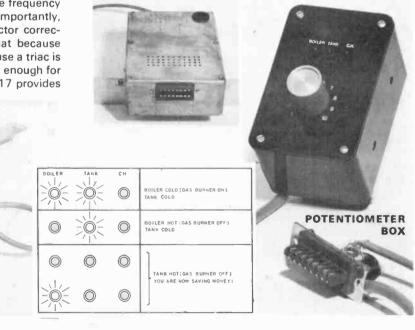
A transistor used in its natural three-terminal mode of operation can integrally perform the dual function of being both temperature sensor and comparator. A potentiometer provides the reference voltage, whilst the transistor's base-to-emitter threshold provides the temperature related voltage. This technique is not original though; it is seen in P. J. McFarlane's "Ice Warning & Lights Reminder" of *P.E. March '83.*

On a more practical note, the boiler sensor TR1 should be situated so as to detect the *water* temperature near the *top* of the boiler jacket, *not the hot gases rising from the burner*. The transistor should, therefore, be positioned with this in mind. Some CH boilers, probably most, have a receptacle at the top-centre of the boiler jacket to accommodate the conventional heat sensor, and this, in the case of the prototype installation, was eminently suitable. It was possible to push the transistor sensor well inside, without removing the original.

Both heat sensor transistors, TR1 and TR4, should be mounted and sleeved as shown in Fig. 6.

CONSTRUCTION

As can be seen in the photographs, Gas Saver is built using stripboards. The main control electronics board is housed



in a diecast alloy box, which is situated *inside* the CH boiler unit. The Potentiometer box (plastic), also containing the l.e.d.s and impedance converter op. amps, is mounted against the outside of the CH boiler housing in such a way that the temperature control knob (VR1) is readily accessed. The two boxes are linked by a multicore cable. This umbilical link should ideally be a 14-way screened cable, although 15 instrument wires bound by "spiral wrap" will probably suffice, or even ribbon cable. The stripboard and box layouts are shown in Figs. 7–9.

This dual box approach was dictated by the geometry of the author's CH system, and the fact that the boiler unit is fitted tightly in a corner with little external space. An alternative approach, if there is space around the boiler housing, would be to put all the electronics, including the potentiometer, in the alloy box, and mount it externally. This could make use of a p.c.b., or a large stripboard (although in the latter case the mains transformers should still be mounted on a separate non-clad board). The "single box" approach would have a number of advantages, one being the avoidance of the multiway connector and cable (and metalwork to fit it). Another is that of finding sufficient space within the boiler unit (space which is not subject to too much heat from the boiler itself) to take the main control box. It also involves one less mounting bracket, for those who do not enjoy metal bashing.

There are no special points concerning construction. Any hobbyist experienced enough to undertake this project will find the illustrations adequate, and will know that the CMOS 4093 requires careful handling and should be mounted in an i.c. socket.

Insulation should be placed beneath each of the component boards in the alloy box. PVC tape can be used, but in the prototype, plastic sheeting cut to the same dimensions as the boards themselves are used. These were cut from a

Fig. 6 (right). Transistor heat sensor assembly (two are required). The top board edge, against which the transistor should be closely mounted, is bevelled to truncate the copper tracks. Heatproof sleeving is required to electrically insulate the assembly from its surroundings, and this should be of a material that is rated to 150 deg. C minimum. Silicone rubber (180°C) will do, or expandable woven polyester (150°C)

Scheen wot Connected Note the coplous ventilation holes jumbo bleach bottle, drilled with the same fixing holes as the stripboards, and dropped over the stand-offs first.

The main unit board is mounted on p.c.b. stand-off clips, and the high voltage (mains) board is mounted on 4BA screws with locking nuts—this providing greater security for the heavy transformers. The easiest and most accurate way of ensuring that the holes in the boxes line up with the mounting holes in the component boards, is to use the component boards as templates for marking out. The stripboards make ideal templates once they have been cut and drilled, but before any components have been mounted on them.

All mains earths must be observed when wiring the main control box, and these earths must be affixed to the metal box itself, using a nut and screw and solder tag for reliable connection.

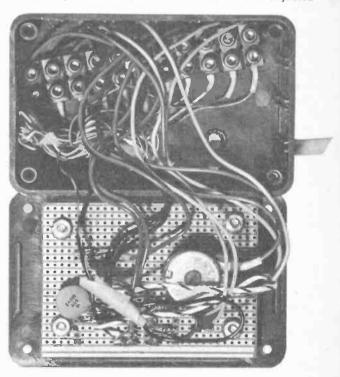
INSTALLATION

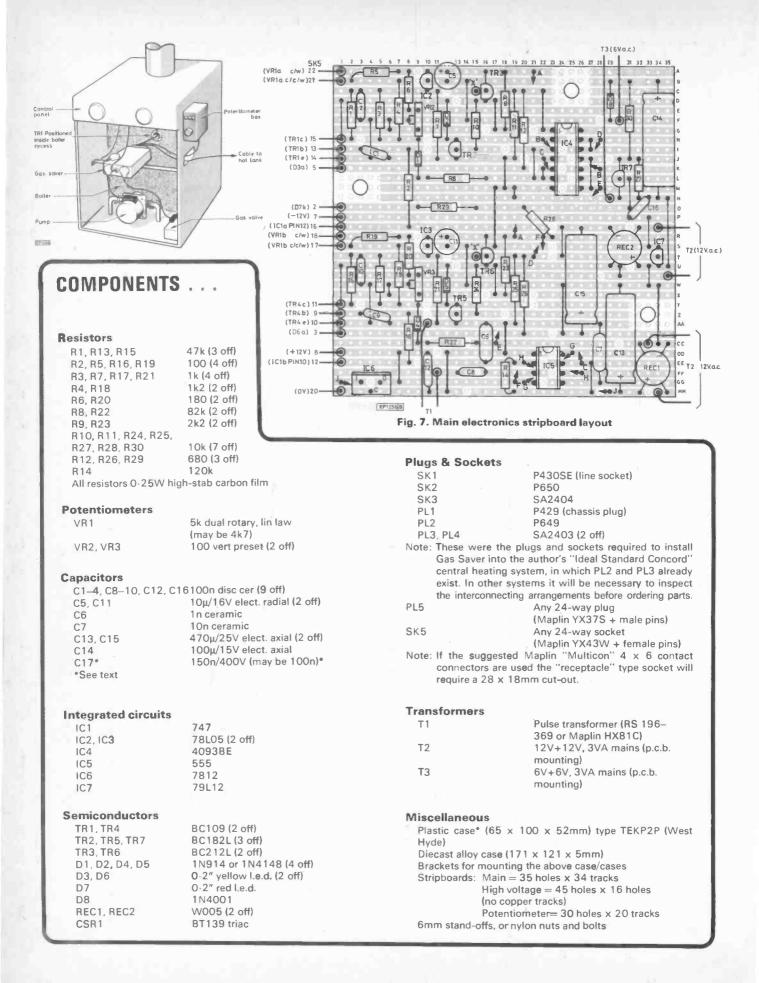
Apart from making brackets appropriate to the particular installation layout, it is necessary to hack out a fairly large hole (about 60mm square) through the side of the boiler housing. This can be done with an Abrafile or nibbler, after which the edges are filed smooth and covered with polythene grommet strip. This hole is required to pass the interconnecting cable between the two boxes (prototype layout), or to carry the pump, valve and other CH unit cables through to the electronics control box (in the case of the "single box" alternative layout).

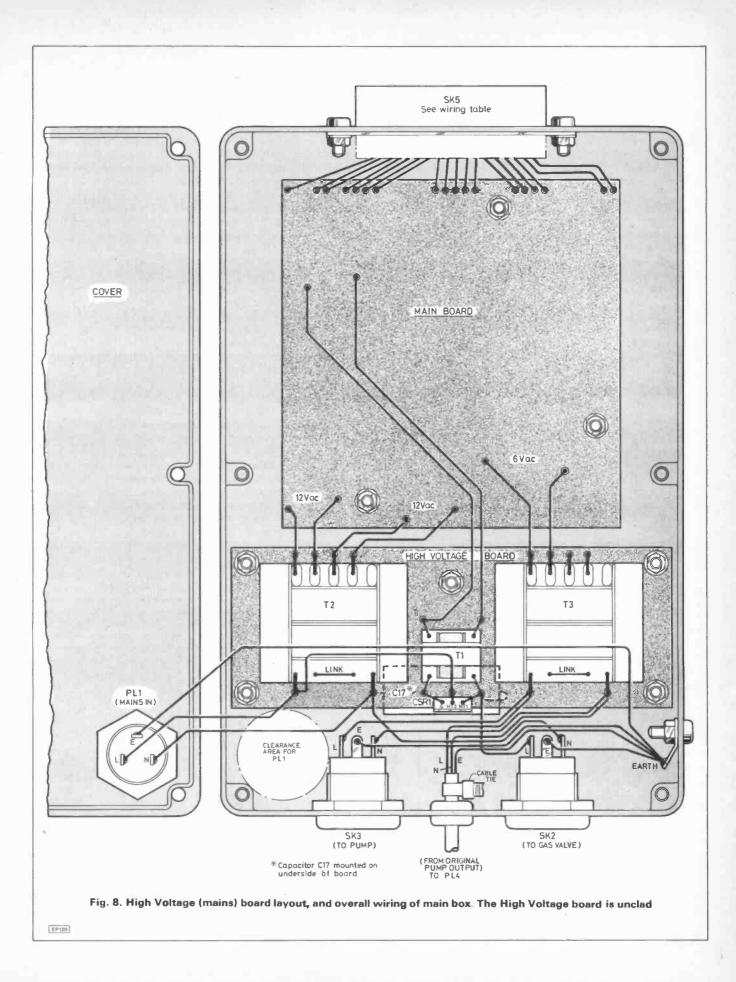
An extended masonry drill will undoubtedly be necessary to drill a hole through an internal wall separating the boiler unit from the hot water tank (airing cupboard). This is to carry the hot water tank temperature sensor lead. If this lead is more than four feet long it will be wise to consider the use of screened three-core cable, this to be wired as shown in Fig. 6.

SETTING UP

The only setting up required is that of VR2 and VR3. The simplest way to do this is to power up Gas Saver with the gas valve socket disconnected, and place both sensors in some recently boiled water. VR2 and VR3 are then adjusted







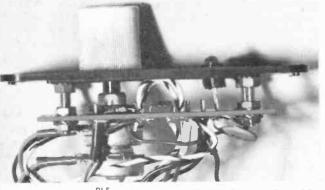
until both sensing transistors can be made to switch at exactly the same point on VR1's scale. The front panel I.e.d.s will indicate the switching points. In fact, it is most probable that Gas Saver will function correctly with VR2 and VR3 both set to midway position, without conducting this test.

The dial on VR1 can be numbered, or simply lettered subjectively *warm, hot, very hot,* with intermediate temperatures being interpolated by the user. The point on VR1's dial that switches the l.e.d.s on and off whilst setting up VR2 and VR3 will, if using recently boiled water, serve as a marker for *very hot.*

SAFETY

Naturally, safety is all important in any equipment using gas and electricity simultaneously, particularly when running continuously without supervision. No short cuts should be taken during construction, and any part of the electronics housed within the boiler unit should be cased in a well ventilated metal (fireproof) box.

The possibility of a lost connection during service has to be considered. For example, it would not be desirable to lose a connection to *any one* of the wires to a heat sensor, and come home to a loft swirling with steam, or worse! A completely independent "over-temperature" cut-out must be considered, that will cut off power to the control box should



the boiler water temperature rise above 100 deg. C.

An obvious solution exists; the original thermostat! Other than ignoring its electrical contacts in the new system, it is left fully functional, so why not wire *its* contacts in series with the mains supply to the electronics controller? Then, if this original thermostat is turned up to a temperature setting way above those being selected for everyday use, it will limit the gas duty cycle should boiler overheating occur due to an electronic "latch-up". With this safety feature, the plug-andsocket compatibility for quick reversion to conventional operation *might* need to be partially sacrificed.

The safety option described here will not shut down the system indefinitely in the event of the type of failure that keeps the gas on, but will limit the gas duty cycle to a "ceiling" temperature. At least a hot bath may be taken whilst the fault is considered.

QUANTITY, AND THE TANK SENSOR

The hot water tank sensor need only be tucked in between the side of the tank and the insulation jacket. The position of this sensor will be about halfway down.

Since hot water builds up from the top of the tank downwards (it is possible to have cool water at the bottom and piping hot water at the top) the vertical position of TR4 needs to be considered with this in mind.

SAVINGS

The important question is: How much money does a Gas Saver save? Just a short summertime test has so far been possible (hot water only), during which Gas Saver reduced fuel consumption by 21%. One manufacturer of a similar energy conservation device guarantees a *minimum* saving of 16%. Savings also depend upon how efficiently the old "pot luck" mechanical system happened to be working, and the operating regime in any particular household.

Throughout 1981/82, gas prices increased four times, each time between 10–15%. It seems that the consumer is to be financially coerced into more efficient use of this "precious" fuel, whilst at the same time being sold equipment that wastes it! After-sale conservation devices will often provide the consumer's only escape route.

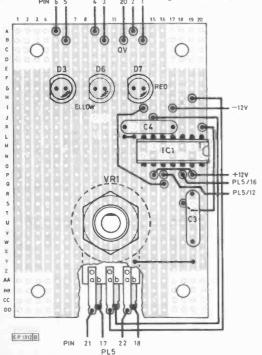


Fig. 9. Potentiometer board layout

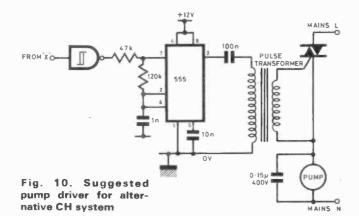
FROM(main unit)	PIN	COLOUR	TO (potentiometer box)	
+12V	1		D7 (CH I.e.d.) anode	
R29	2		D7 cathode	
R26	3		D6 (TANK I.e.d.) anode	
OV	4		D6 cathode	
R12	5		D3 (BOILER I.e.d.) anode	
OV	6		D3 cathode	
-12V	7		-12V	
+12∨	8		+12V	
R16	12		IC1b pin 10	
R2	16		IC1a pin 12	
R18	17		VR1b c.c.w.	
R20	18		VR1b c.w.	
OV	20	Screen*	OV	
R4	21	1	VR1a c.c.w.	
R6	22		VR1a c.w.	
			TO (sensor)	
R16	9		TR4 (TANK) base	
OV	10		TR4 emitter	
R15	11		TR4 collector	
R2	13		TR1 (BOILER) base	
OV	14		TR1 emitter	
R1	15		TR1 collector	
	· · · ·		vise viewed from rear of po	

OTHER CH SYSTEMS

Alas, the author is no expert on central heating systems in general, and therefore not in a position to suggest modifications to Gas Saver to adapt it to all other systems.

However, one common departure from the type of central heating Gas Saver was designed to work with, is one in which the circulation pump runs continuously. In this type, it is believed that for geometric reasons thermal currents cannot be relied upon to transfer hot water from the boiler to the storage tank—perhaps because the airing cupboard is no higher than the boiler. Consequently, a permanently energised pump is needed to circulate hot water from the boiler to the tank, and a motorised valve is used to bring in the radiators when required.

At first, it might seem that the obvious modification to Gas Saver is merely to sense the state of the motorised



Gas Saver is an electronic thermostat that eliminates unnecessary firing in gas central heating, and can cut bills by up to 20%. Gas Saver can be built for around £30, and do the job of economisers costing four times as much.

valve instead of the pump (sensor C of Fig. 3). The problem with this, is that whilst the gas burner is off for prolonged periods (as in the "primary" economising mode shown in Fig. 4) the pump will *remove* hot water from the tank and circulate it through a *cold* boiler jacket—the exact opposite to heating! It is not certain that Gas Saver will deserve its name in this situation.

A further modification is therefore necessary if Gas Saver is to be put to work fruitfully in such a CH system, and a suggested way to do it is illustrated in Fig. 10. It is emphasised that this modification has not been built and tested. The additional circuit is intended to shut down the circulation pump as soon as the boiler jacket becomes cold (having no more useful heat to impart), and re-energise the pump again as soon as the boiler, is at least as hot as the storage tank. Any constructor embarking on this version of Gas Saver will need to run the system under observation, and arrive at his/her own conclusions as to its merits.

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THIS project has its origins in a production meeting of an amateur stage review, when the author was foolish enough to suggest a sci-fi sketch.

The main requirement was for a number of lightweight hand guns, capable of producing a flash of 'death rays' when fired, having a rapid recharge rate and a negligible standby power drain. Directed energy weapon technology being what it is, the source of 'death rays' reduced to a Xenon photographic flash tube. Most commercial flash gun circuits will not fulfil the last two criteria however, and so a suitable circuit was developed.

The other requirement was for the sound which every 'death ray' is presumed to produce, i.e. the 'zap'. Flash tubes themselves are embarrassingly quiet, and so a tone generator was developed, to be triggered by the flash. Such a circuit can be made to work in normal light, even fluorescent, because of the very rapid rise time of the flash tube output. In practice, reflection from dull surroundings at distances of up to 50 ft or so is sufficient to trigger the circuit.

TONE CIRCUIT

The tone generator circuit is shown in Fig. 1. Here IC1 operates as a current to voltage converter, the output of which is high pass filtered by C1 and R1, to produce a spike on receipt of a sudden increase in the light level by way of the phototransistor TR1.

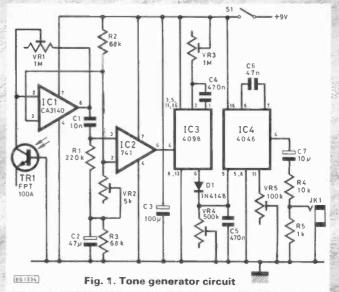
VR1 sets the gain, and VR2 sets the trigger threshold of the comparator IC2.

IC3 is a retriggerable monostable, and VR3 sets the period, and hence the 'sustain' of the tone generated. The output at pin 6 charges C5 via D1, holding the voltage high until IC3 turns off, when C5 discharges at a rate set by VR4. The voltage on C5 is used to control IC4, a PLL chip used only as a voltage controlled oscillator. This has a square wave output which is not really ideal, but it was used because it conveniently switches off when the control voltage drops to zero.

FUN PROJECT

VR5 sets the frequency range of the sweep. The amplitude of the output is reduced by R4 and R5. Control of the tone may be achieved in the power amplifier used.

The tone generator is constructed on Veroboard as in Fig. 2. On completion first set VR1 and VR4 to maximum, VR2



to minimum and VR3 and VR5 to mid position. A jack plug should be connected to a suitable power amplifier and loudspeaker and inserted at JK1.

Increase VR2 until triggering due to circuit noise and residual hum ceases. Obtain a photographic flash gun and charge it up then switch on the

COMPONENTS ...

TONE GENERATOR

Resistors		Semicondu	ictors
R1	220k	IC1	CA3140
R2	68k	IC2	741
R3	68k	IC3	4098
R4	10k	IC4	4046
R5	1k	D1	1N4148
All resistors	1W 5% carbon	TR1	FPT100A
		Potentiom	eters
Capacitors		VR1	1M
C1	10n	VR2	5k
C2	47µ 16V	VR3	1M FOOL
C3	100µ 16V	VR4	500k 100k
C4	470n	VR5	TOOK
C5	470n	Miscellane	ous
C6	47n	S1 - Sing	le pole switch,
C7	10µ 16∨	Veroboard,	, pins

tone generator and fire the flash. If o.k. the circuit will 'sound off' with a briefly sustained tone and then die.

GUN CIRCUIT

The heart of the gun circuit is a push-pull inverter (Fig. 4) each half being driven by non-overlapping square wave inputs. The drive waveforms from the gates (Fig. 3) are inverted and buffered by TR1 and TR3, and the emitter followers TR2 and TR4 drive the output pair, TR5 and TR6. It is important that the output transistors are of the metal can variety, as the plastic versions have a lower minimum current gain, and may not completely saturate. Under normal operating conditions little heat is dissipated and large heatsinks are not needed. D4 and D5 protect the output transistors against reverse biasing voltages, and D6 and D7 with R11 and C5 limit positive going spikes to approximately $2V_{cc}$.

The high voltage output from T2 is rectified by D8-D11 and charges the main storage capacitor C6. The voltage on C6 is sensed by the potential divider R15/16 and R2, and compared with the reference voltage of D15 through IC1.

When the voltage on C6 is less than about 230V, the output of IC1 is high, enabling the CMOS astable oscillator, IC2(a) and providing an inhibit signal to the trigger circuit to prevent the gun being fired before it is fully charged. The output of the oscillator has an unequal mark-space ratio, which is gated by IC2(c) and (d) and IC3, a dual 'D' type flipflop. This produces the non-overlapping drive waveform.

As the voltage on C6 rises to about 240V, IC1 output goes low, inhibiting the inverter, and arming the trigger circuit, TR7 etc. Two possible trigger circuits are given: a single shot circuit (for the bad guys); and a continuous repeat 'automatic fire' trigger (for the good guys). The single shot

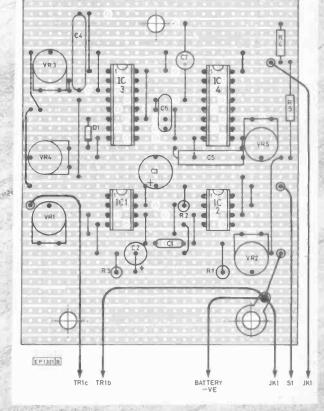
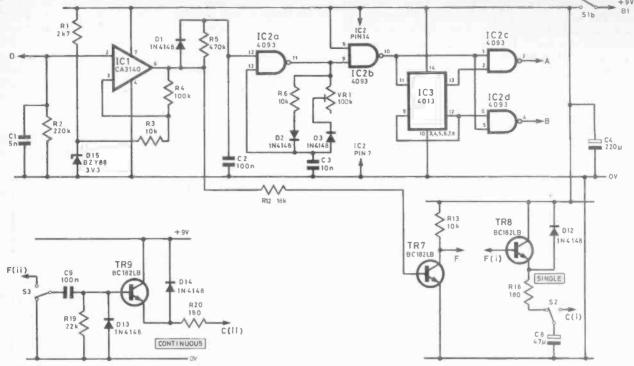


Fig. 2. Veroboard assembly of tone generator



EG 133 5

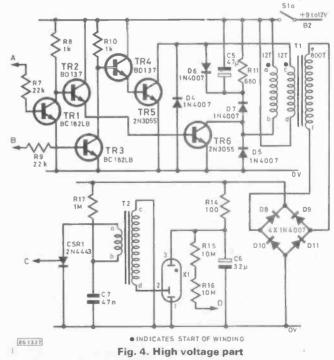
Fig. 3. Low voltage part of gun circuit. In the prototype the single shot trigger was used. An alternative continuous trigger is shown lower left

COMPONE	NTS			
GUN CIRCUIT		Capacitors		
Resistors R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13 R14 R15 R16 R17 R18	2k7 220k 10k 100k 470k 10k 22k 1k 680R 18k 10k 10R 10M 10M 10M 11M 180R	C1 C2,9 C3 C4 C5 C6 C7 C8 Transformers Pri — 12 + 12 24 s.w.g. en Sec — 800 tu 42 s.w.g en Xenon trigger (Maplin YO Xenon tube (M Miscellaneou S1 D.P.S.T.	2 turns of namelled wire urns of transformer 63T) Applin YQ62S) Is toggle switch,	
R19 R20 All resistors $\frac{1}{4}$ W 5	22k 180R	S2-S3 S.P.D.T. toggle with single blas to one position, 20in length of 3 x 3in plastic trunking, VR1-100K,		
Semiconducto	rs	B1—PP3, B2		
TR 1,3,7,8,9 TR 2,4 TR 5,6 D 1.2,3,12,13,14 D 4–11		BC182LB (5 off) BD137 (2 off) 2N3055 metal can (TO3) (2 off) 1N4148 (6 off) 1N4007 (8 off)		
D15 CSR1 IC1 IC2 IC3		BZY88 3V3 2N4443 CA3140 4093 4013		

circuit used charges C8 via TR8 when TR7 is turned on by IC1; this charge is transferred to the gate of CSR1 when the trigger S2 is operated. CSR1 discharges C7 through the pulse transformer T2, providing 3 or 4kV to the trigger electrode of the flash tube, causing the tube to strike.

The falling voltage on C6 causes the cycle to repeat, with a delay due to C2 and R5 to prevent the inverter starting before the discharge is complete.

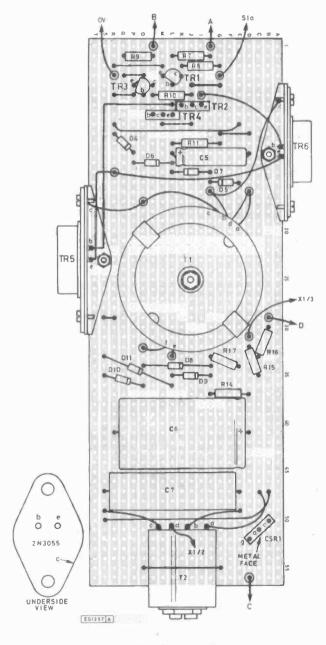
The continuous firing trigger circuit uses C9, R19 and D13 to differentiate the positive going edge generated when S3 wiper connects with TR7 collector, and IC1 output is



low. This pulse briefly turns on TR9, firing CSR1 and hence the tube. The recharge cycle begins, with IC1 output going high, and when C6 is fully charged, going low again. TR7 collector goes from low to high, and if S3 wiper is still connected, this transition initiates the triggering, and so the cycle repeats until S3 is opened.

TRANSFORMERS

The construction of T1 deserves some comment. The primary winding consists of 12 plus 12 turns of 24 s.w.g. enamelled wire, bifilar wound on the core bobbin, and then insulated with a layer of tape. The secondary is made up of 800 turns of 42 s.w.g. enamelled wire, wound in four layers, each insulated from each other by a layer of tape. Sleeving should be placed over the free ends of the windings before covering with insulation to avoid chafing. The core used was a 36mm diameter item, of Siemens T26 material (available from Electrovalue).



Fig, 5. High voltage gun board

T2 may be obtained from Tandy, Maplin, or may be recovered from a complete flash gun. Physical construction may vary so modify layout accordingly. Tubes for the prototype models were obtained complete with reflectors by purchasing a quantity of used photographic flash guns from a dealer (four for £6). Most Japanese items are suitable; tubes from German made units seem to require a higher supply voltage.

TESTING

GUN

BOARDS

The circuit may seem rather complex, but it has the virtue

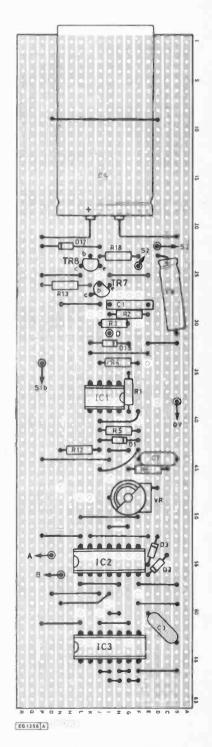
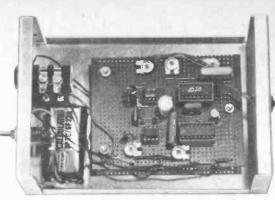


Fig. 6. Low voltage board

Flash gun showing stacked board assemblies consisting of the high and low voltage boards, battery and flash tube to the fore. Two 4in sections of broom handle are used as a butt and steady

that the standby current is only a few milliamps and only fires when fully charged. Recycle time depends on the value of C6 and the internal resistance of the power source. With the value shown, and a PP9 supplying power to the output stages, recycling takes about 0.5S. The prototype automatic version used two 6V lantern batteries in series to give a very satisfying flash rate.

Test the low voltage section of the circuit first. Connect TR1 and TR3 bases to V_{cc} via R7 and R9 to inhibit the output stages. Ground IC1 pin 2, and check that the oscillator is operating. Next, connect a high wattage resistor of about



The separate tone generator with phototransistor on right panel

15–20k in place of C6, and connect the inverter output stages to IC2, still keeping IC1 pin 2 at 0V. About 300V should appear across this resistor. Tune VR1 for a voltage maximum before restoring the circuit to its original form.

Note that if the circuit is tested with a bench power supply, the current trip will almost certainly operate, as the current demand is large.

HOUSING

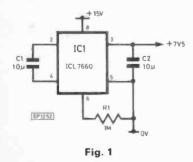
The prototype units were built into 20in lengths of 3 x 3in plastic electrical trunking, with the flash tube secured in the gutted body of a defunct flash gun. Chamfered lengths of broom handle were screwed and glued to the trunking to act as butt and steady, and a biased toggle switch made a very effective trigger.

Six prototypes were built to this circuit and the only problems encountered were due to mechanical considerations, inasmuch as the finished products were thrown about with gay abandon by a troupe of scantily uniformed imperial stormtrooperettes.



Off The Rails

Sir—With reference to Tom Gaskell's article in the August edition Semiconductor Circuits—ICL 7660, the stated absolute maximum voltage of $\pm 10.5V$ applies for the chip when it is used in its negative voltage generating mode only. If the chip is used as a positive to positive voltage converter then this maximum can be increased. An example of this is to provide a $\pm 7.5V$ supply from a $\pm 15V$ rail when using the ICL 7650 chopper stabilized operational amplifier. The 7660 can also be used as a negative to negative voltage



converter in a similar manner. The circuit configuration is shown in Fig. 1:

L. N. Owen BSc., Blackfield, Hants.

Tom Gaskell comments:

My thanks to Mr. Owen for providing us with an ingenious application for the ICL 7660. He is quite right in stating that this circuit allows operation from a + 15V rail, rather than the specified +10.5 volts. In practice, though, the +10.5 volt limit still stands; it is the maximum permitted voltage between pin 3 and pin 8 (the '0V' pin and the +ve supply pin).

In Mr. Owen's circuit, the i.c. is 'floated' up to a nominal +7.5 volts; the 'negative' output of the i.c., pin 5, is connected to 0 volts, which (in combination with a + 15 volt supply) forces pin 3 to +7.5 volts; this is our new supply rail. Reference was made in the original article to a diode in series with pin 5; this should ideally be included (between pin 5 of the i.c. and everything else) for supply voltages in excess of 13 volts (ie. twice 6.5 volts). Allowance should be made for its forward voltage drop. The maximum permitted supply voltage with this circuit arrangement is +21 volts.

Altogether, a most useful and interesting application of the device, showing that even fairly simple i.c.s such as this can often be extraordinarily versatilel

Finally, please note that there is a small but important error in Fig. 3 of August's Semiconductor Circuits, on the ICL 7660: The caption should read D1 is not necessary for supplies <6.5V. Not >6.5V as printed.

Help Sought

Sir— I have just purchased a BBC model 'B' micro and the high resolution graphics, on a large colour TV are good.

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> Seamus McKenna Co. Donegal Ireland.

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 8K FORTH (Extra)

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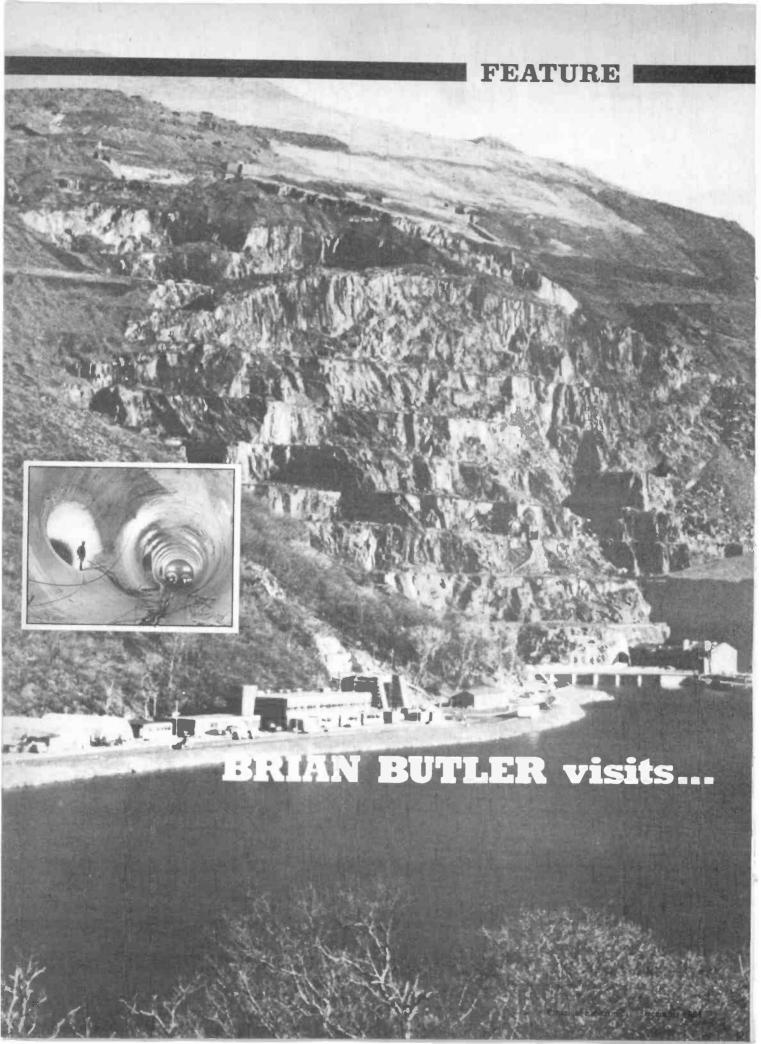
Manuals

- 1. User's Manual, 8 chapters. 1. Over view and Installation 2. Specification (hardware and software). 3. Description of Operation . 4. Operating the MPF-1 Plus . 5. 44 Useful Sub-Routines. 6. The Text Editor. Assembler and Disassembler.
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ALTHOUGH hydro-electric power is a very efficient method of producing electricity—you simply collect a few million gallons of water, store it in a dam and then dispose of it through a turbine-generator placed somewhere well below the water level—an ingenious alternative to the traditional hydro-electric plant has been developed by the CEGB (Central Electricity Generating Board) at Dinorwig, North Wales. The massive underground plant is a pumped-storage system which can supply 1300MW of electricity to the national grid system in just 10 seconds, overcoming the costly business of stand-by stations.

With the introduction into our homes of infinite numbers of electrical appliances over the last couple of decades came the inevitable load on the national grid system. The coal- and oil-fired power stations of the fifties proving less and less capable of supplying large amounts of electricity to the grid system when a sudden demand occurs. To keep these stations in a stand-by mode, awaiting a peak demand, is a costly business.

At the same time as these significant changes were occurring in the daily pattern of electricity demand it was realised that there was an increasing need for reserve generating capacity to make up for the sudden loss of a large modern generator.

EARLY STUDIES

In the late sixties it was estimated that the stand-by capacity needed to cover the worst possible unplanned loss of large generators during the two following decades were calculated by the CEGB to be about 1300MW within some six seconds (a tall order indeed).

It was therefore decided to compare the engineering and ecchomic merits of gas turbines with pumped-storage, for previding generating capacity able to respond rapidly to sudden demands for electricity in the long term. Pumped-storage generation offered a significant advantage for this duty because of its unique ability to spin without load, ready for generation within a few seconds. This technique allows the turbines to be span on compressed air via hydraulically controlled valves when neither generating or pumping, thus allowing a quick-start time for either operation. The results of the economic studies in the late 1960s demonstrated that overall system costs were in farour of pumped-storage. The system costs represented the overall lifet me capital, fuel and other operational charges, but of course the fuel estimates did not anticipate the subsequent drastic change in oil costs. This provided an even more attractive case for pumped-storage, and also changed the balance in favour of nuclear energy compared with fossil-fired stations.

therefore cheaper, being provided by the most efficient base-load power stations. The scheme in effect uses the massive body of water in the upper reservoir to store potential electrical energy for almost instant use at peak demand periods.

ENGINEERING MARVEL

Construction began in 1975 and the station is now undergoing its final commissioning deep beneath the old Dinorwig slate quarry at Llanberis in North Wales. The site was chosen for its geological characteristics, hydrology and reservoir potential, construction requirements, power transmission line routes, environmental impact and, of course, pure economics.

Marchlyn Mawr, an existing lake, has been enlarged to provide Dinorwig's upper reservoir. Llyn Peris, a lake about 500 metres (1,640 feet) below, has also been enlarged to provide the lower reservoir. The high head of water (over 500 metres) available from the top reservoir gives the advantage of a smaller reservoir capacity for a given station output and it was possible to use Llyn Peris with very little extra capacity being required to form the lower reservoir. Indeed work was carried out on both reservoirs to optimise their usage for their precise roles in this project.

At all times during construction the CEGB placed paramount importance on the effect to the environment. Dinorwig is almost invisible from above ground which makes it all the more difficult to relate just how massive the underground workings really are.

This £425 million project is civil engineering on a vast scale. Around three million tonnes of slate has been excavated to create the station's network of huge tunnels and caverns, requiring the use of some 4,500 tonnes of gelignite. The main civil engineering contract was believed to be the largest ever let in the UK when it was placed in 1975. The underground chamber housing the main plant is one of the largest excavated caverns in the world—twice as long and half as wide as a football pitch and higher than a 16-storey building.

HYDROLOGY

Dinorwig uses more than 6.6 million cubic metres (1,462 million gallons) of water during a full generating cycle. The water is retained in Marchlyn Mawr by a 600-metre-long rock fill dam, landscaped on the downward face to blend with the scenery. The upstream side is faced with asphalt to provide the necessary water seal and the flexibility to meet the pressure changes caused by such a vast weight of water being continually moved in and out of the lake.



A pumped-storage power station differs from a normal hydro-electric station because it has two reservoirs---one above and one below the generating plant---and it uses the same water again and again.

After flowing from the upper reservoir to drive the turbines, the water is then pumped back from the lower reservoir to be used again (see Fig. 1). The turbine generators at Dinorwig work in reverse as motor-pumps and use power from the national grid when in the pumping mode. The water is pumped back at night when demand for electricity is low and electricity for pumping is As can be imagined when the generators are shut-down there is a tremendous back pressure created in the feed tunnel and for this reason a massive surge shaft was incorporated in the hydraulic system. Being open ended the 43 metres high, 10 metres diameter surge shaft allows the excess pressure to be dissipated; it surmounts the 439 metre deep, high-pressure shaft.

Maximum station water demand is 420 cubic metres per second, and the system velocities were selected by balancing the cost of various tunnel sizes and their energy losses within limits of previous experience to ensure a satisfactory tunnel lining and

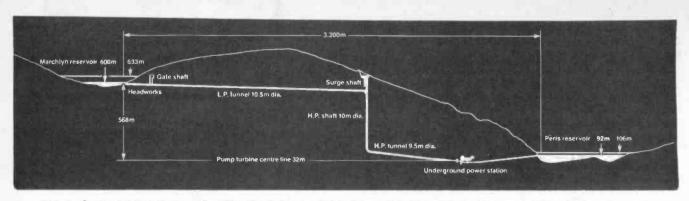
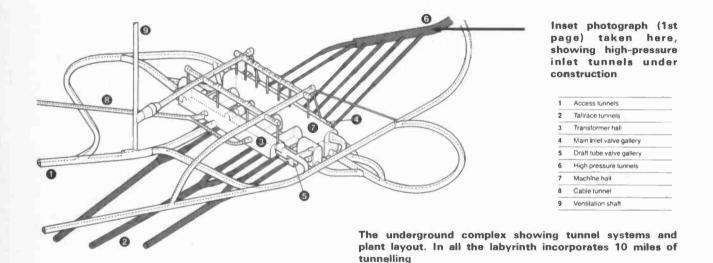


Fig. 1. Cross-sectional view showing the basic layout at Dinorwig. The overall distance between the upper and lower reservoirs is around 3.2 kilometres. The high-pressure and surge shafts together form a vertical shaft 1 kilometre deep-nine times the height of Nelson's Column

acceptable pressure surge levels. The power/time criterion was also an important factor in tunnel sizing because of the need to accelerate the 2km water column from standstill to full flow in just a few seconds.

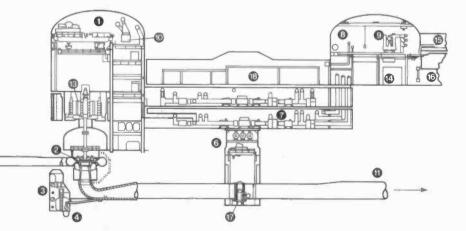
THE STATION LAYOUT

The underground tunnels were integrated with caverns housing the mechanical and electrical plant and access tunnels for construction and operation. An important factor in settling the





D

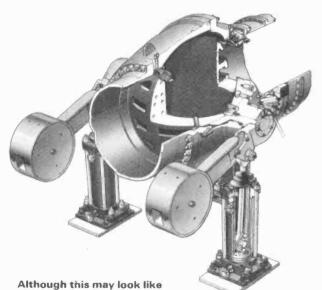


Section through station. Electricity generated in the machine hall at 18kV is conducted via aluminium bus-bars to the transformer hall, then at 400kV to the national grid system through a 6 mile underground tunnel

level of the main cavern was the submergence needed by the pump-turbines below the bottom reservoir to minimise air bubbles forming which could damage the pump-turbines. Other important features include: the cavern width, determined by the structural and water-bearing properties of the rock, provisions for maintenance, disposition of electrical busbars, the number of machines connected to transformers which supply the power to the high voltage grid system, equipment for starting the machines when they are operated as pumps, location of high voltage switchgear, control rooms and welfare facilities.

The 24 metre wide main cavern houses the six pump-turbines, and has adequate maintenace and plant provisions. In elevation, the cavern is a three-level arrangement dictated by access to the turbine/generators (Fig. 2.). The control room and equipment, low voltage switchgear and batteries utilise the space above the machine busbar routes. Workshops are arranged at one end of the main cavern with direct access to the plant. Three main busbar galleries, one per pair of machines, connect the machines and transformers. These galleries also house high voltage switchgear.

The six vertical pump-turbines are directly coupled through intermediate shafts to the generator motors mounted immediately above them. Each pump-turbine is reversible and has a single runner which rotates in one direction as a turbine when generating, and the other direction as a pump when being driven by the generator motor.



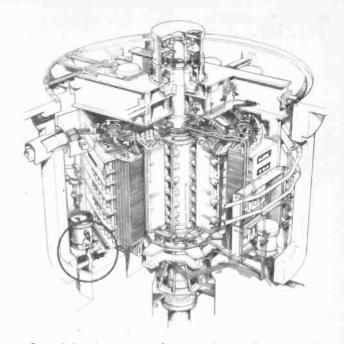
a Star-Wars android, it is, in fact, one of the six main inlet valves. Its diameter is

some 2.5 metres and each of the counterbalances weigh 16 tonnes

TURBINE/GENERATORS

Each generator unit, operating in the turbine/generating condition, has a nominal water flow of 65 cubic metres per second, and delivers 300MW to the national grid. Flow regulation is achieved by 24 guide vanes which incorporates a restraint device to prevent extreme movement. The vanes, operating in conjunction with an electro-hydraulic governor, are powered by balanced double acting servo motors acting on a regulating ring.

The air-cooled generator motors, rated at 330MVA, are designed for direct coupling to the pump-turbine via intermediate shafts. The rotating parts are supported by a thrust bearing above the top joint of the generator-motor. Eight air coolers using circulating water from the lower reservoir are located on the stator frame which is totally enclosed within a



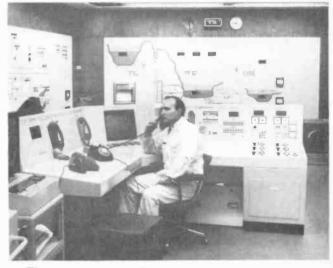
One of the six generator/motors, the rotating parts of which weigh 484 tonnes

concrete housing. Electrical and mechanical braking systems are included so that the operating mode of generating and pumping can be quickly changed.

Under normal working circumstances a generator unit may be required to perform 5.000 start/stops per year. The generator shaft is the largest mild steel forging ever produced by British Steel. The total weight of the rotating parts of each of the generator motors is 484 tonnes.

The construction of the generator assemblies was completed on site, as transportation limitations precluded shipment of the huge one-piece stators.

It is planned that under normal working conditions only four of the six generator units will be used, the remaining two will be reserved for emergency and sudden demand use. These two stand-by units have a slightly different winding in order that comparisons may be made between the two types with regard to circulating current losses, cost and installation considerations. The basic specification remains the same.

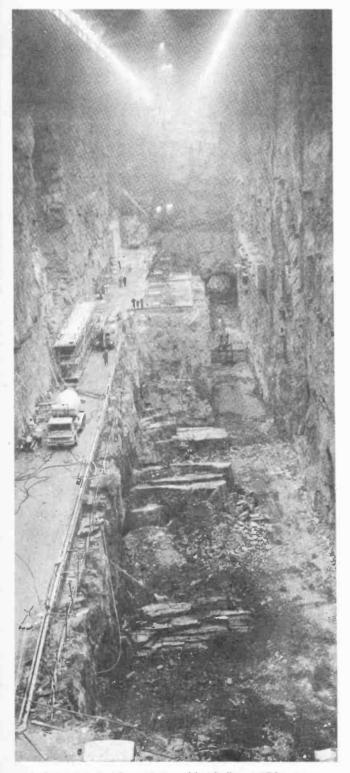


The one-man operations room incorporates many automatic monitoring systems

CONTROL SYSTEMS

The control room is situated in the main cavern overlooking the machine hall. Automatic operation is provided for all generating and pumping conditions, including changing from full load pumping to full load generation.

The control room was designed for use by a single operator



In its excavated form the machine hall was 179 metres long, 24 metres wide and 60 metres high. It is believed to be the largest single man-made cavern in Europe

but the layout allows for two operators if necessary.

The electronic analogue control system enables the equipment racks to be mounted remotely from the control panels and the system is built up from function modules which, in addition to basic control elements such as high/low alarms etc., include computing functions that enable complex signal manipulation to be carried out.

Other interesting control features include environmental equipment, turbine vibration monitoring and pressure resonance detection equipment used to monitor the underground hydraulic system and guard against damaging resonance phenomena. Level switches have been incorporated to guard against station flooding. An ingenious "radiating feeder" communication system has been incorporated throughout the underground network. A narrow co-axial cable can be found all through the labyrinth, this being a feeder cable that "leaks" radio signals which would otherwise be screened by the mass of solid rock; this system enables immediate radio contact to be maintained within the plant. Radio pagers are also incorporated for staff location.

INDUSTRIAL RELATIONS

From the outset of this project the CEGB placed great importance on using local labour wherever possible. For this reason a multi-skill training centre was established at Dinorwig to train the 95% strong local labour force in the many skill requirements needed. Highly experienced representatives were drafted in from each of the contractors supplying equipment to Dinorwig, and it was they who passed on their skills to the new workforce. Of course, there already existed an important skill which only the Welsh could supply, namely their invaluable knowledge of local slate mining techniques.

ACKNOWLEDGEMENTS

The technical data in this article was supplied by the CEGB who also furnished the photographs and illustrations.



The surge-shaft under construction. 584 metres below this point the water from Marchlyn reservoir enters the high-pressure feed tunnel



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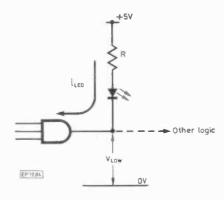
INTRODUCTION FORMULA ELECTRONICS

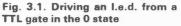
MICHAEL TOOLEY BA DAVID WHITFIELD MAMScCEng MIEE O&A Level Part Three

WE HAVE seen in the PE Logic Tutor how useful it is to be able to indicate logic states using l.e.d.s. This idea may be extended further to the point where the output of a logic network is actually intended to drive l.e.d.s as the primary function of the circuit. For example, with the advent of different coloured l.e.d.s, model engineers are now able to build replica traffic lights using l.e.d.s driven by logic. The problem, however, is how to interface the logic gates to the l.e.d.s?

In order to drive an l.e.d. to reasonable brightness, it is necessary to pass a current of around 10mA to 20mA through the diode. In general, a nominal 10mA will produce adequate brightness for red diodes, with the other colours often requiring somewhat more (due to their lower efficiencies). A typical red l.e.d. will have a voltage drop of approximately 2V across its terminals under these conditions, other colours slightly more (up to around 2.5V). This in effect is a description of the characteristics of the load which we want to drive from a logic circuit. We must now look again at the capabilities of TTL gate outputs to see if this is possible directly, or whether some intermediate circuitry is necessary.

We have already seen that a standard TTL gate output is capable of sinking a current of at least 16mA in the low state. In this condition we know, from the TTL level definitions, that the output voltage will not exceed 0.8 volts. This means that it is possible to connect a load between the gate output and the +5V supply, and draw 16mA through it, with a voltage of 4.2V (minimum) being dropped across the load. A voltage drop of 4.2V is too great for an l.e.d. on its own. However, if we connect a resistor in series with the l.e.d., as shown in Fig. 3.1, the current then flows through both the resistor and diode. Selecting the value of





resistor allows us to set the volt drop across the diode to an appropriate value. Assuming the worst case, we calculate the resistor value such that it will drop (5-l.e.d. drop) volts at the selected operating current, which must be less than 16mA. Thus, typical resistor values are around 220Ω to 270 Ω , although 330 Ω is also a common value. Using this method, we can still use the gate to drive normal logic loads, although the effective fan-out has been reduced by approximately six. The rule, as before, is that the total current which the gate can sink and still operate reliably is unchanged at 16mA. A disadvantage of this method is that the l.e.d. is illuminated when the output is in a O state, and this can be confusing if not properly considered. The second method offers an alternative which overcomes this minor problem.

Looking at the drive capabilities of a TTL gate in the 1 state, we might think that this could not be used directly to drive an l.e.d.; only 400µA of drive is available. The voltage in the 1 state is a minimum of 2.4V, and is typically 3-4V. So much for the theory. In practice, if we are willing to accept that using a gate output to drive an l.e.d. will make its output unavailable for other uses, then something can be done. In fact, the result is one of the simplest ways of driving an l.e.d., since it requires no additional components. Simply connect the diode between the gate output and OV, as shown in Fig. 3.2, and all should be OK. The question now is "Why does it work at all?".

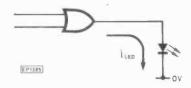


Fig. 3.2. Driving an l.e.d. from a TTL gate in the 1 state

The reasons once again relate to the detailed characteristics of the TTL gate output circuit. Suffice it to say that the load represented by the l.e.d. is such that the output voltage of the gate falls to approximately 2V at a current of around 5 to 10mA. This is exactly what is required to drive the l.e.d., even though it is *not* a legal TTL logic level. All-in-all a very simple (and safe) way of driving an l.e.d. from a TTL circuit. This is in fact a common way of using up 'spare' gates in i.c.s when the main circuit design has been completed without needing all of the gates in

every i.c., and is a useful way of providing test indicators. The nice additional feature of this method is that the l.e.d. is illuminated when the gate output is at a 1 level.

LOGIC INTEGRATION

With the advent of integrated circuit technology, the electronic subassemblies which had previously been used in the construction of logic circuits disappeared. In their place came ranges of integrated circuits (i.c.s) which contained standard gates fabricated on silicon 'chips'. As the technology has advanced, it has become possible to include more numerous and more complex circuits inside such i.c.s. We have seen, from the TTL i.c.s described so far in the series, that one effect of this progress has been the inclusion of more than one of a particular gate within a single package. Indeed, the number of gates is now often limited solely by the number of pins available on the package. In the interests of standardisation, the number of pins is restricted to 14 or 16 wherever possible, although 18- and 24-pin devices are now becoming increasingly common.

To try and give some idea of the scale of integration involved, a typical TTL gate currently occupies a piece of semiconductor which measures less than 0.2mm square; smaller than the proverbial pin head! By comparison, the black package of a 14-pin i.c. is approximately 19mm by 6mm. With such a degree of integration available at low cost (e.g. a 7408 costs around 20p), it is not perhaps surprising, therefore, to find that ever more complex logic functions are being integrated into single i.c.s. It is interesting, therefore, to look at some of the reasons behind this move, and some of the advantages which result.

One of the first things that we notice when we look at complex logic circuits is that they can involve guite a number of i.c.s. This in turn means that there are many interconnections, and that the boards required for the circuit become quite large. Two consequences of this are that such circuits are expensive to make, and that they are less reliable due to the greater number of components. Ideally, the rule in this respect is the smaller the physical circuit, the better. If, therefore, we can identify arrangements of logic gates which occur frequently, it should be possible to make the complete

arrangement *inside* an i.c., and so keep the number of i.c.s required to a minimum.

Much of the rest of this series is concerned exactly with these types of logic elements, i.e. standard arrangements of basic logic gates. Originally, many logical functions were performed by circuits made up from arrangements of discrete gates, but as the TTL family has developed, many of these arrangements have themselves become available as 'standard' components. In this part of the series we will begin by looking at some simple combinations of the basic dates covered so far. Future parts will then move on to look at some of the more complex logic elements which are available in the TTL family. We will also be looking at the ways in which even these familiar gates can be built up from only a single type.

THE NAND GATE

In many discussions of logic, the first gate discussed is the NAND gate. This is often for no other reason than that it is the gate which is fundamental to the TTL family. The internal circuit for a TTL gate shown in part one of the series was in fact that for a 2-input NAND gate. This, however, is incidental to any progressive introduction to logic, and we have therefore waited until this point to consider the NAND gate. We will look on the NAND gate at this point as a gate which represents a combination of two of the basic logical functions already described.

The NAND gate combines the action of an AND gate and an inverter (NOT gate). Fig. 3.3 shows the way in which these gates may be connected to make up a NAND gate. Table 3.1 shows the truth table for a 2-input NAND

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Fig. 3.3. The NAND gate as a combination of AND and NOT gates

INPUTS		OUTI	OUTPUTS		
A	B	AND	NAND		
0	0	0	1		
0	1	0	1		
1	0	0	1		
1	1	1	0		

 Table 3.1. Truth table for 2-input

 AND and 2-input NAND gates

gate, side-by-side with that for a 2input AND gate for comparison. From the table it can be seen that the output of a NAND gate is a 1 unless both of its inputs are a 1, when the output changes to a 0.

Much of the usefulness of the NAND gate stems from the fact that it produces an inverting action, and Fig. 3.4 even shows how a 2-input NAND



Fig. 3.4. Connecting a 2-input NAND gate to behave as an inverter

gate may be connected to behave exactly like an inverter. We shall be returning to this point again later on, but for the moment we will take a look at a practical NAND gate in the TTL family.

7400 TTL NAND GATE

The 7400 is a quadruple 2-input NAND gate whose pin configuration is shown in Fig. 3.5. As before, each of

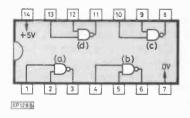


Fig. 3.5. Pin configuration for a 7400 quadruple 2-input NAND gate

the four gates is identical, and the power supply connections are quite standard. The 7400 is perhaps the most commonly used of all the TTL family. There are those, perhaps cynically inclined, who would claim that this is only because it is first in any list of TTL i.c.s! Whilst there may be some truth in this, it is nevertheless true that a 7400 will always 'do' to implement any logic function, and so it is probably deserving of at least some of its popularity.

In the previous section we introduced the NAND gate as a combination of an AND gate and a NOT gate (inverter). If we connect a further inverter to the output of the NAND gate, therefore, we would expect to get back to a simple AND gate, since two inverters connected in series behave as a buffer. Fig. 3.6 shows a circuit which allows us to investigate this AND and NAND gate action available from the

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7400. Indicator D3 should respond as an AND, while D4 should respond as a NAND, in accordance with Table 3.1.

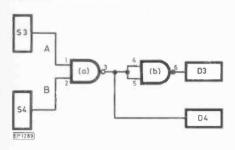


Fig. 3.6. Test circuit for NAND and AND operation from 7400

This circuit is set up by inserting a 7400 into the B socket, with pin 1 in position B1, and adding the following links.

S3	to	B 1	(Input signal A)
S4	to	B2	(Input signal B)
B3	to	D4	(NAND o/p signal)
B3	to	B4	(Link)
B4	to	B5	(Tie i/p together)
B6	to	D3	(AND o/p signal)
B7	to	0V	(Supply)
B16	to	+5V	(Supply)
B3 B3 B4 B6 B7	to to to to to	D4 B4 B5 D3 OV	(NAND o/p signal) (Link) (Tie i/p together) (AND o/p signal) (Supply)

In this circuit we have used a NAND gate connected (as previously shown in Fig. 3.4) to behave as an inverter. The B socket has been used for the i.c. in preference to the usual A socket simply to spread the wear and tear on the breadboarding sockets!

As a final test of skill in constructing truth tables, the circuit in Fig. 3.7 is offered as a challenge. To which 2-input gate does the behaviour of this circuit correspond? The circuit is set up using

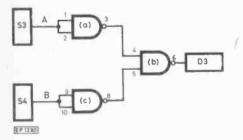


Fig. 3.7. A combination of NAND gates with an equivalent?

the same i.c. as above, but with the following (different) set of links.

S 3	to	B1	(Input signal A)
B1	to	B2	(Tie i/p together)
S 4	to	B11	(Input signal B)
B11	to	B12	(Tie i/p together)
B3	to	B4	(Inverted A i/p)
B10	to	B5	(Inverted B i/p)
B6	to	D3	(Output signal)

B7 to OV (Supply) B16 to +5V (Supply)

We will now go on to look at another common combination of basic gates, the NOR gate.

THE NOR GATE

As with the NAND gate, the NOR gate combines the action of an already encountered gate with that of an inverter. Fig. 3.8 shows how a NOR gate can be considered to be made up of an OR gate and an inverter, and the truth table for the arrangement is shown in Table 3.2. The output from a NOR gate will be a 0 unless both of its inputs are a 0, in which case the output changes to a 1.

EP1291

Fig. 3.8. The NOR gate as a combination of OR and NOT gates

INPUTS		OUTF	PUTS	
A B		OR	NOR	
0	0	0	1	
0	1	1	0	
1	0	1	0	
1	1	1	0	

Table 3.2. Truth table for 2-input OR and 2-input NOR gates

If both of the inputs of a NOR gate are connected together, as shown in Fig. 3.9, it behaves in the same way as a similarly arranged NAND gate, and produces an inverting action. The

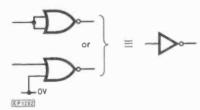


Fig. 3.9. Connecting a 2-input NOR gate to behave as an inver-

figure also shows an alternative method of constructing an inverter, using the second input tied to the 0 volt supply rail.

The NOR gate can equally well be used to construct any of the logic gates in the TTL family. The NAND gate is usually used in preference, however, since its internal gate circuit involves significantly fewer components and also dissipates less power. Either gate type will nevertheless produce an equally valid solution, and in different applications one type may require fewer physical gates than the other.

7402 TTL NOR GATE

The 7402 is a quadruple 2-input NOR gate whose pin configuration and internal layout is shown in Fig. 3.10.

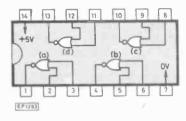


Fig. 3.10. Pin configuration for the 7402 quadruple 2-input NOR gate

Each of the four gates is electrically identical, but it should be noted that the layout of inputs and outputs does *not* follow the pattern of the previous 2-input gates. The power supply connections are, however, quite standard.

The 7402 essentially completes the basic set of 2-input gates for the present. We are now able to select the most appropriate i.c. for the task in hand, knowing that many of the gates may also be adapted to perform the functions of other i.c.s, thereby allowing us to keep the total number of i.c.s in a circuit to a minimum. As a final demonstration for the present, Fig. **3.11** shows two NOR gates connected as a NOR-NOT pair. The circuit is set

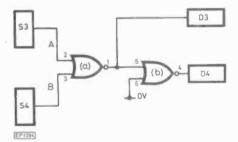


Fig. 3.11. Combination of NOR gates

up by inserting a 7402 in the C socket (for another change!), with pin 1 in position C1, and adding the following links.

S3	to	C2	(Input signal A)
S 4	to	C3	(Input signal B)
C1	to	D3	(NOR o/p signal)
C1	to	C5	(Link)
C6	to	0V	(Tie i/p to ground)
C4	to	D4	(OR o/p signal)

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C7 to OV (Supply) C16 to +5V (Supply)

It is interesting to check the overall operation of the circuit against the truth table. In addition, however, certain similarities may be noticed between the behaviour of this circuit and that shown in Fig. 3.7, and we shall return to this point later. Another point to note is the effect of disconnecting the link from C6, and observing that the result is the same as setting the input to a logic 1.

BOOLEAN ALGEBRA

In Part Two we discovered that the actions of gates can be described in the form of truth tables. A truth table indicates the output that will be obtained from a gate for any given combination of inputs. The technique can be applied equally well to individual gates, and to gate combinations. When more than two inputs are involved, however, the truth tables very quickly become quite large and cumbersome. Each additional input to be considered doubles the number of lines in the table. There is then a very real danger that the overall function of the circuit will become obscured by the sheer quantity of information presented in the table.

What we need then is a more compact way of representing the logical functions involved in logic circuits. It is also useful if any such representation can subsequently be expanded into a truth table should it prove necessary. What we are effectively grasping for here is a form of logical shorthand; something whose meaning we can readily understand, but which can expand at will to provide the same detail as the truth table. To be useful, therefore, we must choose a shorthand which has a very close relationship to the gates whose workings are being represented, and one which is widely understood. The most common shorthand in use today is known as Boolean Algebra.

Boolean Algebra is a logical shorthand which was developed by an English mathematician, George Boole, long before the introduction of digital electronics. Its purpose was to provide a simple way of writing the complicated logical statements encountered in the study of logic. The idea was to be able to determine rapidly whether a particular statement was true or false, depending on the outcome of other related statements. If we now relate logical statements to digital logic gates, and the result true/false to gate outputs of 1/0, we can see how easily Boolean Algebra can be applied to the analysis of logic circuits. It also helps to explain why we so often see digital gates referred to as logic gates.

BOOLEAN EXPRESSIONS

In order to be useful, a shorthand must have an established set of symbols. Algebra in turn must have a set of rules in order to be able to determine the outcome of a particular problem. We will start by looking at the way in which Boolean Algebra allows logic situations to be expressed in what are known as Boolean Expressions. Later on we will return to look at some of the basic rules of Boolean Algebra itself, which will allow us to manipulate such Boolean expressions.

The fundamental gate actions of digital logic are, as we have already seen in this series, those of AND, OR and NOT. Boolean Algebra uses special symbols to represent each of these gate actions, and these are shown in Table 3.3. As with any shorthand, alternative symbols are sometimes used, but the symbols shown in the table are those in common use, and they will therefore be adopted for this series.

Now that we have a set of shorthand symbols, it is appropriate to see how they are used in practice. We will stay, for the moment, with the three basic gate actions of AND, OR and NOT. Fig. 3.12 shows three examples of the use of the shorthand notation introduced

LOGICAL FUNCTION	EXAMPLE
AND	A.B
OR	A+B
NOT	Ā

Table 3.3. Boolean symbols for fundamental gate actions

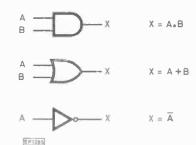


Fig. 3.12. Boolean expressions for basic gate functions

above. In the first example, concerning the AND function, the expression shown as X = A. B indicates that the X output signal is the result of passing signals A and B through an AND gate. Put another way, X equals **A and B**; X is 1 if, and only if, both the A and B inputs are at a logic 1.

The second example treats the OR gate in a similar way. The output from

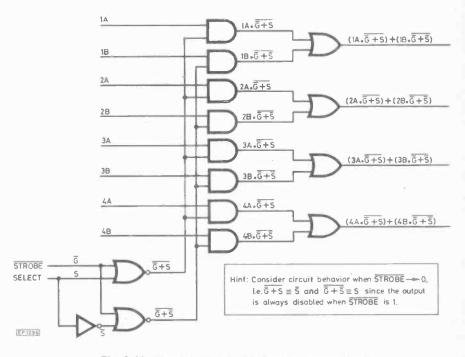


Fig. 3.13. Circuit labelled with Boolean expressions

the gate, X, is shown as X = A + B, indicating that X is the result of passing signals A and B through an OR gate. The action of the gate is **A or B**, and X is 1 if either A or B is a logic 1. The action of the inverter, shown in the third example, is to cause the output X to become equal to \overline{A} , read as A-bar or NOT-A.

Now that we have a shorthand for writing down Boolean expressions, let us pause for a moment to look at the circuit shown in Fig. 3.13. In this circuit the signals still have simple names (A, B, C, etc.), even though these would be replaced in a real circuit by the names of actual signals. The circuit has outputs labelled with their appropriate Boolean expressions, but can you think of a use for this type of circuit? Hint: this is actually available as a standard function in the TTL family. (74LS157)

NEGATIVE LOGIC

At this point it is perhaps timely to stand back for a moment and ask ourselves about the way in which we describe and use logic signals. For example, when we call a signal Motor On or Light Off, what do we actually mean? The answer is hopefully selfevident; we assume that the motor will be turned on, or the light turned off, respectively, whenever the signal is at a logic 1. So far, so good, but is this the only way to use logic signals?

If we always have logic signals of the type described above, there can be unexpected, but predictable, problems with TTL circuits. Consider, for example, part of the motor control circuitry for a milk float, shown in Fig. 3.14. As we can see, the control unit is situated in the driver's cab and only requests motor movement when the ignition key is on, when the driver has pressed the foot pedal, and has also selected either 1st or 2nd gear. Unless all of these conditions are satisfied (i.e. all necessary inputs are 1), the Motor On signal will be a 0, and the milk float will therefore not move. When all of the necessary conditions are satisfied, the Motor On signal will change from 0 to 1. The signal will then pass to the motor compartment, and the motor will be activated via a suitable buffer. In reality there is likely to be further stages between the buffer and the motor, but the principle is unchanged. The circuit seems to work ideally, so what is the problem?

As with many situations in the real world, the danger in this circuit is when something goes wrong. The most common problem in this type of situation, where different parts of the circuit are physically separated, is that the interconnecting wiring may become broken. This is an unexpectedly common occurrence, and may be caused by vibration, traffic damage, or even the printed

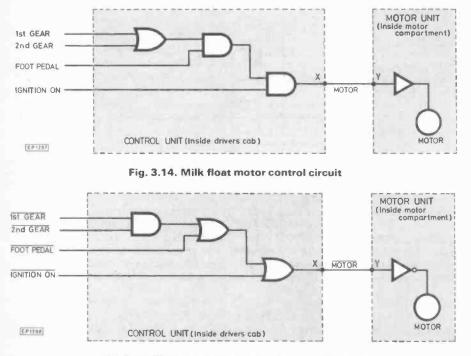


Fig. 3.15. Motor control circuit using negative logic

circuit board containing the control unit being removed from its socket for testing. In any event, the situation we then have is that the link between points X and Y on the circuit is broken, at least in electrical terms. This is where the trouble really starts! Although the control unit output may be a O (say the driver has turned the ignition key to off), because the X-Y link is now broken, the input to the buffer in the motor compartment will float high, turning the motor on. What we have then is a runaway milk float.

The example above is obviously rather contrived, and hopefully nonrepresentitive of milk floats, but it does show the type of situation which we must avoid at all costs. The simplest solution to this type of problem is to use what are known as Negative Logic control signals. What we have explained so far in this series is known as Positive Logic; the signal condition is true (e.g. the motor is to be turned on) when the signal (Motor On) is at a logic 1. In negative logic, we use the opposite convention, and the signal condition is true when the signal level is a logic 0. In order to distinguish between the two types of signal, we use the bar notation introduced earlier. In this way, Motor On should turn the motor on when at a logic 1, whereas Motor On will turn the motor on when at a logic 0.

To illustrate this idea in practice, the motor control circuit described earlier has been re-designed in Fig. 3.1^F to make use of negative logic. Should there be any doubt about the correct operation of the circuit, a truth table will demonstrate that the motor is only turned on when the appropriate combination of negative logic signals are input.

MORE ON BOOLEAN ALGEBRA

In the discussion above we have seen that there are many situations where it is useful to use inverted logical signals. Boolean Algebra has, as we have seen in Fig. 3.12, a shorthand way to indicate the operation of logical NOT, using the over-rule notation. This is readily extended to cover all types of negative logic signals, as shown by the Boolean representations for NAND and NOR in Fig. 3.16.

The general rule for logical inversion, therefore, is that an over-rule is added to the output signal whenever the gate performs an inversion. Thus the output from a NOR gate whose inputs are Å

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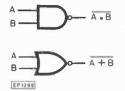


Fig. 3.16. Boolean shorthand for NAND and NOR gates

and B will be $\overline{A + B}$. If the output from this gate is now fed through an inverter, as shown in Fig. 3.17, the output will then have two over-rules, $\overline{A + B}$. If we draw the truth table for this circuit, we can probably deduce that a double over-rule of this type has a very simple equivalent, but what is it?



Fig. 3.17. Boolean doubleinversion

SLOW SIGNALS

In Part One we saw the way in which the output of a TTL buffer changes as a steadily increasing voltage is applied to its input. We saw that the output will be below 0.8V until the input has risen to over 0.8V. Similarly, we saw that the ouput will be at least 2.4V whenever the input voltage exceeds 2.0V. In the middle range, however, where the input is between 0.8V and 2.0V, the behaviour of the gate is somewhat unpredictable. The output could be either above 2.4V or below 0.8V, or anywhere in between, without any real guarantee of where it might be. These features are summarised in Fig. 3.18, in what is known as the Transfer Characteristic of

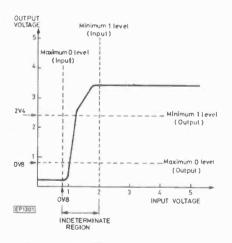


Fig. 3.18. TTL buffer transfer characteristics



Fig. 3.19. Variation of switching point

a TTL gate. From this graph we are able to predict the behaviour of a TTL gate which has an input signal level that is *not* within the indeterminate region. There are, however, problems which can arise when a signal crosses this indeterminate region.

If we look at a signal which has a slowly changing level (in TTL terms at least), we see that, as it varies between 0 and 1, it crosses and re-crosses the indeterminate region. Fig. 3.19 shows a typical such input waveform, together with a corresponding output signal. This seems at first sight to be quite straightforward, but in fact there are a number of hidden problems. The first is that the point at which the output changes from 0 to 1 can be guite variable, being particularly sensitive to changes in temperature and supply voltage. The results of this effect are shown by the dotted lines in Fig. 3.19, and a degree of uncertainty therefore exists in predicting the instant at which the output will change.

The next problem, however, can have a much more dramatic effect on the overall behaviour of a logic circuit. The difficulty arises from the fact that, in any real circuit, the laws of physics dictate that there will always be some electrical noise present. This will usually be small by comparison with the standard TTL levels and, even when it becomes superimposed on the logic signals, it is not usually enough to upset the 0 and 1 levels. The effect of the noise, however, is to cause the signals to vary slightly above and below their ideal levels. The amounts, and the intervals between these variations are random, but they are usually fairly small, and it is only usually when we have a slowly varying signal that this noise can become troublesome.

As before, the problems arise near the point at which the gate output switches between 0 and 1. Near the switching point, the noise on the input signal can be sufficient to take the input above and below the switching point a number of times, before it finally settles down. At all times, the general trend of the signal (which is equivalent to a perfect, noise-free signal), remains steadily upwards or downwards through the indeterminate region. It is while the signal is near the switching point, however, that noise can upset the expected smooth transition of the output level.

The overall effect of noise on the output of a gate being used to process a slowly varying input signal can be as shown in Fig. 3.20. The output here can be seen to 'chatter' quite a number of times, before finally settling down. If the circuit following the input gate is counting pulses, or is affected in any way by the number of transitions, the result of this chattering can be quite dramatic; errors of up to 500% can easily occur for slowly varying input signals. It should be remembered here that TTL gates switch in a very small fraction of a micro-second, so many signals unexpectedly fall into the category of 'slow'.

So much for the problem, but what about a solution? What we ideally require here is a gate which has a 'snapaction' transfer characteristic. By this we mean that, once the input signal has passed a defined level, a significant change in the opposite direction is required in order for it to have any effect. This characteristic would result in a circuit which is immune from small changes in level around the switching point, and would obviate the possibility of the chatter effect. As might have been guessed, special TTL gates exist which have such a snap-action property, and they are known as Schmitt Gates.

SCHMITT GATES

The most important feature of a Schmitt-input TTL gate is its behaviour in the so-called indeterminate region. In almost every other respect a Schmitt gate behaves just like any other TTL gate, but it is more versatile since

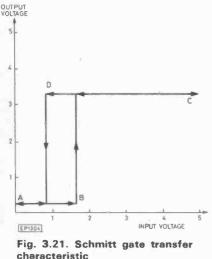


Fig. 3.20. Effect of noise on switching

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Schmitt gates can be used in place of ordinary gates, but not necessarily vice versa.

The transfer characteristic for a Schmitt buffer is shown in Fig. 3.21. When looking at this characteristic, it is important to note the directions of the arrows, otherwise the figure can make no sense at all. If we consider, by way of illustration, an input signal which



varies smoothly from OV to +5V, and then back again, this will help us explain the shape of the graph.

The input signal starts at point A with a level of OV, and the corresponding output here is less than 0.8V, as for any other type of TTL gate. As the input voltage increases from zero towards point B, the output stays at or around this level. At point B, the first switching threshold is reached, and the output jumps (snap-action), from below 0.8V, to above 2.4V. Further increasing the input voltage, towards the maximum allowed input at point C, has little or no effect. Thus, with a steadily increasing input voltage, the gate behaviour follows the path A-B-C.

Following the reverse path, the output level stays above 2.4V until the input level falls to the second switching threshold, at point D. The snap-action switching effect is then repeated, but in the reverse direction, and the output falls to below 0.8V. For the steadily falling input voltage, therefore, the gate behaviour follows path C-D-A.

The real points to note here are the snap-action switching behaviour and the difference that exists between the two switching thresholds known as the 'hysteresis'. Once the gate has switched, therefore, change of input level equal to at least the hysteresis of the gate and in the *opposite* direction, is required in order to make the gate output change state again. This combination of snap-action switching and the hysteresis of Schmitt gates combine to substantially remove the problems associated with slow signals. Other uses of Schmitt gates will be examined later in the series. For the present, however, we will look briefly at some practical details of Schmitt gates.

Typical circuit symbols or Schmitt gates are shown in Fig. 3.22, from

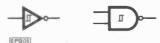


Fig. 3.22. Circuit symbols for Schmitt gates

which it can be seen that the standard outlines are used, but with an additional symbol inside. The extra symbol is, in fact, in the form of the transfer characteristic shown earlier in Fig. 3.21. One of the most common Schmitt gates in the 7400 TTL family is probably the 7414 the Schmitt equivalent of the 7404 hex inverter. The pin-out is shown in Fig. 3.23, and it can be seen that the 7404 and the 7414 are pin-compatible.

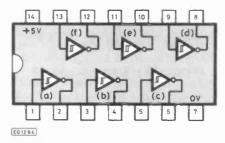


Fig. 3.23. Pin configuration for the 7414 hex Schmitt inverter

To conclude this brief look at Schmitt gates, the circuit of Fig. 3.24 allows us to compare the operation of the 7404 and the 7414. A variable resistor, or a variable power supply, is used here to provide the input signal which varies over the range 0 to +5V.

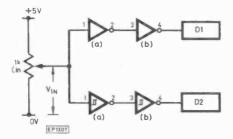


Fig. 3.24. Comparison of gate performance

This circuit is wired by placing a 7404 in socket A, and a 7414 in socket B, and adding the following wire links.

VAR	to	A1	(Variable supply i/p)
A1	to	B1	(Tie i/p together)
A2	to	A3	(Link)
B2	to	B3	(Link)
A4	to	D1	(Ordinary gate o/p)
B4	to	D2	(Schmitt gate o/p)
A7	to	VO	(Supply)
B7	to	0V	(Supply)
A16	to	+5V	(Supply)
B16	to	+5V	(Supply)

The variable supply (VAR) may be taken either from a 0 to +5V supply or, as shown, from a potentiometer connected between the two supply rails. As the input voltage is slowly increased, it should be noted that a point is reached where D1 will flicker on and off for small adjustments of input voltage. D2, on the other hand, will have a definite snap-action, with two quite distinct and separate switching points. It is an interesting exercise to try and plot the transfer characteristics for the two types of gate on the same axes for comparison; the differences are quite noticeable!

NEXT MONTH: The exclusive OR gate and power supplies.

LOGIC TUTOR BOARD KITS Complete kits for the Logic Tutor are available from the following suppliers: Howard Associates, 59 Oatlands Avenue, Weybridge, Surrey KT13 9SU (0932 42376)

Riscomp Limited, Electronic Component Distributors, 21 Duke Street, Princes Risborough, Bucks HP17 OAT (084 44 6326)

TK Electronics, 11 Boston Road, London W7 3SJ (01-579 2842)

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G. D. & P. Cowan Services, 9 Harcourt Terrace, Headington, Oxford (0865 60741)

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Microstate Ltd., 5 Northfield Close, Fernhill Heath, Worcester WR3 7XB (0905 56322)

Bi-Pak, PO Box 6, Ware, Herts (0920 3182)

PLEASE NOTE

The R19 shown on the Logic Tutor Board next to link 3 should be R15. Under the heading 'Initial Tests' diode D9 is referred to as diode D5; diodes D5 and D6 are referred to as D6 and D7 whilst diodes D7 and D8 are referred to as D8 and D9.

Space Watch...

NEW USSR RADAR

A new radar installation has appeared in central Siberia. Since it is near to the heavy missile fields of that region it is to be assumed that it is for the detection of ballistic missiles. If this is so, then it will be highly sensitive as an early warning device. The actual site is situated at approximately $58.08^{\circ}N$ and $92.4^{\circ}E$.

The United States discovered this late last July. It was not detected earlier because for reasons of economy America had restricted activity for some months. The Americans used a Big Bird satellite and analysts have now verified this installation. A map has been released showing where the principal missile bases are together with the radar sites. According to this map there is every sign of readiness exhibited. There has been quite a large coverage of the subject recently. As has been pointed out on previous occasions it is not the policy to deal with these matters in Spacewatch. The excuse for bringing it forward now is that the event of the Korean jet airliner has brought the matter to serious proportions and many people in all parts of the world now seem more concerned with accidents precipitating events leading to a holocaust.

THE SHUTTLE

The success of the latest shuttle flight was a new venture, a night landing and the highly successful launch of another satellite, this time for India. The mission has considerably enhanced the prestige of NASA. Naturally officials are full of a very real pride in all concerned.

An aspect of night landings is the 'seeing' ability available to the crew. The last critical phases of the touch-down were very important in this respect. The portable lighting system was set up on the lake bed at runway 17. Lights were also available for the ground crew. The shuttle could not be seen by observers at Edwards until it entered the Xenon beams before touchdown, but a rumbling sound could be heard before the craft came into view.

A statement made by Lt. General J. A. Abrahamson, a NASA associate administrator, confirming the advantages of night landings,

went on to say, "this opens up another 12 hour door for launching at Kennedy. It is vital because then we can take advantage of the best weather." Abrahamson also said that it was certain that night landings in future would exceed those made in daylight. The Orbitor turnround was delayed because of manifold drainage problems. Altogether there was a 16 hour delay, the reason for this lengthy procedure was the result of playing it safe. There were often minor faults which were referred to investigation.

The set manoeuvres carried out were done so successfully, much new information on the shuttle performance has been gained, hitherto there has been a radio blackout during the landing phase, and there is a suggestion that the aerials on the top of the vehicle should be used. The method of penetrating the ionized atmosphere was overcome some years ago in manned satellite investigation and was given some coverage in these columns. However if it can be done with existing equipment it will save weight. It is to be hoped that the American government will not inflict economies on space activity for scientific and commercial purposes, since there is much to be done if the full use is to be made of advances in these fields.

GERMANY

The German Infrared Laboratory has now completed its first tests and design review. It now remains for the German government to give assent to proceed with the project. The observatory was planned as a successor to IRAS. The plans are aimed at a launch date around September 1987. This is hoped to be attained, and launch is to be by shuttle. The Laboratory was approved by an independent review board in July. The further work required is for the manufacture of the qualification and flight models of the laboratory. Four scientific experiments are proposed and the estimated cost is some 1-9 million pounds. The experiments will be made by subcontractors.

The satellite is expected to provide more detailed observations than is possible with IRAS and is expected to be as much as ten times more sensitive. It will use the infrared survey being carried out by IRAS and help to select targets for more detailed study. The laboratory would be in space for several years before two other observatories are ready. These will be the infrared facility on the Shuttle and the Infrared Space Observatory put up by the European Space Agency. The time scale for these is 1990 for the American telescope and 1992 for ISO. The purpose of these vehicles will be complementary.

The immediate German programme will hope to gather information that could help to clarify whether the Universe is 'closed' or 'open'.—That is to discover if there is enough matter in the Universe to generate gravitational force to prevent expansion. Other questions to be settled include 'ls the Universe made of hydrogen and protostars like Jupiter'? More details of the equipment and programme will be available when the vital question of the funding is settled.

JAPANESE ACTIVITIES

The Japanese are planning a new launcher with a capacity of two tons of payload. They expect to launch 78 satellites between now and the year 2000. It is Japan's stated intention to compete with America. They are asking for an annual increase in funding of up to 10% per annum for 17 years.

The programme is for launching several satellites at the same time. This is necessary because of an agreement they have with local fishermen. The fishermen claim substantial losses at each launch. The agreement being—the authorities will only launch during February and August of each year. A detailed list of the launch plans will be available at a later date.

The long term plan would be to offer launch services to construct ground stations and to develop software.

SOYUZ RENDEZVOUS FAILURE

The Russian Soyuz crew failed to rendezvous with the Salyut 7 Space Station earlier this year. This was because the radar antenna would not deploy and an optically guided approach to the station had to be aborted in darkness when the cosmonauts feared a collision between the two vehicles. The Soyuz crew did not attempt to dock. Mission Commander Vladimir Titov said "I was unable to approach closer than some 525 feet."

The Soyuz problems began on the second orbit when the crew found that the dish antenna mounted on a large boom had only deployed partially. This antenna is a key element in the Soyuz automatic docking system. The crew received ground control permission to make several attitude control manoeuvres at rates high enough to swing the antenna into position. The attempt failed. Titoy said that Soyuz mission rules dictate that after such a failure no attempts to try other methods are allowed. Nevertheless the crew sought and obtained permission to attempt a rendezvous with Salyut by optical means with the aid of ground radar signals. Such a situation was new and the crew had little hope of success.

Without ranging data to provide distance and closing information Titov had to tell the ground 'how large' the Salyut appeared compared with the reticle markings on the sight and then the ground team computed what type of manoeuvring to perform. On the 19th orbit Titov saw the Salyut in his alignment sight and reported the data. The ground control told him to fire the spacecraft's manoeuvring engine for 50 seconds. He then reported to the ground that the Salyut was closing.

At that point both vehicles flew out of the ground radar sight for 35 minutes. Titov was able to approach within 1000 feet of the Salyut and shine a searchlight from Soyuz onto the passing Salyut station until they went into darkness. Commander Titov tried braking and other methods without success. When they were again in radar sight they were found to be about 2.5 miles apart. Further attempts were abandoned. It was then decided to return to earth. Titov was naturally disappointed. Subsequently another vehicle made the rendezvous without any trouble at all.

When one looks back it is quite remarkable that beyond these limited scares, spaceflight seems safe enough.

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V.T.'s views and opinions are entirely his own and not necessarily those of PE

E van Steadman is 45 years old and wellbuilt. He's tastefully tailored, except perhaps for the tie-of-the-day—the badge of your true extrovert. The abundance of greying hair is studiously ruffled and he keeps his loose change—which, in his case, could include a $\pounds 50$ note—in a little purse. A minus point this. Men of substance should at least appear to be less concerned about their petty cash. But I quibble.

Evan is a man who's unoffended, even tickled, to be labelled an exhibitionist. He knows that in the sense people use the description about him it has nothing to do with the dictionary definition of 'one who has a compulsive desire to attract attention by exaggerated behaviour or boasting'. Or, in another listed sense, 'by indecent exposure'.

He is one of the woefully few 'characters' who has survived the creeping standardisation of so much about the electronics industry and whose drive and personality is behind many of the best-known and most-successful high-technology exhibitions to which hundreds of thousands of hobbyists and professionals alike flock year after year.

A one-time schoolteacher, he entered the world of publicity via Fleet Street as a feature writer. "My only gift," he says, "was an ability to turn out 1,500 words to order on any subject my editor demanded. It was usually sheer bloody rubbish, of course. But no matter." This led to work with advertising agencies and publicists and eventually to the post of European advertising and publicity manager for Texas Instruments.

"But deep down," he said, "there was this congenital desire to go off and do my own thing—if only because this was the sole way I could make more money. I was based in the South of France at the time of the big decision. I was living in Christian Dior's villa, if that matters, and life was magnificent. My salary was on an American scale, but even so I managed to spend it. Very naughty of me. So I made tracks for the UK and set up a PR business in the bedroom of the house I'd kept on in Cambridge. My capital was £75."

Today he owns three advertising agencies and three PR companies. "I don't work much on them personally because it wouldn't be cost-effective. I devote the bulk of my time to exhibitions. The sums of money involved here are fantastic. Take the Antiques Fair at Grosvenor House, London-the only nontechnical show I handle. There's a £5 entrance fee, the catalogue costs £4 and we get around 20,000 visitors. That, if your maths are right, adds up to £180,000 in cash taken at the door. Even so, mine's a chancy game. When an exhibition fails I lose a lot of money. When it works I make a lot. But when it fails it only fails once. When it works it works several times."

He pays tribute to the professionalism of the talented guys who run his companies. "But they find exhibition work too nauseating, too stressful, too tough. I thrive on stress and toughness. Don't ask me why. But if you find you like it you try it again."

The All-Electronics Show—an all-Steadman production—started nine years ago at Grosvenor House with 180 stands. The idea was born when some of his clients said that to stage the IEA and RECMF exhibitions at Birmingham when the electronics industry was based mainly in the South was absolute nonsense. "I agreed. And I told them that they didn't have to go to Birmingham. I'd put on a show right here. It worked, and it's been working ever since. Now I've become involved with two major annual exhibitions all in the high-technology sector—plus six conferences. And the list is growing at a rate of four a year."

I asked Evan whether he ever found his lack of formal training in high-technology a drawback. He was visibly affronted. "You underestimate me, madam," he replied with some coolness, putting me by way of riposte into a sexual category to which I clearly do

"I thrive on stress and toughness"

not belong. "I've spent the last 13 years right in the thick of high-technology. And it hasn't been simply a matter of learning the buzzwords. It's been a matter of seizing on definitions and opportunities and employing lateral thinking I won't go on. It would be too boring.

"But let me give you an instance. I claim to be the first to realise that the need for fibre optics had to be brought home to the world. That grandiose statement is justified by the fact that I put on the first-ever exhibition devoted to the subject. That show attracted 7,000 visitors, the next 12,000, Last year 70 companies took part. Next year it'll be 140.

"Of course, I'm no good at design and I couldn't wire up a p.c.b. But I reckon I know more about high-technology and all its implications than any of the guys working on the benches at Marconi or wherever. Certainly they know all about copper and wire and the rest of it. They understand circuit construction inside out. But they don't know where it all fits in, what it's all leading to. It's the understanding, the appreciation of significance and the breadth of experience that matters. And I've got it. That's not meant to be an arrogant statement—though I have been known to make them. "Let me give you another illustration. I recognised when the time was ripe for a p.c.b. exhibition. Internepcon used to cover the subject, but then gave it up and became simply a major general electronics exhibition held on the South Coast. The baby had been passed out with the bath water. So I knew it was time to act.

"So now you'll see why I marginally resent the suggestion that old Evan hasn't got the qualifications, because he bloody well has. Anyone with an IQ of 140 has all the qualifications he needs."

What material rewards has his energy, imagination, acumen and, in large measure, audacity brought? He's quite frank about that. "Oh I'm a millionaire (he might well have been telling me he was an expert on the banjo). I've a chauffeur-driven Bentley, three houses and a farm. I also own the building we work from."

I asked the stock question: had he yet reached the height of his ambition? "I've hardly started," he said. "A person who is really ambitious never really achieves the ultimate. He goes on. I don't mean that it's a sort of drug. It's like a way of life. But it's not just for money, power, sex with your secretary or the other things that people believe that ambitious men seek. It's the need to get into things where there is an aching void which you think you can fill. Nature abhors a vacuum. And I'm one of Nature's abhorrers. There are lots who are prepared to grasp the nettle, given the chance.

Evan Steadman does not rely for his remarkable stamina on such aids as specially prescribed diets, Swedish exercises or transcendental meditation. "I believe in excess in everything," he freely admits. "As a result I'm exhausted all the time. I'm fat. I've got spots before the eyes and spots on the skin and I'm all hyped-up. But, for me, sitting down relaxing in the sun for a couple of hours would be the kiss of death.

"Mind you, that doesn't mean that I haven't any relaxation at all. I've lots of children and I'm heavily into child culture. One son is a drummer and I have a daughter training to be an actress. Then there are my homes, the farm and so on. These need a lot of attention and in a way this adds up to a kind of hobby. Also, I've a private cinema in one of my homes. It's always there if I want it. And I'm a voracious reader. I get through two or three books a week. But the best type of relaxation is the business of living."

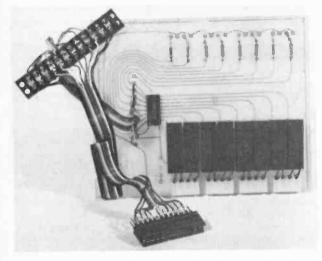
For all the success he has enjoyed and the excitement of his life, Evan Steadman is not a man I envy. Anymore than he envies me. As he says, his life style demands a hefty price. It can mean stress, ulcers, long hours, overindulgence in most of the things which should be taken in moderation. "But," he says. "If you're like me you'll know that it's all part of what your body's been telling you to do since the day you were born. Better to have been a has-been than a never-been.

"Joy, you know, is never present. You always feel you've failed to get to the point you wanted to reach. That's why you go on. You'll find it hard to believe that sometimes when I climb into the Bentley and say: Take me home Bernie, I feel pain."

EXPANDING THE

PART THREE SAM WIT

OUTPUT DRIVER BOARD . . .



FOR AROUND £12

THE next stage in interfacing the Vic 20 involves the dedication of lines as outputs and inputs. As these lines are now to connect the computer to external electrical devices, it becomes necessary to physically isolate the computer from such devices. We can carry this out by using opto-couplers and relays. This makes the interface rather more costly than the l.e.d.s and switches board, but it opens up exciting new fields for the **se**rious computer into a useful precision control instrument.

After much deliberation about the allocation of output and input lines, it was decided to give first consideration to dedicating all lines to output applications. This gives great potential to those who wish to control models and small motors, as well as those who wish to experiment with light displays.

This article deals with two output control boards and an input control board.

INPUT/OUTPUT CONTROL BOARDS

In the first interface, all 8 lines are set up as outputs. They are isolated by relays suitable for use with voltages up to 50V, and current handling capabilities up to 500mA.

The second interface uses opto-isolated triacs capable of controlling mains voltages and currents up to 1.2A. In both circuits, disabling of the outputs, when not under computer control is carried out by pulling the output lines down to ground potential. This must incorporate the use of insulated mechanical switches as with the l.e.d.s and switches board.

Whilst most control applications are likely to be in output mode, there are many instances when the computer has to be called upon to sense inputs. These can include counters and alarm sensors of many types.

The input board (to follow next month) uses opto-isolators. All these options can be mixed by using a simple connector block suggested in this article.

LOW VOLTAGE OUTPUT BOARD.

The principal devices used in the first control interface are the ULN2803A, octal driver integrated circuit and 700R, 500mA relays. See Fig. 3.3.

The ULN2803A is an 18 pin d.i.l. integrated circuit, which contains 8 open collector, darlington driver stages with a maximum operating voltage of 50V and each capable of sinking 500mA. Each stage incorporates a diode, which offers protection when switching inductive loads.

Applications include relay, lamp, solenoid and small motor switching, and individual stages may be paralleled to increase the output sink current.

The relays used incorporate a 6 to 9 Vd.c., 700ohm coil, and normally-open contacts rated at 500mA, 200Vd.c., 10 Watt maximum; whilst other similar relays have contacts rated at 200mA, 50Vd.c., 5 Watt maximum. No diode is included in the encapsulation, but the top of the moulded case of both types clearly illustrate the position of the coil and contacts.

See Figs. 3.4 and 3.5 for construction.

Provision has been made for 4 way, 0.2in. pitch terminals, but these are optional.

There are many options available from the ULN2803A, being capable of directly driving lamps, solenoids and small motors up to 50Vd.c. and the additional advantage of being able to parallel the stages to increase the output sink current. Here it must be remembered that the Vic 20 is only capable of providing 100mA (5V, making the external power supply necessary. Also, without relays, there is not complete electrical isolation between the computer and peripheral.

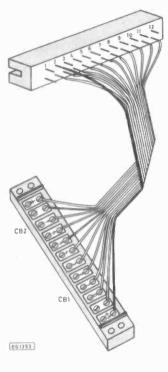
The cost of the i.c. and relays is in the region of £10, whilst the inclusion of terminal blocks would increase the cost by approximately £3. Of course, there is no need to completely build the board at first, as each line of the port can be used individually. Each board can be just partially furnished for initial experiments and extended as required.

The principle of operation is similar to that of the l.e.d.s and switches board. A logic "1" at the port is inverted by the driver. The logic "0" at the output of the driver, being low relative to the supply voltage induces current to flow through the coil, the electromagnetic field of which causes the contacts of the relay to close.

COMPUTING PROJECT

Again, provision is made for disabling the logic "1" at the port by pulling the output down to ground potential. This could be brought about by microswitches to limit the travel of a moving piece of apparatus, the level of liquids or the lapse of a preset period of time.

Making up this connector will allow flexible use of the expansion boards



COMPONENTS

OUTPUT CONTROL BOARD (low voltage)

Resistors R1-R8

470 1W 5% (8 off)

6-9V 700 ohm 500mA reed

4-way 0.2in. pitch terminal block

Semiconductor

ULN2803A

relay (8 off)

S.p.s.t. toggle (8 off)

(optional) (8 off)

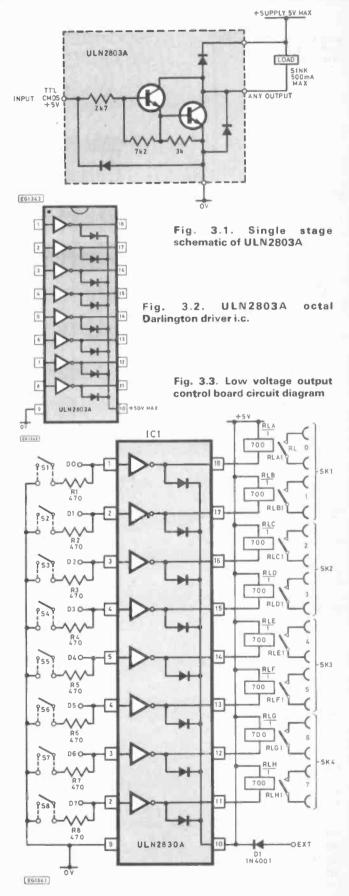
Miscellaneous RLA--H

> S1-S8 (external) TB1-4

P.c.b.

Constructors' Note

Project prices quoted are based on home etched p.c.b.s and "shop around" component prices, therefore some general purpose components are not specified to exact manufacturers or part numbers. If p.c.b.s are found to be unavailable from PE's usual suppliers, they may be obtained from Bradley P.c.b.s Ltd.



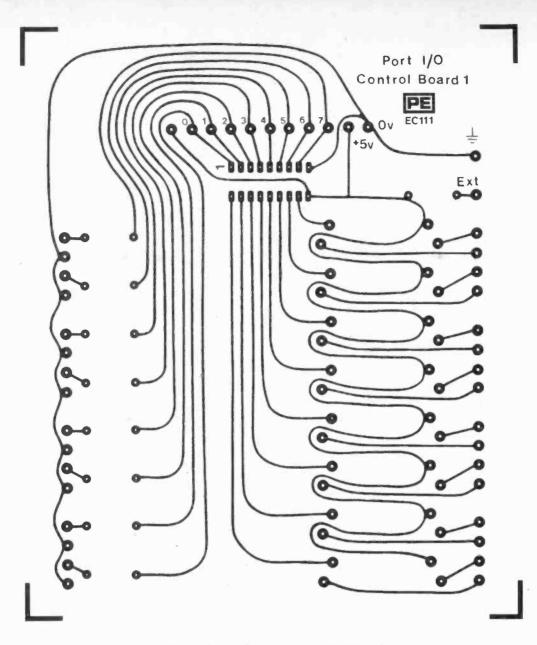


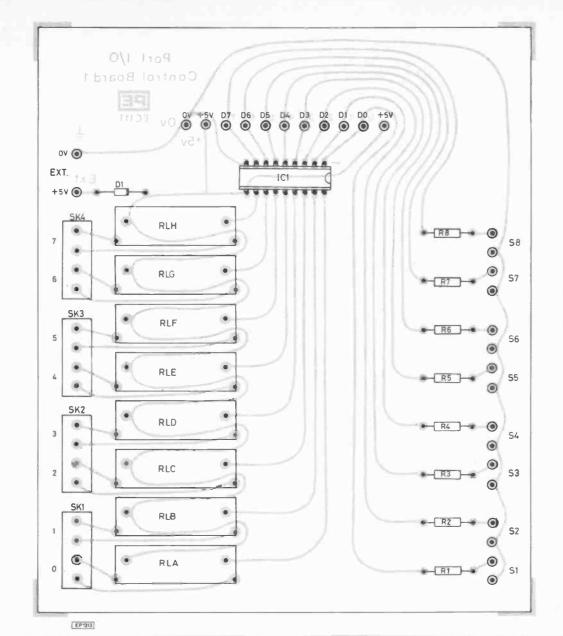
Fig. 3.4. Low voltage output control board printed circuit layout (actual size)

FEEDBACK

The current series on *Expanding the Vic 20* has brought a new and enthusiastic group of computer fans to the pages of *P.E.* A group which, having gained valuable experience at programming techniques, now wants to turn its machines to further use about the home. Some readers with little or no experience in construction have asked for some information on soldering and making p.c.b.'s, whilst others, having some difficulty in understanding the Vic 20 manual, have asked for a little more information on connections to the RAM/ROM Blocks. There is difficulty in choosing the most economical method of buying components. Some want more information about the Super Expander/Vicmon ROMs, and where to get them. Enquiries also concern the feasibility of getting even greater versatility from the RAM and ROM boards. Others ask for an extension of the series to include other computers.

The last request is the easiest to answer, because all the rest of the series, whilst aimed primarily at the Vic 20, is equally suitable for use on any home computer, having been developed some time ago for Nascom. All that is required is a suitable connector for your computer I/O port and some adaptation of the sample program routines.

NEXT MONTH: All the above points will be gone into, the topics being based on readers' letters during the series. A high voltage (mains) driver board will be featured, and an opto-isolated input board, along with details of construction and manufacturers' applications notes.





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LOGIC Part Five D.MANDELZWEIG MSC Eng ANALYSER

PREVIOUS articles in this series have detailed the design and construction of a modular logic analyser. The basic unit as described has TTL compatible inputs. This final article in the series presents a fifth option, allowing the analyser to be adapted for use with CMOS circuitry.

REQUIREMENTS

Let us consider the requirements. Although today most computer systems run from a 5V supply rail, many other circuits use CMOS technology running from higher voltage levels. Thus the first requirement is to be able to level shift the CMOS power rails and logic levels to those compatible with TTL. The next requirement is of sufficient drive capability of the circuit under test. A LSTTL input represents on the average a tenth of the drive capability of a TTL output. Therefore using the analyser on TTL circuitry will not affect the operation of the circuitry (very seldom do good circuit designers allow TTL outputs to drive the maximum number of inputs). On the other hand, however, a common limit to the drive capability of CMOS outputs (as far as TTL is concerned) is two LSTTL inputs. If one, or two LSTTL gates have already been used in the design being tested, another LSTTL load may affect the operation of the circuitry. So the load applied to the circuit by any test probe should be as small as possible, and in the design presented here, is the equivalent of a single CMOS input. A third consideration, closely connected to drive capability, is of test lead length. Long leads, necessary to reach into the circuit being tested, represent capacitive loads to the drive circuitry as the frequency of the signals increase. Circuits with low drive capability may not be able to drive these capacitive loads, and the switching from one true logic level to another may be prevented. To overcome this problem, the input circuitry must be as close as possible to the test points of the circuit being tested.

SOLUTION

There are a few possibilities for the required solution. One would be to use comparators as input buffers, but many i.c.s. would have to be used making the p.c.b. large and cumbersome. A better way is to use i.c.s specifically designed for the task. Two such i.c.s are available. The first is the RCA CD40116 high speed 8-bit bi-directional CMOS/TTL convertor, and is ideal for the application. Propagation delays through the i.c. are typically 15ns-of the order of those in the analyser itself. There are two disadvantages to this i.c. A minor one is that the CMOS side maximum level is 12Vsome CMOS circuits work on 15V. The other one, somewhat larger, is the availability of the i.c., and especially its price, which does not make it (at present!) economical to use. The second i.c., a RCA CD40109B, is more readily available and is much cheaper. This device was designed to be a low-tohigh voltage converter, but can be used in the opposite mode, as is done in the circuit to be described. There has to be a trade-off in performance to approximately 1MHz. If higher speeds are to be encountered (fast CMOS i.c.s are becoming available today) then it is recommended that a design using the first i.c. be considered. Although the i.c. is not pin-for-pin compatible with the one being used here, the three CD40109Bs can be replaced with two CD40116Bs with suitable modification to the p.c.b. layout. Remember, though, that the analysis maximum input frequency is 5MHz, and it is possible to test circuitry at lower speeds, as discussed in previous articles. Another problem arising out of slow propagation delays is that of data skew. (See part 1.) To avoid this problem, it is important that all the signals being observed (and being used for control, such as the clock modifiers etc) must be connected through the translator. This will ensure equal propagation delays, and all signals will arrive at the analyser simultaneously.

CIRCUIT DESCRIPTION AND CONSTRUCTION

Fig. 5.1 shows the logic diagram of a single translator. There are four translators per i.c., and using three i.c.s allows twelve lines to be translated. There are thirteen input lines to the analyser. It was decided that a fourth i.c. was not necessary for translating only one line, and the CQ3 input is therefore not used in this option. Referring to Fig. 5.2, the unused CQ3 input is held high by R513. (When using the analyser, the CO3 input switch should be in the 'don't care' position.) In Fig. 5.2, all the Vcc inputs and the enable inputs are commoned, and taken to point A. All the translator inputs are also held 'high' to this common line via resistors R501-R512. These resistors present minimal loading, and prevent open inputs from floating (and possible damage). Linking A to B makes the option compatible with circuitry using 12V supplies. Linking A to C connects the option to the +Vin line, which must be connected to the supply line of the circuitry being tested. Input voltage range is +5V to +15V. A choice can be made at the construction stage as to which link to use. Alternatively, a small switch can be mounted on the case allowing easy change-over. VDD, the TTL level side of the i.c.s, is supplied by the 5V supply in the analyser. The supply line is decoupled by C501.

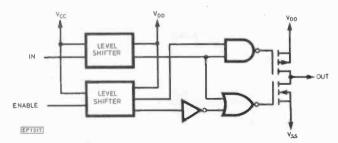
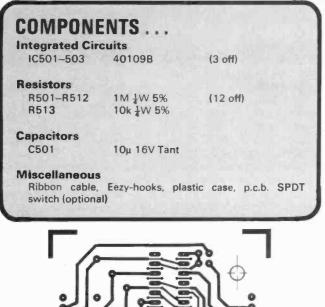


Fig. 5.1. Logic diagram of a single translator in the CD40109B



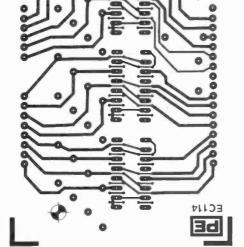


Fig. 5.3. Track side p.c.b. layout of CMOS input option board (actual size)

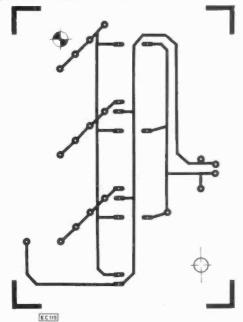
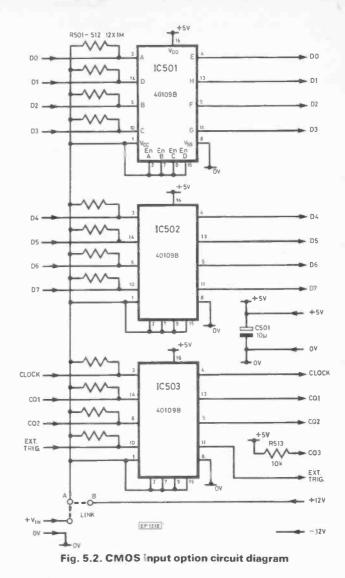


Fig. 5.4. Component-side p.c.b. layout of CMOS input option board (actual size)



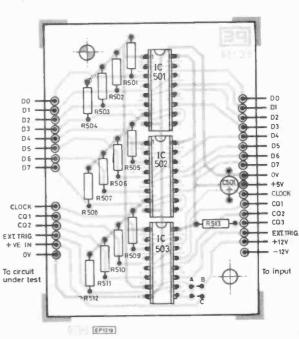


Fig. 5.5. Component layout

Construction of the unit is straightforward. Use soldercon i.c. socket strips for the i.c.s. Handle the i.c.s with care as they are CMOS devices. All necessary through-hole connections are made with component leads soldered to pads on both sides of the board. Extra pins are not necessary.

Link A to B or C, or if a switch is to be used, connect some lead wire to the p.c.b. 16-way ribbon cable is used to connect the p.c.b. to the analyser. This cable can be up to 0.5m long, and is connected to the analyser using the same method as the probe described in Part 2. The order of the connections from the p.c.b. match the order of the inputs to the analyser, so connections are made one-to-one. Part 2 shows these connection details. Note that the -12V supply is not used, and remember that CQ3 is not used. A piece of 14-way ribbon cable, no longer than 100mm, is used between the p.c.b. and the circuit under test. Coloured Eezyhooks are again used as convenient probes on the circuit under test side. The p.c.b. can be mounted in a small plastic case, and the optional switch installed. The nearest sized, commercially available case is the Vero 202-21029J. This may be a little long, though. The author used a suitable plastic case found in the junk-box. Slots are cut on each side of the box to allow cable entry and exit.

TESTING

A circuit similar to that shown in Part 3 can be used for testing. Replace the 74LS90 with a 74C90, and use, say, 12V as a supply voltage. The clock input used must also be a signal switching from 0V to +12V. Use the +Vin line connected to the test circuit supply. (If A was linked to B, then use a supply voltage of 12V, but the +Vin line need not be connected.) A similar display to that seen during the original testing (Part 3) should be observed.

This concludes the series. It is hoped that those who have built the project have found the instrument useful, and those who have just followed the series have learnt something of interest. Finally, the author would like to thank *P. Duggan* for his assistance in checking the analyser timing diagrams.

NOTE: A hex dump of the contents of the IC10 and IC11 of the analyser's display board may be obtained from PE (Poole office) by sending a SAE.

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TANGERINE Micron Keyboard with numeric pad £15. Also Hex keypad £4. Phone 01-732 4884 evenings. L. Wade, 16 Montpelier Road, London SE15 2HF.

PRACTICAL Electronics, Elector, Wireless World 1975 onwards for sale. Offers please (enc s.a.e.). D. Lawrence, 83 Main Road, Shavington, Crewe CW2 5DU.

UK101 Assembler/Editor and four graphic games, two original cassettes. Never used. Cost £14.00. Now £6.00 incl. Mr. P. Niem, 54 Park House. 314 Seven Sisters Road. London N4.

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MICROPROMPT

Appearing every month, Micro-Bus now presents ideas, applications and programs for the most popular microcomputers and all micro-related projects so far published in PE. Ideas must be original, and payment will be made for any contribution featured.

ATARI VARIABLES

OWNERS of Atari 400/800 machines may be interested in this program, sent in by *C. Leech* of Leicester. It lists the names of all scalar and string variables referenced by BASIC, along with their current values. Once loaded, the BASIC program can be deleted and the m/c program called by an X = USR(ADR(VARILIST\$)) command.

- 10 DIM VARILIST\$(283)
- 20 FOR I=1 TO 283
- 30 READ Z
- 40 VARILIST\$(I)=CHR\$(Z)
- 50 NEXTI
- 60 DATA 104,165,130,133,205,165,131,133, 206,169,255,133
- 70 DATA 209,165,206,197,133,208,7,165,205, 197,132,48
- 80 DATA 1,96,230,209.160,255,200,177,205, 16,251,132
- 90 DATA 224,230,224,201,168,208,2,240,60, 41,127,145
- 100 DATA 205,152,72,162,0,169,11,141,66,3, 165,205
- 110 DATA 141,68,3,165,206.141,69,3,165,224, 141,72
- 120 DATA 3,169,0,141,73,3,32.86,228,169,0, 141
- 130 DATA 72,3,169,61,32,86,228,104,168,177, 205,9
- 140 DATA 128,145,205,56,176,6,56,176,164,56, 176,43
- 150 DATA 165,209,133,203,169,0,133,204,6, 203,38,204
- 160 DATA 6,203,38,204,6,203,38,204,24,165, 203,101
- 170 DATA 134,133,207,165,204,101,135,133, 208,160,0,177
- 180 DATA 207,201,0,240,8,208,74,56,176,115, 56,176
 190 DATA 201,162,0,160,2,177,207,149,212,
- 232,200,224 200 DATA 6,208,246,32,230,216,160,255,200,
- 177,243,16 210 DATA 251,41,127,145,243,200,169,155,145,
- 243,162,0 20 DATA 169,9,141.66,3,165,243,141.68,3,165,
- 244 230 DATA 141,69,3,169,40,141,72,3,169,0,141,
- 73 240 DATA 3,32,86,228,16,47,56,176,185,201, 129,208
- 250 DATA 40,160,2,24,177,207,101,140,141,68, 3,200
- 260 DATA 177.207,101,141,141,69,3,162,0,169, 9,141
- 270 DATA 66,3,200,177,207,141,72,3,200,177, 207,141
- 280 DATA 73,3.32,86,228,24,165,205,101.224,

133,205 290 DATA 144,2,230,206,56,176,195

TRANSMISSION LINE CALCULATIONS

Sir—This program, for UK101, is derived from the standard transmission line equations, but assumes a loss-free line, sufficiently accurate for the lengths used by radio amateurs. It is more convenient to use than the Smith chart, and numerous cutand-dry results can be obtained rapidly.

> R. J. Newman, Chesham.

- 30 PRINT"THE MEASURED IMPEDANCE AT LINE INPUT IS R+/-jX"
- 40 PRINT"OHMS. THE LOAD ON THE LINE IS A+/-jB OHMS"
- 50 PRINT" ENTER A OR B FOR REOUIRED TRANSFORM"
- 60 PRINT"A-LINE IMPEDANCE FOR GIVEN LOAD IMPEDANCE"
- 70 PRINT"B-LINE IMPEDANCE TO GIVE REQD. INPUT IMPEDANCE"
- 80 INPUT AS
- 90 IF A\$="A"THEN310
- 100 IF AS="B"THEN110
- 110 INPUT"Give line characteristic impedance";ZØ
- 120 INPUT"Give line length (wavelengths)";LL
- 130 L=LL+2+3.14159
- 150 INPUT"Give desired R";RR:R= RR/ZØ
- 160 INPUT"Give desired jX";XX:X= XX/ZØ
- 170 $D1=((1+X^{*}TAN(L)) \triangleq 2+(R^{*}TAN(L)) \triangleq 2)$
- 180 A=ZØ*R*(1+TAN(L)*TAN(L)) /D1
- 190 B=ZØ*((X-TAN(L))*(1+X* TAN(L))+R*R*TAN(L))/D1
- 200 $P = ((B \triangleq 2) + (A \triangleq 2) (Z \emptyset \triangleq 2))/((A + Z \emptyset) \triangleq 2 + B \triangleq 2)$

- 210 $Q=2*B*Z\emptyset/((A+Z\emptyset) \triangleq 2+B*B)$
- $220 \quad MRC = SQR(P^*P + Q^*Q)$
- 230 SWR=(1+ABS(MRC))/(1-ABS
- (MRC))
- 240 SWR=(INT(SWR*100))/100
- 250 A=(INT(A*100))/100:B=(INT (B*100))/100
- 260 IFB<ØTHEN290
- 270 PRINT"A+/-jB=";A;"+j";B; "SWR=";SWR
- 280 GOTO140
- 290 PRINT"A+/-jB=";A;"-j";B; "SWR=";SWR
- 300 GOTO140
- 310 INPUT"Give line characteristic impedance";ZØ
- 320 INPUT"Give line length in wavelengths";L
- 330 L=L*2*3.14159
- 340 **PRINT"**......"
- 350 INPUT"If the termination is A+jB, enter A";A
- 360 INPUT"Enter the series reactive component jB";B
- 370 $D=((Z \emptyset B^*TAN(L)) \Leftrightarrow 2 + (A^*TAN(L)) \Leftrightarrow 2)/Z \emptyset$
- 390 X=((Z∅ 2 A 2 B 2)* TAN(L)+B*ZØ-B*ZØ*TAN(L) *TAN(L)*TAN(L)/D
- 400 R=(INT(R*100))/100:X=(INT (X*100))/100
- 410 REM: P=Real part of reflection coeff. Q=j part
- 420 $P = ((B \Leftrightarrow 2) + (A \Leftrightarrow 2) (Z \emptyset \Leftrightarrow 2))/((A + Z \emptyset) \Leftrightarrow 2 + B \Leftrightarrow 2)$
- 430 $Q=2^{B^{Z}}/((A+Z)) \triangleq 2+B \triangleq 2)$
- 440 MRC=SQR(P*P+Q*Q)
- 450 SWR=(1+ABS(MRC))/(1-ABS (MRC))
- 460 SWR=(INT(SWR*100))/100
- 470 IFX/D<ØTHEN500
- 480 PRINT"R+/-jX=";R;"+j";X; "SWR=";SWR
- 490 GOTO340
- 500 PRINT"R+/-jX=";R;"-j";X; "SWR=";SWR
- 510 GOTO340



T HE voltage regulator i.c. has been with us for many years, and is an almost indispensable part of the design and construction of simple, compact, and low cost regulated power supplies. Although occasional variants do appear, most regulator i.c.s are the simple three terminal type: 7805, 7812, 7815, LM340, and similar series. This situation is about to change, however, because a new American company, Linear Technology, has produced a brand new and innovative design of 5V regulator: the LT 1005CT.

The i.c. contains two voltage regulators, one providing a 'main' output with supply currents of up to 1A, and the other providing an 'auxiliary' output for lighter loads of up to 50mA. Furthermore, an 'enable' control is provided which allows the main output to be turned on and off by suitable logic or voltage control. This enable facility has no effect on the auxiliary output which remains at +5Virrespective of the state of the main output.

Both outputs are short circuit protected, and the main output has thermal shutdown protection. Again, any such shutdown or fault condition on the main output has no effect on the auxiliary output.

POTENTIAL APPLICATIONS

For such an apparently simple device, the LT 1005CT has a vast array of potential applications in power control and voltage regulation. Because of its unique enable circuitry it can be used as a very effective control element for motors, lamps, and many other types of d.c. load. Several i.c.s can be connected together with suitable timing circuitry to ensure the correct sequencing of power supplies in systems which require the power to be turned on in a certain specific order. Finally, power to a system can be split, to provide some areas with power only occasionally, and others with power continuously. This would enable the bulk of a microprocessor system to be shut down while not in use, while certain areas of RAM could be kept powered up for the storage of important data, for example. Alternatively, any combination of ROM and RAM could be powered up or down, and data transferred between them as required.

The pin out and specifications of the LT 1005CT are shown in Fig. 1. The specifications show quite reasonable performance for both regulators; note that the 'aux' regulator can operate with input voltages down to 6.8V, half a volt less than the main regulator, making it even more suitable for battery use. Also note that the enable input to the i.c. (pin 2) can have up to 20V applied to it; even if the 'input voltage' is lower than this, no damage will be done.

USING IT

Fig. 2 shows a functional diagram of the i.c. As with most i.c. voltage regulators, it is good practice to bypass both the input and the outputs with capacitors, C1, C2, and C3. These should be as close to the i.c. as possible. C1 should be a 1 μ tantalum bead, and the other two should be 220n or more; 1 μ tantalums will be satisfactory here, too. The use of the enable input is very simple; a logic 1 level (high) turns the main output on, and a logic 0

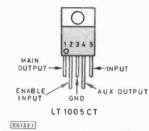


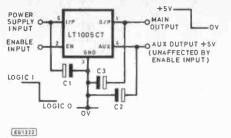
Fig. 1. Pin out with specification below

CI	haracteristic	Notes	Min	Max	Units
Max supply voltage		All specs measured over full range of 7.5–20V d.c.		20	V
Quie	escent current			5	mA
Temperature range		Measured at junction	0	125	°C
	Output voltage	$\begin{cases} Enabled (lout = 5mA-1A) \\ Disabled (Load = 1k) \end{cases}$	4.8	5·2 0·3	v v
1	Load regulation	lout = 5mA - 1A		25	mV
	Line regulation			2	mV/V
	Ripple rejection	Vin = 8V, Freq = 50–500Hz	54		dB
Main output				0.02	%/W
	Max output current		1.0		A
	Short circuit current			2.2	A
	Min input voltage	lout = 5mA-1A	7.3		V
	Output voltage	lout = 0-35mA	4.8	5.2	v
	Load regulation	lout = 0-35mA		25	mV
	Line regulation			1.5] mV/V
Aux	Ripple rejection	Vin = 8V, Freq = 50-500Hz	60		dB
output	Max output current		50		mA
	Short circuit current			150	mA
	Min input voltage	lout = 30mA	6.8		V
Enable	Input voltage	For logic 1 (high) level For logic 0 (low) level	2	1	v v
input (pin 2)	Max input voltage			20	V
	Input current	For input of 0V For input of 2·4–20V		-150 1	μΑ μΑ

level (low) turns it off. It should be connected directly to pin 4 or 5 when this facility is not being used. The input is compatible with both TTL and CMOS, although a pull up resistor will be needed if open collector TTL is used.

Fig. 3 shows a simple example of a circuit using the enable facility for optimising battery usage. A potential divider formed by VR1 and R2 controls the voltage fed to the enable pin. Once the battery discharges to below a certain voltage the main output regulator is disabled. Hence, most of the circuitry is turned off and the power consumption falls. The aux output can then feed warning circuitry, critical areas of the main circuit, etc. R3 adds hysteresis to the system, without which the increase in battery voltage when the load is suddenly turned off could cause the regulator to oscillate. The aux output will continue to supply power, of course, until the battery voltage drops to approximately 6.8V.

Many applications of the LT 1005CT will fall broadly into the category shown in Fig. 4. A small block of circuitry is powered from the aux output, and this circuitry is used to control the enable input to the i.c., amongst other





things. This is exactly the arrangement used in this month's applications circuit. (Note that bypass capacitors C1, C2, and C3 have been omitted for clarity in Figs. 3 and 4; in practice, they are still necessary).

APPLICATIONS CIRCUIT

Fig. 5 shows the circuit diagram of a power supply regulator with automatic shut-down and start-up facilities. The system responds to a short on its main output by shutting down that output, eliminating the large short circuit currents which would otherwise flow for the duration of the short. The circuit tries to turn the regulator on again for brief periods every few seconds. If it fails after seven attempts it then stops trying, and sounds a warning tone. All the circuitry is powered from the aux supply, making it independent of any shorts or other faults in the main system.

When a short occurs, IC4d pin 11 goes to logic 1, which causes IC5a pin 3 to go to logic 0. This then causes IC5b pin 4 to go to logic 1, starting IC2 oscillating at a very low frequency. When IC5a pin 3 goes to logic 0, it

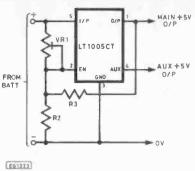


Fig. 3. 'Battery saver' circuit

also turns off the enable input to IC1, via IC5c and IC5d. The oscillator, IC2, gives out a very short pulse every three seconds, the wide mark/space ratio being achieved by the use of D2 in an otherwise standard '555' circuit. The output pulses from IC2 enable IC1 (via IC5c and IC5d) for short periods, and also clock the decade counter IC3. If the short circuit across the main output of IC1 is removed, the next pulse from IC2 will cause the main output of IC1 to go to +5V. This will produce a pulse through C4 which in turn will reset IC3 via IC4c. IC2 will be disabled via IC5b, preventing further pulses from being generated. If, however, the short remains, IC3 will count up until pin 11 goes to logic 1, at which point it will turn on the audio frequency oscillator formed by IC4b, which drives a small piezo ceramic sounder in a simple bridge configuration with IC4a. This warning tone is turned off by removing the short circuit then connecting the 'reset' pins together with a

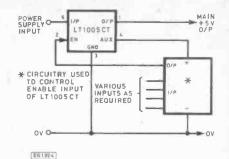
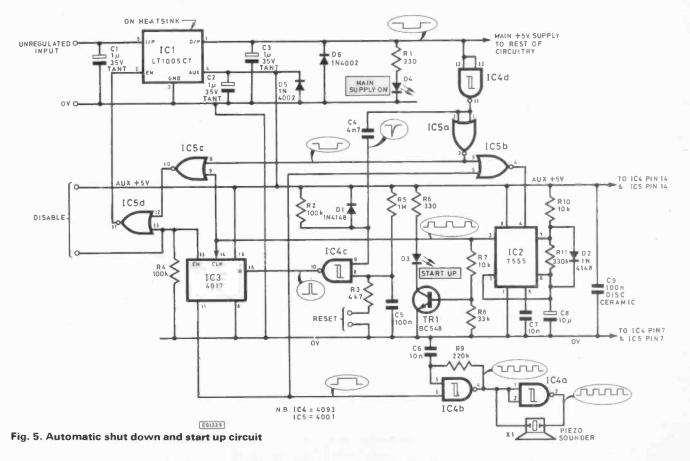


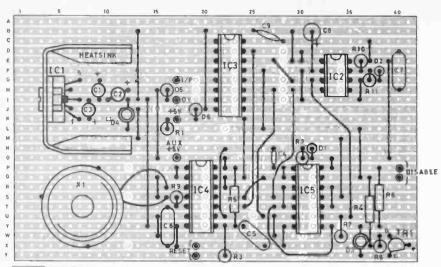
Fig. 4. Using both the 'Enable' and 'Aux' facilities

suitable switch. The main output can be turned off permanently by connecting the two 'disable' pins together, again via a suitable switch. D3 is lit by TRI whenever a pulse is put out by IC2, showing that the circuit is actually 'trying' to turn on the main supply again.

ASSEMBLY

The Veroboard layout for the circuit is shown in Fig. 6, and is largely selfexplanatory. Allow plenty of space for a suitable heatsink for IC I, as large currents can be passed during perfectly normal operation of the main circuitry. Many small piezo ceramic sounders will suffice for producing the warning tone, although their mounting arrangements may vary; do ensure, though, that the sounder chosen only requires a current of a few milliamps at the most; i.e. it is compatible





EG 1326 A

Fig. 6. Board assembly



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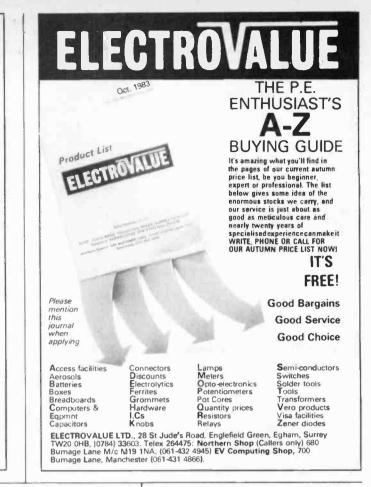
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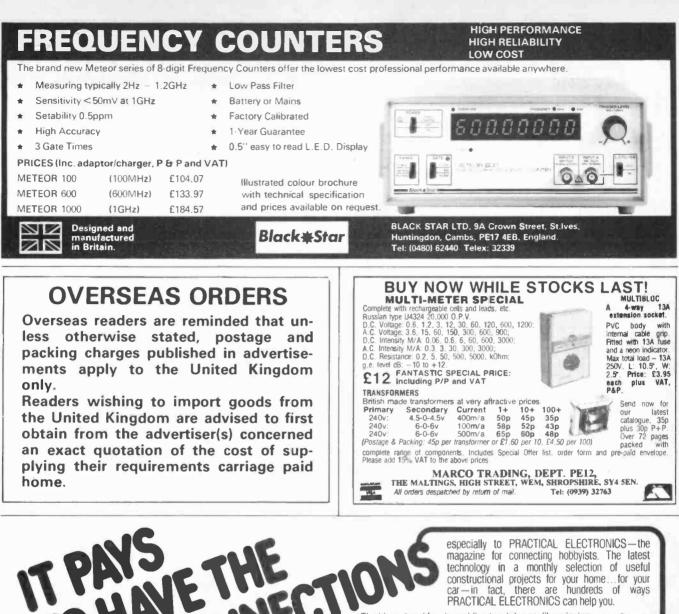
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TANT BEADS PCB 1 255 / 160 MATS 235 / 160 FERRIC 33 35 / 160 FERRIC 47 355 / 160 ChOm 4 - 165 doing 22 355 / 160 ChOm 5 - 165 doing 22 355 / 160 ChOm 5 - 165 doing 33 35 / 160 ChOm 6 - 165 doing 22 355 / 160 ChOm 6 - 165 doing 35 / 160 ChOm 6 - 165 doing 35 / 160 ChOm 6 - 165 doing 147 / 160 / 160 ChOm 6 - 165 doing 15 / 160 / 200 ChOm 6 - 165 doing 16 / 160 / 200 Than 5 / 160 doing 15 / 160 / 200 D Lip assister particular for the 15 / 50 / 200 15 / 50 / 200 D Lip assister particular for the 15 / 50 / 200	Solider 24/9451 Male 1.60 Permaln 2.09 PCB Wirzp 7/8469 Male 1.60 PCB Wirzp 7/8469 Male 1.60 PCB Wirzp 7/8469 Male 1.60 Permain 2.09 Phono Plugs 2/96/20 Bib Het Kin 15p Unre Skis 15p Onio Plugs 7/96/21 Dink Skis 15p Onio Plugs 7/96/21 Dink Skis 15p Oniet 3.0p Oniet 3.0p 2/96/12 2/96/12 Oniet 3.0p 2/96/12 2/96/12 Chiet 3.0p 2/96/12 2/96/12 2/96/12 2/96/130 75 ISTORS	Pape BC/213.A 11 pe BF/257 BC/213.A 11 pe BF/258 BF/258 BC/213.C 10 pe BF/258 BF/457 BC/213.A 10 pe BF/457 BF/457 BF BF/2131.A 10 pe BF/459 BF BF/2131.C 14 pe BF/459 BF BF/2131.C 14 pe BF/459 BF BF/214.1 10 pe BF/439 BF BF/214.1 10 pe BF/849 BF BF/214.1 10 pe BF/849 BF/214.1 BF BF/849 BF/849 BF BF/214.1 BF BF/849 BF BF/214.1 BF BF/849 BF BF/274.1 BF BF/849 BF	30p 11P1/7 84p 32p 11P130 93p 35p 11P132 93p 35p 11P132 93p 58p 11P133 93p 58p 11P137 93p 58p 11P137 13p 62p 11P140 114 86p 11P147 115 92p 11P147 115 22p 11P2455 70p 22p 11P3055 70p 22p 11S88A 62p 22p 11S88A 62p 22p 11V10KM 60p 22p 11V10KM 60p 21p 110K6AF 94p 100 11K6AF 10p 24p 12K108 10p	10220 C sse 1/3366 88p. 1122050/431 66p. M386 88p. 1122550/631 74p. L/381N60 170. 1122560/741 M191N60 170. 170. 1122610/741 116 L/723CL-81 340. 1162/2617/61. 120. L/723CL-81 340. 1162/2617/61. 120. L/723CL-81 140. 1162/2617/61. 120. L/723CL-81. 140. 1162/2617/61. 120. L/723CL-81. 140. 1162/2617/61. 120. L/723CL-81. 140. 1162/2617/61. 120. L/723CL-81. 140. 1162/2617/61. 120. L/732CH-95. 140. 1162/2617/61. 120. L/732CH-95. 160. 1162/2617/61. 120. L/732CH-95. 160. 170. 1162/2617/61. 120. L/732CH-95. 170. 170. 1162/2617/61. 120. L/732CH-95. 170. 170. 1162/2617/61. 120.	7.484 65p //4 7.485 60p //2 7.485 10p //2 7.480 168 //2 7.480 168 //2 7.480 168 //2 7.480 168 //2 7.480 169 //2 7.490 20p //2 7.493 25p //4 7.493 25p //4 7.495 35p //4 7.496 35p //4 7.4100 55p //4 7.4100 55p //4 7.4100 55p //4 7.4100 75p //2 7.4100 75p //2	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	280ACTC 2 60 280ADAH 5 50 280ADMA 5 70 280APH0 2 70 270258 3 39 V.REGS Positive 100mA 78L12A 290 78L12A 290 78L12A 290 78L24A 290 78L24A 390 78L51 390 78121 390 78151 390
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