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# Sinclair ZX Spectr 

# 16K or 48K RAM... full-size movingkey keyboard... colour and sound... high-resolution graphics... From only £125! 

First, there was the world-beating Sinclair ZX80. The first personal computer for under $£ 100$.

Then, the ZX81. With up to 16 K RAM available, and the ZX Printer. Giving more power and more flexibility. Together, they've sold over 500,000 so far, to make Sinclair world leaders in personal computing. And the ZX 81 remains the ideal low-cost introduction to computing.

Now there's the ZX Spectrum! With up to 48 K of RAM. A full-size moving-key keyboard. Vivid colour and sound. Highresolution graphics. And a low price that's unrivalled.

## Professional powerpersonal computer price!

The ZX Spectrum incorporates all the proven features of the ZX81. But its new 16K BASIC ROM dramatically increases your computing power.

You have access to a range of 8 colours for foreground, background and border, together with a sound generator and high-resolution graphics.

You have the facility to support separate data files.

You have a choice of storage capacities (governed by the amount of RAM). 16 K of RAM (which you can uprate later to 48 K of RAM) or a massive 48 K of RAM

Yet the price of the Spectrum 16K is an amazing $£ 125$ ! Even the popular 48 K version costs only $£ 175$ !

You may decide to begin with the 16 K version. If so, you can still return it later for an upgrade. The cost? Around $£ 60$.

## Ready to use today, easy to expand tomorrow

Your ZX Spectrum comes with a mains adaptor and all the necessary leads to connect to most cassette recorders and TVs (colour or black and white).

Employing Sinclair BASIC (now used in over 500,000 computers worldwide) the ZX Spectrum comes complete with two manuals which together represent a detailed course in BASIC programming. Whether you're a beginner or a competent programmer, you'll find them both of immense help. Depending on your computer experience, you'll quickly be moving into the colourful world of ZX Spectrum professional-level computing.

There's no need to stop there. The ZX Printer - available now - is fully compatible with the $Z \times$ Spectrum. And later this year there will be Microdrives for massive amounts of extra on-line storage, plus an RS232 / network interface board.


## Key features of the Sinclair ZX Spectrum

- Full colour-8 colours each for foreground, background and border, plus flashing and brightness-intensity control.
- Sound-BEEP command with variable pitch and duration.
- Massive RAM-16K or 48K
- Full-size moving-key keyboard - all keys at normal typewriter pitch, with repeat facility on each key.
- High-resolution-256 dots horizontally $\times 192$ vertically, each individually addressable for true highresolution graphics.
- ASCII character set - with upper- and lower-case characters.
- Teletext-compatible-user software can generate 40 characters per line or other settings.
- High speed LOAD \& SAVE-16K in 100 seconds via cassette, with VERIFY \& MERGE for programs and separate data files.
- Sinclair 16K extended BASICincorporating unique 'one-touch' keyword entry, syntax check, and report codes.


The Spectrum software library is growing every day. Subjects include games, education, and business/ household management. Flight Simulation...Chess...Planetoids. History ...Inventions ..VU-CALC ...VU-3D
.Club Record Controller...there is something foreveryone. And they all make full use of the Spectrum's colour, sound, and graphics capabilities. You'll receive a detailed catalogue with your Spectrum

## ZX Expansion Module

This module incorporates the three functions of Microdrive controller, local area network, and RS232 interface Connect it to your Spectrum and you can control up to eight Microdrives, communicate with other computers, and drive a wide range of printers.

The potential is enormous, and the module will be available in the early part of 1983 for around $£ 30$.

Sinclair Research Ltd, Stanhope Road, Camberley, Surrey GU15 3PS.
Tel: Camberley (0276) 685311.

## The ZX Printeravailable now

Designed exclusively for use with the Sinclair ZX range of computers, the printer offers ZX Spectrum owners the full ASCII character set-including lower-case characters and high-resolution graphics.

A special feature is COPY which prints out exactly what is on the whole TV screen without the need for further instructions. Printing speed is 50 characters per second, with 32 characters per line and 9 lines per vertical inch.

The ZXPrinter connects to the rear of your ZX Spectrum. A roll of paper ( 65 ft long and 4 in wide) is supplied, along with full instructions. Further supplies of paper are avallable in packs of five rolls.


## The ZX Microdrive coming soon

The new Microdrives, designed especially for the ZX Spectrum, are set to change the face of personal computing by providing mass on-line storage.

Each Microdrive can hold up to 100 K bytes using a single interchangeable storage medium

The transfer rate is 16 K bytes per second, with an average access time of 3.5 seconds. And you'll be able to connect up to 8 Microdrives to your Spectrum|via the ZX Expansion Module.

A remarkable breakthrough at a remarkable price. The Microdrives will be available in the early part of 1983 for around £ 50


## How to order your ZX Spectrum

BY PHONE-Access, Barclaycard or Trustcard holders can call 01-200 0200 for personal attention 24 hours a day, every day. BY FREEPOST-use the no-stamp needed coupon below. You can pay by cheque, postal order, Access,

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EITHER WAY-please allow up to 28 days for delivery. And there's a 14-day money-back option, of course. We want you to be satisfied beyond doubt - and we have no doubt that you will be.

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|  | Sinclair ZX Spectrum-48K RAM version | 101 | 175.00 |  |
|  | Sinclair ZX Printer | 27 | 59.95 |  |
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## PE-THE FUTURE

THE free Scratch Pad on the cover of this issue will be the last of a line, at least for the foreseeable future. Right from the first issue of PE (November 1964) it has been our policy to give readers a "free gift" roughly twice a year. For some time now we have been doubtful of the value of this idea. While we have always tried to ensure our free gifts are worthwhile-and from readers' response to some of them there can be no doubt of that-we have felt that improved value for money on a monthly basis might be a better policy, after all if you are a regular reader you buy the magazine for its content, not what you get free with it. So from now on we will be giving you more content every month instead of any free gifts. In short, the money we would normally spend each six months or so on gifts has been redirected to pay for about 8 extra editorial pages, per month from the May issue onwards. We hope you will like the idea.

We will be starting a new regular
feature in May-see next month's issue for details-and also expanding the range of our other articles. So instead of the free board, book, tool or component etc. every so often, which you may or may not want or use, you will get more info., more circuits and more reading each and every month.

## PE COVERS

Next month you will also notice an immediate change when you buy your issue or when it drops on your doormat. We are changing the style of our covers. In line with our future contents we are also putting more on the front cover, so if you are just browsing at the bookstall it should give you a better idea of just what is inside to interest you. We believe the new style looks more modern, exciting and colourful; your impressions would be interesting.

## FEEDBACK

As we have said before we are pleased to hear your criticisms, likes and dislikes as this feedback helps us to produce a magazine which is just
what you want. Feedback in the form of IUs and material for Microbus and Microprompt is always interesting and keeps the whole area of "hobby designs" alive.

In recent months the number of contributions to IU have tended to fall They are always welcome and we should have more room for them from May onwards. So keep the ideas coming; we do pay for each one published at the rate of $£ 40$ per magazine page-enough to buy some more components anyway!

In line with our policy we are continually striving to meet the needs of our readers and, with your help, will continue to produce what we believe is the best magazine of its type available anywhere.

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David Shortland ASSISTANT
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TECHNICAL EDITOR
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## Letters and Queries

We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in PE. All letters requiring a reply should be accompanied by a stamped, self addressed envelope, or addressed envelope and international reply coupons, and each letter should relate to one published project only.

Components and p.c.b.s are usually available from advertisers; where we anticipate difficulties a source will be suggested.

## Back Numbers

Copies of most of our recent issues are available from: Post Sales Department (Practical Electronics), IPC Magazines Lid., Lavington House, 25 Lavington Street, London SE1 OPF, at $£ 1$ each including inland/Overseas p\&p. Please siate month and year of issue required.

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Binders for PE are available from the same address as back numbers at $£ 4.60$ each
to UK or overseas addresses, including postage and packing, and VAT where appropriate. Orders should state the year and volume required.

## Sulbscriptions

Copies of PE are ávailable by post, inland or overseas, for $£ 13.00$ per 12 issues, from: Practical Electronics, Subscription Department, Oakfield House, Perrymount Road, Haywards Heath, West Sussex RH 16 3DH. Cheques and postal orders should be made payable to IPC Magazines Limited.

Items mentioned are available through normal retail outlets, unless otherwise specified. Prices correct at time of going to press.

# Computer literacy Advances 

It was on a wet Thursday back in January that the press responded to an invitation by the BBC to attend a preview and mini exhibition at the World Trade Centre, in London. The preview was of 1983's "The Computer Programme" Series Two, which by now is well under way. The mini exhibition featured the BBC microcomputer and its various peripheral and software options.

A number of exciting pipeline stage developments were in evidence. What was claimed to be the first public demonstration of telesoftware took place, during which a BBC micro was downloaded with a program from a CEEFAX page. The acquisition worked with simplicity and speed, a condensed format program being transferred. The BBC has committed itself to the transmission of reliable and worthwhile software, the broadcasting of which commences this spring. Acorn Computers are to manufacture the telesoftware adaptors for use with any teletext adapted TV set. At the preview, the idea of a telesoftware club was mooted.

Among the many impressive extensions to the Acorn computer which were demonstrated, was the BBC Buggy, manufactured by Economatics of Sheffield. The Buggy demonstrates the principles of intelligent machine control and robotics, and is delivered in kit form, requiring only a screwdriver to assemble it. This training tool is a small, skeletal unit capable of propelling itself around using two drive wheels, and which has on-the-spot turning capability. The Buggy's party tricks include following a black or white line, seeking out a light source in a maze, exploring miscellaneous objects whilst mapping them on the VDU, drawing its own lines, and a repertoire of bar code stunts, one of which involves composing music. The thirteen well thought out programs are completely modular, enabling the user to call upon them as if a subroutine library. Students following the National Extension College
computer course will also be able to use the BBC Buggy.

Econet, Acorn's LAN (Local Area Network) offers a cost effective link-up for the educational establishment, allowing communication by way of a four wire bus between, for example, the physics lab' computer and the school data base, and the mathematics class processor etc. Up to 254 stations may share expensive peripherals like printers and disc drives, thus creating every opportunity to save cash.

Among the BBC microcomputer interfaces are a digitizer pad, a teletext adaptor (CEEFAX and ORACLE), voice synthesiser (taken from the tones of newsreader Kenneth Kendal), disc storage system (up to 800 Kbytes ), and a second processor unit. This last addition doubles the processing speed by circumventing the need to interrupt calculations whilst outputting data. Data is transferred across "the Tube*". A 6502 or $Z 80$ second processor may be chosen, the latter allowing CP/M operation. At the exhibition, a BBC micro with second processor busied itself designing ULA layouts!

Readers may like to know of the Information Referral service which is run in association with the BBC Computer Literacy Project. This is intended to put viewers in touch with local sources of advice, and provide information about the project. A large S.A.E. should be addressed to: BBC Computer Literacy Project. Broadcasting Support Services, PO Box 7, London W3 6XJ.

## MATHEMATICS EXPRESSED?

In their first step into the educational field Mitsubishi Electric have developed the 'Sansu Meki-Meki', a basic maths teaching aid aimed at pre-schoolers and primary school children. The unit has an LCD display which shows the questions and the child's answers to them. The questions dealing with addition, subtraction, multiplication and division are shown in groups of ten. When the child

answers correctly a tiny steam locomotive toots and chugs down a track and a voice synthesiser telis the operator that the answer is correct. If the wrong answer is given the locomotive crashes and a wrong answer is announced. A test score is given at the end of each group of ten questions, a three minute speed drill can also be incorporated. The unit can also be programmed to concentrate on weak points, or similarly add more difficult problems. The company plan further similar products for teaching language, as well as for other subjects for upper grade students, the unit is supplied with batteries and a mains adapter, the price is expected to be under $£ 50$.

## SYSTEMA CHESS COMPUTERS

Following the launch of the CGI chess computer Systema have now introduced two additional models into their range both with extra features. The CGl unit had a built in sensory board and eight levels of difficulty for a retail price of under $£ 30$.

First of the new chess computers is model CG2 which has a full-sized sensory chess board and pieces. The CG2 has all the features of model CGI plús a "save' switch to "freeze' the game at any stage and retain the chess position in its memory until the player is ready to resume the challenge. Other features include keys for under promotion, taking back moves and to set-up or verify positions.

Also introduced is the CG3 travel chess
computer. It combines the compact design of the original CGI with the extra features of the full-size model CG2. The lid snaps into position to keep out dust and a special compartment is provided for the chess pieces. A soft carrying case is available as an optional extra.

The retail price of full-sized model CG2 will be around $£ 50$ and the CG3 travel model will

be under $£ 40$.
Other features of all three models include changeable levels during play, changing sides during play, en passant capatures, castling and pawn promotion.

Systema (UK) Ltd., 74/76 South Street, Reading, RGI 4LG. (0734 586429).
Government approval of British Rail's plans 10 invest $£ 21$ million in new ticket issuing machines will soon bring $B R$ 's ticket offices firmly into the microchip age. 117 million of the investment is for a new all purpose ticket issuing system, APTIS and the remainder, $£ 4$ million is for PORTIS a portable version of the main system for use by guards on pay trains. Subject to satisfactory evaluation of the prototypes the new machines should come into operation in mid-1984 and be fully established by the middle of 1986.

## MAAPMGE PBACS

# Disc Film! Square Eyes? 

Kodak's innovative disc camera has been described by the Consumers' Association (Which? report Nov. '82) as foolproof but unimpressive in its photographic picture quality. It was summarized as being ideal for the average person wanting straightforward trouble-free snap shots. Eight million disc cameras, however, were estimated to have been sold at the close of 1982 . Eight million people who prefer to have no knobs to twiddle before taking a photograph!

Kodak's eye is still firmly on the future of popular photography. In Cologne, last October, the company demonstrated a possible future option in the shape of a video display unit which allows disc film images to be reproduced on a television screen. The demonstrator scanned a 15 image disc negative, enlarging them and cropping them, and showing the results on a 21 inch TV screen.

A remote control unit allows quick sequencing through 15 disc images under personal control, and facilitates zooming in on a section of image, or recomposing that image for a better view of one of its aspects. Scientists at Kodak say that this system would enable consumers to order prints of their own home-culled pictures.

The heart of the prototype system is an extremely high resolution charge-coupled image sensor which converts and enhances the optical data ready for television display. A colour array of more than 350,000 elements produces a very detailed TV picture from each whole, or portion of the disc exposure. With a luminance signal bandwidth of greater than 3.5 MHz , the potential for picture quality exceeds that of most television receivers.

The video display system is exploratory, with no commitment by Kodak as yet, to manufacture and sell the equipment.

## kodak video display technology



The Kodak colour-corrected enhanced image system promises a kind of editing of photographs, in the home. Encoding of the film disc core would allow recomposed prints to be ordered by the consumer.

## JOYSTICKS

A new range of joysticks has been introduced by Midwich, made of high-quality injection moulded materials, these erganomic designs fit comfortably into the hand. Each joystick or pair of joysticks is fitted with the appropriate connector for the machine in question, the range is aimed at the $B B C$ micro, Dragon 32 and the Spectrum/ZX81. Since the $2 \times 81$ and Spectrum do not have a built-in $A$ to $D$ converter, Midwich have also designed a high-speed, 4 channel controller board, which plugs into the expansion slot. An edge connector is provided for RAM pack, disc drives, etc. Prices as follows: Z×81, Spectrum and Dragon 32 £15.98 per pair, and for the BBC micro 13.00 per pair, the controllers being E22.95 each. All prices include VAT. All units available from Midwich Computers, Rickinghall House, Hinderclay Road. Rickinghall, Suffolk IP22 1HH 10379 898751).


## HIS MASTER'S VOICE

The science of electronic speech synthesis is gaining ground rapidly, but speech recognition is hampered by a lack of precision in human articulation. We humans frequently experience difficulty in understanding one another, so how might an inflexible machine cope with the various dialects, or emotional undulations of an individual's yoice?

Marconi Space and Defence Systems have developed, with financial help from the DoI, a speech recogniser with a capacity of 240 words and/or phrases, costing $£ 10,000$. It is called the SR-128, and it uses dynamic programming to match voice input with pre-recorded voice commands, or templates as they are called.

The voice matching pattern for each user can be prepared in minutes by way of a learning routine in which the operator repeats his or her personal rendition of the control words, prompted by a 40 character plasma display. Data profiles of the user's oration are then stored on minicassette. When in use, the SR-128 recognises quite normal "connected" speech by dynamically pattern matching the content of its solid state memory. It is claimed that the number of active templates may be restricted by applying syntax rules. The system can recognise utterances of up to eight seconds duration, with a response time of 50 ms .

There are both military and civilian applications, many of which would be airborne. An SR-128 is installed at the Royal Aircraft Establishment, Bedford, for map display and waypoint identification. Such systems will not only free pilots from physical switch manipulation when using peripheral equipment, but may one day completely
outspan the flyer. The potential of machines like the SR-128 to ease the four-limbed juggling act of flying a helicopter is axiomatic.

Other applications include security, where response only to an authorised voice is necessary, be it for access to a restricted area, or ensuring that a vehicle is not illicitly commandeered.


A low-profile i.c. socket, which locks the chip into position and also provides an ejecting action when the chip has to be withdrawn, is now available from Aries Electronics. The EJECT-A-DIP socket can be in 14, 16 and 24 pin configurations.

The socket incorporates a pivoting arm at each end. In one position, these arms lock the device into the socket and the arms are available in three heights to cover the various standard thicknesses of i.c.s. When the arms are pushed outward to release the device they also provide a lever action which ejects the i.c. from the socket without damaging the legs.

The socket itself has gold or tin-plated, spring-tempered, phosphor-bronze bifurcated contacts, designed to take both round or flat pins.

Aries Electronics, Unit E, Metrostore House, Eastways Industrial Estate. Witham, Essex. (0376519318).


## Briefly...

The president of National Semiconductor is often quoted as having said that if the progress of microelectronics over the past two decades was applied to the motor industry, a Rolls Royce would cost \$2.75, do three million miles per gallon and deliver enough power to drive the QE2. By the look of things. Mr. Sporck's Rolls Royce will soon have to fly to keep that parallel up to c'ate, for the bioelectronic device is herein the laboratory at least!

In the United States, a molecule that can exist in two states, and therefore represent a binary bit, has been successfully synthesised. Mississippi University would seem to be a mere heartbeat away from the birth of the molecular diode. A memory capacity of 1 K using this technology would theoretically fit into a line only one micran long, it is reported. This breakthrough brings closer the day of implanted electronics, and its interface to living tissue.

Coals to Newcastle, fridges to the Eskimos, fair enough, but British electronic equipment to Japan? Well its true, and we have the Thandar Electronics company to thank for this encouraging reversal in the waves of electronics from the East. The equipment in this initial order, worth $£ 100,000$, was a quantity of logic analysers and were purchased by a
"major" Japanese instrument manufacturer. The model ordered, the TA 2080, was favoured because of its ability to offer a combination of performance, reliability, quality and most importantly price which was, to quote Thandar Chairman Mr Taylor, "unbeatable even in Japan". The company based in St Ives, Huntingdon, export over $50 \%$ of their output.

As if by magic, forecast figures are regularly conjured up which naturally find their way into the electronics press. Appearing in Electronics Times ( $2 / 9 \mathrm{Dec}$.), these figures are attributed to International Resources Development, whose study revealed the following: Sales of personal computers will rise from the current level of $\$ 1 \cdot 3 \mathrm{bn}$ a year to $\$ 3 \cdot 5 \mathrm{bn}$, and then plunge to $\$ 480 \mathrm{~m}$ by 1992 . Sales of the personal computer's rival, the "multifunction workstation" will rocket from zero to $\$ 650 \mathrm{~m}$ in 1984, to \$3.76bn in 1987, and on to \$14bn in 1992.

Evidently computers are not so prone to misting over as tall, dark strangers.

## POINTS <br> ARISING...

STYLOCHORD (Dec. '82)
The 5024 Top Octave Generator may be replaced directly with the M083 device available from Maplin Electronics (order code YY81C).

# Houndidnun... 

Please check dates before setting out, as we cannot guarantee the ac curacy of the information presented below. Note: some exhibitions may be trade only. If you are organising any electrical/electronics, radio or scientific event, big or small. we shall be glad to include it here. Address details to Mike Abbott.

Microsystems Feb 23-25. West Cntr. Hotel, Fulham, London. Z1 CAD North Mar. 1-3. Belle Vue Ex. Cntr., Manchester ZI Mailing Efficiency Mar. 1-3. Bloomsbury Cntr. Hotel, London. Z Local Networks Mar. 8-10. Royal Lancaster Hotel, London. O Component Fair (Pontefract \& District Amateur Radio Society) Mar. 13. Carlton Grange Comnunity Centre, Carlton, Pontefract. F2

## Brighton Electronics March. T

BEX Leeds Mar. 16-17. Dragonara Hotel. K
INSPEX Mar. 21-25. National Exhibition Cntr. Birmingham International. $\mathbf{Z} 1$
Sensors \& Systems Mar. 22-24. The Forum, Wythenshawe. T
Compec Wales Mar. 22-24. Cardiff University. Z1
ETM (Electronic Test/Measurement) Mar. 22-24. The Forun1, Wythenshawe, Manchester. T
Laboratory Manchester Mar. 23-24. New Century Hall, Corporation St. E
American Holography Mar.-June inc. Light Fantastic Gallery, Covent Garden, London. A8
London Computer Fair April 14-16. Central Hall, Westminster. B5 All Electronics Show Aprif 19-21. Barbican Cntr., London. E

Fibre Optics April 19-21. Porter Tun Rooms, The Brewery (!), Chiswell St., London ECI. E
International Materlals Handling April 19-26. Earls Court. II
International Packaging Exhibition April 25-29. NEC B/ham. I
HEVAC (Heating, Ventilation \& Air Cond.) Apr, 26-28. Barbican. I
Biotech May 4-6. Wembley. 0
Micro City May 10-12, Bristol Exhibition Complex. F3
The Business Computer Show May 10-12. Wembley. 0
Defence Componeats Expo May 10-12. Metropole, Brighton. I
Weish Amateur Radio, TV \& Electronics Rally May 22. Barry
Memorial Hall, S. Glam. C
Computers In The City (conf. \& ex.) May 24-26. Barbican. 0
Business Telecom May 24-26. Barbican. O
International Wood Processing May 24-27. Wembley Conf. Cntr. Z
Laboratory Edinburgh July 18-20, Appleton Tower, Univ. E

[^3]
## Space Watch...

## MISSILE FROM MARS

Headlines were generated when a meteorite was discovered recently in Antarctica because it is the opinion of most of those who have examined it, that it is from Mars. At the Johnson Space Centre considerable excitement was demonstrated when the piece of rock weighing about 17.51 b was displayed. The characteristics are different from any previous bodies of meteoritic origin. Naturally it now raises the problem that there must be more of this material on the surface of the Earth. This sample was found by a team funded by the National Science Foundation, the Smithsonian Institution and the National Aeronautical and Space Administration.

At the moment the main problem being studied is the manner of this piece of another planet arriving and impacting the Earth. There are some dissenters that it is in fact from Mars but Laurence E. Nyquist, who has identified it, has shown thai such an event is possible. He is head of the Planetary and Sciences Division at the Johnson Space Centre where his latest work was undertaken. His conclusions are based on the study of the Viking orbiter photographs. A number of the craters on Mars have a configuration which would indicate that they were made by the impact of bodies arriving at an oblique angle. It has been calculated that bodies of the size to have caused the craters observed, would need to have a velocity of about $9.98 \mathrm{~km} / \mathrm{sec}$. This would make it likely that parts ejected could have a velocity of the order of $7.88 \mathrm{~km} / \mathrm{sec}$. This is greater than the escape velocity from Mars which is $4.89 \mathrm{~km} / \mathrm{sec}$. Debris from the impact would be scattered across the Martian orbit. The dynamics of the relative orbits of Mars and the Earth would certainly make it possible for material to fall as meteorites. The real question is how often would that final condition arise. This can only be stated as "It could happen'. However there is another way of looking at the probability. The age of the Martian material at the crater is reasonably well established and is about 1,300 million years old. It is also reasonably likely that Mars was volcanically active then. The age of the meteorite is given as 180 million years or thereabouts. Since all other finds so far have on average been assessed at 4,500 million years the argument is reasonable. This could be a talking point for renewed proposals to bring back again the original plan for a man ned landing on Mars. However the immediate
active plans are to re-examine earlier finds made by a Japanese expedition. They found that many objects were lying about on the sur face of the ice and easily detected. An American expedition in 1976 found large numbers of meteorites on the surface easily identified. On the latest expedition there were two other bodies which may be Martian. They are still undergoing tests. One of them may well be from the Moon and its origin similar to the Martian bodies in the mode of transmis sion to Earth. In this case there are a number of samples of Moon rock collected by astronauts from the Apollo landings. The suspect sample found in Antarctica weighs 32 grammes and was frons the same Elephant Morraine area where the Martian samples were found. A section of the sample appears to be identical with the original Moon samples. The Martian sample on examination would show some indication of how long it might have been in space. This is regarded as not longer than 2 million years. In that case it must have come from a large body nearby. The sample is large enough to support years of study. There are a number of areas of glass like appearance which may have been part of the original material. One crucial task will be to penetrate these areas for they may have trapped in them the atmosphere of the time. This will add much to our knowledge of the early conditions on the Planet.

## SPACE MOTION SICKNESS

Much has been made of the small incidence of space sickness particularly on Shuttle Mission 5 . It seems that there is always someone to exaggerate small incidents for which they have little evidence. The practice of shock headlines is increasing for the purpose of creating adverse opinions and boosting sales in certain parts of the media. It labels the writers who follow this policy as the backward looking cranks akin to those 'experts' who earlier in the century were forever prattling about the inability of the humans to withstand speeds of 20 miles per hour. A little thought about the effects of statements that are entirely without attested foundation, have repercussions on the families and serve to spread needless alarm. As a first example the fact that the Russian cosmonauts have set a new record for time spent in space should make people hesitate about a journey of a few days involved in this so-called motion sickness.

A number of critical issues have been raised which have repercussions both in the personal lives of the astronauts and in the various legal aspects. There arise the questions of confidentiality between doctor and patient. There is the psychological effect on the individual astronaut who may feel that continued work in the field may be jeopardised, the legal difficulties due to various existing codes of practice and the Federal Privacy Act.

The entire situation has been built up into unnecessary proportions. There is a very important point at issue here for it so happens that there has been an increasing number of the same symptoms in the general populations who live in cities. There are a number of things which could assist the explanation of this trend. Indeed a great deal is going on in the areas of the changing eating habits of the communities. So much indeed that within a
very short time it will be found that many projects will be set up and grants applied for the proliferation of useless knowledge. Perhaps a little exposure of some of the so-called research in the area of man and his environment would sort the valid from the invalid. As the Year 2000 approaches perhaps it will be possible to get feet set on a useful path which deals with the true reality.

## NEW MINERAL FINDINGS

Some of the findings using the instruments of the second shuttle mission have now been published. It was possible to detect other clay minerals where in the previous observations from space only Limonite was readily identified. Using the multispectral infrared radiometer in an area between Kharga and Aswan in Egypt, it was possible to identify exactly kaolinite a hydrous aluminium silicate clay mineral and another of the same group, montmorilionite. These are important finds because they are useful guides to the finding of ore deposits of iron. This new facility means that both topographical data and minerological data can be acquired simultaneously over wide areas. A first saving is then to halve the costs and time compared with previous geological surveys.

## JOINT VENUS MISSION

Cooperation between Russia and France has been working very well and now the most ambitious project is being discussed regarding what might be called a massive assault on the problem planet Venus. The target date for this mission is 1989. A French statement indicates that there will be probes, landers and balloons used in this attempt. It is not deemed practical to land a moving vehicle at this time, mainly because more certain knowledge of the terrain is needed.

Leading up to the 1989 mission will be two others. The first will involve the Soviet Vega Venus/Halley Comet mission. Originally the French were to supply two large balloons with meteorological instruments which would be released into the Venusian atmosphere. Because of the modifications for the Halley Comet part of the mission the large balloons were later deleted. In place of them the Sovieis are preparing two small balloons and the French are designing suitable meteorological instruments for attachment to the balloons.

A new project has been agreed and this will involve the launching of polar and equatorial satellites for the study of the Earth's atmosphere and ionosphere. This mission will be called Interball.

Initial studies have begun on another project called Satellite d'Astonomie Gamma, (SAGA) to study gamma ray sources. This was a mission offered to France by Russia wher for financial reasons they had to abandon it earlier in the year. The instruments already developed can be incorporated in the new satellite. It would seem that the Soviets are anxious to push this project, perhaps to beat the American mission for a Gamma ray observatory. The final decision on this project will be made at the next space reunion between the French and the Russians this year.

# + <br> <br> WARNING <br> <br> WARNING \&HCHIS REMNDER <br> <br> P.J.McFARLANE 

 <br> <br> P.J.McFARLANE}


#### Abstract

The brow of a hill; the dip in a country lane-each can cause surprise local freezing. With no change in lustre, a road can turn from being just plain wet to iced over. Building this device is a worthwhile project, for it will discreetly' arm the driver with a piece of information which could save his life.


MANY motorists have tales of skidding on so called "black ice". most of these, fortunately, without unhappy endings. It is possible to drive perfectly safely on ice, providing of course, that you know you are doing so. Cocooned in a car with the heater going, a driver can often fail to realise just how cold it is outside, and get caught by surprise when he tries to go in one direction and the car goes off in another.

The PE Ice and Lights Alarm can be put together for a few pounds, and as well as providing a visual and audible warning that the temperature outside is freezing, it also provides an audible warning that the car lights have been left on. The device can save you from both a flat battery and a flat car!

## OPERATION

The unit operates as follows: If the ignition is switched on whilst the temperature outside is freezing, or the car is driven into a region where the temperature is freezing, then the unit will emit a bleep for about a second. A l.e.d. illuminates all the time the ignition is on and the outside temperature is freezing.

Also, if the ignition is switched off and the car lights are still on, the unit will emit a pulsed bleep for about two seconds. This is long enough to remind that the lights are still on, but not so long as to be annoying if this is intentional.

## CIRCUIT

The circuit diagram is shown in full in Fig. 1. The temperature sensor is the base-emitter junction of a BC182L transistor. A silicon p-n junction has a temperature coefficient of about 2.2 mV per degree centigrade.

A 78LO5 voltage regulator i.c. supplies a stable reference voltage to the base of the sensor transistor, TR1, via a potential divider chain. When the sensor temperature is higher than zero, the threshold voltage of the transistor is low enough for the voltage on its base to turn it on. This removes the current from the base of TR2, so this is off, and so is TR3.

Should the temperature of the sensor drop, the baseemitter threshold voltage will rise, and there will not be enough voltage at the base to turn the transistor on. When this transistor is off, TR2 is turned on via R1. This turns on TR3 via R6, and hence the l.e.d. Resistor R5, and D 1 provide a small amount of hysteresis, by reducing the base voltage of TR 1 .

When the car lights are off, the l.e.d. current passes through both R10, and R9 through the lights, and the I.e.d.
glows brightly. When the lights are on, the end of R9 is at +12 V . hence the l.e.d. is dimmed, to prevent dazzle at night. Diode D2 prevents the l.e.d. from being excessively reverse biased when the lights are on.

Resistor R13, C4, IC1a and IC2a form a positive edge-topulse converter. Pin 11 of IC2 has a positive going pulse of about one second duration following a 0 to 1 transition on pin 1 of IC1. This circuit is used in preference to the usual series capacitor one, as the latter puts a reverse voltage on the capacitor.

Fig. 2 shows the essence of the circuit, and Fig. 3 the associated waveforms. The input to the gate can be considered to be a logic ' 0 ' if the input is below the threshold voltage (approximately half the supply voltage). The output of a NOR gate is low if either of its inputs is high.

When this pulse output is high, this causes the output of IC2c to be low. This enables the oscillator IC2d and IC1d. IC1e and IC1f provide a bridge output drive to the ceramic sounder $\times 1$, giving 24 volts peak to peak signal here, producing a very adequate sound.

The remainder of the circuit is concerned with the lights left on alarm. D3 and D4 are included so that the circuit is powered from either the lighting, or the ignition circuit

The circuit has another edge-to-pulse converter, this time a falling edge is converted to a negative going pulse of about 2 seconds duration at the junction of R15 and D5. These two components give the equivalent of an OR function. The pulse occurs when the ignition input becomes low. If the lights are still on, then there is power to operate the rest of the circuit.

The pulse enables the oscillator IC2b and IC1c which operates at about 10 Hz , and this gates the output oscillator on and off via IC2c.



E61030


## CONSTRUCTION

Fig. 4 shows the p.c. layout for the P.E. Ice Alarm, and Fig. 5 the component locations. Leave the l.e.d. wires long if it is to be mounted in the box.
The sensor transistor, TR1, is fitted at the end of about two metres of wire, as this obviously has to be outside the car. Cut the transistor legs to about 6 mm in length. Strip about 3 mm from the connecting wire insulation and slide about 10 mm of 1 mm bore rubber sleeving over the wires. Carefully solder the black wire to the emitter of the transistor and pull the sleeving over the join. Repeat with the green wire for the base and the brown wire for the collector. Fig. 6 shows the transistor base connections.

Fig. 3. Waveforms of Fig. 2

Fig. 2. Edge-to-pulse converter


Fig. 4. Printed circuit layout (actual size)


## 656057

Twist the three transistor wires together and thread them into 2 metres of 5 mm bore PVC sleeving. Glue the transistor into the sleeving with the end of the transistor level with the end of the sleeving, and make sure that the end is water tight.


Fig. 5. Component layout

## COMPONENTS...

| Integrated Circuits |  |
| :---: | :---: |
| IC1 | 4069 |
| IC2 | 4001 |
| IC3 | 78 LO5 |


| Transistors \& Diodes |  |
| :--- | :--- |
| TR1, TR2 | BC182L (2 off) |
| TR3 | BC212L |
| D1-D5 | 1N914 (5 off) |
| D6 | $3 m m$ Red l.e.d. |

Capacitors
C1, C2, C6 C3
C4, C5
C7

100 n 25 V . Ceramic disc (3 off) $10 \mu 16 \mathrm{~V}$. Electrolytic
$2 \mu 263 \mathrm{~V}$. Electrolytic ( 2 off )
1n 50V. Ceramic disc

Resistors
R1
R2, R8, R9, R10 1 k (4 off)

| R3 | 15 k |
| :--- | :--- |
| R4, R7 | 2k2 (2 off) |
| R5, R15, R19 | $120 \mathrm{k}(3 \mathrm{off})$ |
| R6, R12 | $10 \mathrm{k}(2 \mathrm{off})$ |
| R11 | 47 |
| R13, R17, R1B | $470 \mathrm{k}(3 \mathrm{off})$ |
| R14, R16 | IM (2 off) |
| All resistors +W | $5 \%$ carbon film |

## Potentiometers <br> VR1 500 preset

Verobox type 21024
Wire
Sleeving
Cable ties
P.c.b.

Adhesive pads
X1 PB2720 Ceramic sounder
A complete kit of parts is available from: PIMAC Systems Lid., 20 Bloomfield. Road, Birmingham B13 9BY. Price £6.95 includes VAT and postage.


EG1053]
Fig. 6. BC182L connections

[E6 1036

Fig. 7. Box and lid drilling details

dashboard using more adhesive pad. Thread the wires under the dashboard, this can often be done through the windscreen demisting vents.

Connect up the power cables as follows:
Black Chassis
Orange Side lights or panel lights
Yellow Ignition power
Thread the sensor cable through into the engine compartment, then into a position under the car, where it will not be in the air stream from the radiator, for example. Fix it securely in place. Keep the cable away from hot parts of the engine and the ignition wiring. Use cable ties to hold the cable in place.

## POSITIVE EARTH VEHICLES

The circuit is designed for use with vehicles having a negative earth system. The easiest way to adapt the circuit for positive earth is to reverse everything. Thus, fit all the diodes and electrolytic capacitors the other way about. Use a 79LO5 in place of the 78LO5 (but be aware, the pin-out is different), and use a 4011 i.c. in place of the 4001 for IC2. In addition, cut the p.c. tracks to pins 7 and 14 of the i.c.s, and reverse them. Use BC212L transistors for TR1 and TR2, and BC182L for TR3.


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## PE MICROCONTROLLER: DATA SHEET 2

## PROGRAMMABLE FUNCTIONS

THE Microcontroller keyboard has two keys labelled "PF1" and "PF2" whose use has not so far been discussed. These keys can be configured to provide two user-definable function keys, instead of the dummy functions which are pre-defined by DISBUG at start-up. After power-up, or restart, pressing PF1 or PF2 causes a display with a prompt of "1" or "2". respectively, with the next key depression causing a return to the ". . . dIS . . ." prompt.

The map of the DISBUG RAM region shown in the first datasheet indicates that there are two two-byte stores labelled PF1SRA and PF2SRA which start at locations 03F2 and 03F0, respectively. These are used by DISBUG to hold the start addresses of two subroutines which will be called by the command processor to service the PF1 and PF2 keys. The initialisation values in these two stores are the same (F964), and point to a default service routine within the monitor. Each pointer may, however, be changed using the memory editor to point to an alternative routine (usually in RAM) to perform a special function. As a demonstration of this facility, changing the contents of PF1SRA from F964 to F98F will redefine PF1 to behave exactly the same as the RESTART key. It should be remembered, however, that PF1SRA and PF2SRA will both be restored to their initial values following a power interruption or a restart

## DEFINING USER FUNCTIONS

Fig. 1 shows how a user function routine must be structured. When PF1 or PF2 is recognised by the command processor, the "1" or "2" prompt will be displayed, and the key code will be passed to the defined routine in register $A$. The routine may choose to over-write the prompt (see datasheet 3), depending


Fig. 1. Structure of a user-defined key function on the required function and/or available space for code. At the end of the user function, a reply code must be placed in rA before calling the final RTS. This code should be a zero if more keys are expected (e.g. at least four more keys will be required, plus ENTER, if an address is to be specified), or a one if the defined function is finished and a return to the ". . . dIS . .." prompt is required.

The main routines within DISBUG will only call the
defined function routine once for each keystroke, so the number of calls may usefully be counted to keep track of where the function has reached. DISBUG uses the CSW store to hold such a value, and calls an appropriate lower level processing routine (using a lookup table) to process each key entry in turn. Other functions which are performed by DISBUG, and hence are not required within the user routine, are keyboard scanning, decoding, code conversion, latching (looking for new key depressions) and display driving. The userdefined function routines will use the DISBUG stack by default, and may assume that there are eight bytes free on entry; real time clock interrupts and further subroutine levels may, however, extend stack usage into the user stack area. If necessary, therefore, the user stack should be redefined to start at 0390 (rather than 03AO) at the start of the user program by coding LDS\#0390 (8E 0390 ) as the first instruction.

## REAL TIME CLOCK FUNCTION

The routine listed below is one which can be used to redefine PF1 to provide a display of the current value of the real time clock. DISBUG maintains a count of seconds since power-up or restart in RAM location called TICK; the count is two-byte, and can thus indicate up to approximately 18 hours. Pressing the PF1 key will cause the seconds count to be displayed as four hex digits, and the display will be updated with the new value every time the """ key is pressed. The CANCEL key will return to the ". . . dIS . . ." prompt. To use the function, enter the routine in RAM using the memory editor; the routine is relocatable and so may be placed in any convenient memory space. Then, use the memory editor again to set PF1SRA to point to the start of the routine. The new function is now ready for use.

| INSTRUCTION |  |  | CODE | COMMENT |
| :---: | :---: | :---: | :---: | :---: |
| PFI: | CMP | A $H^{\circ}$ | 81 1D | ; Key= ${ }^{\text {? }}$ |
|  | BEQ | STAR | 2710 | ; Yes |
|  | CMP | A $1 / \mathrm{PF} 1$ | 8114 | ; Key=PF1? |
|  | BNE | IGNORE | 2618 | ; No-ignore it |
|  | CLR | DATADIS | 7F 03 F8 | ; Clear all |
|  | CLR | DATADIS + 1 | $7 \mathrm{~F} 03 \mathrm{F9}$ | ; except the . . |
|  | CLR | SEPDIS | 7F 03 FA | : 4 address . . . |
|  | CLR | STATOIS | 7 F 03 FF | ; digit display |
| STAR: | LDA | A TICR+1 | B6 03 E3 | : Seconds LS byte |
|  | LDX | HADDDIS | CE 03 FB | ; Output 2 LS . |
|  | JSR | TWODIG | BD F8 F7 | ; display digits |
|  | LDA | A TICK | B6 03 E2 | ; Seconds MS b |
|  | JSR | TWODIG | BD F8 F7 | ; 0/P 2MS digits |
| IGNORE | CLR RTS | A | $\begin{aligned} & 4 F \\ & 39 \end{aligned}$ | ; Set reply code |

## Real time clock display routine

Readers may wish to experiment further with this example by using PF2 to allow the seconds count to be reset to zero or, more difficult, allow the count to be set to pre-defined value. It should be remembered that power-up and restart will reset the values of TICK, PF1SRA and PF2SRA, but that any code written in RAM should be preserved and thus may be reactivated by re-writing the pointers in PF1SRA and PF2SRA.
M. Tooley BA and D. Whitfield MA, MSc

# $41 / 2$ DIGIT <br> FREDUENCY <br> METER 

THE frequency meter published in Practical Electronics in May ' 82 was designed to measure radio frequencies up to 600 MHz , and for the necessary resolution, 8 digits were needed. However, there are many occasions when much lower frequencies need to be measured, and only $4 \frac{1}{2}$ digits would be perfectly adequate, this project aims to cater for these needs. Intersil produce a series of $4 \frac{1}{2}$ digit counters: 7224, for I.c.d.; 7225, for I.e.d.; (and 7236 for vacuum flourescent), and details will be given as to how to use either of these. Complicated logic could be designed to provide the necessary store, reset and inhibit pulses, but a much neater and more convenient solution is to use another Intersil device, the 7207A, designed specifically for this function. The chip requires a crystal frequency of $5 \cdot 24288 \mathrm{MHz}$, and will give 0.1 , and 1 second gates, as well as the necessary pulses to control the main counter. Fig. 1 shows the block diagram of the meter.

## HOW IT WORKS

The complete circuit diagram of the meter is shown in Fig. 2. The input frequency is fed via blocking capacitor C1 to the input of IC1, a CMOS op-amp connected as a comparator with hysteresis. The other input pin is d.c. biased with R2 and R3, whilst R5 and R6 provide a Schmitt trigger with approx. 0.1 V hysteresis, changing state at 2.45 V and 2.55 V (assuming a 5 V supply), to give some protection against noisy input signals. The output from the op-amp consists of square waves at the same frequency as the input, and these go via S2a (explained later) into the input (pin 32) of the counter IC2, that will drive the I.c.d. display direct. This has count inhibit, reset, and store pins on 31,33 and 34 respectively, and these all require a pulse to OV to operate, but in a specific sequence.

First the count inhibit must go high so that the counter is enabled. At the end of the gate time ( 0.1 or 1 sec ) this must go low as the store pin goes low to update the output
latches, then the reset pin must go low to finish the sequence ready for the next count. The 7207A produces all the necessary pulses in the right order, with the exception of the count inhibit pulse which needs to be inverted. This is performed by one gate of IC5. The 7207A has its own builtin oscillator, requiring a crystal between pins 5 and 6, and two capacitors, which allows trimming to 5.24288 MHz . There is also a multiplex frequency output of 1.6 kHz but this is not used. The count inhibit pin is also used to control a pnp transistor that activates an l.e.d. to show when a count is in progress.

The outputs from the 7224 are connected direct to the display segments, with the backplane pin of the display connected to pin 5 of the 7224. Whilst the meter was intended primarily for the audio range, $0-20 \mathrm{kHz}$, it is very easy to insert a switch and a 4017 to divide the incoming square waves by 10 , enabling much higher frequencies to be measured, with a 10 Hz resolution. S 2 is a double pole switch, and the second pole is used to enable another gate of the 4070 to invert the backplane frequency and activate


Fig, 1. Block diagram

## AN AUDIO RANGE FREQUENCY METER FOR MEASURING OTO 20 kHz



Fig. 2. Complete circuit diagram of the Frequency Meter
one of the 'flags' incorporated in the display to show that the prescaler is activated. Fig. 3 shows what pin is connected to each 'flag', and the author chose to use pins 2 and 39 to give a divide $(\div)$ sign, but this is simply a matter of preference.

The power supply, if included, consists of a $0-6 \mathrm{~V} 250 \mathrm{~mA}$ p.c.b. transformer, smoothing capacitor and 5 V regulator, with C6 and C7 included to aid stability (Fig. 4). The current consumption of the unit is very low, using all CMOS devices, and battery power is a realistic possibility . . e.g. a PP3 9V battery connected to the input of the regulator. BE WARNED
the maximum supply voltage for the counter is 6 V . When a battery is used R12 and D4 can be ignored to conserve battery power. If readers would prefer an l.e.d. display (nonmultiplexed), the 7225 can be used with exactly the same circuit and p.c.b., except that pin 36 must be grounded (via the pads provided on the p.c.b. for a link), and pin 5 changes
from being the backplarie pin to a brightness pin $1+V$ for max, OV for mink. A variable brightness control can be provided if a 100 k pot is connected across the supply, with the wiper to pin 5 . Otherwise, display segment pins are the same. The second pole of S2 can then be used to switch on an l.e.d as an indication of which range is in use.
For those readers who would like to use a vacuum fluorescent display, the 7236 can be employed, with minor changes to the p.c.b., however, the power supply requirements are more complicated, and constructors are advised to consult the Intersil data before selecting this option.

## CONSTRUCTION

Constructors are advised to use a p.c.b. for ease of construction, and a suggested design is given in Fig. 6 with the component layout shown in Fig. 7. Mount the components,

| COMPONEN |  | $\begin{aligned} & \text { IC2 } \\ & \text { IC3 } \end{aligned}$ | 4017 <br> ICM 7224 (7225 see text) |
| :---: | :---: | :---: | :---: |
| Resistors |  | IC4 | ICM 7207A |
| R1, R10, R11 | 10k (3 off) | IC5 | 4070 |
| R2, R3, R5 | 6 k 8 (3 off) | IC6 | 7805 |
| R4 | 1 M | Miscellaneous |  |
| R6 | 330k | N1 |  |
| R7. R12 | 220 (2 off) | X1 | $4 \frac{1}{2}$ digit l.c.d. (RS 587-311) |
| R8 | 100k | XL1 | Crystal 5.24288 MHz |
| R9 | 27k | Display bezel Case | RS587-282 |
|  |  | Transformer | 6-0-6V at $\cdot 25 \mathrm{~A}$ (RS207-829) |
| Capacitors |  | REC1 | 1 A rectifier |
| C1 | 470n polvester | FS1 | 100 mA fuse |
| C2 | $10 \mu 16 \mathrm{~V}$ elect | Fuse clips |  |
| C3 | 22p | 2 pole push-push latching |  |
| C4 | $1000 \mu 16 \mathrm{~V}$ elect | switches ( 3 off) |  |
| C5 | 220 n polyester | - $A$ ' type knobs red (3 off) | SUE series Ambit |
| C6 | $22 \mu 16 \mathrm{~V}$ elect | 3 way 15 mm bracket |  |
| VC1 | 5-65p trimmer | -Phono socket |  |
|  |  | -4mm socket (2 off) |  |
| Semiconductors |  | Strain relief bush |  |
| D1, D2 | 1 N4 148 (2 off) | l.e.d. (red) |  |
| TR1 | 2N3702 | 1.e.d. (green) |  |
| IC1 | CA 3140E | -See text |  |


$\longrightarrow$ PIN4O

$$
\text { PIN2 } 2-\frac{\square}{\square-}-\operatorname{PIN} 39
$$

LCO FLAGS
[66000]
(6990)


661004
Fig. 3. Pin layout for the 7224 and 7225, rear view of the display, LCD flags and the pin orientation of the 7805


Fig. 4. Circuit diagram for the p.s.u.

$A B E=1.001 \mathrm{~A}$
B.C.D.G\& $1=4.0 \mathrm{DIA}$
$H=3.501 \mathrm{~A}$.
EG1048

Fig. 5. Front panel drilling details


Fig. 6. P.c.b. design
ensuring correct polarity where necessary, and then check the board before mounting it in the case using two bolts and spacers. The interconnections were made using ribbon cable and Veropins and if done carefully there should be no problems. A SUE series switch bank from Ambit was used and the front panel was drilled and cut according to Fig. 5
and the bezel, l.e.d.s etc were mounted. In the prototype, two types of input sockets were included: a phono, and two 4 mm sockets, but this is of course dependent on individual requirements. The rear panel was drilled to accept the mains lead strain-relief bush, and then all the necessary connections were made according to Fig. 8. After a final check the


Fig. 7. Component layout

[651015
Fig. 8. Display wiring


unit can be switched on, calibrated against a known frequency, and then the case can be bolted together.

## CONCLUSION

With no incoming signal, the unit will probably read 50 (mains pick-up), but this will disappear as soon as a signal is applied. Please note that the 'low' input of IC1 is not at earth potential and so should be isolated if an earthed metal case is used. The unit will easily read up to 100 kHz , depending on amplitude, above which is is suggested that readers build the 8 digit meter. On the $0-20 \mathrm{kHz}$ range, if a frequency above 20 kHz is applied, the $\frac{1}{2}$ digit will remain lit, and misleading results will be displayed. To avoid confusion, it is suggested that readings always be made on the high range to start with, and then switched down if necessary.

# SEMICONDUCTOR UPDATE R.W.Coles 

FEATURING MAA 20007114 BU215

## DIGITAL TV

If anyone out there is still convinced that the current craze for microprocessors and digital circuits is nothing but a passing fad, I may have some bad news for them, because yet another analogue circuit bastion is about to come under siege, and this time battle will be joined right under our noses in the living-room TV set.

Of course, we have already seen some peripheral skirmishing and some easy victories for the digital cavalry in the form of the first teletext and remote control irregulars, but the latest onslaught will strike right to the very heartland of analogue circuitry, where defences were once thought to be impregnable.

The strike force now being assembled by ITT comprises six divisions of battle hapdened VLSI digital chips armed to the teeth with gates, flip-flops, and registers, all determined to rid the living room of those simple but harmless residents of the analogue i.c.s, who themselves once ousted the valves and transistors of yesteryear.

Masterminding the attack will be the MAA2000 8-bit microprocessor which will strike terror into the defenders with infrared remote control, phase-locked-loop tuning, l.e.d. displays, and a serial bus to control the supporting forces. Next in the order of battle comes the MAA2 100 Codec battalion which has some awesome heavy equipment including analogue-to-digital and digital-to-analogue converters, an RGB matrix, a beam current limiter, black level white balance and brightness controls, plus inputs for the dreaded teletext.

In charge of psychological warfare is the MA2200 which handles real-time video signal processing including chroma taping, filtering, and contrast, colour, and hue control. Heavy artillery support will be provided by the MAA2500 deflection controller which can muster video clamping, horizontal and vertical sync, east-west correction and signal standard detection from its comprehensive armoury.

Two digital infantry divisions will also be in support. The MA2400 provides stereo audio processing and the MA2300 contains two 14-bit analogue to digital convertors for the sound channels.

The assault forces make an imposing sight in their 24 and 40 pin landing craft, and their promise of automatic compatibility with NTSC PAL and SECAM standards, dual channel stereo or bilingual sound, and the option of pictures within pictures so that two channels can be viewed at once, is expected to endear them

## to the local populace.

The result of course, is a foregone conclusion, but isolated guerilla activity from local defence volunteers is expected to continue for some years in such hostile areas as the horizontal and vertical deflection amplifiers.

Personally, I am already dug in behind my favourite armchair, beam-tetrode at the ready, determined to take a few of the blighters with me at least!

## BIG BUBBLE

Imagine your pet microcomputer shorn of that troublesome cassette recorder and running a full blown disc operating system with half a megabyte of available storage to hold your compiler, word processor, and space-invader software. Nice thought, but a double-density mini-floppy disc drive is likely to result in a second mortgage or a divorce, and then there are all those motors and heads and drive belts to go wrong, so maybe cassettes aren't so bad after all.

But wait! Now there is an alternative way to that $C P / M$ Utopia you have long dreamed of, in the form of a big new bubble chip from Intel. Hot off the press, their monster new bubble memory, the 7114 , offers 4 million bits of non-volatile storage capacity on a chip about half an inch square living in a small 20 pin package.

Bubble memories don't use capacitors or flip-flops to store data like other semiconductor read/write memories. Instead they store data bits as microscopic magnetic domains which can be propagated around loops formed on a magnetic substrate manufactured using a similar fabrication technology to that developed for more conventional devices. Although they operate in a mainly serial mode, more like a floppy disc than a RAM chip, the time to reach a random data item is much shorter than anything that can be achieved with an electromechanical device such as a disc drive, with the 7114 achieving an average access time of only 40 milliseconds.

Perhaps the two most important advantages of bubble memories are their high storage density and their ability to store data without power for long periods, and these features make devices like the 7114 an attractive proposition for disc replacement. The big disadvantage of bubbles is that each chip is expensive, so it is usually uneconomical to make them removable like discs. This could be overcome by loading data into the bubble initially from say, a cassette, a process which would only be necessary occasionally when system
software needs to be changed or expanded.
In the 7114 , data is stored in 8 octants, each of which has 80 minor loops of 8,192 bits for a total of 4,194,304 bits of storage capacity. Also on the chip are some spare storage loops and a "boot-loop" which is programmed by the manufacturer with information on any faulty loops which testing shows up. During system initialisation, the contents of that boot-loop are read by the 7224 bubble memory controller chip (also available from Intel) which subsequently patches in spare loops to make good any deficiencies.

It is too early to find a 7114 based bubble-disc system available for general use on home computers, but I think we can expect to see one before too long!

## LOSS LEADER

Power transistors using MOS rather than bipolar technology have been around for several years now, and their high input impedance and fast switching capability has enabled them to replace bipolar devices like the famous 2 N3055 for many applications.

One problem that these MOS devices have always had however, is that when used as a switch, their "ON" resistance has been rather too high for comfort, and certainly a lot higher than that of comparable bipolar transistors. A high "ON" resistance can impose unacceptable losses in some applications, so MOS device manufacturers have been trying hard to improve matters.

A new device in the Siemens SIPMOS power family, the BUZ15, seems to have the measure of the problem since it sports ań RDS $_{\text {on }}$ of only 30 milliohms. This new capability will undoubtedly open up many new applications areas for MOS devices, especially where power has to be switched with the minimum of resistive loss. The BUZ 15 is a low voltage device for use in 50 volt systems, but in the SIPMOS range there are about 60 different devices, with some operating up to 1 kV . One application worth considering is the remote digital control of car lighting systems to reduce the wiring costs, but no doubt there are many others.

## AVAILABILITY

Devices featured in Semiconductor Update should, under normal circumstances, be available from good component retailers, but bear in mind that retailers will often not receive stocks of a device until some time after it has been featured in Update.

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## Getting it Wrong

Sixty years ago when Henry Ford's massproduced, cheap, universal car (the Model T) was rolling off the production line he saw it as a tool of liberation for the common man. He went even further, forecasting that when the automobile became as numerous in Europe and Asia as in the United States there would be no more war. The automobile, he proclaimed, was the product of peace.

In the event the internal combustion engine became the prime mover in modern warfare and a prime cause of violent death in peacetime, some 8,000 killed on the roads in the UK last year.

Industrialists, of course, tend to be engrossed in the quality of their products, their marketing, and profit. Ford made a fortune through knowing his job. A clever man but not perhaps in the first rank of great thinkers.

Let's then switch to an acknowledged great thinker, an economist who had little interest in money except in its redistribution. Although Karl Marx was buried 100 years ago his name lives on, respected, reverenced or reviled according to political taste. He developed a political and economic theory from social conditions prevailing in Western Europe and more specifically from observation of industrial activities in England, in his day the great centre of the 19 th century industrial revolution.

Marx held the conviction that a communist revolution could come only from an industrial society, spearheaded by workers. In fact the revolution came in pre-industrial Russia, subsequently in peasant China and other non-industrial countries. Apart from inspiring a diluted form of socialism, communism has had little impact on industrial Western Europe and, in the UK, which Marx imagined to be fertile revolutionary ground, the impact has been least of all.

Henry Ford got it wrong because the great massacre in the trenches of the 1914-18 war was still fresh in the mind
and, in 1923, nobody anticipated the appearance of Adolf Hitler. Similarly, Karl Marx was a creature of his own time seeing only the then long hours and drudgery of factory life. He can be excused for not seeing that in an enlightened capitalist society a production line worker at Dagenham or Detroit would enjoy in years to come a far higher standard of living that he himself enjoyed or even dreamed about. And, moreover, a standard superior to that achieved by ordinary people in what Marx had conceived as a worker's paradise

## Guessing

It is possible for us, in the late decades of this century, to be more percipient? We now have the benefit of instant world-wide communications plus advanced electronic data processing of statistics gathered yearly, weekly, daily. even hourly. Economists, social scientists, defence experts have constructed computer models to handle their respective data to predict the outcome of events. Yet with all this elaborate apparatus, as an end result we only have general trends, feeble guides easily distorted by disturbances that can and do happen almost anywhere.

Looking back to only three years ago I find I was not reporting unemployment. The great topic then was the oil crisis and how alternative energy was needed for salvation. Today the world is awash with oil. There is rationing at the wells in an effort to maintain prices instead of rationing of use at the filling station. Nobody with or without computing aid foresaw such a complete reversal. Equally, nobody predicted the Iranian revolution, the invasion of Afghanistan, the Iran/Iraq war, the Falklands war, the Israeli thrust into the Lebanon, Solidarity and martial law in Poland, or EastWest warmish detente degenerating to an approximation of cold war. All these affected international trade and continue to do so.

Who, three years ago, predicted 10 million unemployed in Western Europe, or 10 million jobless in the United States? Or that oil-rich Mexico would become virtually bankrupt, not to mention most of South America, large parts of Africa and the whole of Eastern Europe?

Such is the fragility of society that modern economic forecasting with all its electronic aids is little more than guessing, the computer today's equivalent of the crystal ball, an aid to concentration. A recent comparison of Treasury forecasts for 1983 with those of three respected independant teams, all using computers and economic modelling, revealed none of the leading economic indicators in agreement although thay had a common trend. None were sufficiently up-to-date to reflect an unforeseen fall of five percent in the value of sterling.

## Productivity

In the ocean of uncertainty that has afflicted us in the past three years the only consistent current has been the good overall performance of the electronics industry, predicted throughout in these
columns without aid of computer but with little fear of being proved wrong. A pity that the NEB-backed electronic office company Nexos foundered with a write-off of $£ 34$ million of taxpayers' cash, but heartening to see ICL pulling back into profit. But, viewed as a whole, electronics has been and is the only major growth sector in manufacturing industry.

The snag is that all the expansion in turnover, profits and order books does not lead to a corresponding increase in employment in the industry itself. At shop floor level the old hand-wired products have disappeared and with increasing use of LSI and VLSI the labour content ever decreases.

Even worse from the national and political viewpoint, a substantial proportion of the output is skilfully designed to put people in other industries out of work in the interests of productivity gains.

It is interesting to note that even now, after massive shedding of labour in the older industries, Britain is only 13 th in the world league table for competitiveness, actually slipping back a place on the previous year. It is little comfort to learn that France slipped worse, from 8 th to 15 th, or that New Zealand slumped from 10th to 20th place

Leaders are Japan, Switzerland, USA, West. Germany, Canada and Australia in that order according to the European Management Forum which uses a complicated formula reported to consist of 245 criteria to arrive at their conclusions. When crowing about productivity gains we too often averlook the probability that all our competitors are making similar progress. We have to run to stand still in relative terms. We need to run even faster to gain on the leaders.

On the employment front the one heartening aspect is the apparently insatiable demand for graduate electronics engineers and applied physicists, particularly those with expertise in microprocessor hardware and software, and in telecommunications.

## Offshore

Electronics has done well for North Sea oil and had done well out of it in exploration, monitoring, control and communications equipment. A typical oil platform can have as many as 20.000 information points to provide an operational overview and malfunction alarm system, with input data constantly scanned and analysed by computer. The experience gained in the hostile environment of the North Sea has snowballed into export orders from other parts of the world.

A general misconception is that Britain has now an oil-based economy. True we are self-sufficient, even a nett exporter. True we are listed in the top ten producers in the world. But offshore oil and gas together represent only some five percent of our Gross National Product, less than the construction industry and a mere fraction of that of manufacturing industry. In fact, despite present difficulties, we remain relatively a rich country with a welldiversified economic base.


IFF you have never built projects before and particularly if you have never tried a solderless breadboard this is for you. For qualification, a breadboard is a basic framework on which electronic components can be mounted and wired for preliminary circuit tests, so called because the foundation units were actually wooden breadboards. However, with the 'Experimentor' System from GSC featured here we have moved far from this rudimentary description.

The most fascinating aspect of solderless breadboarding is that designs can be conceived or copied and their applications explored with the promise of total component retrieval at the end. This means that the suitability of a published circuit for a particular role can be explored or rejigged before hard-wiring.

The key feature of the 'Experimentor' breadboard is the basic connector. This is slotted to form five independent pairs of sprung fingers which are placed behind a row of holes in an insulating carrier. The five contacts are electrically common, that is, any leads plugged into the same group are connected together.

Beside the groups of common contacts run the two supply lines made up of parallel strips of connector.

The hole spacing on the board makes up a 0.1 in . grid which is compatible with virtually all electronic component mounting arrangements. A 0.6 in . centre channel allows the mounting of integrated circuits and other devices with wide spaced leads.

## THE SYSTEM

The Experimentor System (Model Exp-304) consists of the breadboard, two preetched pre-drilled p.c.b.s and a 50 sheet Scratchpad with each sheet printed with a full sized layout of the hole and connection pattern of the breadboard on which design layouts can be recorded. This means that the p.c.b.s can be loaded from the breadboard and soldered once a design has been finalised.

A simple circuit is now presented to demonstrate the system. To gain familiarity you should try to relay the design to the free Scratchpad with the intention of cutting back on link wires and board space used. It can then be built on a breadboard and tested.

This unit is designed for use in a brief-case, shopping bag, etc., and an audible alarm is activated if someone opens the case or bag in which it is placed. It could also be used in a cupboard or drawer in which valuables are stored. The alarm is triggered by a transition from darkness to relatively bright conditions, and modifications to the case (or whatever) in which the alarm is used are therefore unnecessary.

The circuit has both switch-on and switch-off delays so that there is time to place the unit in the case and close the case before the alarm becomes active, and there is a short delay before it sounds once the unit has been triggered. This gives the user time to switch it off before the alarm operates. The switch is a simple combination type so that there is no easy way for anyone who is unfamiliar with the combination to quickly switch the unit off.


(E6.007)
Fig. 1. Block diagram of Case Alarm

## THECIRCUIT

Fig. 1 shows the block diagram of the Case Alarm.
A photocell is used to monitor the light level and it triggers a monostable multivibrator when a suitable increase is detected. However, a delay circuit is coupled to the monostable and prevents it from operating until a few seconds after switch-on so that the delay is obtained. The two-tone audible alarm is operated from the output of the monostable, but another delay circuit is connected between these two stages so that the switch-off delay is produced.

The alarm would normally be switched off soon after it has sounded, but it will anyway after about one minute since this is the length of the output pulse from the monostable.

The full circuit diagram is shown in Fig. 2.
IC1 is used as the basis of the monostable, and the 7555 is used in preference to the standard 555 because of its lower current requirement. This gives the circuit a current consumption of only about 90 microamps and enables the circuit to be powered economically from a small (PP3 size) 9 volt battery.

At switch-on C3 is uncharged and zero volts is supplied to pin 4 of IC 1 , thus preventing the monostable from functioning since an input potential of about 0.5 volts or more is needed at pin 4 of IC1 in order to permit normal operation of the monostable. C3 charges by way of R5 though, and after about ten seconds the potential fed to IC1 pin 4 from C3 (via the potential divider formed by R6 and R8) is sufficient to give normal operation of the monostable.

R1 and photocell PCC1 form a potential divider connected across the supply lines, and with the cell subjected to dark conditions there is virtually the full supply potential at their junction due to the consequent high resistance of the latter. If PCC1 is subjected to a transition from dark to light conditions a negative signal is produced due to the large fall in its

COMPONENTS

## Resistors

| R1 | $22 k$ |
| :--- | :--- |
| R2, 3,4 | 1 M 8 (3 off) |
| R5,8 | 1 M (2 off) |
| R6 | 8 M 2 |
| R7 | 150 k |
| R9 | $5 k 6$ |
| R10 | $4 k 7$ |
| R11 | $2 M 7$ |
| R12 | $15 k$ |
| R13 | $100 k$ |
| All $1 / 3 W 5 \%$ |  |

## Capacitors

C1, 5
C2
C3
C4
C6
C7 $\quad 1 \mu 63 \mathrm{~V}$ axial elect.

## Semiconductors

| IC1 | ICM7555 |
| :--- | :--- |
| IC2 | 4001 BE |
| TR1 | BC109C |
| TR2 | BC179 |
| D1,2 | 1N4148 (2 off) |
| D3 | BZY88C6V2 Zener |

## Miscellaneous

LS1

$$
\begin{array}{ll}
\text { S1.2 } & \text { 6.way } 2 \text { pole rotary switches (2 off) } \\
\text { PPC1 } & \text { ORP12 cadmium sulphide photocell } \\
\text { B1 } & \text { 9VPP3 size }
\end{array}
$$

PB2720 ceramic resonator

Case type PB1 (Maplin)
Printed circuit board
Battery connector
Control knobs
resistance. This signal is coupled to the trigger input of IC1 by C1 and the monostable is activated. R2 and R4 bias IC1's trigger input above the trigger threshold voltage under stand-by conditions.

R2 and C2 set the nominal output pulse duration at a little


Fig. 2. Circuit of Case Alarm
over a minute, and the alarm signal is switched off at the end of this period. The circuit as a whole is not switched off though, and the unit will be retriggered if PCC1 is again taken from dark to light conditions. If preferred, the automatic switch-off feature can be eliminated by omitting R3 and replacing C3 with a shorting link.

## ALARM SIGNAL

The two-tone alarm signal is generated by two CMOS astables which use the two input NOR gates of IC2 in a well known configuration. IC2c and IC2d are used to generate an audio frequency squarewave signal at a frequency of about 2 kHz or so, while IC2a and IC2b are used as a low frequency (about 2 Hz ) astable which generates a squarewave modulation signal. The output of the modulation oscillator is coupled to the input of the tone generator via R13, and this provides frequency modulation with the tone generator being switched either side of its normal operating frequency.

The output of the tone generator is fed to a piezoelectric transducer which is very efficient at the frequencies involved here, and gives a reasonably loud alarm signal despite the limited output current available from IC2d.

Under stand-by conditions the output of IC1 is low, TR1 and TR2 are cut off, and the positive supply is not applied to the alarm generator circuit. When the unit is triggered and IC1's output goes high, TR1 is biased on and in turn biases TR2 hard into conduction so that the positive supply is fed to the tone generator circuit and the alarm sounds. However, there is a short delay while C4 charges via R7 to a high enough potential to bring TR1 into conduction, and this gives a delay of about two seconds between the circuit being triggered and the alarm being activated.

When the unit is switched off D2 largely discharges C4 into the supply lines and D1 similarly discharges C3. C2 discharges via internal circuitry of IC1. The circuit is therefore ready to operate properly as soon as it is switched on again.
The on/off switch is actually two six way rotary switches connected in parallel, and each switch only cuts the supply in one of its six positions. It is therefore necessary to have both switches in the correct position in order to turn off the alarm. Without knowing in advance which is the correct


The cased, final unit
position for each switch it is obviously impossible to quickly find these positions, and nothing more elaborate than this simple combination lock technique is needed in this application.

IC2 is a CMOS device and the appropriate handling precautions should be taken when dealing with this component, but as it is a very inexpensive device it is probably not worthwhile fitting it in an IC socket. IC1 is also a CMOS integrated circuit, but as it has a very effective internal protection circuit it does not require any special handling precautions.

When checking the completed unit bear in mind that it responds to a change from dark conditions to comparitively bright conditions after the switch-on delay has elapsed. Simply switching the unit on and leaving it in a normally lit room, or taking the unit from darkness to light immediately after switch-on will not result in the alarm being triggered.

Photograph of the built breadboard



FEW people realise that the increasingly popular industrial robot has been in working existence since 1962; the result of a patent filed in 1954 by George Devol and the business acumen of Joseph Engelberger. However, much of the literature we come across today is not so much concerned with these dumb, reprogrammable, open-loop machines, but with the 'sensible' or second generation robots-these are the impetus behind the recent upsurge of robotic interest. The initial stages of robot development have already passed, the next stage of growth is in robot intelligence and its associated 'intelligent' functions.

The first point, which must by fully appreciated, is that robots are machines and, providing our ethics are stable, always will be. As machines they are provided with a limited intelligence in order that they may make a number of valid decisions and thus reduce human intervention to a minimum. In other words robots are versatile machines which can work on their own. To achieve this the robot can be on one of two possible levels of development: The first generation robot, as mentioned above, is generally senseless (n.b. it has an open-loop control system). The operation of such machines depends upon the robot's repeatability-the action of repeating tasks over and over with little or no loss in accuracy. The open-loop system assumes good repeatability and thus assumes a specific and constant output will be achieved for a given input. Hence, first generation


Fig. 1. Anthromorphology of a robot arm
robots fall short of our requirements: Being open-loop they have no feedback concerning the actual output and, the robot has no knowledge or perception of either its surroundings or its workpiece.

The second generation is attempting to overcome this unawareness by equipping machines with senses and applying artificial intelligence techniques in order that the senses may be used in a human-like manner. In a nutshell, the most critical element in robotics is the interface with the environment, both in sensing (input) and manipulating (output). Until recently the current technology offered much on the output side but was lacking considerably on the input interface. Obviously the direction for research and development is in the application of sensors, the question therefore is how?

## HUMAN MODELLING

The human being is the best all round example of a perceptive, receptive and reactive control system. Because of this engineers are continually looking towards the human 'modus operandi' for solutions to robot design. First generation robots based many of their characteristics on the human arm (Fig. 1). For example many robots can swivel and sweep about a shoulder joint, bend at the elbow, bend and swivel at the wrist, and have various hands fitted (end-effectors). Naturally, second generation robots are inspired by human senses and the operating mechanism of the brain.

Each of our senses is dependent upon a particular, dedicated sensor mechanism which converts informative stimuli from the environment into electrical signals for the nervous system, and thence processed in the brain. The extremely complex arrangement which constitutes the human sensory system can be summarised according to the type of physical input and hence the corresponding response mechanism evoked. They are:

VISION
HEARING

TASTE/SMELL TOUCH

The perception of electromagnetic radiation in the $400-760 \mathrm{~nm}$ wavelength.
The perception of oscillations of air pressure in the $20-20 \mathrm{kHz}$ frequency band.
Chemoreception of odorous molecules.
a) The perception of physical contact
b) The perception of electromagnetic radiation in the form of heat ( $>760 \mathrm{~nm}$ wavelength).

All these sensory mechanisms are also capable of providing the associated direction of a particular stimulus, the reason being that more than one receptor acts at any one time and thus the brain is able to compare stimuli within both dynamic and directional frames of reference.

To generalise, the ability to detect a stimulus, find its range and to determine its relative direction are both necessary and sufficient for the intrinsic intelligence of the control system to respond to, and interact with. The greater the amount of useful data supplied to the controller, then the more complex an interaction is possible to increase the complexity of the workpieces and their orientation, handle a variety of components, and cope with non-uniform backgrounds (e.g. picking single components out of a bin of many types of workpieces all mixed together).


Experimental robotic hand and arm (Courtesy of Southampton University)


Fig. 2. Comparison between human and machine vision
The process which gives sight to robots is directly based on the human model. This consists of a lens, receptor and control unit (Fig. 2). Our control over such a system is unconscious, but to a computer there are two major problems: Firstly the scene has to be analysed into a number of discrete areas, each of which must be quantified according to the system's operating parameters. These are a series of rules which are valid for interpreting the nature of the image. By 'nature' we mean the length and position of boundaries between objects, light intensity, surface orientation to the camera, reflectance, etc. Secondly, all this data has to be analysed to make a tangible 'picture'. This is usually a vast amount of input information. For a single television picture, in colour, approximately 1 Mbit of data is required for digital transmission (e.g. 1 page of PRESTEL, TELETEXT, etc.). This amount of data handling contains the intrinsic problem of speed. If the computer is unable to process its data extremely quickly then the manipulator may be given instructions which are simply too late to be of any use. In practical systems this means that the robot's camera receives an image, processes the useful data, and then directs the manipulator in the appropriate direction. The whole operation must take place in the order of 0.1 seconds. This demands a great amount of computer power and even then is pushing it to the limits.

The basic difficulty involved is the way in which computers operate. Information is processed serially, that is, one caclulation after another. Attempts have been made to improve the data handling of microprocessors by using hardware solutions such as arithmetic units and other miscellaneous functions. Also, there have been recent developments in array processors which act as a series of independent CPUs but constrained to a function within a single architecture. This then provides a form of parallel processing which is ideally suited to pattern recognition systems. Present research is also concerned with the development of new languages incorporating a decision making structure which therefore lends itself to sensory analysis applications (e.g. LISP, PROLOGUE, etc.). The techniques employed are drawn from current artificial intelligence findings.

The remaining parameter controlling robot vision is cost. Even with the recent drastic reductions in hardware prices, the vast power required does not come cheap. Due to this fact there are a varicty of seeing robots on the market ranging from rudimentary, 'low cost' types, to the higher end of the market offering a very sophisticated machine. Typically the basic intelligence levels can detect a specific wavelength of light (in some cases this is infra red), assess its intensity and thus define crude borders for a fixed presentation of the work piece.

The intermediate state-of-the-art offers 'grey-level' processing which in simple terms means that the analysis of a scene incorporates an undescribed state between the black and white pixels, and the associated software allows for this; thus light intensity thresholds are accounted for, which in turn allows for slight variations in the workpiece finish, background complexity, and non-uniform illumination.

The most sophisticated levels of intelligence offer a full analysis of the scene; the number of available pixels is generally
greater thus offering a finer definition, the speed of operation is close to real time, and other factors such as colour recognition, shape and orientation, limit setting and adaptive manipulator control are generally available at considerable cost.

## TOUCH

For intelligent behaviour the use of vision as a sensory organ not only has excellent power and a peculiar fascination for researchers, but, as mentioned above, has severe practical limitations such as cost, speed and definition when applying our current technologies. On the other hand, a much more viable proposition on the grounds of finance, speed and ease of implementation is that of tactile sensing. In addition to these 'pros', is the argument that the final result of a vision system is for the manipulator's end effector to touch the workpiece and so, development of a strong tactile sensory perception minimises the emphasis of the less practical visual facet.


Fig. 3. Cross section of the human skin showing sensory receptors
Referring again to the human model we find that touch is a collection of several types of sensory receptors (Fig. 3). The sense embraces a position sensory (stretch receptors), two types of pressure detectors (one for impact, one for continuous loading), overload sensors (pain detectors such as structural damage or chemical attack interpretors), and heat detection mechanisms. In other words our skin is provided with a multiplicity of multifunctioning sensory organs. However, despite the vast amount of data input there is very little pattern recognition possible. In fact a simple experiment with two pins will show that many areas of the body are indiscriminant over a distance of less than 20 mm between points (e.g. shoulder blade region, chest, etc.). The reasons for this are many, the major one being that many sensors use the same nerve channels. This is exemplified by our peculiar response to certain stimuli (e.g. a tap on the back of the neck can often make the toes twitch) and forms the basis for acupuncture. This apparent lack of ability is probably due to our high visual competence in pattern recognition thus eliminating any need for a duplicate role in the tactile system.

What then is the value in developing tactile sensing? Firstly, touch has the distinct advantage over vision in that it is three dimensional in its geometric and its physical structure; hence the vast amount of interpolation which is necessary with a 2 D picture is redundant in the tactile domain. It is the geometric factor which makes touch such a valuable sense; this is exemplified in
the fact that we use our hands to feel things-the culmination of shape and skin give us direct spatial feedback.

In denigrating the direct pattern recognition abilities of skin the other sensory assets must not be overlooked. The pressure sensors, for example, are extremely sensitive in detecting movement of several microns: (e.g. hairs, grit and dust are perceived when merely falling onto the skin). The remaining major ability touch exhibits is the detection of surface texture. This is achieved by picking up vibrations set up when our fingers are passed over the surface of a material. Our finger ends are specially developed for this purpose in the form of ridges and whorls (fingerprints). Texture detection is largely dependent on relative movement of the skin surface against the sample surface.

Transferring our knowledge from the biological touch processes to the industrial robot gives quite a range for inventiveness; the reason being that there is very little known about the actual workings of biological touch sensors. This being the case, many ideas are currently being experimented with.

For impact detection simple on/off switches are sufficient. Several methods have been used, from sprung flexible circuit board to small piezo-electric elements.

The measurement of load requires some quantitative system, but there are no commercially available sensors which copy the biological model; the response being logarithmic with approximately 30 discrete levels. Strain gauges have been used to some extent but they are limited due to their physical size and their poor susceptibility to noise. Various other resistive devices are being tested. One example is the use of graphite loaded neoprene rubbers in the form of two cords crossed over one another (Purbrick, Univ. of Warwick). The system works logarithmically due to the compression of the two circular cross-sections against each other; however there are still some problems with fatigue life. Another system is to use a felt made form carbon fibres (Dr. M.H.E. Larcombe, Univ. of Warwick). The method of working is the same as with the rubber cords but multiplied many times. Arrays of piezo-electric elements have been used for load sensing, the method being that of measuring the developed potential difference across the device due to the exerted force. The problem arising with piezo-electric cells, however is that the developed voltage decays with time due to the finite input impedance of the measuring equipment. Thus due consideration must be given to time constants when using this method.

Piezo-electric cells are also used in surface texture and slippage detection. The reason being that they are basically microphonic and as such are suitable vibration transducers. Similarly very small acoustic microphones have successfully been used in many different configurations.


Early prototype robotic hand (Courtesy of Southampton University)


A more recent prosthesis shown during gripping exercises (Courtesy of Southampton University)

Various other techniques are still being developed to give a full sensory complement to a manipulator. Research is being undertaken for both industrial robots and in the realm of prosthetics (bionics). Due to the relative 'simplicity' of tactile sensing (to vision), the concept of producing a fully articulate human hand is very close to reality. Several establishments have undertaken such work, one of which, Southampton Univer sity/Hangar Ltd., is now preparing the production models. Producing a hand is far more viable, and probably useful, than aiming for artificial sight. This particular hand uses sensory feedback in several of the forms described above to aid control of a number of preprogrammed manoeuvres and hence acting closely to that of a human hand. Knowledge gained from this research is being re-routed into industry by applying the techniques to commercial robots. (hand in hand?)

## hearing

Auditory sense is not widely used in robotics, however there are one or two cases. The subject of hearing can be divided into two sections: rangefinding and spoken instruction.

The rangefinding aspect provides an intermediary between vision and touch. Several systems have been developed using radar and sonar 'send and return' techniques. work has been undertaken at the University of Alabama, USA, in modelling an echolocation system directly parallel to that of the bat. It involves energising an array of transmitter elements and then comparing the phase differences of the reflected waves. The system is then able to determine the direction and the range of an object. within its environs. A similar technique is being used by the Wolverhampton Polytechnic/Chubb \& Son's Lock and Safe Co. Ltd. teaching company, where an echolocation system presents a viable measurement of distance between a welding rod and a steel joint. In such a hostile environment this proves to be a worthwhile method since touch sensing defeats the objective and vision is usually saturated due to the extreme brightness of the welding arc.

The purpose of using speech recognition is that human beings can tell robots what to do without being tied down to any programming language. This delves deep into complex artificial intelligence techniques. Unimation (UK) Ltd., displayed a speech receptive PUMA robot at the Automan exhibition in summer 1982, but it was confined to a very limited amount of short syllable words. To make robots understand human speech, we must in turn understand the way in which we construct our language. In its base terms, speech recognition is the receipt of aural stimuli and the subsequent interpretation into sounds, then words, then sentences. As humans we are able to focus on one person speaking and developing our own understanding of what he means. Obviously this is very difficult for a computer and because of this little development has taken place. The major problem is not one of signal analysis, despite the complexity of the human voice, but that of understanding. People do not enunciate words clearly and crisply, words merge into each other, word endings are not either clear or correct, words are omitted, etc., and so continuous speech recognition is well beyond our present grasp. Nevertheless much work is being undertaken in this field and there are signs that the barrier is not insuperable.

For the time being industry is only just accepting first generation robots, the operations of which are obviously limited. Very shortly there will be demands for more sophisticated, second generation robots, fully employing their senses of vision, touch and hearing. The more a robot can do for itself, the less it needs to be instructed, the safer and more efficient it becomes. In the twenty or so years of robot existence many people have spent a lot of time copying human characteristics in order to achieve greater machine intelligence and perceptiveness of the environment. From our basic understanding of ourselves it becomes very easy to accept that a human being is human and a robot is, and always will be, a complex and limited machine.

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## LOUDSPEAKER DIAPHRAGM

Loudspeaker designers are continually searching for the perfect material from which to mould their cones and diaphragms. Paper produces good results, but is affected by atmospheric conditions. Plastic is immune from atmospheric effects, but is often insufficiently rigid or chemically unstable, especially when exposed to daylight with its ultraviolet content. Over recent years there have been numerous patents for "magic" additives to improve the stability and performance of plastic loudspeakep cones. The latest, and one of the most interesting, comes from Kuraray Company of Japan. European patent 0061270 suggests that the performance of a polymer sheet can be greatly improved by incorporating mica flakes in the mix. The patent claims are very broad, covering virtually any percentage by weight of mica additive. So if the patent is granted, and held valid, it would cover any loudspeakep diaphragm made from a mix of polymer and mica flakes

## PSEUDO STEREO

RCA, in New York, has filed a string of European patent applications (numbered 0060 097) on further developments in the company's ongoing quest for a system to synthesise acceptable pseudo stereo from a mono TV signal. The company has previously patented (see Practical Electronics May 1981, page 53) a synthesis chip for incorporation in a TV set. Now RCA proposes a modified system that can be hooked up to a home hi fi system. The circuit diagram given may suggest ideas for the home experimenter who would like to build their own synthesis circuits.

The crux of the RCA idea is to split the incoming mono audio signal into two halves and put notches in the frequency response of each half. These notches are deliberately mis-matched so that a peak in one channel copresponds to a dip in the other channel. So overall energy dispersion is constant. As shown in Fig. 1 the incoming audio signal is fed to transformer 20 with a centre tap on the secondary to produce two similar outputs, which are oppositely phased. One of these outputs is fed through circuit 50 which modulates the signal in intensity and phase in the function of frequency. In practice this means that notches are introduced at 150 Hz and 4.6 kHz . Part of this notched signal is bled across to the other channel by resistor 74. As a result of the antiphase relationship the signal at 94 has peaks


Fig. 1


Fig. 2


Fig. 3
where the signal at 92 is notched and viceversa.

Fig. 2 shows the layout of the transfer circuit 50, with suitable component values. The first notch filter 30 includes capacitors 32,36 serially coupled between one end of the transformer secondary and the second notch filter 40 . This includes serial capacitors 42,46.

The phase response curves 196,198 in Fig. 3 show how the two channel signals are in a constant phase relationship of $90^{\circ}$
between the three notch frequencles. According to RCA this produces a distributed sound field that just extends between the loudspeakers. If the phase differential is less than $90^{\circ}$ the distribution is too narrow and at phase angles above $90^{\circ}$ it is too wide. The frequency distribution was chosen through analysis of the spectral characteristic of bass, tenor, alto and soprano voices, so that all voices appear to emanate from front centre while the sound of music is spread.

#  Michael TOOLEY b.A. <br> David WHITFIELD m.a.m.Sc. 

## Provides switched negative earth supplies of $4.5 \mathrm{~V}, 6 \mathrm{~V}, 7.5 \mathrm{~V}$ and 9 V at 1 A

THERE are many situations in which it is convenient to use a normal domestic cassette player, radio or other item of battery powered equipment in the car. Unlike car radios, CB sets and specially designed car accessories, most domestic equipment is usually unsuitable for operation directly from a vehicle's 12 V supply. Typically, portable equipment is designed to use a number of 1.5 V cells, with an overall supply somewhere in the range 4.5 V to 9 V ; portable televisions are the usual exceptions, and most will operate from a 12 V supply.

The use of dry batteries can be satisfactory if only occasional and limited use is required. Frequent usage, however, quickly becomes expensive and inconvenient; battery changes are needed more often due to higher volume levels required. The accessory power supply to be described allows most items of battery powered equipment to operate from a standard car cigarette lighter socket. Any equipment which requires a negative earth supply of $4.5 \mathrm{~V}, 6 \mathrm{~V}, 7.5 \mathrm{~V}$ or 9 V may be used, and a regulated output of up to 1 A is available. Should no appropriate socket be available, the unit may be wired directly to the vehicle's accessory circuit.

The accessory supply is protected against overload, is simple to construct and install, and requires no setting up procedure. The unit is protected against incorrect connection, and is built in a rugged and compact case. The overall cost is less than $£ 10$; a sum which will rapidly be covered by the savings on replacement dry batteries.
The input voltage is likely to contain a substantial amount
of noise, and the decoupling capacitors are used to obviate this.

## CIRCUIT DESCRIPTION

The circuit for the accessory power supply is shown in Fig. 1. The vehicle's 12 V supply is taken from the cigarette lighter socket via PL1,or from the vehicle's accessory circuit if no such socket is fitted. An in-line automotive fuse, FS 1 , is used to provide protection against possible failure conditions. Reverse polarity protection is provided by D1 as a wrongly polarised supply will forward bias it and cause FS 1 to blow.

The voltage regulator, IC1, features internal over-current and over-temperature protection. The device is intended for use in variable voltage power supply applications, and is supplied in a 4-pin plastic package with a metal heatsink tab (which is connected to the common supply rail). The output voltage is set by means of the resistor network R2 to R6, selected by means of S1b. The way in which the output varies with resistor setting is shown in Fig. 2. An indication of which output has been selected is provided by four miniature I.e.d.s, D2 to D5, via S1a. The series output diode, D6, serves two distinct functions. Primarily it allows an output voltage of less than 5 V to be produced (this is not otherwise possible, as shown by the equation in Fig. 2) by providing an almost constant voltage drop of approximately 600 mV . In addition, the diode provides protection against the unit being accidentally connected back-to-front.


[651059
Fig. 2. Setting the output voltage for IC1. Here $\mathbf{V}_{\text {out }}=5\left(\mathbf{R}_{\mathrm{a}}+\right.$ $\mathbf{R}_{b} / / \mathbf{R}_{b}$ and should typically be at least 2 V lower than $\mathrm{V}_{\text {in }}$

## ASSEMBLY

A small printed circuit board is used to mount the majority of components inside the diecast box which is used to house the accessory power supply. The component layout for this board is shown in Fig. 4, with the corresponding copper foil pattern shown in Fig. 3. If preferred, constructors may use a similar sized piece of 0.1 in . pitch Veroboard in place of the p.c.b. The use of terminal pins is recommended in order to facilitate the connection of the wiring and of the regulator device. The regulator i.c. itself is fixed directly to the base of the diecast box, which it uses as a heatsink, and is positioned close to the p.c.b.
The diecast box should be drilled to allow mounting of the printed circuit board, regulator i.c., l.e.d.s, and the switches. Two additional holes at the rear, lined with rubber or plastic grommet material, are required in order to allow the passage of the input and output power leads; suitable measures should also be taken to anchor these leads. A wiring and layout drawing for the complete unit is shown in Fig. 5. It should be noted that the components in the resistive divider chain are mounted directly on the tags of the selector switch, S1, and two of these resistors, R3 and R6, are actually composites of two components wired in series to give the required value.



Fig. 5. Case wiring and layout

## COMPONENTS

| Capacitors |  |
| :---: | :---: |
| C1 | $100 \mu 16 \mathrm{~V}$ electrolytic |
| C2 | 100 n polyester |
| C3 | 100n polyester |
| C4 | $100 \mu 16 \mathrm{~V}$ electrolytic |
| Semiconductors |  |
| D1 | 1 N 4001 |
| D2 | 0.1 in . green I.e.d. plus clip |
| D3 | 0.1 in . yellow 1.e.d. plus clip |
| D4 | $0 \cdot 1 \mathrm{in}$. amber l.a.d. plus clip |
| D5 | 0.1 in . red I.e.d. plus clip |
| D6 | 1N4001 |
| IC1 | $\mu A 78 G U 1 C$ |

Resistors

| Resistors |  |
| :--- | :--- |
| R1 | 1 k |
| R2 | 100 |
| R3a | 1 k |
| R3b | 100 |
| R4 | 680 |
| R5 | 470 |
| R6a | 1 k |
| R6b | 1.5 k |

All 1/4W 10\% carbon

## MIscellaneous

S1 3P4W rotary switch
S2 SPST ultra miniature toggle switch
FS1 1.5A fuse and in-line holder
PL1 Accessory socket plug
PL2 Auxiliary supply connector for the portable equipment (e.g. the Maplin universal plug HH38R)
Diecast box $120 \mathrm{~mm} \times 65 \mathrm{~mm} \times 40 \mathrm{~mm}$
Printed circuit toard
Terminal pins
Knob

## TESTING

When the p.c.b. assembly is complete, it should be carefully checked for dry joints and solder bridges between tracks. A careful visual inspection of the whole unit should include verifying that polarised components (l.e.d.s, diodes, etc,) are correctly orientated. The unit is now reest on either a 12 V d.c. supply or on a car battery, via the accessory socket if appropriate.

Connect PL1 to the 12 V supply, with S 1 set to the 4.5 volt position and S2 set to 'Off'. Connect a suitable voltmeter to PL2. When S2 is switched on, D2 should be illuminated and an output indication of 4.5 V should be obtained; any reading within $\pm 5 \%$ of the expected value is acceptable. Should no output be obtained and no l.e.d. illuminated, a check on the polarity of the supply, on FS 1 , and the polarity of D1 and D6 should be made prior to a more detailed examination of the circuit. An incorrect output voltage indicates a possible problem with the resistive divider chain, R2 to R6. A correctly illuminated I.e.d. but no output, could be due to an incorrectly polarised output diode D6.

When the initial test has been completed satisfactorily, the three other ranges should be checked to ensure that their diodes function correctly, and that the output voltage is correct.

In use, range changing should be done without any load connected since momentary transients are possible. When installing in a vehicle, it should be borne in mind that the diecast box is connected to the negative rail by virtue of the common tab on the voltage regulator i.c. As mentioned


Control layout of PSU
earlier, the accessory power supply may either be connected in to the vehicle's accessory circuit (when S2 and PL1 may be omitted), or supplied from the cigarette lighter socket. The unit may be simplified for dedicated use at a fixed output voltage by removing S1 and three l.e.d.s, and replacing the resistive chain R2 to R6 by two single resistors of appropriate value.
The accessory power supply may be used to supply continuous currents of up to 1 A at the selected output voltage. It should be remembered, however, that with a 13.2 V input and an output of 4.5 V at 1 A , the unit will be dissipating approximately 9 W and a certain amount of warmth is to be expected


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\begin{aligned}
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\end{aligned}
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| Auditorium | 8. 16 | 15 | 60 | Woofer | 637 | 6 |
| Group 45 | 4.8.16 | 12 | 45 | PA | C16 | C2 |
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| Group 100 | 8.16 | 12 | 100 | Guitar | 626 | 62 |
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THE first part of this series was devoted to interfacing systems which involve purely digital control．This month we shall be concentrating on the interface between the digital world of the microprocessor and the analogue world associated with many external systems and devices．

Digital signals（i．e．those which exist in two states；logic 0 and logic 1）are essential to the operation of logic systems． Analogue devices，by definition，require a continuously variable control signal rather than one which is just simply ＇on＇or＇off＇．Hence there is a need for an interface which can convert digital input signals to analogue output signals，and vice versa．

This second article in the series therefore considers some of the techniques and devices used for digital to analogue conversion．Also included is a practical example of a com－ plete 8－bit digital to analogue converter．Full constructional and setting－up details are also given．This unit is eminently suitable for interfacing a wide range of analogue output devices and thus will be invaluable to anyone using an 8 －bit microcomputer（including the PE Microcontroller）in control applications．

## DIGITAL VERSUS ANALOGUE SIGNALS

In digital systems two voltage levels are used to represent the binary digits， 0 and 1 ．Most signal waveforms encoun－ tered in the digital world are therefore composed of a series of pulses；the voltage level at any particular instant being in one state or the other．In analogue systems an infinite num－ ber of voltage levels are employed and waveforms are therefore continuous rather than discrete．Typical time related digital and analogue signal waveforms are shown for comparison in Fig 1．A digital representation of an analogue quantity may be produced by sampling the waveform at regular intervals．The smoothly changing curve of Fig 1b can， for example，be sampled repeatedly and a digital code generated which approximates to the actual voltage level at the instant of sampling．This process，which is illustrated in Fig 2，is known as analogue to digital conversion and we shall be looking at this in a little more detail next month．If，

$$
\begin{aligned}
& \text { ำ刃ito the } \\
& \text { REAL } \\
& \text { WORLD } \\
& \text { рр玉rt } 2 \\
& \text { CONTROL CIRCUITS }
\end{aligned}
$$



Fig．1a．A typical digital signal waveform


Fig．1b．A typical analogue signal waveform
Fig．1．Comparison of digital and analogue signals


Fig．2．Digital sampling of an analogue signal（4－bit code）


Fig．3．Synthesis of an analogue signal using digital in－ formation（4－bit code）
alternatively，a digital code is used to generate a particular voltage level，an analogue signal can be synthesised in dis－ crete steps，as shown in Fig 3．This technique is，of course， known as digital to analogue conversion．

## DIGITAL TO ANALOGUE CONVERSION

One simple method of digital to analogue conversion involves the use of a binary weighted resistor network. A typical arrangement of such a device is shown in Fig 4. The switch symbols are used to represent the logic conditions present on each of the four input lines; logic ' $O$ ' is represented by $O V$ whilst logic ' 1 ' is represented by $+V$. Since four input lines are provided the device caters for a 4-bit natural binary coded input, however extra lines can be added as appropriate. An eight bit digital to analogue converter would, for example, require a further four inputs such that one resistor was provided for each input line.


Fig. 4. Simplified form of 4-bit digital to analogue converter using " $1-2-4-8$ " resistor weighting

The binary weighted resistor network is followed by a high input impedance amplifier stage which effectively buffers the network and helps to minimise the adverse effect of loading on the circuit. The amplifier is usually a conventional operational type with the summing junction of the resistor network taken to the inverting "virtual earth" input of the integrated circuit.

The lowest value resistor, R , of the network corresponds to the highest binary weighted input or 'most significant bit' (MSB). The highest value resistor, 8R, corresponds to the lowest binary weighted input or 'least significant bit' (LSB). The current through each resistor is inversely proportional to its resistance and hence the most significant bit will produce a current which is twice that produced by the next most significant bit, and so on. The currents are summed at the input of the amplifier and a voltage is developed which is an analogue representation of the digital input applied to the


Fig. 5. Simplified form of 4-bit digital to analogue converter using an ''R - 2R'' resistor network
four lines. The principal disadvantage of this method of conversion is that it requires a number of different resistor values and, where a digital input of 8 -bits, or more, is to be catered for, the values of the resistors become somewhat impractical.

Another method of digital to analogue conversion employs a somewhat more complex resistor network which is known as an " $R-2 R$ " ladder network. Since only two values of resistor are required, this arrangement overcomes the most significant disadvantage of the binary weighted method. Fig 5 shows a simple 4-bit digital to analogue converter which uses an R-2R network. Each successively lower weighted input produces an output voltage which is exactly half that produced by the preceding input.

The analogue output of a simple 4-bit digital to analogue converter is shown is Fig. 6. Since there are 16 different in-


Binary Input Code
Fig. 6. Coding states of a 4-bit digital to analogue converter
put conditions, the output voltage can take any one of 16 different voltage levels. The output voltage resulting from a binary input code of 0000 is, of course, OV . The further 15 different input codes permit 15 additional voltage levels, incrementing by the same voltage interval on each successive binary step. The increment in voltage level will be equivalent to the voltage produced by a binary input code of 0001 which is known as the 'least significant bit voltage', VLse. The voltage level resulting from a binary input code of 1111 will be the full scale output voltage less $\mathrm{V}_{\text {LSB }}$ (remember that there are only 15 discrete voltage steps excluding the first step which is, of course, zero). The output voltage level produced by a binary code of 1000 (decimal 8) will be exactly half the full scale voltage, and so on. If desired, the output voltage of the converter can be scaled by means of subsequent amplification or attenuation.

Before examining a practical solution to the problem of digital to analgue conversion it is, perhaps, worthwhile introducing some of the terms and expressions which are often encountered with such arrangements. Of these, 'accuracy' and 'resolution' are by far the most important.

## ACCURACY AND RESOLUTION

The accuracy of a digital to analogue converter is a comparison of the actual output with that which would be predicted from a particular input condition and is usually expressed as a percentage of the full-scale or maximum output voltage. The output of a binary ladder network will, unfortunately, only be as 'good' as its input and hence the applied voltage (often called the 'reference') must be highly accurate and extremely stable. Furthermore, the resistors used in the network must themselves be highly accurate. Conventional 'off the shelf' resistors are neither accurate nor stable enough for use in ladder networks. Close matching of resistors is, fortunately, not a problem when monolithic construction is employed.

The accuracy of a digital to analogue converter is generally equivalent to half the voltage change associated with the least significant bit. The typical accuracy of a 4-bit converter is plus or minus $3 \%$ whereas that of an 8 -bit converter is plus or minus $0.2 \%$.

The resolution of a digital to analogue converter is the smallest increment of voltage that can be produced. It is thus essentially the voltage which is represented by the least significant bit (i.e. the smallest obtainable voltage increment). Resolution is thus a function of the number of input bits and the reference voltage. A 4-bit converter permits 16 different output states and thus its resolution is equivalent to $6.25 \%$. With a reference input voltage of 1 V this is a resolution of approximately 63 mV . An 8 -bit converter would improve this resolution to approximately 3.9 mV .

Besides accuracy and resolution, three further terms are often encountered in conjunction with digital to analogue conversion. These are 'offset error', 'setting time' and "monotonicity". The offset error associated with a digital to analogue converter is the output voltage produced in response to a digital input consisting of all zeros. The settling time of a digital to analogue converter is the time taken for the output to settle within plus and minus 1 LSB of its final value after a change of input code. Finally, a digital to analogue converter is said to be monotonic if it does not miss a step (or take any reverse steps!) when a full digital input sequence is applied.

## THE ZN428 D TO A CONVERTER

The ZN428 is a versatile monolithic digital to analogue converter which incorporates an input data latch facility for updating from a microprocessor data bus. It is thus ideal for interfacing to the system data bus of any 8-bit microcomputer or microcontroller. The internal architecture of the ZN428 is shown in Fig 7. The principal internal elements of the ZN428 are; a data latch, a switch array, an R-2R resistor network and an accurate voltage reference. The latching action is


Fig. 7. Internal architecture of the ZN428 digital to analogue converter
controlled by an ENABLE input. When this input is held low the data inputs drive the device directly. When the ENABLE input is held high the input data word is held in the data latch and the output remains unaffected by the state of the data bus. In this condition the digital to analogue converter appears transparent to the microcomputer.

The ZN428 requires a nominal +5 V supply at a typical current of 20 mA . The device offers true monotonic operation and the offset voltage is less than 5 mV . The settling time is typically less than $1 \mu \mathrm{~s}$ and linearity better than plus or minus 0.5 LSB . (This corresponds to plus or minus 10 mV in a 5 V full scale arrangement). The accuracy and resolution will, of course, depend upon the external components and particular circuit configuration employed. It is thus unrealistic to quote typical figures in this context other than to mention that the accuracy and resolution of the practical digital to analogue converter described later can be expected to be better than plus or minus $0.25 \%$.

The equivalent circuit of the data latch inputs of the ZN428 are shown in Fig. 8. The input current consumed by the data inputs is typically less than $50 \mu \mathrm{~A}$ for a high (logic 1) condition and $-5 \mu \mathrm{~A}$ for a low (logic 0 ) condition. In common with nearly all logic devices, the input voltage must NEVER be allowed to exceed the positive supply voltage.


Fig. 8. Equivalent circuit of the data inputs of the ZN428

The equivalent circuit of the output of the ZN428 is shown in Fig. 9. This consists of a voltage source connected in series with an output resistance of approximately 4 kohm. In order to prevent loading of the output, the following stage


Fig. 9. Equivalent circuit of the output of the ZN428
should have an input resistance which is very much greater than 4 kohm. Hence, for most applications, a high input impedance buffer stage is required. The analogue output voltage is given by the relationship:

$$
V=(n / 256) \times V_{\text {REF }}
$$

where $n$ is the decimal value of the binary input code and $V_{\text {REF }}$ is the reference voltage.

The ZN428 contains its own internal reference. This is provided by an active band-gap device, the simplified equivalent of which is shown in Fig. 10. The device is equivalent to a highly accurate 2.5 V Zener diode and exhibits an extremely low internal impedance. Two external


Fig. 10. Simplified equivalent circuit of the ZN428 internal reference
components are required in conjunction with the internal reference; a resistor to set the reference current and a capacitor to decouple the reference voltage. In order to achieve a high degree of voltage tracking where several converters are employed, a single voltage reference may be used to drive up to five ZN428's. If required, the internal reference may be dispensed with and an external voltage reference may be connected to the reference input, pin-6. Such external reference should exhibit a slope resistance of less than $2.5 \mathrm{ohm} / \mathrm{N}$, where N is the number of converters employed.

## A PRACTICALD TO A CONVERTER

Having described the basic features and characteristics of the ZN428 i.c., we shall now consider the design of a prac-

## SPECIFICATION

OUTPUT VOLTAGE RANGE: 0 to $\pm 12 \mathrm{~V}$ max. (unipolar operation-see note). 0 to $\pm 12 \mathrm{~V}$ max. (bipolar operationsea note)
OUTPUT CURRENT: 10 mA max. (without power amplifier module). 1A max (with power amplifier module)
OUTPUT IMPEDANCE: Normal output; 500ohm typical. Complementary output; 40 ohm typical. Power amplifier module; less than 0.1 hm
ACCURACY: Typically better than $0.2 \%$ ( 10 V output)
RESOLUTION: Typically better than 20 mV ( 10 V output) SETTLING TIME: Typically better than $10 \mu \mathrm{~s}$ ( 5 V step)
D.C. SUPPLY VOLTAGE: $7 V$ to 15 V
D.C. SUPPLY CURRENT: 75 mA typical at $\mathrm{V}_{\mathrm{S}}=12 \mathrm{~V}$

DATA INPUT CURFENT: High' (logic 1 ) input; $60 \mu \mathrm{~A}$ max. 'Low' (logic 0) input; $-5 \mu \mathrm{~A}$ max
ENABLE PULSE WIDTH: 100 ns min .
NOTE: Complementary outputs are available in both operating modes
tical digital to analogue converter which may be configured for either unipolar (single polarity output) or bipolar (dual polarity output) operation. The circuit diagram of the practical digital to analogue converter is shown in Fig. 11. Eight data inputs are provided with simple resistive voltage dividing networks comprising R1/R11 to R8/R18. A similar network, R9/R10, is provided for the ENABLE input. The


ENABLE $\qquad$ Fig. 11. Complete circuit diagram of the practical digital to analogue converter
values have been chosen so as to permit direct connection to the PE Microcontroller integrated peripheral drivers (IC16, 17, 18 and 19 of Fig. 1.9 in November 1982, PE). The voltage level produced by these drivers is approximately 17 V in the high (logic 1) condition and OV in the low (logic $0)$ state. Where the digital to analogue converter is required to be interfaced with a conventional TTL compatible data bus, resistors R1 to R9 should be replaced by short circuit links and R10 to R18 should be omitted. To cater for other values of data bus logic levels it is, of course, only a relatively simple matter of selecting appropriate resistor values for R1 to R18 inclusive (e.g. for operation from a 12 V system, R 1 to R9 should be 1.5 k and R10 to R18 should be 1k). Note that, as mentioned earlier, it is important that the voltage level on the data bus should not be allowed to exceed the ZN428 supply voltage. In this particular circuit arrangement the ZN428 operates from a regulated +5 V supply and hence the voltage appearing at the data and ENABLE inputs of IC1 should not be greater than +5 V .

The analogue output of the ZN428 at pin 5 takes a value

| DIGITAL INPUT CODE | ANALOGUE OUTPUT <br> VOLTAGE |
| :--- | :--- |
| 00000000 | 0 |
| 00000001 | $V_{\text {LSB }}$ |
| 01000000 | $\frac{1}{4} V_{F S}$ |
| 01111111 | $\frac{1}{2} V_{F S}-V_{\text {LSB }}$ |
| 10000000 | $\frac{2}{2} V_{F S}$ |
| 10000001 | $\frac{1}{2} V_{F S}+V_{\text {LSB }}$ |
| 11000000 | $\frac{3}{4} V_{F S}$ |
| 11111110 | $V_{F S}-2 V_{\text {LSB }}$ |
| 11111111 | $V_{F S}-V_{\text {LSB }}$ |

## TABLE 1 Coding table for unipolar operation

between 0 V and 2.56 V (the value of the internal reference voltage). This results in an output voltage characteristic of $10 \mathrm{mV} /$ bit (i.e. $V_{\text {LSB }}=10 \mathrm{mV}$ ). For many applications, however, a greater full-scale output voltage is required and hence IC2 is included to provide both amplification and a degree of isolation for IC1. IC2, a conventional operational amplifier, is used in a normal non-inverting configuration. Internal offset adjustment (null) is provided by VR1, whilst

| DIGITAL INPUT CODE | ANALOGUE OUTPUT VOLTAGE |
| :---: | :---: |
| 00000000 | $-\mathrm{V}_{\mathrm{FS}}$ |
| 00000001 | $-\mathrm{V}_{\text {FS }}+\mathrm{V}_{\text {LSB }}$ |
| 01000000 | $-\frac{1}{2} \mathrm{~V}_{\text {FS }}$ |
| 01111111 | - $\mathrm{V}_{\text {Lse }}$ |
| 10000000 | 0 |
| 10000001 | $+\mathrm{V}_{\text {LSB }}$ |
| 11000000 | + ${ }_{2} V_{\text {FS }}$ |
| 11111110 1111111 | $+\mathrm{V}_{\text {fS }}-2 \mathrm{~V}_{\text {LS }}$ |
| 11111111 | $+\mathrm{V}_{\mathrm{FS}}-\mathrm{V}_{\text {LSB }}$ |

TABLE 2 Coding table for bipolar operation
offset and gain adjustment are provided by means of VR2 and VR3 respectively. C2 improves HF stability and, in conjunction with the slew-rate limiting characteristic of the operational amplifier, increases the overall settling time. A second operational amplifier, IC3, operates as a unity gain inverting stage to provide a complementary output voltage. Low value series resistors, R24 and R28, provide a measure of protection in the event of an inadvertent short circuit at the output.

Selection of either unipolar or bipolar operation is provided by means of R21. When R21 is omitted the circuit provides unipolar operation with the logic coding shown in

Table 1. This shows that a data input of 00000000 produces an output of 0 V whereas a data input of 11111111 results in an output of $\left(+V_{F S}-V_{\text {LSB }}\right)$. For bipolar operation R21 must be added. In this case offset binary coding is employed such that a data input of 00000000 produces an output of $-V_{\text {FS }}$ whereas a data input of 11111111 again produces an output of $\left(+V_{F S}-V_{\text {LSB }}\right)$. An output of OV results from a data input of 10000000, as shown in Table 2.

The circuit requires several internal supply rails; a well regulated +5 V supply for the ZN428 and plus and minus 15 V rails for the operational amplifiers. In order to permit simple interconnection to almost any system, these rails are derived from a single nominal +12 V d.c. input. This voltage may, however, be anywhere in the range of +7 V to +17 V , thus permitting direct connection to the $+12 / 17 \mathrm{~V}$ unregulated supply rail of the PE Microcontroller. The input current will not normally exceed 100 mA . The +5 V regulator, IC4, is a conventional monolithic type. An encapsulated d.c. to d.c. converter module, IC5, is used to provide the plus and minus 15 V output rails. This device operates with an efficiency of approximately $75 \%$ and the output voltage is maintained within $\pm 4 \%$ of the nominal 15 V at load currents of up to 34 mA . Constructors should, however, note that, unlike IC4, this device is not short circuit protected.

## COMPONENTS

## Resistors

R1-R9
R10-R18
R19 470
R20 390
R21 10k (see text)
R22 22 k
R23 10k
R24 $27 \frac{1}{2} \mathrm{~W}$
R25, R26 10 k ( 2 off)
R27 4k7
R28 $27 \frac{1}{2} W$
VR1, VR2 22k miniature horizontal skeleton pre-set (2 off)
VR3 10 k miniature horizontal skeleton pre-set
All fixed resistors, except where otherwise stated, are IW 5\%

Capacitors

| C1 | $2 \mu 2$ | 35 V tantalum |
| :--- | :--- | :--- |
| C2 | 47 p | ceramic |
| C3, C4 | 10 n | polyester (2 off) |
| C5 | $220 \mu$ | 10 V axial electrolytic |
| C6, C7 | $100 \mu$ | 16 V p.c. electrolytic (2 off) |

## Semiconductors

| D1 | red LED |
| :--- | :--- |
| IC1 | 2N428E |
| IC2, IC3 | 741 (2 off) |
| IC4 | 7805 |
| IC5 | d.c.-d.c. converter module |

Miscellaneous
PCB Terminal pins (5 required) 24-way edge connector
Low profile di.il. sockets $(2 \times 8$-pin and $1 \times 16$-pin $)$
Constructor's Note
Components and PCB are available from Howard Associates, 59 Oatlands Avenue, Weybridge, Surrey KT13 9SU (s.e.e. for details).


Fig. 12. P.c.b. foil pattern design for the $D$ to $A$ converter


Fig. 13. Component layout

## CONSTRUCTION

The digital to analogue converter is assembled on a single-sided p.c.b. measuring approximately $77 \times 147 \mathrm{~mm}$. The p.c.b. is designed to mate with a 24 -way edge connector thus, allowing ease of connection to a system whilst retaining the ability to interchange boards where necessary. The p.c.b. foil layout is shown in Fig. 12 and the corresponding component overlay is given in Fig. 13. Low profile sockets should be used for each of the dual-in-line integrated circuit devices. Heat sinking will not normally be required for the series regulator, IC4.

Construction is extremely straightforward and the following sequence of component assembly is recommended; terminal pins, i.c. sockets, resistors, capacitors, l.e.d., pre-set
resistors, IC4 and IC5. When complete the p.c.b. should be carefully examined for correct placement and, in the case of polarised devices, oriertation of components. The remaining i.c. devices should then be carefully inserted into their sockets and the unit is then ready for testing and initial adjustment.

## TESTING AND INITIAL ADJUSTMENT

Testing and initial adjustment is best carried out using the arrangement shown in Fig. 14. Nine miniature s.p.s.t. toggle switches are required; eight to simulate the data bus inputs and one to provide the ENABLE input. These toggle switches should be connected to the appropriate pins of a 24 -way edge connector, carefully following the pin numbering


Fig. 14. Arrangement used in the initial testing and alignment of the $D$ to $A$ converter
shown in the figure. To prevent confusion, it is recommended that the switches be labelled appropriately! A regulated 12 V d.c. supply should be connected to pins 1 and 2 , taking care to observe the correct polarity. The power supply
should preferably include a means of electronic over-current protection. However, if no such supply is available, a 500 mA fuse in the positive supply lead will at least offer a measure of protection. A d.c. voltmeter, or multi-range meter on the d.c. voltage range, should be connected with its negative input lead to $O V$ and its positive lead to a probe or miniature crocodile clip.

A d.c. milliammeter should, initially, be included in the positive supply lead. The supply should then be switched "on" and the supply current should be noted. This should be in the range 50 mA to 80 mA and under no circumstances should it exceed about 150 mA . If the fuse blows, or if the overcurrent trip operates, carefully check for inadvertent short circuits or incorrect wiring of the edge connector. Having established that the d.c. supply current is normal check that D1 is illuminated. This I.e.d. indicates the presence of the +5 V supply rail and indicates that the unit is "active". The voltage of the +12 V and +5 V rails can then be checked; the latter should be within plus or minus 200 mV of its nominal value and should remain within $1 \%$ of its actual value as the d.c. supply input is varied over the range 8 V to 15 V . The adjustment procedure for either unipolar or bipolar operation will be given next month.

NEXT MONTH: Power amp module.

# FREE! READERS' ADVERTISEMENT SERVICE 



VALVE stereo amplifier, to Shirley labs design, Chapman tuner, Wearite deck, Lenco turntable, column speakers E40. A. J. Wood. 22 Denham Way. Denham (S. Bucks), Uxbridge UB9 5AX. Tel: 0895832854.

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CB transceiver, Superstar 360FM VGC many extras inc. antennae $£ 110$ o.n.o. Phone: West Drayton 47355 (Middx.). Mr. R. A. Bishopp, 4 Park View. Yiewsley, West Drayton, Middx. UB7 7BG.

CANNON 'D' plugs and sockets: 37 Way90p/50 Way- 120 p each. (Ex equipment). Mr. J. Fleming, 279 Upton Lane, Forest Gate, London E7 9PR. Tel: 01-472 5929.

TWO Radio Spares Toroidal transformers 50VA 15-0-15V E7 each. Compukit PIA (P.E.) board £18. Sound board f15. P. S. Robinson, 168 Firwood Ave., Urmston, Manchester. Tel: 865 3930.

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WELLER 3102 desoldering iron with stand, transformer, vacuum pump f50, recovers ICs from plated hole p.c.b.s. Tel: 01-451 0520 (Willesden).

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# Ultimum Computer Interface 

## WILLIAM EDWARDS WATFORD ELECTRONICS

THIS month's add-on card for the ULTIMUM is a ROM Emulator (Romulator). It complements the PROM programmer of last month making the development of control systems, based on commercially available single board controllers or home brew systems, possible. The Romulator described here can emulate most of the single rail $1 \mathrm{k}, 2 \mathrm{k}$ and 4 k PROMs as well as a large proportion of the three rail varieties.

## EMULATION

Emulators come in two forms. The larger development system emulators plug into the microprocessor socket and will emulate memory (ROM and RAM) as well as the processor itself. These emulators are very useful when designing a development system from scratch. They usually provide some form of real time tracing which makes fault finding simpler. Such emulators usually cost several hundred pounds and, as such, are rather too expensive for the average enthusiast.

The second class of emulator emulates ROM only. The ROM being emulated is substituted with a header/cable assembly which is connected to memory filled from another system. For most applications this simpler (and much cheaper) form of emulation is quite adequate. The Romulator falls into this latter category.

The Romulator is a card which plugs into one of the sockets on the ULTIMUM motherboard. It can be used as standard static RAM by your computer, and may be filled with any code (or used as extra space for BASIC programs) and accessed as any other RAM in your system. When switched into emulation mode, it may be inserted into any other system to emulate PROM or ROM. Fig. 5.1 shows a typical example of such a set up.


The main advantages of this type of system are:

1) Programs can be developed on your host system which has many tools available (such as an assembler/disassembler and a screen to monitor RAM contents). The system being developed may only have an absolute minimum of tools and interfaces.
2) Any programs developed on the Romulator can be protected from overwriting by the system being developed as they can only be accessed as ROM. This is particularly useful when developing machine code programs which have a marked tendency to crash.
3) Once a program has been tested, it can be programmed onto ROM using the PROM programmer board. No intermediate paper or magnetic tape increases the chance of a successful transfer of data. Once programmed, the PROM can be inserted into the system being developed and the emulator removed for further use.
4) The alternative is to buy a separate emulator complete with its own processor. These usually cost as much as your main computer and it seems (to us at least) a waste of a computer to buy another system just for development.

## THE CIRCUIT

The circuit is shown in Fig. 5.2. It does not need to be complicated as most of the extra circuitry for an emulator is provided by the motherboard and your own system.

There are three basic elements: the decoding (motherboard and off-board), the multiplexing circuits (selecting between emulator signals and mother board signals) and the memory itself.

The decoding allows the board to be mapped into your computer as 2 k or 4 k portions. If you do not require a full 4 k emulator, some memory can be omitted and the board can be mapped to occupy $2 k$ of memory. The main decoding is done by IC8 which provides 16 lines each corresponding to a 4 k boundary. Selection of a 2 k boundary is achieved by additional decoding provided by bringing A11 into IC8.

The multiplexers are made of three quad two-to-one line multiplexers which can be set to accept signals from the motherboard or the emulator header. The selection is handled by a line fed from the port on the motherboard. When the port is set up, the multiplexers are set to accept signals from the motherboard, so that the board looks like ordinary memory.

Toggling the port line (marked as External Select in Fig. 5.2) will cause the signals from the motherboard to be deselected and the read/write control signals are disconnected to prevent spurious accesses from conflicting with signals on the emulator header.



Fig. 5.3. Component layout

# COMPONENTS 



The memory itself is fast static RAM organised as two off $2 k \times 8$. Various speeds of RAM are available allowing development of the newer fast processors. Allowing for leads and buffers a $200 \mu$ s PROM can be emulated using 100 ns RAM. The static RAM is always powered from the motherboard. A buffer is provided on the data lines to drive the emulator cable assembly.

## ASSEMBLING AND SETTING UP

Refer to the overlay (Fig. 5.3) for component placement. The usual order of assembly applies, ie. Sockets and discrete components first, followed by a thorough check of your soldering and then insertion of the i.c.s. The header is prefabricated, as this is an insulation displacement assembly requiring special equipment.

To test the board, set up the mapping options. A maximum of three links are needed. Leave the emulator cable and the d.i.p. header off for the moment. Insert the card into the motherboard and power up (not the other way round). Set up the port on the motherboard fas per ULTIMUM article) so that the external select 2 line is a logic ' 0 ' and the external select 1 line is a ' 1 '. You should then be able to read and write to the memory on the Romulator. Any memory test can be used to check for proper function.

To test the emulator, you can either use a second system or plug the emulator into a PROM socket somewhere in your own system, as the Romulator is quite capable of emulation on your own computer.

Once you have loaded a suitable program into the Romulator memory, you switch to emulator mode by making the External Select 2 a logic ' 1 ' and the external select 1 a '0'.

Two points to note. Firstly, do not insert the emulator socket in upside down, this may damage the Romulator memory or the system. Secondly, some systems make rather strange uses of the Read and Chip Select control signals to decode the ROM. Check a circuit diagram if in doubt.

NEXT MONTH: The phoneme speech card.

# Ingenuity Unlimited 

A selection of readers' original circuit ideas.
Why not submit your idea? Any idea published will be paid for ai $£ 40$ per magazine page.

Each idea submitted must be accompanied by a declaration to the effect that it has been tried and tested, is the original work of the undersigned, and that it has not been offered or accepted for publication elsewhere. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not in the text.

## WINE HEATER

THERMOSTAT

ALTHOUGH this circuit was devised to control a 22 W heater plate under a 1 gallon wine demijohn to hold a nominal temperature of $22^{\circ} \mathrm{C}$, with the larger relay used it can be used to switch much higher powers, and with the components shown, it has an adjustment range of $9^{\circ} \mathrm{C}$ to $31^{\circ} \mathrm{C}$ with a dead range from $0.9^{\circ} \mathrm{C}$ to $1.4^{\circ} \mathrm{C}$. Other applications could be in darkrooms or greenhouses, etc.
TR1 and D2 are deliberately over dimensioned. D1 prevents negative $\mathrm{V}_{\mathrm{be}}$ $>0.6 \mathrm{~V}$ on TRI. An alternative approach would be to use a single-ended supply, although this could cause problems with the increased values of R1, R3, VR1, if one tries to keep the signal and reference near the middle of the 741 's input range rather than relying on common-mode rejection; the ratio of R4×VR2 to the input

source impedance ( $2 / \mathrm{R} 3+$ VR1) governs the dead range, and for about $1^{\circ} \mathrm{C}$ this ratio needs to be around 6000 , leading to impractical values for a source impedance
much greater than the present 1040 or so.
Andrew Fogg, Maidenhead,

Berks.

EVEN with constant weekend use during the summer months, most boats, or caravans, are left unattended for an extremely high proportion of the year and some form of alarm system is therefore highly desirable to frighten off intruders.

It will be evident from the circuit diagram that the unit is switched-off until the trigger connects it to the battery negative, whereupon TR2 immediately conducts, closing the relay and thereby latching on the supply, since initially the capacitor is uncharged and the base of TR1 is also positive. C1 now commences to charge slowly via the 100 kilohm resistor causing the base to become progressively more negative until the transistors cease conducting and the relay opens. With the values shown, the alarm cuts off after one to one and a half minutes, but different periods of operation can be obtained by trying other capacitors. After discharging the alarm resets itself, but to speed up this process to about a

## INTRUDER ALARM

minute or so, a 1.5 to 2 Megohm resistor (R3) can be connected as shown dotted in the diagram. The diode across the relay coil protects TR2 by damping the back-e.m.f.

The alarm to be operated is largely a matter of choice and can be a 12 V d.c. siren or bell, a lamp, or even a car horn. In the latter case, particular care must be taken to check polarity and current consumption. Although the relay contacts used are rated at 5 amps , some car horns take a very large current.

The trigger contacts are constructed from spring-metal strips (e.g. draught excluder), or, more elegantly, with reed contacts and magnets. In this system the trigger contacts are required to be open, so ensure that they can be closed by the magnet (or are of the 'change-over' type) to suit your particular installation requirements.

The foregoing also applies if you decide on switch-pads for placing under carpets.


A possible method of contruction could consist of two sheets of tin separated by a thin layer of plastic foam, which would be penetrated when trodden on by the spikes of a few panel pins, or tacks, soldered to the lower piece of metal; the whole being fitted into a strong plastic bag.
R. P. Machrell,

Lytham St. Annes,
Lancashire.


THIS circuit counts digital pulses up to 9999 and then resets to 0000 , a socket is provided to carry out the 10,000 th pulse. Clock pulses enter IC2 via the "Clock In" socket. This counts the input pulses to ten and resets to zero carrying the tenth pulse to IC3 via pin 12.

Similarly IC3 counts tens carrying the hundredth pulse to IC4, and so on along the chain. The unit can have the chain shortened or extended if desired.

The pulses are counted visibly on four rows of ten l.e.d.s (TIL209) which the

4017 drives directly. The small circuit around IC1 is an anti-switch bounce circuit generating clean pulses when S1 is pressed which reset the display to zero When S1 is open circuit the voltage to the reset pins is 0 V , the condition necessary for automatic reset on the tenth count. After any count from 0000 to 9999 a facility is added to trigger an external circuit, e.g. an alarm, model railway points, the radio etc.

IC6a is wired as an inverter to save using extra space with inverter chips.

The circuit can be used to measure digital frequency, as a stop watch once the input frequency is known, as a photographic or kitchen timer and as if this is not enough it can be used as an intriguing display on the mantlepiece at parties.
Power is applied externally in the prototype but can be built into the unit very simply.

Michael J. Walker,
N. Ascot,

Berks.

## TTL LOGIC PROBE

THIS probe uses a 7400 N consisting of quad dual input NAND gates, and detects high, low and open circuit with pulse stretching on high and low indications so that any combination of signal will be visually detected. The thresholds are 0.8 V and 2 V .

The assembly should easily fit into a plug case with flying leads for the supply and 0 V connections and a needle probe. Pulse stretching may be increased with larger capacitors.
G. Coleman, Rochester, Kent.


THIS circuit will vary the speed of a car's windscreen wipers from one sweep per second to around one sweep per minute. When switched on IC1 will start to oscillate at a rate set by VR1 which in turn switches RLA1. This in turn supplies the wiper motor power at the same time intervals. The conventional switch can now be disregarded. If the washer pump is wired to a pushbutton it can be removed altogether. D1 soaks up any back e.m.f. from the relay coil while C2 and C3 suppress the spike cause by ICl's output going high.
A.D. Billington,

Rotherham,
S. Yorkshire


## TOUCH-SWITCHED

## SPEAKER MUTING

THIS unit was designed for a friend who had a set of extension speakers conflicting with a telephone in his bedroom. He specifically requested touch switches since, although much more expensive than a 2 pole 3 way switch, they are more pleasant to use and are aesthetica!ly pleasing.
The touch switches are a resistive type as these are easier to implement than the capacitive variety. The three switches set up the JK flip-flop inputs when touched, and the oscillator formed by IC3b,c, C2, R11 clocks the outputs to the required state which will then be held by the flipflops. An ordinary 7473 is used to allow the l.e.d.s to be driven directly.

Note that points $M$ and $F$ should be connected to identical circuits as to the left of point X .

The relays are 2 pole to provide switching for a stereo pair of speakers. R1 at 1007 W is adequate for up to around 35 W input. For amplifiers which dislike high impedance loads, R1 could be shunted with a 1025 W resistor to ground (or the return line if the speaker connections are left floating as shown). R1 is calculated for about 20db of muting against 8 speakers. Clearly a single secondary transformer and a bridge rectifier could be substituted for T1, D4 and D5.

Andrew Fogg,
Maidenhead,
Berks.


IN


## electronic Cruise control

COMMERCIALLY available cruise control units for petrol engined cars work on the principle of a speed sensor connected to an electronic unit which converts speed differences to an electrical output. This drives a solenoid valve on a vacuum operated servo (Fig. 1) which operates the throttle. When the speed is below the preset cruising speed the servo is activated and it opens the throttle to regain speed. When the speed is above the preset speed the throttle is released by the servo. Fig. 2 shows the valve arrangement in the servo.

Speed may be sensed by means of a device on the speedometer cable, on the drive shaft or by using the pulses on the ignition coil primary (or points).

The servo should be purchased from suppliers or servicers of cruise equipment and a suitable unit is manufactured by Associated Engineering. The solenoid valve in the servo has a coil resistance of about 45 ohms and operates as follows;

| rising voltage: suction opens | 6.3 V |
| :---: | :---: |
| vent closes | 7.3 V |
| falling voltage: vent opens | 6.6 V |
| suction closes | 5.0 V | The circuit can be divided into D/A converter, memory with clock and bistables, difference amplifier and servo drive, as shown. The power supply is regulated to 11.0 volts by means of TRS, TR6. TR7, D4. An i.c. regulator cannot be used due to the small voltage drop available.

The engine (and therefore car) speed is sensed from the pulses at the points by TRI, IC1 and IC2 which form a D/A converter and the output of IC2 is linearly proportional to revs. TRI acts as an inverter to trigger the 1.5 ms monostable IC 1 . The voltage across C4 is proportional to revs. IC2 acts as a voltage follower with gain. VRI adjusts the gain to give an output on pin 6 of a maximum of 5.5 V at maximum revs ( 6000 r.p.m. for a 4 cylinder engine).

IC3, a dual counter, forms a digital memory for the analogue voltages. The two counters are cascaded and the outputs feed a ladder resistor network which is used as a 256 step memory from 0 to 5.5 V . The memory resolution is thus 0.02 IV per step or approximately 0.4 m.p.h. The memory is activated in the following manner. When $\$ 1$ is switched to

Fig. 1. Showing the carburettor servo


"Engage" the "a" section of electronic switch IC7 closes and produces a reset on IC3. When S1 is released to its centre off position the negative edge trigger formed by IC5a is activated which in turn sets the bistable formed by IC6c and d. The output on pin 11 goes low to produce an "enable" on IC3. Since IC5 "c" and "d" form a clock running at 1.5 kHz continuously, IC3 now commences to count upwards from zero and the voltage output of the ladder network rises in 0.021 V steps per 0.6 ms . As soon as the voltage equals or exceeds the D/A output the output of IC4 goes low and triggers the negative edge trigger of IC5b via IC7b which was switched on by the bistable IC6c and d. The trigger pulse switches the bistable into a reverse condition with pin 11 high and pin 10 low and IC3 is then disabled from counting further. At the same time the output of IC4 is isolated. The memory is therefore fixed at the D/A voltage the moment S 1 is released to off.

A second bistable IC6a and $b$ was triggered on when SI was switched to "Engage" and this in turn switched on IC7 "c" and TR4. Current is thus able to flow through the solenoid of the servo. During "Engage" IC7d and TR3 were also switched on to energise the solenoid. The servo now produces an acceleration. On releasing S1 to off, IC7c and TR4 remain on while IC7d and TR 3 are switched off, removing the acceleration.

IC8 acts as a differential amplifier with a large offset voltage. If the voltage on pin 3 equals that on pin 2 (D/A output equal to memory) then the output on pin 6 equals the offset voltage set by VR3. TR2 acts as a voltage follower to drive the solenoid in the servo. Should the car slow down then the voltage on pin 2 is less than on pin 3 of IC8 and the difference is amplified in the ratio of VR2 to 10 k and added to the offset voltage. The servo is thus energised more and exerts more pull on the throttle to accelerate the car. The reverse happens if the speed is in excess of that se in the memory and the amplified difference is subtracted from the offset voltage. The servo is then de-energised and a deceleration occurs.

To release the cruise control the bistable IC6a and $b$ is de-activated by pressing the "Stop" switch S2. This turns off IC7c and TR4, isolating the solenoid. However, the memory is not affected and if subsequently S 1 is switched to "Resume", the bistable is re-activated and TR 4 is switched on again without disturbing the memory as D2 isolates the positive pulse from the negative edge trigger and reset circuit.

A similar release action is obtained by pressing the brake pedal to activate the stop lights and therefore putting a positive pulse on the bistable to de-activate it. A similar switch to that of the stop lights can be fitted to the clutch pedal to de-activate when the clutch is depressed. This prevents the engine racing during gear changing.



# MICRO-EUS <br> Compiled by DJD. 

Appearing every two months, Micro-Bus presents ideas, applications, and programs for the most popular microprocessors; ones that you are unlikely to find in the manufacturers' data. The most original ideas often come from readers working on their own systems; payment will be made for any contribution featured.

TWO of the most popular microcomputers currently on sale, the Acorn Atom and the Dragon, have many similarities because they both use the same video display chip, the Motorola 6847. This versatile chip not only contains 64 programmed characters for a 32 $\times 16$ text display, but will also give graphics displays up to a resolution of $256 \times 192$. However the 6847's internal character set does not include lower case, and so both the Atom and the Dragon display lower case as inverted upper-case characters.

## NEW CHARACTERS FOR ATOM

The following circuit shows how to add an external character generator to the 6847 to provide 256 different characters in the alphanumeric display modes. The circuit can be added as a plug-in module, and no alterations are necessary to the Atom (or Dragon).

With careful choice of the new character set lower-case can be added, while retaining compatibility with the Atom's existing VDU software. The circuit was designed by W. A. Chadwick of Welwyn, and the following description is based on his letter.

## CIRCUIT DESCRIPTION

The new character generator is stored in a $4 \mathrm{k} \times 8 \mathrm{EPROM}$. The top 8 address lines of this are connected to the 6847's Video Data Bus; see Fig. I. When the 6847 puts data onto these lines they will select one of 256 groups of 16 EPROM locations. Each displayed character is 8 dots wide and 12 scan lines deep, so that the first 12 EPROM locations of each group contain the precise bit patterns for each character.
The lowest 4 address lines of the EPROM are connected to a row counter (74LS 161).

This counter is clocked once per scan line by the HS signal from the 6847 and so will sequentially select character row information from the EPROM. The 6847 provides a clear control, RP, every 12 scan lines for the row counter. The beginning of each display frame contains a border of non-active display lines, but unfortunately the 6847 does not provide an RP pulse to clear the character counter for the first character row. Thus, to ensure that the top line of the display is displayed correctly the Frame Synch (FS) signal is used to preset the row counter to 9 at the beginning of each frame.

When using the graphics modes of the 6847 the video data must be passed on to the chip unchanged. The control line $A / G$ is used to select between alphanumeric and graphics modes. Since only 12 out of every 16 ROM locations are used for each character dis-

Fig. 1. Circuit adds an external character generator to the 6847 Video chip in an Atom or Dragon micro.

played, the 4 unused locations in each case are programmed to contain logical mappings of the top eight address lines. The signal A/G is used to hold the row counter preset to row 12 or above when graphics are selected, thus causing the ROM to appear transparent. To illustrate how this works the coding of a single character is shown in Fig. 2.


ADDR
$\begin{array}{lll}0 & 7 & 0 \\ 0 & 7 & 1 \\ 0 & 7 & 2 \\ 0 & 7 & 3 \\ 0 & 7 & 6 \\ 0 & 7 & 5 \\ 0 & 7 & 6 \\ 0 & 7 & 7 \\ 0 & 7 & 8 \\ 0 & 7 & 9 \\ 0 & 7 & A \\ 0 & 7 & 8 \\ 0 & 7 & C \\ 0 & 7 & D \\ 0 & 7 & E \\ 0 & 7 & \mathrm{~F}\end{array}$

D ATA
in the socket of IC31. The board should easily fit inside the Atom's case, though be careful to avoid the case support pillar. The only precaution needed is to keep wires carrying video signals as short as possible.

Fig. 2. (left) ROM programming details for a sample character.

Fig. 4. (below) Circuit increases the speed of the Atom to $\mathbf{2 M H z}$.
generator EPROMS, one compatible with the existing Atom software, and the other giving 256 totally different display characters.

## FASTER ATOM

Three other ways of improving the Atom's circuit have been submitted by Richard Brain of Devon. First, the Atom can be persuaded to run at 2 MHz rather than the usual 1 MHz by fitting a switch as shown in Fig. 4. Note that some of the other components, such as the memory or the 8255 , may not be capable of the extra speed, and faster versions may have to be fitted.


## POSSIBLE EXTENSIONS

In the Atom the 6847's Colour Select Signal CSS is used to select between two possible sets of four colours. However, on monochrome displays this signal has little use, and so it can alternatively be used to add extra facilities to the character generator board. At power-up the CSS signal is set to logic 0 . By gating CSS with $A / G$ it is possible to cause the counter to preset to either row 14 or row 15 depending on the state of CSS. If rows 13 and 15 are programmed with a true logic mapping, and rows 12 and 14 with an inverted mapping, as shown in Fig. 2, then the CSS signal (which is controlled by bit 3 of the port at address B 002 ) can be used to invert the entire graphics display. The circuit in Fig. I shows the links made for monochrome use to provide this facility; for colour use, both links should be changed so that CSS operates in the usual way. Good animated effects can be achieved with the colour graphics modes by drawing the background colour, then chang-

Two examples of possible charactrer sets following this design are given in Fig. 3. The only limitation of this set is that if the cursor is placed on an upper-case character it will appear as lower-case and vice-versa.

GAECDEFGHIJKLMHOFQRETUYHXVZ[:]


## CIRCUIT CONSTRUCTION

The circuit can be constructed on a piece of perforated board using a 40 -pin header to fit
ing CSS to make the drawing instantly visible.
An alternative use for CSS would be to switch between one of two character-

The second modification, shown in Fig. 5, speeds up the rate at which characters are printed to the Atom's screen. Normally, character output is synchronised with the FS output of the 6847 to prevent screen noise. By feeding a high frequency to the 8255 this delay of $1 / 60$ th of a second is circumvented.

The third modification, shown in Fig. 6, elimitates screen noise on the Atom by giving the micro and the 6847 display generator the same clock source. Note that this means that the microprocessor speed will not be exactly 1 MHz , and programs recorded using the cassette interface will have to be re-saved at the new frequency.

## 2X81 ANAGRAMS

Over the past year several readers have sent in programs for the ZX81 to produce random anagrams of a given word. The shortest of these, taking 13 lines, is shown in Fig. 7 and it fits into an unexpanded ZX81. It was submitted by $G$. Wheaton of Bolton, and he writes:
"This program is of use to anyone trying the 'make-up' or 'break down' anagrams. Any


Fig. 5. Circuit increases the speed of displaying characters on the Atom screen.
word or combination of words is inputted; the program will then print possible combinations of the letters. To enter a new word press key ' 0 ', and then type the new word followed by NEWLINE. Of course the program doesn't guarantee a solution, but is of help in finding


Fig. 6. Circuit eliminates screen noise on the Atom display.

```
10 INPUT A\$
20 LET L=LEN A\$
30 FOR \(\mathrm{F}=1\) TO L
40 LET \(\mathrm{X}=\mathrm{INT}(\) RND \(\times \mathrm{L})+1\)
50 LET \(Y=1 N T(R N D * L)+1\)
60 LET \(Z \$=A \$(X)\)
70 LET \(A \$(X)=A \$(Y)\)
80 LET \(A \$(Y)=Z \$\)
90 NEXT F
100 PRINT AT 21,0; A\$
110 IF INKEY\$ 〈〉 \(\cdots \cdot 1\) THEN GOTO 10
120 SCROLL
130 GOTO 30
```

Fig. 7. Program generates random anagrams of a word on the $\mathbf{Z X 8 1}$.
the answer. The program helped me win the coveted prize of a Piccadilly T-shirt from the local commercial radio station."
"The program is fairly easy to understand; lines 70 to 130 shuffle the letters of the inputted word and lines 140 to 180 print out the resulting arrangements of letters; lines 30 to 60 set up the array B."

## ZX81/SPECTRUM MORTGAGE REPAYMENTS

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Fig. 8. Programs calculate mortgage repayments on the (a) $2 \times 81$, and (b) Spectrum. <br> \title{

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out; they were submitted by J.W. H. King of York. In use simply type RUN, and then answer the questions presented on the screen. When satisfied that all is correct, type CONT followed by ENTER (or NEWLINE), and the repayment amounts for each period, and annually, will be shown.
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