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400V: 1nF. 1n5, 2n2, 3n3, 4n7, 6n8 11p; 10n, 15n, 18n, 22 12p; 33n, 47n, 68n 16p; 100n, 150n 20p; 220n 30p; 330n 42p; 4/0n 52p; 680n 60p; 1µF 68p; 2µ2, 4µ7 85p. 160V: 10nF 12n, 100n 11n; 150n, 220n 17n; 330n 470n 30n; 680n 38n; 1µF 42p;	BC1096 12 BD205 110 MJE371 100 11543 32 2N3614 199 225C2166 165 BC1096 12 BD245 45 MJE520 95 TIS44/45 45 2N3615 199 225C1679 190 BC117/8 20 BD378 70 MJE2955 99 TIS48 50 2N3663 15 2SD234 75
45p; 2µ2 48p; 4µ7 58p. 1000V: 1nF 17p; 10nF 30p; 15n 40p; 22n 36p; 33n 42p; 47n, 100n 50p.	BC140/42         30         BD434         55         MJE3055         70         TIS88A         50         2N3702/3         10         2SK45         90           BC143         30         BD517         75         MPF102         66         TIS90         30         2N3702/3         10         2N128         112           BC147/8         9         BD695A         99         MPF103         36         TIS91/33         32         2N3706/7         10         3N140         112
POLYESTER RADIAL LEAD CAPACITORS: 250V; 10n, 15n, 22n, 27n 6p; 33n, 47n, 68n, 100n 7p; 150n, 220n 10p; 330n 470n 13n; 68(n) 19n; 1/2 3n; 1/4 3 fbn; 2/2 4 6n; 1000n5 5 50V, 8n;	BC147B         10         BD696A         99         MPF104         36         UC734         65         2N3708/9         10         40097         88           8C148B         10         BDY56         180         MPF105         36         VK1010         80         2N3710         10         40100         215           BC148C         10         BDY56         160         MPF105         36         VK1010         80         2N3710         10         40100         215
ELECTROLYTIC CAPACITORS (Values in µF). 500V: 10 52p; 47 78p; 63V: 0.47, 1.0, 1.5, 2/2, 3.3, 80: 4.7, 9p; 6.8, 10 10p: 15, 22, 12p; 33, 15p; 47, 12p; 100, 19p; 1000	BC149 9 BF115 35 MPSA05 25 VN46AF 78 2N3771 179 40250 85 BC149C 12 BF167 29 MPSA06 25 VN66AF 85 2N3772 195 40251 97
70p; 50V: 47 12p; 68 20p; 220 24p; 470 32p; 2200 90p; 40V: 47, 15, 22, 9p; 330 90p; 4700 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 160 12p; 220 15p; 220 15p; 220 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 160 12p; 220 15p; 220 15p; 220 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 160 12p; 220 15p; 220 15p; 220 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 150 12p; 220 15p; 220 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 150 12p; 220 15p; 220 15p; 220 120p; 25V: 1.5, 6.8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 150 12p; 220 15p; 220 15p; 220 100 100 100 100 100 100 100 100 100	BC157/8 10 BF177 25 MPSA55 30 ZTX107/8 11 2N3819 22 40313 130 BC159 11 BF178 30 MPSA56 30 ZTX107/8 11 2N3819 22 40313 130
<b>9p</b> ; 125 12 <b>p</b> ; 220 13 <b>p</b> ; 470 20 <b>p</b> ; 680 34 <b>p</b> ; 1000 27 <b>p</b> ; 1500 31 <b>p</b> ; 2200 36 <b>p</b> ; 3300 74 <b>p</b> ; 4700 79 <b>p</b> .	8C167A 10 BF180 38 MPSU02 58 ZTX212 28 2N3822/3 65 40316 95 8C167A 10 BF180 38 MPSU02 58 ZTX300 13 2N3866 90 40317/20 60 8C168C 10 BF194/5 12 MPSU05 55 ZTX301/2 16 2N3903/4 18 40360 40
TAG-END TYPE: 70V: 4700 245p; 64V: 3300 198p; 2200 139p; 50V: 3300 154p; 2200 110p; 40V: 4700 160p; 25V: 10,000 320p; 15,000 345p.	BC169C         10         BF196/7         12         MPSU06         55         ZTX303         25         2N3905         15         40381/62         50           BC170         15         BF198         16         MPSU52         65         ZTX304         17         2N3905         15         40381/62         50           BC171/2         11         BF199         18         MPSU55         60         ZTX314         25         2N3905         15         40301/62         50
35V: 0-1µ, 0-22, 0-33 15p; 0-47, 0-68, 1, 0, 1-5 16p; 2-2, 3-3 18p; 4-7, 6-8 5000 1K 8-2V // IN/ ONLY) Sincle 28p	8C173 11 BF200 30 MPSU56 60 ZTX320/26 30 2N4058 10 40408 70 BC177/8 20 BF224 25 OC33/26 170 ZTX341 30 ZNA061/2 10 40411 285 BC179 20 BF244 28 OC39 20 2TX341 30 ZNA061/2 10 40411 285
22p; 10 28p; 16V; 2:2, 3:3,16p; 4-7. 6:8, 10 18p; 15 36p; 22 30p; 33, 47 40p; 100 75p; 220 88p 10V; 15, 22 5KG-2MΩ single gang 29p 5KG-2MΩ single gang 29p	BC181         20         BF2448         29         OC36         120         ZTX502/3         18         ZN4427         80         40467         130           BC182/3         10         BF2468         35         OC41/42         120         ZTX502/3         18         ZN4427         80         40467         130           BC182/3         10         BF2568         35         OC41/42         120         ZTX504         25         ZN4859         78         40468         85
26p; 33, 47 35p; 100 55p.         Shipe Wind dang and stered         add           MYLAR FILM CAPACITORS:         Shipe potterioustrees         Shipe potterioustrees	BC182L         10         BF23/8         32         OC45/70         40         Z1X531         25         ZM871         55         40594         105           BC182L         10         BF274         42         OC71/72         40         ZN550         25         ZN5135/6         20         40595         110           BC182L         10         BF274         42         OC71/72         40         ZN596         30         ZN5138         18         40603         110
100V: 1nF, 2n, 4n, 4n7, 10 6p; 15nF, 22n, 30n, 40, 47 7p; 56, 100n, 200 9p; 470n/50V: 12p. 0.25W log and linear values 60mm track 5KΩ, 500KΩ Single gang 70p	BC184         10         BF336         40         0C76         50         2N697         23         2N517         18         40636         175           BC187         26         BF451         35         0C81/82         50         2N698         40         2N5179         45         40673         95           BC212/3         10         BF594         30         0C83/84         40         2N699         48         2N5180         45
CERAMIC CAPACITORS: (50V) Range: 0-5pf to 10pF 40p	BC2 12L         10         BF595         39         OC170/1         85         2N706A         19         2N5191         75         Access           BC2 13L         10         BFR39/40         23         OC200         85         2N708         19         2N5191         75         Access           BC2 13L         10         BFR39/40         23         OC200         85         2N708         19         2N5194         80           BC2 14         10         BFR34/79         23         TIP30A         86         2N916/5         32         2N5205         34
15nř, 22nř, 33nř, 47nř         5p         PRESET POTENTIOMETERS           100nř/30V         7p;         220nř/6V         8p           0.1W 50Ω-2-2M Mini Vert. & Horiz,         7p           0.2W 10ΩΩ-33MQ Horiz Jarrer         100	BC2 14L         10         8FR80/81         25         TIP29C         60         2N918         35         2N5457         36           BC2 37/8         14         BFR98         105         TIP30A         48         2N930         20         2N5457         36
POLYSTYRENE CAPACITORS:         0.25W 250Ω.4.7MΩ Vert.         10p           10pF to 1nF 8p         1.5nF to 12nF 10p.         Precision Cermet 1W 100Ω-100K         80p.	MC14412 800 7438 27 74170 168 LS40 16 LS251 40 4024 45 4440 999 MM52800 695 7440 17 74172 290 LS42 35 LS253 40 4025 19 4450 350 R0-32513 700 7441 68 LS47 40 LS253 40 4025 19 4450 350
SILVER MICA (pF) 2,3-3,4-7,6-8,8-2,10. SIEMENS multilayer miniature capacitors. Miniature High Stability, Low Noise.	RO-3-2513U 600 7442 38 74174 72 LS48 80 LS258 40 4027 38 4490 350 SFF936364E 800 7443 90 74175 72 LS49 60 LS259 85 4028 58 4500 675
47, 50, 56, 68, 75, 82, 3n3, 4n7, 6n8, 8n2, RANGE Val, 1-99 100+ 85, 100, 120, 150, 180 10n, 12n, 15n, 22n 7p 0-25W202-4-M7 E24 2p 1p	TMS2716-3V 8757445 65 74177 75 LS54 15 LS261 195 4029 77 4501 28 TMS2716-3V 8757445 65 74177 75 LS54 15 LS26 25 4030 50 4502 90 TMS6011 3657446 55 74178 95 LS55 30 LS273 90 4031 170 4503 50
15peach 18n, 27n, 33n, 47n 8p 0.5W 2Ω2-4·M7 E12 2p 1p 220, 250, 270, 330, 39n, 56n, 68n 9p 1W 2Ω2-10M E12 5p 3p 360, 390, 470, 600, 100V:100n 120n 10p: 2% Metal Film 100-1M 6p 4p	ULN2003 100/747 50 74179 68 L563 150 L5275 290 4032 125 4504 105 UP080C35C £11 748 50 74180 65 L573 25 L5279 88 4033 165 4506 65 .280CPU 2-5 360 7450 16 74181 140 L574 25 L5280 250 4034 195 4507 40
800 & 820pF 21p each 1000, 1200, 1800 300 a 80 b 89 470n 23p; 1000, 1200, 1800 300 a 80 b 89 470n 23p; 1000, 1200, 1800	280ACPU 4M 550 7451 16 74182. 75 LS75 28 LS283 45 4036 95 4508 265 280 P10 350 7453 16 74184 99 LS76 20 LS290 57 4036 275 4510 68 280A P10 400 7454 16 74185 99 LS78 24 LS29 346 4037 115 4511 68
3300, 4700 60 p each $2\mu 2$ 50 p. LINEAR IC's MAKEN 200 52 TO 100 TO 10	Z80 CTC 350 7460 16 74188 290 LS83 50 LS295 215 4038 110 4512 75 Z80A CTC 400 7470 35 74190 70 LS85 70 LS298 130 4039 290 4513 199 Z80A 200 200 200 200 200 200 200 200 200 4513 199
555 CMOS 80 ICM7217A 790 MM5303 635 IDA1034 350 CMOS RAM 855 702 75 ICM7224 785 MM5307 1275 IDA1034 350 CMOS RAM 855 702 75 ICM755 80 MM5307 1275 IDA1040 290 6502 CPU 450	280510-1 £15/7473 30 74192 70 LS90 35 LS300 175 4041 78 4515 198 280A510 £23/7474 25 74193 65 LS91 80 LS302 175 4042 60 4516 75
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CA3081 190 M252AA 625 TAA661A 155 1702 299 8228 250 CA3086 48 M253AA 1150 TAA700 250 1802CP 750 8251 400 CA30887 215 MC1303 88 TAA700 250 2101-2	7408         16         74143         250         LS05         15         LS183         275         4000         14         4096         90         4585         99           7409         16         74144         250         LS06         15         LS190         58         4001         14         4096         90         4585         99           7409         16         74144         250         LS06         15         LS190         58         4001         14         4097         320         4597         330
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CA3140 48 MC1458 40 TBA5500 330 2118-320 T26 99 CA3140 95 MC1458 40 TBA5500 330 2118-3 250 8727 150 CA3160 95 MC1494 694 TBA641-A12/	7413         24         74150         601         511         15         15194         40         4008         62         14161         99         40100         215           7414         32         74151         45         LS12         15         LS195         40         4009         35         4162         99         40101         130           7416         25         74153         45         LS13         30         LS196         58         4010         4014163         99         40102         180
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8T28A	1.50	4013	0.58	741,500	0.11	74LS192	0.69
8T95	1.50	4015	0.58	74LS01	0.11	74LS193	0.59
8T97A	1.50	4016	0.28	74LS02	0.12	74LS194	0.39
8T98	1.50	4017	0.45	74LS03	0.12	74LS195	0.39
		4018	0.58	74LS04	0.13	74L\$196	0.58
DATA CONVE	RTERS		_			74L\$197	0.65
ZN425E-8	3.50	CDEC	141	OFFE	29	74LS221	0.60
ZN426E-8	3.00	J SPEC	IAL	OFFER	10	74LS240	0.89
ZN427E-8	0.28	I M	EMO	DRIES		74LS241	0.89
ZN428E-0	4.78					7415242	0.79
ZN429E-0	28.00	2114L200 ns	0.99	4116 150 ns	0.75	7415243	0.79
ZN433C 1-10	22 50	2708 450 ns	1.48	4116 200 ns	0.67	741 5245	0.70
2N440	56.63	2716 450 nS	2.10	6116 200 ns	0.50	741 5247	1.34
214440	30.05	2532 450 ns	3.99	5516 200 ns	3 85	741 5248	1.00
MISCELLANE	OUS	2132 430 115	9.20	4110 200 115	0.00	74LS249	0.68
AY-3-1015	3.90	OFFERVA	LID FRC	M DEC 1st-JA	N1st	74LS251	0.39
AY-5-1013	3.45	SUBJ	ECT TO	AVAILABILITY	-	74LS253	0.39
AY-5-2376	6.95		-	_	_	74LS257	0.44
MC1488	0.64	4019	0.29	74LS05	0.13	74LS258	0.38
MC1489	0.64	4020	0.58	74LS08	0.13	74L\$259	0.38
MC14411	6.94	4021	0.60	74LS09	0.13	74LS261	1.90
MC14412	7.99	4022	0.62	74LS10	0.13	74LS266	0.23
R0-3-2513L	7 70	1023	0.17	741011	014	1 7415273	0.90
	1.10	4023		74L511		1400210	
R0-3-2513U	7.70	4024	0.38	74LS12	0.15	74LS279	0.34
R0-3-2513U ZN450E	7.70	4024 4025	0.38	74LS11 74LS12 74LS13	0.15	74LS279 74LS283	0.34
R0-3-2513U ZN450E 7805	7.70 7.61 0.50	4024 4025 4026	0.38 0.18 0.99	74LS11 74LS12 74LS13 74LS14	0.15 0.22 0.44	74LS279 74LS283 74LS290	0.34 0.44 0.56
R0-3-2513U ZN450E 7805 7812	7.70 7.61 0.50 0.50	4024 4025 4026 4027	0.38 0.18 0.99 0.30	74LS11 74LS12 74LS13 74LS14 74LS15	0.15 0.22 0.44 0.13	74LS279 74LS283 74LS290 74LS293 74LS293	0.34 0.44 0.56 0.45
R0-3-2513U ZN450E 7805 7812 7905 7012	7.70 7.61 0.50 0.50 0.55	4024 4025 4026 4027 4028	0.38 0.18 0.99 0.30 0.55	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20	0.15 0.22 0.44 0.13 0.12	74LS279 74LS283 74LS290 74LS293 74LS265	0.34 0.44 0.56 0.45 0.34
R0-3-2513U ZN450E 7805 7812 7905 7912	7.70 7.61 0.50 0.50 0.55 0.55	4024 4025 4026 4027 4028 4031 4031	0.38 0.18 0.99 0.30 0.55 1.65	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS21	0.15 0.22 0.44 0.13 0.12 0.14	74LS279 74LS283 74LS290 74LS293 74LS365 74LS365 74LS366	0.34 0.44 0.56 0.45 0.34 0.34
R0-3-2513U ZN450E 7805 7812 7905 7912 ZU 06 280 5	7.70 7.61 0.50 0.50 0.55 0.55	4024 4025 4026 4027 4028 4031 4033 4034	0.38 0.18 0.99 0.30 0.55 1.65 1.60	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS22 74LS25	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.18	74LS279 74LS283 74LS290 74LS293 74LS365 74LS365 74LS367 74LS367	0.34 0.44 0.56 0.45 0.34 0.34 0.34
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG 280 Fr 280 CPU	7.70 7.61 0.50 0.50 0.55 0.55 AMILY 4.00	4024 4025 4026 4027 4027 4028 4031 4033 4034 4035	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS26 74LS27	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14	74LS279 74LS283 74LS290 74LS293 74LS365 74LS366 74LS366 74LS368 74LS373	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG Z80 Fr Z80 CPU Z80A CPU	7.70 7.61 0.50 0.55 0.55 AMILY 4.00 4.82	4024 4025 4026 4027 4028 4031 4033 4034 4035 4040	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57	74LS11 74LS12 74LS13 74LS15 74LS20 74LS21 74LS22 74LS26 74LS27 74LS28	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.18 0.14 0.19	74LS279 74LS283 74LS290 74LS293 74LS365 74LS365 74LS365 74LS367 74LS368 74LS373	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG Z80 Fi Z80 CPU Z80A CPU Z80 CTC	7.70 7.61 0.50 0.50 0.55 0.55 AMILY 4.00 4.82 4.00	4024 4025 4026 4027 4028 4031 4033 4034 4034 4035 4040 4041	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.69	74LS12 74LS12 74LS13 74LS15 74LS20 74LS20 74LS20 74LS26 74LS26 74LS28 74LS28 74LS28 74LS28	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.18 0.14 0.19 0.12	74L5279 74L5283 74L5290 74L5293 74L5365 74L5365 74L5367 74L5367 74L5368 74L5373 74L5374	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.74
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG Z80 F- Z80 CPU Z80 CPU Z80 CPU Z80 CCU Z80 A CTC	7.70 7.61 0.50 0.55 0.55 0.55 AMILY 4.00 4.82 4.00 4.82	4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.69 0.54	74LS12 74LS12 74LS13 74LS15 74LS20 74LS20 74LS20 74LS26 74LS26 74LS26 74LS28 74LS30 74LS30 74LS32	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.18 0.14 0.19 0.12 0.14	74LS279 74LS283 74LS290 74LS293 74LS365 74LS365 74LS368 74LS368 74LS373 74LS374 74LS375 74LS375	0.34 0.46 0.56 0.34 0.34 0.34 0.34 0.34 0.34 0.74 0.74 0.47
R0-3-2513U ZN450E 7805 7812 7905 7912 ZIL06 Z80 Fi Z80 CPU Z80A CPU Z80A CPU Z80A CTC Z80 DART	7.70 7.70 7.61 0.50 0.55 0.55 0.55 AMILY 4.00 4.82 4.00 4.82 4.00 4.00 7.18	4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4041 4042 4043	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.69 0.54 0.59	74LS12 74LS12 74LS13 74LS15 74LS25 74LS21 74LS22 74LS22 74LS28 74LS28 74LS30 74LS32 74LS33	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.18 0.14 0.19 0.12 0.14 0.16	74L5279 74L5283 74L5283 74L5290 74L5305 74L5365 74L5366 74L5366 74L5373 74L5373 74L5373 74L5375 74L5375	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG Z80 F Z80A CPU Z80A CPU Z80A CPU Z80A ACTC Z80A AART Z80A DART	7.70 7.61 0.50 0.55 0.55 0.55 AMILY 4.00 4.82 4.00 4.82 4.00 4.00 7.18 7.18	4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042 4043 4044	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.57 0.54 0.59 0.54	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS26 74LS27 74LS28 74LS30 74LS33 74LS33 74LS33	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.12 0.14 0.12 0.14 0.16 0.16	74L5279 74L5283 74L5283 74L5290 74L5390 74L5365 74L5366 74L5366 74L5373 74L5373 74L5375 74L5375 74L5375 74L5375	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89 0.84
R0-3-2513U ZN450E 7805 7812 7905 7912 Z80 CPU Z80 CPU Z80 CPU Z80 CPU Z80 A CPU Z80 A CPU Z80 A CTC Z80 DART Z80 DART Z80 DART	7.70 7.70 7.61 0.50 0.55 0.55 0.55 AMILY 4.00 4.82 4.00 4.82 4.00 4.82 4.00 7.18 7.18 11.52	4024 4025 4026 4027 4031 4033 4033 4034 4035 4040 4041 4042 4043 4043 4045 4045	0.38 0.18 0.99 0.30 0.55 1.65 1.55 0.72 0.57 0.59 0.54 0.59 0.64 1.65	74LS12 74LS12 74LS13 74LS15 74LS25 74LS26 74LS22 74LS26 74LS27 74LS27 74LS30 74LS32 74LS33 74LS33 74LS33 74LS33 74LS37	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.18 0.14 0.19 0.12 0.14 0.19 0.12 0.14 0.16 0.16 0.16	74L5279 74L5283 74L5293 74L5293 74L5365 74L5365 74L5365 74L5366 74L5366 74L5366 74L5376 74L5377 74L5377 74L5377 74L5377 74L5377 74L5378	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89 0.89 0.84 0.28
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILDG Z80 F. Z80 CPU Z80 CPU Z80 A CPU Z80 A CPU Z80 A CTC Z80 DART Z80 DART Z80 MA Z80 A DMA	7.70 7.61 0.50 0.55 0.55 0.55 AMILY 4.00 4.82 4.00 4.82 4.00 7.18 11.52 9.99 7.70	4024 4025 4026 4027 4031 4031 4033 4033 4034 4035 4040 4041 4042 4043 4044 4043 4044 4045 4046 4047	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.69 0.54 1.65 0.64 1.65 0.64	74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS22 74LS22 74LS26 74LS28 74LS32 74LS32 74LS32 74LS33 74LS33 74LS33 74LS38	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.13	74LS279 74LS283 74LS290 74LS290 74LS365 74LS365 74LS366 74LS367 74LS376 74LS377 74LS377 74LS377 74LS377 74LS377 74LS377 74LS378 74LS378 74LS378 74LS378	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89 0.89 0.84 0.28 0.59
R0-3-2513U ZN450E 7805 7812 7905 7912 200 CPU 280 CPU 280 A CPU 280 A CPU 280 A CPU 280 A A CPU 280 A A A A 280 A DART 280 A DART 28	7.70 7.761 0.50 0.55 0.55 0.55 <b>AMILY</b> 4.00 4.82 4.00 7.18 7.18 7.18 11.52 9.99 3.78	4024 4025 4026 4027 4028 4033 4034 4033 4034 4040 4044 4042 4042	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.69 0.54 0.54 0.64 1.85 0.68 0.64	74LS12 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS22 74LS27 74LS28 74LS28 74LS33 74LS33 74LS33 74LS33 74LS34 74LS34 74LS34 74LS34 74LS34 74LS34	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.13 0.34 0.20	74LS279 74LS283 74LS293 74LS293 74LS365 74LS365 74LS365 74LS367 74LS375 74LS375 74LS377 74LS377 74LS379 74LS390 74LS393	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89 0.89 0.84 0.28 0.59 0.59
R0-3-2513U ZN450E 7805 7812 7905 7912 200 CPU 280 CPU 280 CPU 280 A CPU 280 A CPU 280 A CTC 280 DART 280 A DART 280 A DART 280 A DMA 280 A PI0 280 A PI0 280 A PI0	7.70 7.70 0.50 0.50 0.55 0.55 <b>AMILY</b> 4.00 4.00 4.00 4.00 7.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	4024 4025 4026 4027 4028 4031 4033 4034 4034 4040 4041 4043 4044 4045 4046 4047 4048 4049	0.38 0.18 0.99 0.30 0.55 1.65 1.65 1.55 0.72 0.57 0.57 0.59 0.54 0.54 0.54 0.54 0.58 0.68 0.68 0.68 0.68	74LS12 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS22 74LS22 74LS26 74LS28 74LS32 74LS32 74LS33 74LS33 74LS37 74LS38 74LS38 74LS42 74LS42 74LS42	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.16 0.16 0.16 0.13 0.34 0.34 0.34	74LS279 74LS283 74LS283 74LS290 74LS290 74LS365 74LS365 74LS365 74LS367 74LS375 74LS374 74LS377 74LS377 74LS378 74LS379 74LS386 74LS393 74LS393 74LS393	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.74 0.74 0.89 0.89 0.89 0.84 0.28 0.59
R0-3-2513U ZN450E 7805 7812 7905 7912 ZILDG Z80 F- Z80 CPU Z80 A CPU Z80 A CPU Z80 A CTC Z80 A DART Z80 A DART Z80 A DAR Z80 A DMA Z80 A PIO Z80 SI0-0 Z80 SI0-0	7.70 7.71 0.50 0.55 0.55 <b>AMILY</b> 4.00 4.82 4.00 7.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4042 4044 4042 4044 4044 4046 4046	0.38 0.18 0.99 0.30 0.55 1.65 0.75 0.57 0.57 0.59 0.54 1.65 0.64 1.65 0.64 1.65 0.68 0.58 0.54 0.54 0.30	74LS11 74LS12 74LS13 74LS14 74LS25 74LS20 74LS20 74LS22 74LS22 74LS26 74LS22 74LS32 74LS32 74LS32 74LS33 74LS33 74LS33 74LS34 74LS42 74LS48	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.12 0.14 0.12 0.14 0.16 0.16 0.16 0.13 0.34 0.39 0.60	7415279 7415283 7415283 7415290 7415293 7415365 7415365 7415365 7415367 7415377 7415377 7415377 7415375 7415376 7415339 7415339 7415339 7415339 7415339	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.47 0.89 0.89 0.89 0.84 0.28 0.59
R0-3-2513U ZN450E 7805 7812 7905 7912 ZUDG Z80 FP Z80 CPU Z80A CPU Z80A CPU Z80A CPU Z80A CPU Z80A CTC Z80A DART Z80A DART Z80A DART Z80A DART Z80A DART Z80A DART Z80A DART Z80A DART Z80A DIO Z80A SI0-0 Z80 SI0-1	7.70 7.61 0.50 0.55 0.55 <b>AMILY</b> 4.00 4.00 4.00 4.00 4.00 4.00 7.18 7.18 11.52 9.99 3.78 3.78 13.95 13.95	4024 4025 4026 4027 4028 4031 4033 4033 4034 4035 4040 4041 4044 4042 4044 4044 4044 4044	0.38 0.18 0.99 0.30 0.55 1.65 1.60 1.55 0.72 0.57 0.54 0.54 0.64 1.65 0.64 1.65 0.64 0.64 0.64 0.68 0.54 0.30 0.30	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS26 74LS28 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.19 0.12 0.16 0.16 0.16 0.16 0.13 0.34 0.60 0.59 0.60	74LS279 74LS283 74LS283 74LS293 74LS365 74LS365 74LS365 74LS365 74LS367 74LS375 74LS373 74LS373 74LS377 74LS377 74LS378 74LS379 74LS386 74LS390 74LS377 74LS777 74LS7777777777777777777777777	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.47 0.89 0.89 0.89 0.84 0.259 0.59
R0-3-2513U           ZN450E           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7805           7807           2800 A CPU           2800 A DART           2800 A DMA           2800 A DMA           2800 A DMA           2800 A DMA           2800 A SIO-0           2803 SIO-0           2803 SIO-1	7.70 7.71 0.50 0.50 0.55 0.55 0.55 4.82 4.00 4.00 4.82 4.00 7.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	4024 4025 4026 4027 4031 4033 4033 4033 4034 4034 4040 4041 4042 4044 4044 4045 4046 4045 4046 4047 4048 4049 4050 4050	0.38 0.18 0.99 0.30 0.55 1.65 1.55 0.72 0.57 0.69 0.54 0.54 0.59 0.64 1.65 0.68 0.68 0.54 0.30 0.30 0.59	74LS11 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS22 74LS26 74LS27 74LS28 74LS33 74LS33 74LS33 74LS33 74LS33 74LS34 74LS48 74LS49 74LS51	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.14	74LS279 74LS283 74LS283 74LS290 74LS290 74LS365 74LS365 74LS365 74LS367 74LS375 74LS377 74LS375 74LS377 74LS379 74LS385 74LS390 74LS385 74LS393 LDW PROFILE C SOCKTS Number of Pars 8	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.74 0.74 0.89 0.89 0.89 0.84 0.59 0.59
R0-3-2513U           ZN450E           7805           7805           7812           ZB0 CPU           Z80 CPU           Z80 CPU           Z80 CTC           Z80A CPU           Z80 A CPU           Z80 A CTC           Z80A A CPU           Z80 A CTC           Z80A A CTC           Z80A A CPU           Z80A A CPU           Z80A A CPU           Z80A A CPU           Z80A A DART           Z80A JMA           Z80 A J00           Z80A SI0-0           Z80 A SI0-1           Z80A SI0-1           Z80A SI0-2	7.70 7.71 0.50 0.50 0.55 0.55 0.55 0.55 0.55 4.00 4.00	3024         3024           4025         4026           4027         4027           4028         3031           4033         4034           4035         4040           4042         4042           4044         4044           4045         4046           4046         4047           4045         4051           4051         4051           4051         4052	0.38 0.18 0.99 0.30 0.55 1.65 1.55 0.72 0.57 0.69 0.54 1.65 0.59 0.64 1.65 0.68 0.68 0.68 0.54 0.30 0.30 0.30 0.59	74LS12 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS27 74LS28 74LS28 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS48 74LS48 74LS49 74LS49 74LS55	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.14	741.5279 741.5283 741.5290 741.5290 741.5365 741.5365 741.5366 741.5367 741.5376 741.5377 741.5377 741.5377 741.5377 741.5377 741.53370 741.53390 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5393 741.5396 741.5396 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.5376 741.53777 741.53777 741.53777777777777777777777777777777777777	0.34 0.46 0.56 0.45 0.34 0.34 0.34 0.74 0.74 0.89 0.89 0.84 0.28 0.59 0.59
R0-3-2513U           ZN450E           7805           7805           7912           ZIL0G ZB0 F:           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A CTC           Z80A A CPU           Z80A A CTC           Z80A DART           Z80A DMA           Z80A DMA           Z80A DMA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2	7.70 7.71 0.50 0.50 0.55 0.55 0.55 4.00 4.00 4.00	4024 4025 4026 4027 4028 4031 4033 4034 4034 4034 4040 4041 4042 4043 4044 4045 4046 4047 4048 4047 4048 4047 4049 4050 4051 4052 4053	0.38 0.19 0.30 0.55 1.65 1.65 1.65 1.65 0.72 0.57 0.54 0.54 0.54 0.54 0.68 0.68 0.54 0.30 0.30 0.59 0.68 0.59 0.68 0.59	74LS11 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS22 74LS26 74LS28 74LS32 74LS32 74LS32 74LS32 74LS38 74LS38 74LS38 74LS37 74LS38 74LS35 74LS55 74LS55	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.12 0.14 0.12 0.14 0.16 0.16 0.16 0.13 0.34 0.34 0.34 0.5 0.514 0.15 0.514	74LS279 74LS283 74LS283 74LS293 74LS293 74LS365 74LS365 74LS365 74LS365 74LS375 74LS375 74LS377 74LS377 74LS377 74LS379 74LS385 74LS390 74LS390 74LS393 LOW PROFILE 0 SOCKETS Number of Pins 8 14 16	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.74 0.89 0.89 0.89 0.89 0.89 0.89 0.89 0.59 0.59
R0-3-2513U           ZN450E           7805           7805           7812           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A ACU           Z80A AUART           Z80A DART           Z80A DART           Z80A DART           Z80A NMA           Z80A NMA           Z80A SI0-0           Z80A SI0-0           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2	7.70 7.71 0.50 0.50 0.55 0.55 <b>AMILY</b> 4.00 4.85 4.00 4.00 7.18 11.55 7.18 11.55 3.78 3.78 3.78 3.395 13.95 13.95 13.95	1024           1024           1025           1026           1027           1028           1031           1033           1034           1035           1041           1042           4043           4044           4044           4044           4044           4045           4046           4047           4048           4049           4051           4052           4053           4053           4054           4053           4054           4055	0.38 0.19 0.30 0.55 1.65 1.65 1.60 1.55 0.72 0.54 0.54 0.54 0.54 0.64 1.65 0.64 1.65 0.68 0.58 0.30 0.30 0.30 0.59 1.20	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS30 74LS30 74LS30 74LS33 74LS33 74LS37 74LS40 74LS40 74LS47 74LS45 74LS55 74LS57 74LS57	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.13 0.34 0.34 0.59 0.59 0.15 0.59 0.15 0.22	7415273 7415283 7415290 7415290 7415365 7415365 7415366 7415366 7415366 7415367 7415377 7415377 7415375 7415375 7415376 7415376 7415377 7415377 741577 741577 741577 741577 741577 741577 741577 7415777 7415777 7415777 7415777 7415777 7415777 7415777 7415777 7415777 7415777 7415777 7415777 74157777 74157777 74157777777777	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7912           ZULGG Z80 FR           Z80 CPU           Z80A ACTC           Z80A SI0-0           Z80A SI0-1           Z80A SI0-2           EPCIS 6800	7.70 7.71 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024           4025           4026           4027           4028           4031           4033           4034           4035           4040           4041           4042           4044           4045           4046           4047           4045           4046           4047           4045           4045           4051           4052           4051           4052           4053           4054           4055           4055           4056           4055           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056	0.38 0.19 0.30 0.55 1.65 1.65 1.65 0.72 0.57 0.54 0.54 0.54 0.64 1.65 0.68 0.88 0.54 0.30 0.30 0.30 0.59 0.68 0.59 0.59 0.59 0.59 0.59 0.59	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS22 74LS26 74LS26 74LS28 74LS38 74LS32 74LS37 74LS37 74LS37 74LS47 74LS48 74LS51 74LS55 74LS55 74LS75	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.12 0.14 0.12 0.14 0.12 0.16 0.16 0.16 0.16 0.13 0.34 0.59 0.59 0.59 0.14 0.15 0.20 0.59 0.14 0.15 0.20 0.20 0.20 0.20 0.20 0.20 0.20 0.2	74LS279 74LS283 74LS283 74LS293 74LS365 74LS365 74LS365 74LS365 74LS367 74LS375 74LS373 74LS373 74LS377 74LS377 74LS377 74LS378 74LS379 74LS393 LDW PROFILE 0 SOCKETS Number of Pris 8 4 16 18 18	0.34 0.44 0.45 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7805           7905           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A ACTC           Z80A DART           Z80A DART           Z80A DART           Z80A DART           Z80A DART           Z80A PI0           Z80A NO-0           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2           Z80A SI0-2           Z80A SI0-2           Z80A SI0-2           Z80A SI0-2	7.70 7.71 0.50 0.50 0.55 <b>AMILY</b> 4.00 4.00 4.00 4.00 4.00 4.00 4.00 4.0	4024 4025 4026 4027 4028 4031 4033 4034 4034 4034 4040 4041 4044 4043 4044 4045 4046 4047 4048 4050 4055 4055 4055	0.38 0.19 0.30 0.55 1.65 1.65 1.55 0.72 0.57 0.59 0.54 0.54 0.54 0.54 0.30 0.30 0.30 0.30 0.30 0.30 0.30 0.3	74LS11 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS22 74LS26 74LS27 74LS28 74LS33 74LS33 74LS33 74LS37 74LS37 74LS38 74LS47 74LS55 74LS55 74LS55 74LS55 74LS55 74LS73 74LS75 74LS75	0.15 0.224 0.44 0.13 0.14 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	74LS279 74LS283 74LS283 74LS290 74LS290 74LS365 74LS365 74LS365 74LS365 74LS375 74LS377 74LS377 74LS378 74LS379 74LS395 74LS397 74LS395 74LS395 74LS397 74LS395 74LS397 74LS377 74LS397 74LS39	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.74 0.34 0.74 0.89 0.89 0.59 0.59 0.59 0.59 0.59 0.59 0.59 0.5
R0-3-2513U           ZN450E           7805           7805           7805           7905           7912           280A CPU           280A DART           280A DART           280A DMA           280A SI0-0           280A SI0-0           280A SI0-1           280A SI0-1           280A SI0-2           EFCE 8800           EFCE 8802	7.70 7.71 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	024         024           0225         4026           4027         4027           4028         4031           4033         4034           4035         4040           4042         4041           4042         4042           4044         4044           4045         4046           4047         4045           4051         4055           4051         4052           4055         4053           4055         4060           4066         6	0.38 0.18 0.99 0.35 1.65 1.65 1.55 0.72 0.57 0.69 0.54 0.59 0.64 0.30 0.59 0.68 0.68 0.54 0.30 0.59 0.59 0.59 0.59 0.59 0.59 0.59 0.5	74LS12 74LS12 74LS13 74LS14 74LS15 74LS21 74LS20 74LS20 74LS22 74LS28 74LS28 74LS28 74LS38 74LS38 74LS38 74LS38 74LS38 74LS39 74LS35 74LS55 74LS55 74LS55 74LS78 74LS76 74LS76 74LS78	0.15 0.22 0.24 0.13 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.14	7415279 7415283 7415293 7415293 7415365 7415365 7415365 7415365 7415376 7415375 7415377 7415377 7415377 7415377 7415377 74153376 74153393 74153393 74153393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415375 7415775775 74157757757757757757777777777	0.34 0.46 0.45 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7912           ZB0 CPU           Z80A CTC           Z80A A DMA           Z80A A DMA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2           EFC15 6800           EF6802           EF6803           FF8803	7.70 7.71 0.50 0.50 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4028 4031 4033 4033 4034 4035 4040 4041 4042 4044 4044 4045 4046 4047 4048 4047 4048 4047 4049 4050 4051 4055 4055 4055 4055 4055 4066 4068	0.38 0.18 0.99 0.35 1.65 1.65 1.65 1.75 0.57 0.57 0.59 0.64 0.59 0.64 0.59 0.68 0.59 0.68 0.59 0.68 0.59 0.59 0.59 0.59 0.59 0.59 0.59 0.59	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS22 74LS22 74LS28 74LS32 74LS32 74LS32 74LS32 74LS32 74LS37 74LS37 74LS35 74LS35 74LS55 74LS55 74LS55 74LS55 74LS73 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS77 74LS77 74LS77 74LS76 74LS77 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.18 0.14 0.19 0.12 0.12 0.16 0.16 0.16 0.13 0.34 0.34 0.30 0.50 0.14 0.12 0.12 0.12 0.15 0.12 0.12 0.12 0.12 0.12 0.12 0.12 0.12	74LS279 74LS283 74LS283 74LS293 74LS293 74LS365 74LS365 74LS365 74LS367 74LS375 74LS375 74LS377 74LS377 74LS377 74LS379 74LS379 74LS390 74LS377 74LS376 74LS377 74LS37	0.34 0.45 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7902           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A ACC           Z80A ANA           Z80A NMA           Z80A NMA           Z80A NO           Z80A SIO-1           Z80A SIO-1           Z80A SIO-1           Z80A SIO-2           EF6800           EF6803           EF6803           EF6803           EF6803           EF6803	7.70 7.71 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	3024         4024           4025         4026           4027         4028           4033         4034           4035         4040           4041         4042           4044         4044           4045         4044           4046         4047           4046         4047           4051         4053           4055         4060           4055         4053           4055         4060           4055         4060           4056         4056           4058         4056           4058         4056	0.38 0.18 0.99 0.35 1.65 1.65 1.65 1.55 0.72 0.57 0.69 0.64 0.59 0.64 1.65 0.68 0.68 0.68 0.30 0.30 0.30 0.30 0.59 1.20 1.20 0.59 1.20 0.59 1.20 0.55 0.59 0.59 0.59 0.59 0.59 0.59 0.5	74LS12 74LS12 74LS13 74LS15 74LS14 74LS25 74LS26 74LS27 74LS27 74LS28 74LS28 74LS30 74LS30 74LS33 74LS33 74LS33 74LS37 74LS37 74LS54 74LS55 74LS55 74LS75 74LS76 74LS77 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.13 0.34 0.50 0.50 0.50 0.14 0.15 0.20 0.20 0.20 0.20 0.20 0.20 0.20 0.2	7415273 7415283 7415290 7415290 7415365 7415365 7415365 7415366 7415367 7415377 7415377 7415377 7415377 7415375 7415375 74153376 74153375 74153375 74153375 74153390 74153390 7415390 7415390 7415390 7415390 7415390 7415390 7415390 7415390 7415375 74153275 74154275 7415475 7415475 7415475 7415475 7415475 7415475 7415475 7415475 7415475 7415475 741547575 7415475 7415475 741575775 7415475775 7415775775 74157757757757777777777	0.34 0.45 0.45 0.45 0.34 0.34 0.34 0.74 0.74 0.89 0.89 0.59 0.59 0.59 0.59 0.59 0.15 0.07 0.09 0.09 0.15 0.17 0.21 0.25 0.22 0.29
R0-3-2513U           ZN450E           7805           7805           7805           7912           ZB0 CPU           Z80 CPU           Z80 CPU           Z80 CTC           Z80 A CPU           Z80 A DART           Z80 A DAR           Z80 A DAR           Z80 A DAR           Z80 A SI0-0           Z80 A SI0-1           Z80 A SI0-1           Z80 A SI0-2           Z80A SI0-2           Z80A SI0-2           EF6802           EF6803           EF6804           EF6805           EF6810           EF6810           EF6810	7.70 7.71 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	1024           1024           1025           1026           1027           1028           1031           1033           1034           1035           1040           1041           4041           4042           4044           4045           4046           4047           4045           4046           4047           4051           4052           4051           4052           4053           4054           4055           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4056           4057	0.38 0.99 0.30 1.60 1.55 0.55 0.55 0.57 0.57 0.54 0.54 0.54 0.54 0.54 0.54 0.54 0.54	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS28 74LS30 74LS32 74LS33 74LS37 74LS37 74LS37 74LS35 74LS55 74LS55 74LS75 74LS75 74LS75 74LS75 74LS76 74LS77 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.19 0.12 0.12 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	74LS279 74LS283 74LS283 74LS290 74LS290 74LS365 74LS365 74LS365 74LS366 74LS375 74LS375 74LS373 74LS377 74LS377 74LS377 74LS378 74LS379 74LS393 LDW PROFILE 0 SOCKETS Number of Pros 8 4 16 18 18 20 22 24 28 40 CRESTALS	0.34 0.44 0.45 0.34 0.34 0.34 0.34 0.34 0.74 0.74 0.74 0.74 0.28 0.59 0.59 0.59 0.59 0.59 0.59 0.15 0.09 0.15 0.17 0.09 0.23 0.25 0.23 0.25 0.23
R0-3-2513U           ZN450E           7805           7805           7805           7805           7905           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A A CPU           Z80A A CTC           Z80A DART           Z80A DART           Z80A DART           Z80A DART           Z80A DART           Z80A DART           Z80A ANA           Z80A NO-10           Z80A SIO-2           EF6803           EF6803           EF6803           EF6804           EF6810           EF6821           E6804	7.70 7.71 0.50 0.50 0.55 0.55 AMILY 4.00 4.00 4.00 4.00 4.00 4.00 4.00 7.18 7.18 7.18 13.95 13.10 13.1	1024           1024           1025           1026           1027           1028           1031           1033           1034           1035           1041           1042           4044           4045           4046           4047           4046           4047           4046           4047           4046           4051           4052           4053           4055           4056           4055           4056           4057           4058	0.38 0.48 0.30 0.55 1.60 1.55 0.72 0.57 0.57 0.59 0.54 0.54 0.54 0.59 0.64 1.65 0.68 0.68 0.59 0.64 0.30 0.59 0.68 0.30 0.30 0.30 0.30 0.30 0.55 1.20 0.30 0.55 1.20 0.30 0.55 0.55 0.72 0.55 0.72 0.55 0.75 0.75 0.75 0.75 0.75 0.75 0.75	74LS11 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS22 74LS20 74LS22 74LS26 74LS22 74LS30 74LS37 74LS37 74LS38 74LS38 74LS38 74LS42 74LS47 74LS47 74LS47 74LS55 74LS55 74LS55 74LS78 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	7415279 7415283 7415283 7415290 7415293 7415365 7415365 7415365 7415365 7415367 7415377 7415377 7415377 7415377 7415379 7415390 741590 7415390 7415979 7419779 74159779 74159779 74159779 74159779 741597779 7415977777777777777777777777777777777777	0.34 0.44 0.56 0.56 0.34 0.34 0.34 0.74 0.74 0.89 0.89 0.89 0.84 0.59 0.99 0.99 0.15 0.17 0.23 0.17 0.23 0.12 0.17 0.23 0.25 0.29
R0-3-2513U           ZN450E           7805           7805           7805           7805           7905           7912           280 A CPU           280 A DART           280 A DAR           280 A DAR           280 A DAR           280 A DAR           280 A SI0-0           280 A SI0-1           280 A SI0-1           280 A SI0-1           280 A SI0-2           EFG800           EF6800           EF6802           EF6803           EF6809           EF6810           EF6821           EF6840           EF6841           EF6842	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	1024           1024           1024           1025           1026           1027           1028           1031           1033           1034           4035           1044           4042           4044           4044           4045           4046           4047           4048           4051           4055           4055           4055           4055           4056           4055           4056           4056           4056           4056           4066           4066           4069           40701           4072	0.38 0.18 0.055 1.65 0.55 1.65 0.57 0.57 0.57 0.57 0.57 0.57 0.57 0.5	74LS12 74LS12 74LS13 74LS15 74LS14 74LS25 74LS20 74LS20 74LS22 74LS27 74LS28 74LS28 74LS32 74LS32 74LS33 74LS37 74LS37 74LS37 74LS49 74LS47 74LS49 74LS45 74LS55 74LS75 74LS76 74LS76 74LS78 74LS76 74LS77 74LS76 74LS76 74LS77 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.18 0.14 0.19 0.12 0.14 0.16 0.13 0.34 0.34 0.34 0.34 0.34 0.50 0.54 0.15 0.20 0.15 0.20 0.21 0.28 0.20 0.24 0.12 0.12 0.12 0.12 0.12 0.12 0.12 0.12	7415279 7415283 7415293 7415293 7415293 7415365 7415365 7415365 7415367 7415375 7415375 7415375 7415375 7415375 7415375 7415376 7415339 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415393 7415375 741575 741575 741575 741575 741575 741575 74157577 74157577 74157777777777	0.34 0.44 0.45 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.74 0.74 0.74 0.89 0.89 0.59 0.59 0.59 0.59 0.59 0.59 0.59 0.5
R0-3-2513U           ZN450E           7805           7805           7805           7805           7905           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A A CPU           Z80A DART           Z80A DMA           Z80A DMA           Z80A DMA           Z80A DMA           Z80A DMA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2           EF6800           EF6803           EF6803           EF6810           EF6845           EF6840           EF6845           EF6845	7.70 7.71 0.50 0.50 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4028 4031 4033 4033 4033 4034 4035 4040 4041 4044 4043 4045 4046 4047 4048 4047 4048 4047 4051 4055 4055 4055 4055 4055 4055 4055	0.38 0.18 0.99 0.35 1.65 1.55 1.55 1.55 0.72 0.57 0.54 1.65 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.30 0.59 0.54 0.30 0.59 0.30 0.55 0.40 1.20 0.17 0.30 0.45 0.40 0.45 0.45 0.45 0.45 0.45 0.4	74LS11 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS22 74LS28 74LS32 74LS32 74LS32 74LS32 74LS32 74LS37 74LS37 74LS37 74LS35 74LS35 74LS56 74LS57 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.18 0.14 0.19 0.12 0.19 0.12 0.16 0.16 0.16 0.16 0.13 0.34 0.34 0.34 0.34 0.50 0.20 0.27 0.28 0.20 0.20 0.20 0.20 0.20 0.20 0.20	74LS279 74LS283 74LS283 74LS293 74LS293 74LS365 74LS365 74LS365 74LS367 74LS375 74LS375 74LS377 74LS377 74LS377 74LS379 74LS379 74LS390 74LS377 74LS390 74LS300 74LS30	0.34 0.44 0.46 0.56 0.45 0.34 0.34 0.34 0.34 0.74 0.74 0.74 0.74 0.89 0.89 0.84 0.59 0.94 0.59 0.59 0.59 0.15 0.52 0.25 0.25 0.25 0.25 0.25 0.34 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A ACTC           Z80A ADART           Z80A ADART           Z80A ANA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2           EF6802           EF6803           EF6804           EF6805           EF6840           EF6840           EF6845           EF6845           EF6850	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	1024           1024           1024           1025           1026           1027           1028           1031           1033           1034           1035           1040           1041           1042           4041           4042           4043           4044           4045           4046           4047           4048           4051           4053           4055           4056           4053           4054           4055           4053           4054           4055           4056           4057           4058           4059           4054           4055           4060           4066           4071           4072           4071           4072           4073           4075	0.38 0.18 0.90 0.30 0.35 1.65 1.55 1.55 0.72 0.57 0.54 0.54 0.54 0.54 0.54 0.54 0.54 0.54	74LS12 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS28 74LS30 74LS30 74LS30 74LS30 74LS33 74LS33 74LS37 74LS49 74LS40 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.14	741.5279 741.5283 741.5283 741.5290 741.5290 741.5366 741.5366 741.5366 741.5367 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5379 741.5386 741.5386 741.5386 741.5386 741.5386 741.5386 741.5386 741.5386 741.5386 741.5376 741.5377 741.5477 741.54777 741.54777 741.547777777777777777777777777777777777	0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.74 0.34 0.74 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.3
R0-3-2513U           ZN450E           7805           7805           7805           7905           7912           ZB0A CPU           Z80A DART           Z80A DART           Z80A ANA           Z80A DART           Z80A ANA           Z80A ANA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-1           Z80A SI0-2           EFCIS 6800           EF6802           EF6803           EF6804           EF6805           EF6845           EF6845           EF6850	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	1024           1024           1024           1025           1026           1027           1028           1031           1033           1034           4035           1040           4041           4042           4044           4045           4046           4047           4048           4051           4052           4055           4055           4056           4055           4056           4056           4056           4056           4056           4056           4056           4056           4057           4070           4070           4071           4075	0.38 0.18 0.055 1.65 0.55 1.65 0.72 0.57 0.69 0.54 0.64 0.64 0.64 0.64 0.64 0.64 0.64 0.6	74LS12 74LS12 74LS13 74LS14 74LS15 74LS21 74LS20 74LS20 74LS20 74LS22 74LS28 74LS32 74LS32 74LS33 74LS33 74LS37 74LS47 74LS48 74LS45 74LS45 74LS55 74LS55 74LS55 74LS76 74LS77 74LS77 74LS76 74LS76 74LS76 74LS77 74LS78 74LS77 74LS78 74LS77 74LS78 74LS77 74LS78 74	0.15 0.24 0.44 0.13 0.14 0.14 0.14 0.19 0.12 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.16 0.16 0.13 0.34 0.34 0.59 0.59 0.14 0.15 0.20 0.59 0.12 0.12 0.12 0.12 0.12 0.12 0.12 0.12	741.5279 741.5283 741.5283 741.5290 741.5290 741.5365 741.5365 741.5366 741.5366 741.5375 741.5375 741.5375 741.5375 741.5375 741.5379 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5375 741.54757 741.54757 741.547577777777777777777777777777777777	0.34 0.44 0.56 0.56 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7805           7905           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A DART           Z80A DMA           Z80A SI0-0           Z80A SI0-1           Z80A SI0-2           EHEIS 6800           EF6803           EF6803           EF6804           EF6805           EF6805           EF6806	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024           4025           4026           4027           4028           4031           4033           4034           4035           4040           4044           4045           4046           4047           4048           4051           4052           4053           4054           4055           4055           4055           4056           4056           4057           4058           4059           4051           4052           4053           4054           4055           4056           4057           4058           4070           4071           4075           CREDIN	0.38 0.38 0.99 0.30 0.35 1.65 1.55 1.55 1.55 0.57 0.57 0.57 0.59 0.64 1.55 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.30 0.59 0.54 0.30 0.30 0.55 0.40 1.20 0.55 0.40 0.55 0.55 0.59 0.54 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.59	74LS12 74LS12 74LS13 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS28 74LS30 74LS32 74LS32 74LS32 74LS37 74LS37 74LS35 74LS35 74LS55 74LS55 74LS55 74LS55 74LS55 74LS76 74LS27 74LS38 74LS36 74LS37 74LS36 74LS35 74LS37 74LS36 74LS35 74LS37 74LS36 74LS35 74LS36 74LS35 74LS36 74LS37 74LS36 74LS36 74LS37 74LS36 74LS37 74LS37 74LS37 74LS37 74LS37 74LS37 74LS37 74LS36 74LS37 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.19 0.12 0.12 0.12 0.12 0.12 0.12 0.16 0.16 0.16 0.16 0.16 0.16 0.34 0.34 0.34 0.34 0.20 0.17 0.20 0.20 0.20 0.20 0.20 0.20 0.20 0.2	74LS279 74LS283 74LS283 74LS293 74LS293 74LS365 74LS365 74LS365 74LS367 74LS375 74LS375 74LS377 74LS377 74LS377 74LS377 74LS378 74LS390 74LS377 74LS37 74LS3	0.34 0.44 0.44 0.56 0.45 0.34 0.34 0.74 0.74 0.89 0.59 0.59 0.59 0.59 0.59 0.59 0.59 0.5
R0-3-2513U           ZN450E           7805           7805           7805           7805           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A DART           Z80A A DART           Z80A A SI0-0           Z80A SI0-1           Z80A SI0-2           EF6803           EF6804           EF6805           EF6840           EF6840           EF6840           EF6845           EF6850	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	0224           0225           0226           0227           0228           0331           034           0355           0400           4041           4042           4043           4044           4045           4046           4047           4048           4051           4055           4055           4056           4055           4055           4056           4055           4056           4055           4056           4053           4054           4055           4066           4066           4071           4072           4071           4072           4073           4075           CCREDIT           CCME	0.38 0.18 0.48 0.30 0.35 1.65 1.55 1.55 0.72 0.57 0.57 0.57 0.57 0.57 0.57 0.59 0.54 0.54 0.54 0.54 0.54 0.54 0.54 0.54	74LS12 74LS12 74LS13 74LS15 74LS15 74LS27 74LS20 74LS20 74LS20 74LS27 74LS28 74LS28 74LS38 74LS33 74LS33 74LS33 74LS37 74LS49 74LS49 74LS49 74LS49 74LS45 74LS49 74LS55 74LS75 74LS75 74LS76 74LS78 74LS49 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.13 0.34 0.34 0.50 0.50 0.14 0.15 0.20 0.20 0.20 0.20 0.20 0.20 0.20 0.2	7415279 7415283 7415293 7415293 7415293 7415365 7415365 7415365 7415367 7415377 7415377 7415377 7415377 7415377 7415377 7415377 74153376 74153376 74153376 74153393 74153393 74153393 74153393 74153393 74153393 74153393 74153393 74153393 7415327 7415327 7415327 7415375 7415375 7415376 7415376 7415376 7415376 7415376 7415376 7415377 741547 74154	0.34 0.44 0.46 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7912           ZILGG 280 FR           Z80 CPU           Z80 CPU           Z80 CPU           Z80 CTC           Z80 A CPU           Z80 A CPU           Z80 A CPU           Z80 A CPU           Z80 A DART           Z80 A DART           Z80 A DIO           Z80 A DIO           Z80 A DIO           Z80 A SI0-0           Z80 A SI0-1           Z80 A SI0-1           Z80 A SI0-2           EFG800           EF6802           EF6803           EF6804           EF6845           EF6845           EF6845           EF6850	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024         4025           4026         4027           4028         4031           4034         4035           4040         4041           4044         4045           4044         4045           4045         4046           4047         4048           4045         4046           4045         4046           4051         4052           4053         4054           4055         4056           4056         4068           4069         4070           4072         4073           4075         4075           CCREDIT         COME           ude         post and	0.38 0.38 0.99 0.30 0.55 1.85 1.85 1.85 1.85 1.85 1.85 0.72 0.57 0.54 0.69 0.64 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.30 0.59 0.54 0.30 0.55 0.40 0.30 0.55 0.40 0.55 0.59 0.54 0.55 0.59 0.54 0.55 0.59 0.55 0.59 0.55 0.59 0.54 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.55 0.59 0.59	74LS12 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS30 74LS32 74LS32 74LS37 74LS30 74LS31 74LS47 74LS47 74LS55 74LS55 74LS75 74LS90 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS91 74LS95 74LS91 74LS91 74LS91 74LS95 74LS95 74LS91 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.13 0.34 0.34 0.50 0.514 0.20 0.514 0.20 0.514 0.20 0.514 0.20 0.514 0.50 0.514 0.515	74LS279 74LS283 74LS283 74LS293 74LS365 74LS365 74LS365 74LS366 74LS367 74LS375 74LS377 74LS377 74LS377 74LS377 74LS377 74LS377 74LS377 74LS377 74LS378 74LS386 74LS393 LDW PROFILE 0 SOCKTS Number of Pins 8 14 16 18 18 20 22 24 28 40 CRYSTALS 1 Mitz 1.8432 Mitz 4 Mitz 4 Mitz 1.8432 Mi	0.34 0.44 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7805           7912           ZILOG 280 FR           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CTC           Z80A ACTC           Z80A ACTC           Z80A ACTC           Z80A ACTC           Z80A ATC           Z80A NMA           Z80A NO           Z80A NO-12           Z80A S10-1           Z80A S10-1           Z80A S10-1           Z80A S10-1           Z80A S10-2           EFG803           EF6804           EF6805           EF6810           EF6821           EF6820           EF6821           EF6820           EF6840           EF6840           EF6840           EF6841           EF6845           EF6845           EF6845           EF6845           EF6845           EF6845           EF6845           EF6845           EF6845	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4028 4031 4033 4034 4033 4034 4042 4043 4044 4044	0.38 0.18 0.48 0.30 0.35 1.65 1.55 0.72 0.57 0.54 0.54 0.54 0.54 0.54 0.54 0.54 0.54	74LS12 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS30 74LS30 74LS30 74LS33 74LS33 74LS33 74LS34 74LS40 74LS47 74LS47 74LS45 74LS57 74LS57 74LS57 74LS77 74	0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	7415273 7415283 7415283 7415283 7415365 7415365 7415365 7415365 7415366 7415367 7415377 7415377 7415377 7415377 7415377 7415377 7415377 74153376 74153377 74153377 74153376 74153376 74153376 74153376 74153376 74153376 74153376 74153376 74153376 74153377 74153377 74153377 74153376 74153376 74153376 74153376 74153377 7415377 7415377 7415377 7415377 7415377 7415377 741547 741547 741547 741547 741577 741577 7415777 7415777 7415777 74157777 74157777777777	0.34 0.44 0.46 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7912           ZB0A CPU           Z80A ACTC           Z80A ADART           Z80A DART           Z80A DART           Z80A ADART           Z80A ADART           Z80A ADART           Z80A SI0-0           Z80A SI0-0           Z80A SI0-1           Z80A SI0-2           EFG5800           EF6802           EF6803           EF6804           EF6840           EF6840           EF6845	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4027 4028 4033 4034 4035 4040 4041 4042 4044 4042 4044 4044 4044	0.38 0.18 0.055 1.65 0.55 1.65 0.57 0.57 0.57 0.57 0.57 0.57 0.57 0.5	74LS12 74LS12 74LS12 74LS15 74LS15 74LS21 74LS25 74LS20 74LS22 74LS27 74LS28 74LS32 74LS32 74LS33 74LS33 74LS37 74LS38 74LS37 74LS46 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.18 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	741.5279 741.5283 741.5283 741.5290 741.5290 741.5365 741.5365 741.5366 741.5367 741.5375 741.5375 741.5375 741.5377 741.5377 741.5379 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5390 741.5376 741.5377 741.5477 741.5477 741.5477 741.5477 741.5477 741.5477 741.5477 741.5477 741.5477 741.54777 741.54777 741.54777 741.54777 741.547777 741.547777777777777777777777777777777777	0.34 0.44 0.56 0.56 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
R0-3-2513U           ZN450E           7805           7805           7805           7912           ZB0A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A CPU           Z80A A CPU           Z80A A CPU           Z80A A DART           Z80A DART           Z80A DART           Z80A A DART           Z80A A DART           Z80A A DART           Z80A A DAR           Z80A A NO-0           Z80A SIO-1           Z80A SIO-1           Z80A SIO-2           Z80A SIO-2           Z80A SIO-2           EF680           EF680           EF680           EF680           EF680           EF681           EF6845           EF6850           OFF10           ORDE           All privat (1)           VAT (1)           F1	7.70 7.71 7.61 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4028 4031 4033 4034 4033 4034 4042 4043 4044 4042 4044 4044	0.38 0.38 0.99 0.30 0.35 1.65 0.55 1.65 0.57 0.57 0.57 0.57 0.57 0.59 0.64 1.65 0.59 0.54 0.59 0.54 0.64 0.68 0.64 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.59 0.54 0.55 0.54 0.59 0.54 0.59 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.55 0.54 0.54	74LS12 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS30 74LS32 74LS33 74LS30 74LS30 74LS31 74LS42 74LS47 74LS47 74LS45 74LS55 74LS55 74LS75 74LS75 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS76 74LS77 74LS76 74LS76 74LS77 74LS76 74LS77 74LS76 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74LS76 74LS77 74LS77 74LS76 74LS77 74LS76 74LS77 74	0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	7415279 7415283 7415283 7415283 7415283 7415365 7415365 7415365 7415365 7415367 7415377 7415377 7415377 7415377 7415379 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415339 7415328 7415375 741577 741577 741577 741577 7415777 7415777 7415777 7415777 7415777 74157777 741577777 74157777777777	0.34 0.56 0.56 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
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R0-3-2513U         ZN450E         7805         7805         7805         7912         ZILOG 280 F.         Z80A CPU         Z80A CPU         Z80A CPU         Z80A CPU         Z80A CPU         Z80A CPU         Z80A CTC         Z80A CTC         Z80A CTC         Z80A CTC         Z80A DART         Z80A DART         Z80A DART         Z80A ANA         Z80A ANO         Z80A SI0-0         Z80A SI0-1         Z80A SI0-1         Z80A SI0-1         Z80A SI0-1         Z80A SI0-2         EFC15 6800         EF6802         EF6803         EF6804         EF6845	7.70 7.70 7.71 0.50 0.55 0.55 0.55 0.55 0.55 0.55 0.5	4024 4025 4026 4027 4027 4028 4033 4034 4033 4034 4035 4040 4041 4042 4044 4044 4044 4044 4044	0.38 0.18 0.99 0.055 1.65 0.55 1.60 0.55 0.57 0.57 0.57 0.57 0.57 0.57 0.5	74LS12 74LS12 74LS12 74LS15 74LS15 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS20 74LS30 74LS30 74LS31 74LS31 74LS31 74LS31 74LS31 74LS35 74LS36 74LS36 74LS36 74LS36 74LS37 74	0.15 0.24 0.44 0.13 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.19 0.12 0.14 0.16 0.19 0.12 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	741.5279 741.5283 741.5283 741.5386 741.5366 741.5366 741.5366 741.5367 741.5376 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5377 741.5378 741.5386 741.5386 741.5387 741.5377 741.5477 741.5477 741.5477 741.5477 741.54777 741.54777 741.54777 741.547777 741.547777777777777777777777777777777777	0.34 0.44 0.56 0.56 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34

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HY 74	Sterec mover	Two channels, each mixing five signals into one — with treble and bass controls.	20 mA	£13.17	£11.45

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HY 73	Guitar pre-amp	Handles two guitars (bass and lead) and mic with separate volume/bass/treble and mix	20 mA	£14 09	£12 25
HY 76	Stereo switch matrix	Provides two channels, each switching one of four signals into one	20 mA To be announced		nounced
HY 77	Stereo VU meter driver	Programmable gain/LED overload driver.	20 mA	£10 64	£9.25

For easy mounting we recommend: B 6 mounting board for modules HY6 - HY13 ±0.90 inc. VAT (0.78 ex. VAT.) B 66 mounting board for modules HY66 HY77 ±1.12 inc. VAT (0.79 ex. VAT.) "All modules are encapsulated and include clip-on edge connectors. All operate from ±15V minimum to ±300 maximum. needing dropper resistors for higher voltages. HY67 can be used only with the PSU 30 power supply unit. Modules HY6 to HY13 measure 45×20×40mm. HY66 to HY77 measure "All operate from ±15V

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Space-saving efficient ILP power supplies are designed to give you flexibility in planning audio assemblies. Nine of the eleven models have toroidal transformers manufactured on new cost-efficient high technology machines in our own lactory. So we keep the quality up, and the price down,

ILP power supplies are compatible with all other ILP modules — combine them to produce almost any audio system. All carry the ILP 5 year no quibble guarantee and include full connection data. So send your order on the Freepost coupon below today!

#### POWER SUPPLY UNITS

Model No.	For use with	Price Inc. VAT	Price ex. VAT
PSU 30	$\pm 15 V$ combinations of HY6/66 series to a maximum of 100 mA or one HY67.	£5 18	£4 50
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PSU 36	1 or 2 HY 30	£9.32	£8.10
PSU 50	1 or 2 HY 60.	£12.58	£10.94
PSU 60	1 × HY 120/HY 120P/HD 120/HD 120P.	£15.00	£13.04
PSU 65	1×M0S 120/1×M0S 120P	£15.32	£13 32
PSU 70	1 or 2 HY 120/HY 120P/HD 120/HD 120P.	£18.31	£15 92
PSU 75	1 or 2 MOS 120/MOS 120P.	£18 63	£16.20
PSU 90	1 × HY 200/ HY 200P/HD 200/ HD 200P.	£18.63	£16.20
PSU 95	1 × MOS 200/ MOS 200P.	£18.77	£16.32
PSU 180	2 × HY 2007HY 2007HD 2007HD 200P or 1 × HY 40071 × HY 4007HD 4007HD 400P.	£24.54	£21.34
PSU 185	1 or 2 MOS 200/MOS 200P/1 x MOS 400 1 x MOS 400P.	£24.68	£21,46

All models incorporate ILP toroidal transformers except PSU 30 and PSU 36 which include our own laminated transformers.

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Use this coupon, or a separate sheet of paper, to order these modules, or any products from other ILP Electronics advertisements. No stamp is needed if you address to Freepost. Cheques and postal orders must be crossed and payable to ILP Electronics Ltd; cash must be registered, C.O.D. — add £1 to total order value. Access and Barclaycard welcome. All UK orders sent post free within 7 days of receipt of order.

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#### **COMING OF AGE!**

This issue marks the start of a new year for PE—our 18th. Yes, even though we still think of ourselves as a "new" magazine, publication of PE started with the November 1964 issue. Hopefully many readers also continue to feel PE is "new", since this indicates we are keeping up with their needs and continuing to break new ground on projects and technology.

Before we forget it, let us take this opportunity of thanking you for your support over the years and wishing you seasonal greetings and a prosperous new year. We are planning some further steps forward for PE during 1982 and we hope you will all take them with us.

#### '81 CHANGES

We came some way in '81, 'though times have been difficult for many with the recession really biting. Unfortunately, it now seems that we will have to weather its ravages for the best part of next year as well. It is generally felt that even the "iron lady" will have to bend towards the end of the year so that things improve before the next election!

Another effect of the recession has been to attract more commercial companies into producing and marketing electronic projects as a means of keeping stock and financial turnover at a reasonable level. Thus there are probably more specialist professional engineers now working on projects for the "amateur" than ever before.

UK sales of PE have increased during the last six months and are higher now than they have been for 18 months, even though cover prices have had to rise and people's general wealth has fallen. Our overseas sale, which is in excess of 20,000 copies, is also very buoyant especially in the emerging nations.

One change, made earlier in the year, which readers may not have noticed was the introduction of a new front cover logo. This rather subtle increase in lettering size came in the July issue—check if you don't believe us! Thankfully, we have been able to continue to produce many "first time" projects; CB has come; we gave readers a Free Instrument Case, an I.C. Removal Tool and four suppliers' catalogues during the year. All of which were well received and we believe proved useful.

Our aim has been to make PE the best possible value for money and this will continue in '82. If we can be even better at this time next year everyone will be pleased. After all it's the "newness" of the mag. that keeps staff and readers interested and the whole thing on the boil. The quality, range and ingenuity of our projects is now better than ever.

#### CB

As we go to press we are hearing disturbing stories that European standard CB rigs and a.m./f.m. rigs are being sold as legal equipment with a CB 27/81 label on them. Readers should therefore be very careful when buying a rig to ensure that it is built to the British specification. Our rig review will assist with identification.

Mike Kenward

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We regret that lengthy technical enquiries cannot be answered over the telephone (see below).

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#### **Technical Queries**

We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in PE. All letters requiring a reply should be accompanied by a stamped, self addressed envelope, or international reply coupons, and each letter should relate to one published project only.

Components and p.c.b.s are usually available from advertisers; where we anticipate difficulties a source will be suggested.

#### **Back Numbers**

Copies of some of our recent issues are available from: Post Sales Department (Practical Electronics), IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SE1 OPF, at 95p each including Inland/Overseas p&p.

#### **Binders**

Binders for PE are available from the same address as back numbers at £4.30 each to UK or overseas addresses, including postage and packing, and VAT where appropriate. Orders should state the year and volume required.

#### **Subscriptions**

Copies of PE are available by post, inland or overseas, for £13.00 per 12 issues, from: Practical Electronics, Subscription Department, Oakfield House, Perrymount Road, Haywards Heath, West Sussex RH16 3DH. Cheques and postal orders should be made payable to IPC Magazines Limited. "HEUS

Edited by David Shortland & Jasper Scott

# More Micros in Schools

Mr Kenneth Baker MP, Minister for Information Technology, recently announced an extension of the Department of Industry's Micros in Schools scheme to secondary schools which already have computer facilities.

Speaking at the launch of Information Technology '82 he said:

"The Micros in Schools scheme has proved a great success. Since the scheme was opened on June 1, almost 1900 applicants have been approved for 50% funding towards the cost of a microcomputer. Both maintained and independent schools in England, Wales. Scotland and Northern Ireland are participating.

"The Department of Industry estimated 2500-3000 secondary schools were without equipment and so we are well on the way to achieving my first objective of a micro in every secondary school by the end of 1982.

I am pleased to announce that, as from January 1, 1982, this scheme will be extended so those secondary schools already with equipment may take advantage of the grant.

With the back up teacher training provided for in the scheme, this initiative is a vital complement to the Microelectronics Programmes under way by the Education Departments. I hope teachers from all disciplines will be able to take advantage of these programmes so that an awareness of new technology is an integral part of all pupils' experience across the curriculum."

## SECONDS AWAY!

The wonder of liquid crystal never ceases to amaze, and one of Casio's latest offerings uses the ubiquitous l.c.d. to take the "executive toy" to new heights.

Casio's BG15 is based on the now familiar clock/calculator format, with the usual added attraction of a game. But, instead of Alien Invaders, you have a boxing match, with excellent animated graphics and a realistic scoring system. Now you can beat the stuffing out of your opponent without sustaining a single bruise yourself!

The one criticism that could be levelled at the BG15 is that the instruction booklet is rather an epic work which takes a long time to get through—particularly the chapter on the game.

Anyone who has had a trying day

and feels like inflicting a touch of GBH on their boss will find this machine a far less troublesome way of letting off steam. It is available at a discount price of £16.95 (inc VAT and p&p) from Tempus, Dept. PE, Freepost, 164–167 East Road, Cambridge, CB1 1DB. (0223 312866)

POINTS ARISING

ULTRASONIC CLEANER (Jan. '80) PE CONGRESS (April '80)

Wicca Electronic Systems Ltd, who supplied kits for the above projects have recently ceased trading. While components for the Congress should be available from other sources, we do not know of another supplier of certain parts for the Ultrasonic cleaner. However, a complete kit for a similar device is currently available from Heathkit. Further information is available from them on 0452 29451. Technical queries should be addressed to us.

## DON'T!

Rodnay Zaks, computer scientist, author, and president of the publisher Sybex Inc., has been putting pen to paper (or perhaps finger to word processor) again. The latest book we have received is called:

Don't! (Or How To Care For Your Computer).

The front cover is striking, and with quotations of wisdom from Shakespeare's Macbeth through to Confucius leading you in to each chapter, it makes fulfilling reading. The book is openly "dedicated to the allegedly mythical trouble-free computer". It shows how most hardware problems are either directly or indirectly brought on by mishandling.

Make your hobby room comfy for humans and your home computer will like it too, says Mr. Zaks of temperature, vibration and dust considerations.

Among the often quite funny cartoons, photographs and diagrams you will find such "horror" stories as that recounted of the man who was unwittingly ruining discs by writing identity information on their sleeves with a hard ball-point pen, thereby embedding loose grit into the oxide.

You are also warned against smoking near disc drives. One picture shows the relative sizes of common pollutants. With the flying height of the head at 100µ, the cross section of a human hair shown adjacent looks something like a football next to the gap under the door. The *least* offensive, fingerprint oil, would be more than enough to swamp the gap! And that effluvium donated to the atmosphere so freely by our smoking companions looks as though it could cause havoc, comprising sticky particles of 300µ diameter.

The book's title, *Don't!*, applies to its *own* diagrams on page 157 if you are European, which illustrate typical NEMA receptacles and their mains wiring. In the USA there is no such thing as a brown "Live" wire, it's black, and it's "Hot" (probably pronounced "hut"). The book is written at a light-hearted level and makes enjoyable and informative reading.

You could say it is aimed at interfacing the *low event horizon user* to *leading edge technology practices*. Honestly, you could!

This soft-back is available through **Computer Bookshop**, 30 Lincoln Road, Olton, Birmingham, which is the UK outlet for Sybex. It costs £9.65 and consists of 213 pages measuring  $150 \times 230$ mm.



Items mentioned are available through normal retail outlets unless otherwise specified. Prices correct at time of going to press.

## Briefly...

To cope with the shortage of training aids in microwave engineering, the Microwave Products Division of Marconi Instruments, a GEC-Marconi Electronics company, have introduced a new low cost audio-visual course—"Understanding Microwave Equipment". It consists of six C90 cassettes held in a ring binder containing 175 supporting diagrams and photographs. This course can be used by anyone who has an interest in microwave equipment.

The six sessions cover a survey of microwave systems and devices, transmission lines and components, solid-state sources, tubes, low-noise receivers, antennas, radar, telecommunications and electronic warfare systems.

The cost of the course is £65. Further details are available from Harold Read, Marconi Instruments Ltd., Longacres, St Albans, Herts AL4 0JN (0727 59292)

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The winner of our questionnaire competition was Mr Ray Causer of Wantage, whose estimate was 971. Mr Causer will receive a cheque for £50.

We would like to thank all our readers who took the trouble to fill in the questionnaire.

The new 12-page catalogue from Ace Mailtronix is now available price 30p. The catalogue covers a wide range of components and as always Ace will try and obtain any type component for constructors. For a quote by return of post please send a S.A.E.

Ace Mailtronix Ltd., 3A Commercial Street, Bately, West Yorkshire (0924 441129).



Please check dates before setting out, as we cannot guarantee the acsuracy of the information presented below.

> IDEA (Domestic appliances) Jan. 12–14. Birmingham. B6 OEM Assemblies Feb 2–4. Royal Hort. Halls London. T BEX Bristol Feb. 3–4. K BEX Bournemouth Feb. 17–18. K Microsystems Feb. 24–26. West Centre Hotel London. Z1 Seminex Mar. 29–Apr. 2. Imperial College London. H1 CAD Mar. 30–Apr. 1. Metropole Brighton. Z1 Laboratory Edinburgh Mar. 30–31. Ass. Rooms. E

Test & Measurement Mar. 30-Apr. I Forum Wythenshawe. T



Thousands of budding organists and pianists struggling through the first stages of learning to play will welcome a new electronic tutor developed by a Preston company.

Called Prelude, it gives an instant visual guide to more than 600 chords as well as all major and minor scales. It's a small, hand-held device with keys for the musical notes, chords and inversions, and a liquid crystal keyboard display.

The unit is designed to help tutored or self-taught students learn the basic 'alphabet' of music; to teach classically-trained musicians modern harmony and to help string or wind players to convert to keyboards.

Two professional organ, teachers who helped in Prelude's design say that it is far easier and quicker to use than a printed tutor. Not only does it show notes making up the basic chord, but the user can add progressively more complex components, and show all the inversions—the different ways of playing it.

Prelude is priced at £19.95 including VAT, plus 40p p&p, and is available from Speedyplain Ltd., Freepost, Longton, Lancs PR4 5YL.

Sensors & Systems Mar. 30–Apr. 1 Forum Wythenshawe. T Peripherals Mar. 31–Apr. 2. West Centre Hotel London. Z1 Laboratory Manchester Apr. 7–8. New Century Hall. E All Electronics Show Apr. 19–21. Barbican London. E BEX Brighton Apr. 28–29. K Compec Europe May 4–6. Centre Int. Rogier. Z1 CETEX May 30–June 2. Earl's Court London. B6 BEX Leeds Jun. 9–10. K Transducer/Tempcon Jun. 29–Jul. 1 Wemb. Conf. Cntr. T

- B6 Andry Montgomery Ltd. @ 01-486 1951
- E Evan Steadman. Saffron Walden @ 0799 22612
- HI Seminex Ltd., Tunbridge Wells & 0892 39664
- K Douglas Temple. Bournemouth & 0202 20533
- T Trident, Tavistock & 0822 4671
- ZI IPC Exhibitions, Sutton @ 01-643 8040





#### Punch Drunk

I still feel slightly punch drunk after the autumn marathon of the political conference season. Hours and hours of waffle from the platform. Even more hours of pundits interpreting, analysing, what was said or, more sinister, what was not said. The acres of newsprint as a follow-up in the daily and weekly press.

It was all in the line of duty. Alas, my hopes of discovering just a small spark of inspiration in relation to industry and its problems were unrealised. The politicians had been wasting my time.

When the parties were not running down their opponents they were squabbling among themselves. But there was some consensus. All, in the face of facts, more or less agreed that British industry has been in decline for 20 years. If we accept this, as we must, we had the spectacle of those who assisted in the decline, indeed, probably accelerated it, putting themselves forward as the new messiahs. Yesterday's failures claiming they are tomorrow's saviours.

Dishonesty was the order of the day. But of course it is too much to expect public figures to even hint they had flopped in the past, or to admit they are powerless in the face of events over which they have no control.

Then there was the new alignment. The party labels are now confusing. Labour retains its name not its old character which is now re-born under the label Social Democratic, heavily loaded at the top with old-style Labourites. The Liberals, once proudly independent, after enjoying a brief moment of influence in the Lib-Lab pact have now found it expedient to enter an uneasy alliance with the SDP. The Tories are what they have always been except more so under Mrs Thatcher, but even there a shift is apparent.

Meantime, in the midst of political manoeuvre and intrigue, industry has to get on as best it can. It was a great pity that even the Tories, who are more honest than the others in presenting the brutal facts of life, did not ram home more forcefully the message that there is only one formula for industrial success—the right product at the right time at the right price. Only then can profits be generated to satisfy the social conscience in welfare and other benefits. The principle, simple as it is, applies right across the board from toilet rolls to jet aircraft. Government macroeconomics can help or hinder. But it is management and workers who produce the goods.

I can see no industrial future in the school of thought which recommends wholesale nationalisation, withdrawal from the EEC, import controls, currency controls, plus hostility to multinational operation which, in electronics, is the life-blood of all the larger companies including those which are British-based.

To institute central government control in place of free enterprise which stimulates technological advance and keeps prices down would certainly not benefit the elctronics industry. There is a real-life model on which I base this observation. It has the uninspiring title of the State Collective Electronic Communications Combine of the Soviet Union. Its products are also uninspiring. Could this be the fate, say, of GEC in 10 years time?

#### Turnround

Not that electronics has been universally successful. Few companies win all the time. ICL is running through a bad patch as I write and it is somewhat humiliating, even though common sense, that Japanese technology is being injected to boost sales. The same, of course, applies to BL with the Triumph (i.e. Honda) Acclaim. But in business you need to bend with the wind and seize every advantage.

Decca was on the skids. Now no longer so, How was this achieved?

When Racal moved in, paying £106 million at what was then thought to be a silly price, the new management made no secret of what they were after. It was capital goods in growth areas with electronic warfare in the lead. Racal knows electronic capital goods and its markets but didn't want to know music or domestic TV. These Decca activities were sold off to those who could make better use of them. Similarly, Racal saw no point in maintaining an imposing Thames-side headquarters in the middle of London. So Decca House on the Albert Embankment is being sold at a figure in the region of £6 million.

The Decca companies have been reorganised into eight major product or business areas, defence systems, marine radar, navigation etc. Each of the new Racal-Decca companies is expected to stand on its own feet and generate products or services, and profit. Marine radar is still losing money but is expected to get back in the black in the current financial year.

True, Decca is somewhat slimmer than before. But this is a small and necessary price to pay for ensuring jobs for the great majority now and in the future. Racal-Decca accounts for 30 percent of all Racal Electronics Group business and now, for the first time, Racal has joined the elite club of companies who can boast a world-wide turnover of a billion dollars.

One of Racal-Decca's real growth areas is in underwater exploration. It is strong in North Sea oil, and it was Racal-Decca Survey Ltd who supplied the know-how, the men and the equipment to locate the wreck of HMS Edinburgh, sunk in World War 2 in the Barents Sea, from which £45 million of gold bullion was recently salvaged.

#### Sugar-sweet

If you've got a product or service that people want to buy there is never a slump. On the general consumer front Marks and Spencer have turned in record results. A consistent high flyer with a value-formoney reputation.

In the more specialised field of consumer electronics, always a topic of gloom, we have Alan Sugar, heading up Amstrad, who took his company to the public in April 1980. His forecasts looked optimistic, if not downright rash, in the prospectus. But the public backed him to their own, and his, profit.

At the end of his first year of trading as a publicly quoted company, Sugar achieved 61 percent increase in turnover and 75 percent increase in profit. Not bad in a period of 'the greatest recession ever experienced'. Sugar is forecasting further gains in the current year and on his track record he should succeed.

#### **Hooray for Russia!**

Russian military aid to the third world countries is turning out to be good business for British electronics. Shifting political allegiance has left many countries with vintage Soviet defence hardware, out of date but in fair physical shape.

Modernising is not a new game but when you have fallen from grace with your benefactor you can hardly go back to him, and if you haven't native skills you must look elsewhere to those who have.

Thus, the prospect of over £500 million of work on modernising eight Russian-built destroyers in service with the Chinese navy. They will be fitted with new missile systems, new radars and other sensors, and new operations rooms. Contracts are reported to be near signing with British Aerospace as principal contractor for the missiles and electronics systems.

Elsewhere in the world there are hundreds of Russian tanks recently up-dated with British radio and gun-ranging equipment and Russian radar systems with new British signal processing and display systems.

#### Talking Exchange

Britain's 1,000 blind telephone operators will have the benefit of a speaking PABX exchange. GEC, British Telecom and the Royal National Institure for the Blind have devised a plug-in black box for the latest Monarch 120 PABX. This microprocessorcontrolled exchange has a visual display. Any information displayed is automatically 'spoken' to the blind operator via a speech synthesizer. **E**VERYONE must make decisions on a routine basis. The microelectronic revolution has provided us with hand held calculators to assist us in handling day-to-day number problems. Why not apply this same technology to day-to-day decision making?

This project has been developed after years of research into the decision making patterns of the man on the street. Operation has been made as simple and straightforward as possible. Gone are the days of feeding a computer with information for hours on end and spending the same amount of time deciphering what the computer's exact response was. Modern advances in microelectronic technology have enabled us to design and build one of the most sophisticated and powerful decision making aids in the world. It is as easy to operate as flipping a coin which can only provide an answer based on chance. This modern marvel of engineering ingenuity can not only provide instantaneous answers based on the information presented by the operator and its relationship with time and space but also eliminates the fatigue problem caused by flipping coins.

The basic model has been designed to offer the optimum responses to the widest range of questions that can be presented. The computer's basic responses are:

/ES	RE-THINK	MAYBE	RE-ENTER	NO

#### **ALTERNATIVES**

There are many other response patterns that can be programmed into the computer. Some examples of these are shown:

#### **Pilot's Collision Avoidance Computer**

	RIGHT	LEFT	PANIC	UP	DOWN
--	-------	------	-------	----	------

Navigator'	s Decision (	Computer			
NORTH	EAST	PANIC	SOUTH	WEST	
Diplomat's	Decision C	omputer			
PERHAPS	MAYBE	POSSIBLY	CONCEIVABL	POTENTIALLY	
Pay Rise Computer					
NO CHANCE	IMPOSSIBLE	NO	NEVER	TRY NEXT YEAR	
Idi Amin's Decision Computer					
MAIM	EXECUTE	KILL	MURDER	VANISH	
COMPONENTS					

Resistors	
R1-R20	10M (20 off)
R21	10k
R22-R24	1M (3 off)
B25	470k
R26	10
All 10% <del>1</del> W	
Capacitors	
C1-C3	4µ7 10V tantalum bead
C4	10n polyester
Integrated Circ	cuits
IC1-IC2	4011B (2 off)
1C3	4022B
IC4	4049B
Miscellaneous	

#### Miscellaneous

EENY ... MEENY

MINY ....

B1--9V battery, D1-D5-Red I.e.d. 0.2in.

All parts can be obtained from **Compu-Tech Systems**, Gaymer Way Industrial Estate, North Walsham, Norfolk NR28 OAN

# Indecision Eliminator

W.C. DICKINSON



#### **OPERATION**

The two touch contacts on the front panel are provided to accept data via direct contact with the operator. The operator's brainwaves are conducted into the unit and enter a brainwave to digital data converter. This data is instantly accepted and simultaneously locked to time and space vectors.

Upon acceptance of the information the five data output

indicator l.e.d.s will begin to flicker, indicating that your input data is being processed. The unit is now searching for parity between time and space vectors and your question. Process time has been extended to approximately three seconds in order to ensure compatibility with the optic nerve.

After the data has been processed the scanning of the l.e.d.s will cease and one of the data output l.e.d.s will remain illuminated. If the 'RE-ENTER' l.e.d. illuminates parity did not exist between time and space vectors and your question. In other words you asked the question at the wrong time.

If the 'RE-THINK' I.e.d. illuminates the computer has found a fundamental error in the data presented to it. In other words you asked it a question that it could not answer.

The remaining three l.e.d.s are the computer's response to your question after an



Fig. 3. Component overlay



Format of circuit box

analysis of all data presented and the relevance of your question to the overall operation of the universe.

Once the computer has given a response the l.e.d. will remain illuminated for approximately three seconds and then return to the quiescent but ever alert state. In the power down state negligible current is drawn from the batteries thus eliminating the need for an on-off switch.

This easy to build 3 band stereo AM/FM tuner kit is designed in conjunction with Practical Electronics (July issue). For ease of construction and alignment it incorporates three Mullard modules and an I.C. IF. System.

FEATURES: VHF, MW, LW Bands, interstation muting and AFC on VHF. Tuning meter. Two back printed PCB's. Ready made chassis and scale. Aerial: AM - ferrite rod, FM - 75 or 300 ohms. Stabilised power supply with 'C' core mains transformer, All components supplied are to P.E. strict specification. Front scale size 10%"x 2%" approx. Complete with diagrams and instructions

## SPECIAL OFFER! TUNER KIT PLUS

 Matching I.C. 10+10 Stereo Power amplifier kit (usually £3,95 + £1,15 p&p)
 Mullard LP1183 built preamp. suitable for ceramic and auxiliary inputs (usually £1.95 + 70p p&p)

• Matching power supply kit with trans-former (usually £3.00 + £1.95 p&p)

· Matching set of 4 slider controls complete with knobs for bass, treble and volumes (usually £1.70 + 80p p&p)

21.95 plus £3.80 p&p



## O AMPLI

· Featuring latest SGS/ATES TDA 2006 10 watt output IC's with in-built thermal and short circuit protection.

Mullard Stereo Preampilier Module,
Attractive black vinyl finish cabinet, 9"x 8%"x 3%" (approx)
10+10 Stereo converts to a 20 watt Disco amplifier.

To complete you just supply connecting wire and solder. Features include din input sockets for ceramic cartridge, microphone, tape or tuner. Outputs - tape, speakers and head-phones. By the press of a button it transforms into a 20 watt mono disco amplifier with twin deck mixing. The kit incorporates a Mullard LP1183 pre-amp module, plus power amp assembly kit and mains power supply. Also features 4 slider level controls, rotary bass and treble controls and 6 push button switches. Silver finish fascia with matching knobs and contrasting cabinet. Instructions available, price 50p. Supplied £14.95

FREE with the kit. SPECIFICATIONS:

Plus £2,90 p&p

Frequency response Input sensitivity

Suitable for 4 to 8 ohm speakers. 40Hz - 20KHz. P.U. 150mV. Aux. 20JmV. P.U. 150m V. Aux. 200m Mic. 1.5m V. Bass ±12db @ 60Hz Treble ±12db @ 10KHz 0.1% typically @ 8 watts 220 - 250 volts 50Hz.

Tone controls Distortion Mains supply

STEREO MAGNETIC PRE-AMP CONVERSION KIT Includes FREE Magnetic cartridge with diamond styli, All components including p.c.b. to convert your ceramic in-

put on the 10+10 to magnetic. Only available with 10+10 amp. £2.00 includes p&p.

8" SPEAKER KIT Two 8" twin cone domestic speakers. £4.75 per stereo pair plus £1.70 p&p, when purchased with amplifier, Available separately £6.75 plus £1.70 p&p.

#### PRACTICAL ELECTRONICS SERIES II **CAR RADIO KIT**

2 WAVE BAND MW -- LW

\* Easy to build

• 5 push button tuning • Modern design

. 6 watt output . Ready etched and punched PCB . Incorporates suppression circuits

All the electronic components to build the radio, you supply only the wire and the solder, featured in Practical Electronics March issue. Features: pre-set tuning with 5 push button options, black illuminated tuning scale. The PLE. Traveller has a 6 watt output neg, ground and incorporates an integrated circuit output stage, a Mullard IF Module LP1181 ceramic filter type pre-aligned and assembled, and a Bird pre-aligned puts hutton tunion unit

aligned push button tuning unit. Plus £2.00 p&p

Suitable stainless steel fully retractable aerial (locking) and speaker (6"x 4"app.). available as a kit complete £1.95/pack. Plus £1.15 p&p.





#### **30+30 WATT STEREO AMPLIFIER**

Viscount IV unit in teak simulate cabinet, silver finished rotary controls and pushbuttons with matching fascia. mains indicator and stereo jack socket, Functions switch for mic magnetic and crystal pickups, tape and auxiliary. Rear panel features fuse holder. DIN speaker and input socket 30+30 watts RMS, 60+60 watts peak. For use with 4 to 8 ohm speakers. Size 14%"x 10" approx. BUILT AND TESTED. £32.90

Plus £3.80 p&p

#### PHILIPS BELT DRIVE RECORD PLAYER, DECK GC037 (Size: 15%"x 12%"approx.)

HiFi record player deck, 2 speed, damped cueing, auto

shut-off, belt drive with floating sub chassis to minimise acoustic feedback. Complete with GP401 stereo magnetic cartridge LIMITED STOCK.

UNBEATABLE OFFER AT £27.50 COMPLETE Plus £3.16 p&p

CALLERS ONLY 323 Edgware Rd, London W2. Tel: 01-723 8432. Open 9.30am - 5.30pm. Closed all day Thursday. Persons under 16 not served without parents authorisation

ALL PRICES INCLUDE VAT AT 15%



P.E. QUAJAR

generously rated components, result, a high powered rugged unit. The PC Board is back printed, etched and ready to drill for ease of construction and the aluminium chassis is preformed and ready to use. Supplied with all parts, circuit diagrams and instructions.

#### ACCESSORIES:

Suitable LS coupling electrolytic for 125W model	£1.00 plus 25p p&p.
Suitable LS coupling electrolytic for 200W model	£1.25 plus 25p p&p.
Suitable mains power supply unit for 125W model	£7.50 plus £3.15 p&p.
Suitable Twin transformer power supply for 200W model	£13.95 plus £4.00 p&p

## MONO MIXER AMPLIFIERS



50 WATT Six individually mixed inputs for two pick ups (Cer. or Mag.), two moving coil microphones and two aux iliary for tape, tuner, organs, etc. Eight slider controls - six for level and two for master bass and treble, four extra treble controls for mic and aux inputs, Size: 13%"x6%"x3%"app. Power output 50 watts R.M.S. (continuous) for use with 4 to

8 ohm speakers. Attractive black vinyl case with matching fascia and knobs. Ready to use

£39.95 Plus £3.70 p&p

Brushed Aluminium fascia and rot ary controls Size: approx. 14"x4"x10%"

**100 WATT** 

Five vertical slider controls, master volume, tape level, mic level, deck level, PLUS INTERDECK FADER for perfect graduated change from record deck No. 1 to No. 2, or vice versa. Pre fade level controls (PFL) lets YOU hear the next isc before fading it in. £76.00 VU meter monitors output. 100w RMS output (200w peak) Plus £4.60 p&p

#### MAIL ORDER ONLY

21B HIGH STREET, ACTON, W3 6NG. Note: Goods despatched to UK-postal addresses only. For further information send for instructions 20p plus stamped addressed envelope. Please allow 28 days for delivery

All items subject to availability. Prices correct at 1/11/81 and subject to change without notice. RTVC Limited reserve the right to update their products without notice

200 W Model 125 W Model 200 watts 70 - 95 max. Max, output power (RMS) 125 watts 50 - 80 max. 4 - 16 ohms 4 - 16 ohms measured @ 100 watts 25Hz - 20KHz Sensitivity for 100 watts 400m V @ 47K Typical T,H.D. @ 25Hz - 20KHz 400mV @ 47K

**PRACTICAL ELECTRONICS - STEREO** 

ONI Y

**IGH POWER** 

KIT £10.50

Plus £1.15 p&p

Plus £1.15p&p

£14.95

READY BUILT OR IN KIT FORM

125 WATT MODEL

200 WATT MODEL

SPECIFICATIONS:

Operating voltage (DC)

Frequency response

oads

£17.95

Plus £1.15 p&p.

50 watts, 4 ohms 0.1% 0.1% Dimensions (both models) 205 x 90 and 190 x 36mm The power amp kit is a module for high power applicat-ions — disco units, guitar amplifiers, public address systems and even high power domestic systems. The unit is protected against short circuiting of the load and is safe in an open

circuit condition. A large safety margin exists by use of

# Semiconductor UPDATE.... FEATURING RCA1804 WTV008 R.W. Coles

#### **1802 UPGRADE**

About the only thing good you can say about the RCA 1802 microprocessor is that it is a CMOS device and therefore uses very little power. That simple fact has assured it a place in the micro hall of fame, despite the fact that only masochists would choose to use it if they were not forced to by power supply limitations!

There are other CMOS devices of course, like the IM6100 which was an early competitor and even more ghastly than the 1802 in some respects, and the much newer NSC 800 which looks as though it will be a winner once National have learned how to make it in quantity and the price has dropped. For the moment however, if you want a cheap CMOS microprocessor with reasonable hardware and software support it has to be the cranky old 1802, which is why any improvements RCA can dream up are very welcome indeed.

RCA are selling 1802s at an ever increasing rate (over a million in 1980, according to their ads.) but they do realise that those unfortunate enough to be designing with the chip would happily change their allegiance if something better came along, and so they have been trying hard to make the 1802 more attractive by the addition of new family members with improved performance and extra features.

Already available at low cost is the 1802A which is directly compatible with the basic chip but made with an advanced CMOS process which has reduced the chip area and therefore increased both the speed of operation and the yield per wafer. At, five volts the 1802A runs with a 3.2MHz clock as against 2.5MHz for the basic 1802, and some "tweaking" of the chip layout has improved the operation of functions such as Power On Reset to give a plug-in performance booster for new or existing 1802 circuits.

In the pipeline are two brand new members of the 1802 family which go much further than the 1802A in overcoming the limitations of their predecessor. First on the scene will be the 1804 which will run at 6.0MHz and has many enhancements to both the hardware facilities and instruction set of its parent. One of the big limitations of the 1802, the lack of explicit Jump-to-Subroutine and Return instructions, has been rectified with the 1804 to give a five times speed improvement when performing these functions. New instructions are also provided to allow the direct manipulation of 16 bit data, and to control a new on-chip timer function which can greatly simplify system design. Even more impressive is the ability of the 1804 processor to do the whole job single handed in many applications since it has a built-in 64 byte RAM array and a mask programmable 2Kbyte ROM to hold programs or data, in addition to its ability to address 64Kbytes off-chip. Add to this the fact that the 1804 is largely pin and instruction set compatible with the 1802 and it becomes a very attractive chip indeed.

Of course, not everyone needs the luxury of on-chip masked ROM programs, and so there is a companion device with all the other goodies but without the ROM, coded the 1805.

It looks as though RCA have managed to snuff out the "I Hate the 1802" movement before I even managed to get the badges made!

#### **TALK CONTROL**

Electronic speech synthesis is now a lost cost technique, and before long, all selfrespecting vending machines and domestic appliances will be bending our ears with condescending announcements from their built-in speech chips and loudspeakers. , Speech recognition, on the other hand, is a more difficult problem for the chip makers to solve, and it will be some time before the day dawns when we can expect to hold an intelligent conversation with our electric toaster about how we would like our toast done today (Thank goodness!).



Do not doubt for a minute, however, that this day will dawn eventually. Already the first terrifying steps have been taken on the slippery slope leading to smart Alec toasters and supercilious coffee machines with the introduction of a new device coded the WTV008 from a bunch of subversives called the Weitek Corporation of Santa Clara, California.

The WTV008 looks harmless enough in its demure 28-pin plastic package, but in fact it is the first of a long line of speech recognition chips which will make all our lives a misery if we don't act immediately to stamp out their proliferation. Oh, it seems innocuous all right, it only recognises eight words and then it only gets it right 90% of the time for 90% of the population, but this is only a beginning remember; before long the vacuum cleaner will be hanging on our every word, and no doubt reprimanding us when we swear at it.

If you really welcome this sort of thing, you may be pleased to know that the WTV008 has a unique output line for each word in its active vocabulary and a four line BCD output which gives a number code too. A strobe line shows that a valid output is present, and you can switch between two alternate eight word ROM based vocabularies by toggling the USEL input.

You have been warned!

#### THE BLUE LAMP

The possibility of a wall sized flat TV display unit based on l.e.d. lamps has now moved a step nearer with the introduction of a new semiconductor material which emits BLUE light when forward biased. RED and GREEN light emitting diodes based on gallium phosphide material are already with us of course, but the new devices based on silicon carbide complete the primary colour triad essential for the full colour spectrum of television reproduction.

The silicon carbide process has been developed by Sanvo in Japan, and rather than just offer a new colour for panel lamps the ingenious designers have recognised the potential for TV type applications and have mounted the new chip alongside red and green emitters in the same package. The four package pins allow independent control of brightness for each colour, but allowance has to be made for the different band gap voltages and efficiencies of the different materials. The silicon carbide l.e.d. produces about 2 millicandelas for a current of 20 milliamps at 3.5 volts, whereas the green chip gives 3 millicandelas for 10 milliamps at 2 volts, and the red chip gives 3 millicandelas for 5 milliamps at 1.9 volts.

If the prospect of buying and soldering in 25,000 l.e.d. lamps to make a TV display does not appeal to you, you can amaze your friends with just one of the little perishers used as a multi-coloured panel lamp!

## PHILLIP GAFFNE

Part 1

T HIS is a monochrome television camera you can build yourself, which is ideal for closed circuit home video or security applications. This camera requires only a 24V/1A supply and may be wired using a single coax lead carrying both signal and power. The output is UHF modulated for direct "aerial socket" input to a standard television receiver.

#### **POWER SUPPLY**

The unit may be powered by a simple 24V PSU without semiconductor regulation circuits. That is to say, the standard "transformer-bridge-capacitor" configuration will do, although the line should be protected with a 1A quick-blow fuse.

Also, the supply line from wherever it may be derived, should include the filter circuit of Fig. 1.1 to allow the same wire to carry both signal and power.

24 volt power from point A is passed through L1 without hindrance, via the coaxial socket on the front panel, to the camera. The sheath of the coaxial cable is at earth potential, the core being at 24V. The d.c. is, however, blocked from going into the television set as C1 can be regarded as an open circuit to d.c. voltages. UHF received from the camera is blocked by the small inductor L1 preventing it from being dropped to ground by the low impedance of the power supply. Capacitor C1 has a very low reactance at UHF and allows the passage of the UHF signal to the television receiver.

Thus it can be seen that this simple circuit allows us to utilise the same coaxial cable to deliver power to the camera and convey signals from the camera to the television sets. When the unit has been fully assembled, and the voltage at point "A" checked, about 24V d.c. should be found at the centre pin of the coaxial socket on the front panel with respect to the case.

#### **INVERTER AND HIGH VOLTAGE BOARDS**

The PE (Seescan) camera is of modular construction, with the printed circuit boards connecting together with the coil assembly and back plates to form an extremely robust unit.

The inverter and high voltage power supply boards are used also as "cross-members" in the structure.

The inverter board steps up the low voltage camera supply to about 400V which is used to provide all the high voltages required to drive the pick-up tube (Vidicon in standard camera kit). The high voltage power supply board uses this 400V a.c. to produce several regulated d.c. outputs at the levels required by the picture tube. This board carries the three primary set-up adjustment presets for the pick-up tube, namely beam current, electrostatic focus, and target voltage. These three controls are easily accessible from the rear of the camera to make the final setting-up easy.



Fig. 1.1. PSU line filter arrangement, The L/C configuration separates VHF from the d.c. so that a single lead may service the camera



#### INVERTOR CIRCUIT DESCRIPTION

The invertor, a small p.c.b., mounted vertically in the camera, is used to step up the low voltage camera power supply rail to a high d.c. voltage suitable for driving the Vidicon pick-up tube. As can be seen from the circuit diagram, the invertor electronics are fairly straightforward. Line sync. pulse from the logic board are used to drive TR15 into saturation. Therefore, an inverted replica of the line sync pulse is developed across R202. This signal is used to drive T15 which acts as a switch, applying current pulses to the invertor transformer TR200. R203 and C201 decouple the output transformer from the main regulated 12V supply. C202 tunes the transformer to peak the large fly back pulse produced, thereby increasing efficiency. As all switching in the transformer takes place during line blanking there is no problem with any interference produced appearing on the camera picture.





#### **HIGH VOLTAGE POWER SUPPLY CIRCUIT**

This small sub-board is used to rectify the high frequency a.c. from the inverter board and regulate the d.c. output to supply the necessary voltages for the pick-up tube. Both positive and negative voltages with respect to ground are required for the tube, and both are produced on this board. Rectifier diode D13, half-wave rectifies the 350V a.c. and the resulting d.c. is smoothed by C43. It should be noticed that D13 is a fast recovery diode, and it is important that the types specified should be used. If ordinary general purpose rectifiers are used, the efficiency will be lower, and sharp switching spikes will be generated in the inverter, which will appear as lines on the television picture. R78 and C49 further decouple the supply at high frequencies before being applied to D15–D19. These series connected Zener diodes crudely regulate the high voltage supply at about 350V, which is applied directly to the mesh of the Vidicon tube. R79, VR80, R81 and VR82 form a potential divider chain, voltages for the second grid, focusing anode and target, all being tapped at various points from this divider. C49 and C46 are used to decouple the grid and target respectively.

D8 is a fast recovery rectifier diode used as a half-wave rectifier, the d.c. from which is smoothed by C44. Diode D14 is used to stabilise the supply at round about -95V with respect to ground. Resistor VR84 allows a negative grid bias (Beam current) to be set between OV and -95V. R83 and C45 effectively decouple the supply at high frequencies from G1.





Test	voltages
А	380
8	350
С	280
D	100
E	-110
F	0 TO 100

All voltages are measured with respect to Ground and are measured with a meter having greater than 25K ohms per volt sensitivity. All voltages are measured with no light reaching the Vidicon target. The voltages at these test points should be within 5% of the stated values.







Fig. 1.4. Invertor board p.c.b. (actual size) Fig. 1.5. Invertor board component layout

ed

1M Cermet preset (3 off)

Note: The photographs of the prototype show the original invertor board, which has since been redesigned to use only one power transistor.

#### ACKNOWLEDGEMENT

We would like to thank the Bondi Judo Club of Poole for their assistance with our front cover photograph.

#### COMPONENTS

Invertor and High Voltage Boards Resistors

esistors	
R78	120k
R79, R83	470k (2 off)
R81	1M5
R85	100k
R201	22k
R202	470
R203	22 1W
All resistors 1W	5% unless otherwise specifi

#### Potentiometers

VR80, VR82, VR84

#### Capacitors

C49	22µ
C43	100n
C44	100n/400V
C45, C46	10n/750V (2 off)
C48	100n
C201	47µ/16V
C202	2n2

#### **Transistors and Diodes**

TR15	BFX85
TR16	BC182
D8, D13	BY207 (2 off)
D14, D16, D17, D19	100V 1.3W Zener (4 off
D15	51V 1.3W Zener



Fig. 1.6. High voltage board p.c.b. (actual size).



Fig. 1.7. High voltage board component overlay

#### **Miscellaneous**

Inverter p.c.b. High Voltage p.c.b. Varelco connectors (for PL1–PL4) Heatsink for TR15 Heatsink jointing compound Screws. 6mm M3 (2 off) Nuts. M3 (2 off) Transformer T200 (special)

#### **Constructor's Note**

A complete kit of parts is available from Marshall's of Kingsgate House, Kingsgate Place, London NW6 4TA.



#### **IN RETROSPECT**

In the matter of processed data, the presentation to the world at large varies a great deal as to time of release. Sometimes this is short and sometimes long. Usually the final release proves that science had profited. Such a situation arose in 1979. On August the 30th 1979 the Naval Laboratory coronagraph on board a spacecraft photographed a collision between a comet and the Sun. The reason for the delay in making this outstanding event public is that the data has only recently been analysed.

The coronagraph known as the Solwind, was designed to occult the disc of the Sun so that the corona is clearly visible-particularly the outer corona where there is much to be studied. In this special event a great deal of debris resulting from the encounter with the Sun was thrown out into this area. The spacecraft which carried the coronagraph is a Ball Aerospace vehicle which is similar to the NASA orbiting observatories. Because there were delays in releasing the data to the Naval Research Laboratories for analysis, the details have only just been discovered. It is thought that this comet may be one of a group of about eight comets which are known as Sun grazers. During the last three hundred years these eight comets have been observed from time to time. Because of the difficulty in making observations of any bodies close to the Sun and also the difficulty of recognising a body which when away from the Sun might be seen only by accident.

The images were to reveal that the tail of this comet going to its death was of the order of three million miles long. The closing speed was estimated to be about 645,000 miles an hour. The result of the impact threw the cometery debris millions of miles into space away from the solar surface. The comet was not actually reported as being seen from Earth even though the images returned show that it was as bright as Venus since the planet was in the frames of the photographs with the Sun itself. Doubtless more of such events will be observed and the secret of the comets with the mission to Halley's comet in 1986 should finally be within mankind's knowledge. Had there yet been any forecasts of dire events? Did August the 30th 1979 mark something or other?

#### SOVIET SPACE RELATIONS TAKEA NEW TREND

It is so often said of the old adage "It's an ill wind that blows nobody any good" that it is a way of excusing benefits in the face of sore distress. Originally there were wide and comprehensive hopes for the special combined missions to extract the maximum data and number of observations of Halley's comet. Much of the hope seemed to evaporate because of fiscal difficulties and part at least of the co-operative plans were delayed and then dropped. The Soviet Union offered a modification of one of their missions to Venus in an effort to maximise the failing situation.

It now seems that this has grown into a much wider possibility of joint effort. A joint planning meeting in Italy last September laid foundations for an international framework involving the Soviet Union. Italy, the European Space Agency. Japan and the United States. It is fortunate that these plans are now under ' way with greater momentum. If this pass of the comet inside the Earth's orbit were missed the opportunity would not come again until 2061.

The Soviet plan for two spacecraft is confirmed, the Japanese and the European Space agency have their programmes. Only the United States is yet uncommitted. However the American contribution at the moment will come through the involvement in the international Halley Watch. NASA has plans at the Jet Propulsion Laboratory and a proposal for a mission to collect samples from the comet. This however is not yet funded.

The Soviet dual mission is part of the separate plan to launch two spacecraft in December 1984 which will include a Venus pass in 1985 with comet encounter in 1986. The Venus study will be by two probes which will descend into the Venusian atmosphere and be deployed down to the surface. The spacecraft will then pass on to the Halley rendezvous. A drawback to this plan lies in the fact that the speed of the spacecraft will be higher than the comet during the encounter. Data transmission will be about 85 kilobits a second. Approximately 30 kilobits of this data will be devoted to the television cameras with wide and narrow angle facilities.

It was stated by an official of the Soviet group dealing with the mission that the camera system will be brought into the operational mode two days before the closest approach. At this time there will be general observations of the nucleus. Some three hours of observations will be made during the closest approach. The automatic operation of the system is set for the cameras to seek the brightest parts of the comet.

In addition to the imaging experiments there will be a three channel spectrometer, an infra-red spectrometer and a dust spectrometer. There will also be a dust particle counter with magnetometers and analysers of plasma waves at both high and low frequencies. Both the spacecraft will be through the same launch window late in December 1984. This will require accurate planning because it means two launches within a short period of a few days.

Japan's contribution will be the Planet-A spacecraft in August 1985. The launch vehicle will be an upgraded Mu launcher. The spacecraft will monitor the ultra-violet radiation from the comet and also measure the solar wind plasma near the comet. The two probes will be spin stabilised. The European Space Agency Giotto has already been described in a previous Spacewatch issue. The programme as it stands at the moment is for the launch by Ariane launch vehicle through a window available for fifteen days during July 1985. The encounter is planned for a date in March 1985. The actual encounter will be on March 12/13 1986. The planned sequence of manoeuvres will begin about 30 hours before the time of closest approach. During this period there will be a two hour rehearsal of the experiment and calibration checks. It is hoped that the encounter will last at least 4 hours. The expected time of this event is 3hrs 45min. before the closest point of approach. The visible corona will be entered at about 1hr before the closest approach point. It is hoped that it will be possible for this to occur in real time.

#### THE USSR AND FRENCH TECHNOLOGY

Since September a Soviet spacecraft, the Arcad satellite, has been undergoing tests and now it begins its programme. The joint payload vehicle is in an orbit 1,192 at apogee and 236 miles at perigee, inclined at 82-6 deg. It weighs 2,200lb. The French share of this project is four of the experiments with onboard programming and the telemetry unit.

The data programme is directed at the magnetosphere in the higher levels of the atmosphere, particularly at higher altitudes. The instrumentation is contained in a cylindrical body. The power is derived from eight solar panels spaced round the main body The cooperation was carried out with mixed personnel, and the facilities in both the USSR and France were used.

#### THE SHRINKING SUN AGAIN

A new paper in support of this experiment regarding the shrinking sun gives more information that the regular pulsation with a period of 76 years is confirmed and a new statement that the accepted radius of the Sun is greater than that hitherto used for the calculations.

#### SUBMILLIMETRE WAVELENGTHS

At a recent conference on submillimetre wavelengths in radio astronomy new important information was revealed. Using the special airborne observatory, which is able to fly above the blanket of absorbing water vapour in the atmosphere, revealed a neutral atomic gas cloud near the centre of the Galaxy. The gas was detected by its emission from neutral oxygen. 63-2 micrometres.

# A movina



The new MC88E represents a breakthrough in high output moving coil cartridges. No step-up device or amp is required and it is available at a sensation-

al price of only £39.95. yet generating high magnet-The high output voltage of 2.5mV does away with the step-up transformer, which add to the expense of using most previous moving coil cartridges.

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ic flux density; compliance of 17 cu's. The result is a need for a head amplifier or cartridge with flat frequency response over the super wide range of 20Hz - 40KHz, removing the distortion caused by certain frequencies, which can be found in many conventional cartridges. Coral's considerable experience in moving coil cartridges has enabled them to offer the ultimate in quality and performance at this incredibly low price.

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Updated version of a very popular ignition system published seven years ago. Couples the advantages of easier starting, smoother running, better fuel economy and longer spark plug life in a unit almost half the size of the original design, and at a lower cost.



## BENCH P.S.U

Everyone developing and building electronic projects requires a variable power source. Unfortunately, a good commercial stabilized, protected and metered supply giving up to 30V and 2A would cost about £80. Our p.s.u. should fulfill the requirement for less than half that figure.

## INFRA·RED REMOTE CONTROL



Michael Tooley B.A. David Whitfield M.A.M.Sc. PART ONE

BASE S

N COMMON with other hand-held transceivers, the PE Ranger in its most basic form is primarily intended for short-range portable communication and, although the operational range can be considerably extended by the use of an external supply and aerial, there may be many occasions when additional coverage is required. The Base and Mobile Adaptor unit described not only increases the r.f. output power from 0.5W to 4W (the maximum permitted) but also provides some additional r.f. amplification and selectivity on receive. The result is a three to four-fold increase in effective range when compared with the basic unit with the same antenna system. Furthermore, an audio power amplifier is also incorporated and this is particularly useful when the Ranger is to be used in a relatively noisy mobile environment. The Base and Mobile Adaptor operates from a nominal 12V d.c. supply and also provides power for the Ranger transceiver. A 240V a.c. supply may also be used by the addition of the optional mains power unit which is also described. Transmit/receive control switching is automatic and a meter is provided to indicate the strength of received signals and the relative power output. To comply with Home Office regulations a 10dB switched attenuator is incorporated within the equipment. Two l.e.d.s provide 'at a glance' status indication; an important consideration when the equipment is to be used mobile. The unit is housed in an identical size case to that used for the basic Ranger transceiver.

#### SYSTEM DESCRIPTION

The Base and Mobile Adaptor performs three basic functions:

- (a) r.f. power amplification (Transmit)
- (b) r.f. pre-amplification (Receive)
- (c) a.f. power amplification (Receive)

The block schematic of Fig. 1.1 shows the basic arrangement of the unit and, as can be seen, the transmit and receive paths within the unit are quite separate. Switching is achieved by means of a relay and this provides a high degree of isolation between the two circuits.

On transmit the output signal of the Ranger is applied to a single stage power amplifier which provides a gain in excess

of 10 dB. This implies that, when driven with a nominal 0.5W from the Ranger, the r.f. output power from this stage will be somewhat greater than 5W. The r.f. amplifier stage is followed by a band-pass filter which improves the spectral purity of the output signal by imposing a high rate of attenuation both below and above the design cut-off frequencies. Since the filter exhibits a loss of the order of 2dB, the actual r.f. output to the aerial will be approximately 4W. The precise value of r.f. output power can be adjusted by setting the r.f. output control within the Ranger transceiver itself.

DARAAA

The unit also incorporates a simple form of switched output attenuator which provides an approximate 10 dB reduction in output power when required by the terms of the Home Office licence.

On receive the incoming signal from the aerial is applied to a single stage r.f. pre-amplifier. This stage provides a modest value of gain which, although not strictly necessary, does help to improve the performance when receiving weak signals under quiet band conditions. A further advantage of this stage is that the additional funed circuit provides further rejection of the image channel (910kHz below the wanted signal frequency) and this is all important in reducing interference from strong local signals operating in the illegal band around 26 9MHz. The gain of the r.f. stage is variable and offers a typical range of adjustment of some 40dB or more. At minimum gain settings the r.f. pre-amplifier stage effectively acts as an attenuator. This is beneficial in reducing the levels of strong signals and thus prevents overloading in the front-end stages of the Ranger.

The audio power amplifier operates in the receive mode only and increases the audio power when an external loudspeaker is used. The unit also incorporates a dual function meter which is used to indicate the strength of received signals in the receive mode and power output in the transmit mode. The S-meter signal is derived within the Ranger by means of an optional add-on module which consists of an amplifier/detector operating on the 455kHz i.f. signal. Connections to the Ranger and external power sources are shown in the block diagram of Fig. 1.2. Only two interconnecting cables are required; one to carry the control voltages, audio and S-meter signals and one to carry r.f.



RANGER TRANSCEIVER MAINS POWER +121 240V SUPPLY MODULE S-METER 455 kHz MODULE AERIAL 50 0 COAX-PL259-PL259 6 WAY --DIN-DIN 500 COAX BASE/MOBILE ADAPTOR 0 12 V (NOM.)

#### EP 761

Fig. 1.2. Interconnection arrangement for the Base/ Mobile Adaptor (N.B. Dotted lines indicate optional items)

#### **CIRCUIT DESCRIPTION**

The circuit diagram of the base station is shown in Fig.1.3. Red and green I.e.d.s, D1 and D2, provide status indication for 'transmit' and 'receive' respectively. The relay, RLA, operates whenever the transmit supply rail from the Ranger is enabled. The a.f. power amplifier, IC1, employs a conventional arrangement with voltage gain determined by R4 and R5. The supply voltage for the a.f. power amplifier is derived from the receive supply rail within the Ranger thus obviating the need for additional changeover contacts on the relay. The r.f. power amplifier, TR2, uses a silicon r.f. power transistor designed specifically for use in 27MHz CB equipment. The device is operated in common emitter mode under class-C conditions. Input matching to the base is provided by VC1, VC2, L3 and C11 while output matching from the. collector is by means of L6, VC3 and VC4. The harmonic content of the output signal is reduced by two bandpass filter modules. The first of these removes the bulk of the spurii before the signal arrives at the changeover relay. The second is directly connected to the aerial socket and provides a 'last ditch' trap for unwanted harmonic signals. These two filters are required, as in the basic Ranger

ODEOLELOATION	
SPECIFICATION	
RF POWER AMPLIFIER	
Power output:	4W .
Input/output impedance:	50Ω
Attenuator:	10dB (nominal)
Power gain:	10dB (nominal)
AF POWER AMPLIFIER	
Power output:	2W (into 4Ω)
Load impedance:	$4\Omega$ to $16\Omega$
Input impedance:	100kΩ
voltage gain:	5
RF PRE-AMPLIFIER	
Voltage gain:	16dB
Input/output impedance:	50Ω 40dB (autom)
Gain variation:	40dB (approx.)
GENERAL	
Power supply:	11-14.5V d.c. negative ground
	220-250V a.c. 50Hz with
	optional mains adaptor
Status indicators:	Transmit (red), receive (green)
Controis:	Power on/on
	r.i. gain Attenuator on/off
External connections:	Aerial and transceiver (S0239)
	Control and audio (6-pin d.i.n.)
	External d.c. input
	Loudspeaker
Meter:	Power output (transmit), signal
	strength (receive)
Dimensions:	200mm x 120mm x 40mm
Weight:	0.8kg



Fig. 1.3. Circuit diagram of the Base/Mobile Adaptor

transceiver, to ensure that the equipment meets the Home Office specifications concerning spurious emissions from CB equipment.

As with all r.f power amplifiers careful consideration has to be given to supply rail de-coupling and this is provided by R10, C12 and C13. These components ensure that the supply rail exhibits a negligible impedance over a very wide range of frequencies. D9 and D10 sample the r.f. output level and provide a signal for the power output meter. When an attenuated output is required to comply with the Home Office regulations concerning elevated aerial systems, R9 is switched into the r.f. power amplifier's supply rail by means of S2. This series resistor reduces the d.c. input power to the stage and consequently reduces the r.f. output power. The result is an approximate 10dB reduction in output from the amplifier stage.

The receive pre-amplifier, TR1, employs a junction gate f.e.t. operated in common gate configuration. This provides a high value of power gain coupled with a low input and high output impedance. Noise performance is of comparatively little concern at 27MHz due to the residual level of cochannel signals and thus no attempt has been made to optimise the stage for low noise performance. The r.f. gain is made adjustable by varying the static drain-source current whilst silicon diodes, D5, D6, and D7, D8, provide input and output protection for the pre-amplifier. This protection is important in the case of an inadvertent misconnection of the



The prototype base station

r.f. input/output or in the event of a failure in the changeover relay. The equipment is also protected against reverse polarity supply connection. When the supply is wrongly connected D4 conducts and this causes the fuse to rupture. This may appear to be somewhat crude but it is highly effective and can prevent extensive and costly damage to the rest of the circuit.

**NEXT MONTH:** Construction and alignment of the Base and Mobile Adaptor plus details of the S-meter module.
COMPONENTS		Inductors					
COMPONENTS.	• •	L1/L2, L3	KXNSK 4612 (2 off)				
Resistors		L4-L6	see text				
P1 P2 P6 690/2 o	<del>ff</del> )						
P2 92 11/		Semiconducto	IFC				
	<del>65</del> 1	Di	rod Lo d				
P5 22	117		aroon Lo.d				
D7 1L		D2 D5 D6 D3					
PQ 10.2\V/ 6	coo toxt)	03, 05, 00, 07	INA001				
R10 12.5\/	(Wirewound)	D9 D10	OA91/2  off				
All resistors are 1W carbo	n unless otherwise stated	101	TDA 2002				
	in unices other wise stated	TR1	25K55 2N3819 or a T15588A				
Potentiometers		TR2	MRF472				
VR1 100k lin		1112	WITH 772				
VR2 1M pres	et	Miscellaneous					
Canacitana		RI1	Relay type 221 D012 p.c.b. mounting 12V				
Capacitors	2.2.1 CV alast		2n c/ó				
CI		M1	200µA meter 'Signal strength/Power				
02			output'				
		S1. S2	s.p.s.t. min. togale (2 off)				
04, 06, 08, 010, 013	100n ceramic (5 on)	SK1	6-way d.i.n. socket				
	2200µ 25V elect	SK2	Non-reversible chassis plug				
0,012,015,016	4n7 ceramic (4 01)	SK3	2.5 jack socket				
011	100n eeromie	SK4. SK5	Round SO239 socket (2 off)				
014	Fp6 coromic	FL1	Bandpass filter module (see text)				
All electrolytic conscitor	s are vertical n ch mounting	FL2	FL2 Bandpass filter module (see text)				
All electrolytic capacitor	s are ventical p.c.p. mounting	Case with front panel					
types		P.c.b.	and the second				
Variable capacitors		Knob					
VC1–VC4	60p min solid dielectric (4 off)	12V d.c. lead f	itted with non-reversible socket				

**Constructor's Note** 

1

A complete kit of parts for the base station is available from Autumn Products Ltd, Park Drive, Baldock SG7 6EW

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This month we have got together with Watford Electronics to bring you a quality tool at an unbeatable price.

This drill will prove an invaluable aid to the serious constructor. It is made in England, and designed to run from a 12V d.c. power supply. For occasional use, a wet or dry battery giving 9–12 volts may be used, but a mains transformer-rectifier with a 12V d.c. output is recommended. The drill is housed in a durable plastic case, and comes complete with 4 different sized collets, so that drills up to  $\frac{1}{8}$ " can be accepted. It is guaranteed for 6 months.

CK CAPITALS	To: Watford Electronics, 33/35 Cardiff Road, Watford, Herts. Please send me PCB Drill(s) @ £8.60 each I enclose P.O./Cheque No Value
f the coupon in BLC	Address
e both parts of	OFFER CLOSES 29th JANUARY 1982 Name Address
Complete	From: Watford Electronics, 33/35 Cardiff Road, Watford, Herts.



Realistic is the biggest name in Citizens Band Radio and accessories – and you will be able to buy the full range at Tandy – the world's largest retailer of CB equipment!



PART 3 A.J. BOOTHMAN

TO complete assembly of the Band-Box, the Display board requires to be joined to the System board and various interconnecting leads prepared. A seven pin DIN socket is also required to be fitted to the Master Rhythm to provide the necessary control pulses and drum audio to the Band-Box.

### **CONSTRUCTION OF THE SYSTEM BOARD**

Fig. 9 shows the track layout and component overlay for the p.c.b. and it is advisable to closely inspect this before proceeding. All components apart from the composition keyswitches and power l.e.d. are mounted from the side containing least tracks, and should be carried out in the order of resistors, diodes, track pins, i.c. sockets, capacitors, output sockets and switches.

It should be noted that a number of resistors require soldering on both sides of the p.c.b. and that the orientation of the keyswitches is important as indicated by the flat portion.

All rotary switches, apart from the key selector, require to be set to operate on the first four positions before insertion into the board. This is achieved by first turning the switch fully anti-clockwise to position one and then removing the mounting nut and washer. A metal ring, concentric with the shaft, will be seen when the washer is removed and it contains a small tab which will be pushed in one of the holes in the plastic body. The ring should be removed and repositioned such that it enters the hole between the numbers 4 and 5 marked on the plastic. The ring is retained in position when the washer and fixing nut are replaced. To obtain the twelve position action required by the key selector the metal ring should be discarded.

After mounting the switches on the p.c.b. bare metal links can be used to connect the switch tags, with an insulated conductor to the centre tag or tags. Before inserting i.c.s it is well worth the additional quarter of an hour required to check for shorts between adjacent tracks using a meter.

### **CONSTRUCTION OF THE DISPLAY BOARD**

The Display board contains, displays, input keyswitches with caps, and mounting positions for potentiometers. Track layouts and the component overlay are shown in Fig.10. The two diodes are mounted on the back of the board, and it should be noted that the 24 pin sockets have spare pins beyond the outer edge of each display pair when inserted.

### JOINING THE BOARDS

Twenty-six solid wire links are required to join the System board to the Display board and will provide sufficient mechanical support during initial testing provided that reasonably careful handling is given to the system. When mounted in the case the boards will be approximately 1–2mm apart so that a small amount of adjustment slack should be covered by giving the links a comfortable radius.

The links are shown in Fig. 11, as are all remaining interconnections within the Band-Box. When the links have been fitted, the three Level potentiometers should be wired to the corresponding pins on the System board using twin core screened wire. Note that the lead orientation for each set of terminals is different. A mounting hole is provided for a Pclip which will help retain the relatively heavy screened cable in a comfortable position. The required lead lengths are shown in the diagram against each relevant potentiometer.

### MASTER RHYTHM LINK

Twelve inch leads should be prepared to connect between the System board and the seven pin DIN plug used for the Master Rhythm link. A screened cable is used to carry the audio into the Band-Box and its screen makes the ground connection between board and plug. The connections are shown in Fig. 11 and represent the view from the back of the plug showing the actual solder points.

### **MICROCONTROLLER LINKS**

Power for the Microcontroller is provided from the pins on the left-hand side of the System board and terminates in a three pin connector which plugs into the Microcontroller board. The Molex connector tags can be crimped to the end of the wires using pliers, and if the special tool is not available a small amount of solder can be used to ensure that a good joint is made. Only +12V and ground are required to power the Microcontroller, the third (3V6) connection is provided to supply the System board with the secondary battery back-up voltage which may then be routed to the Master Rhythm if required as discussed later.

A 16 pin double ended jumper is used to link the Microcontroller signals to the System board mating with standard DIL sockets. This type of jumper is very convenient when splitting the system, but contacts can be a danger and





Fig. 9. Track layout and component overlay for the System board

it is recommended that the pins are very slightly bent inwards with the fingers before the first insertion into the socket. The jumper plugs are marked with pin numbers and orientation should be carefully checked.

### MAINS CONNECTION

All mains components are mounted in the lower half of the case. The mains lead enters at the side, protected by a plastic grommet, and terminates with live to the fuse, neutral to the switch, and earth to a tag in the case. A P-clip clamps the cable to the base. A link is made between the fuse holder and switch, and the two transformer input wires soldered to the switch. The top two tags on the switch should be used for neutral in and out, whilst the bottom (hidden) tags should be used for live connections.

A Molex connector is used to transmit the two 9 volt a.c. windings and the earth to the System board.

### MASTER RHYTHM SOCKET

A seven pin DIN socket requires to be fitted to the front of the Master Rhythm box in order to provide the connections shown in Fig. 12. The connections are shown on the rear of the socket in the actual solder tag positions. Play and Rest switch connections are taken from the live side of each switch and a common connection from the ground track below the switches. The clock pulse is obtained from the track at pin 11 of IC2 and is a 5-6 volt positive pulse occurring at one per measure in the Master Rhythm. The Start signal is taken from pin 1 of IC3 and is at ground when the Master Rhythm is at Rest, rising to 5-6 volts when playing. This signal level ensures that the Band-Box knows when to play and also resets the Band-Box to the beginning of a score when the Master Rhythm stops. The connection to the Long Cymbal pin on the Control board provides rhythm pulses for the Chord Instrument in the Band-Box and replaces the connection to the Master Rhythm instrument board. If the constructor prefers to use one of the other instrument triggers in the Master Rhythm to provide this facility they are all identical in terms of the pulses required. The audio can be obtained by a screened lead soldered to the output jack socket. The screen should be connected at the jack end but not at the DIN socket.

This modification does not affect normal operation of the Master Rhythm which may still be removed from the Band-Box and used on its own. The new connections provided allow for Play/Rest footswitch connections and also give all the signals required for external sequencer operation.

### MECHANICAL ASSEMBLY

A photograph was shown on page 39 of the November issue which indicated the mechanical mounting of the System board, Display board, and Master Rhythm. The latter is fixed to brackets, which suspend from the upper half of the case, using self-tapping screws, whilst the two boards are fixed with 6BA screws at various mounting points. In order to obtain the correct distance from the front panel a screw is first inserted through the case, then a  $\frac{1}{4}$  in plastic spacer is placed over the thread and retained with a 6BA half nut. The boards should slide onto the mounting points and are retained with a further set of nuts.

Since little clearance exists between the body of the seven pin DIN plug and the Display board it is useful to clip protruding display socket pins close to the board. This point should also be taken into account when fixing the position of the Master Rhythm socket.

The Microcontroller is fixed to the lower half of the case with a spacing from the base of two full nuts.



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Fig. 11. Interwiring the Band-Box system

Assembly of the two halves of the case requires some care due to their relative shapes. The right-hand end is first slid into position and then the case moved to the left. This operation should not be carried out with mains connected to the unit.

### **POWER OPTION FOR MASTER RHYTHM**

Three connections have been incorporated into the System board which can supply power to the Master Rhythm if required; however, a number of options are open to the constructor on how to make use of the facility.



CONNECTIONS TO BACK OF MASTER RHYTHM SOCKET

### EG750

Fig. 12. The socket should be mounted below the Play button on the front panel at a height which just clears the inside lip of the lower half The simplest option is to leave the Master Rhythm as a battery-operated unit which will ensure that it always retains its rhythm pattern information independent of the Band-Box. The disadvantage involved is remembering to switch the Master Rhythm off when the Band-Box is not in use.

The second option is permanent connection of the +5 volt, 3V6 and ground rails to the Master Rhythm, discarding the battery. If this is chosen, the link to the Microcontroller, which contains the secondary battery for storage, should never be broken and soldering used in preference to connectors.

A third option is shown in Fig. 13, and gives the dual possibility of battery/mains operation. With this system it is still essential to be careful to ensure that battery power is available from the Microcontroller when the power socket is connected to the Master Rhythm.

It is recommended that all testing of the complete Band-Box is carried out with a normal configuration Master Rhythm with 4 X HP7 batteries, and possible power conversions considered when the full system is operating satisfactorily.

### THE BAND-BOX IN OPERATION

As promised at the beginning of the series, a detailed step by step operating procedure is now given for the Band-Box. The earlier warning is repeated that the procedures are more difficult to describe than execute, but it is hoped that the information in Figs. 14 to 17 will assist the operator in the early stages.

Next Month. Completion of the series.





#### EG 751

Fig. 13. Optional power supply to the Master Rhythm for battery/mains operation

Playback procedure							
1.	Switch on mains						
2.	Display reads	01	CLEF	En.			
3.	If not press Reset						
4.	Now press Enter						
5.	Display reads	En.	PAGE	No.			
6.	Key page number (e.g. 03)						
7.	Display reads	En.	PAGE	03			
8.	Press Enter						
. 9.	Display reads	En.	LINE	No.			
10.	Key line number (e.g. 15)			1.0			
11.	Display reads	En.	LINE	15			
12.	Press Enter			_			
13.	Display reads	03	GO	-15			
14.	Press Play on Master Rhythm						
15.	Score plays and display is blank						
16.	*Press Rest on Master Rhythm						
17.	Display reads	03	GO	-15			
18.	If score is not required again press Reset						

\*Rest will not normally be used to stop playback. The coda key allows the score to continue to its natural end at which automatic stop will occur. Coda is pressed to indicate that a further repeat chorus is not required and subsequent actions will depend on the programme in the score store as explained later.

Fig. 14. Summary actions for the playback procedure

Number	Description	Format
1a	Chord type—1-14 plus Tacet (Silent)	1-15 En 1-8 >
1b	Chord duration—measured in beats (8 maximum)	1-10 km, 1-0 2
2	Change chord group to 0, 1, 2,11	0 En. 0-11 >
3	Start repeat here—Segno abbreviated S	0 En. S. >
4	Repeat from Segno—Dal Segno abbreviated d	0 En. d. >
5	Spare instruction—Labelled J	0 En. J. >
6	Finish—Fin abbreviated F	0 En. F. >

Fig. 15. Instructions recognised by the score store

### **Composition procedure**

Ì	1.	Press Reset			
	2.	Display reads	01	CLEF	En.
	3.	Press > which indicates that the composit	ion m	ode is	
		required			
	4.	Display reads	En.	PAGE	No.
	5.	Key page number (e.g. 03)			
	6.	Display reads	En.	PAGE	03
	7.	Press Enter			
	8.	Display reads	En.	LINE	No.
	9.	Key line number (e.g. 15)			
l	10.	Display reads	En.	LINE °	15
	11.	Press Enter			
	12.	Display reads	RE	SET	Н
	11	This is the automatic lock which deters un	autho	orised	
	· · · · ·	meddling. If Reset is pressed the machine	rever	ts to the	
		start of the normal playback procedure			
	13.	To unlock—Key "9", then "0"			
		Any other combination will return to the pl	ayba	ck proce	dure
	<b>14</b> .	After correct unlocking procedure	-	~ .	
		Display reads	En.	Chrd.	GP
	15.	Key first chord group required (e.g. 1)	-	01.1	~
	10.	Display reads	En.	Chra.	GI
	17.	Press Enter	18	4 11	V
	10.	Display reads	10	T X	Y
		tins displays the instruction A, T stored at	Page	13,	um (1
	10	Either Sor the score together with the curr	ent c	the cont	up (I.
	13.	of the port (16) or provious (14) line with	pect	arotion t	ents otho
		contents of the store. See text for score m	adific	ation	o the
		contents of the store. See text for score int	Junic	auon	

Fig. 16. Summary actions for the composition procedure

Intro	CM///	F6/// CM	///   G7///	-
Chorus	:CM/Am/	Dm7/G7/ CM	/G7S/ FM7/G7/	5.
Coda	CM///	F6/// G7/	/// CM//-	H1.

Score	Con	tents o	f d	ispl	Commonte	
Symbol	Score	Chord	ord Instruction			Comments
-	Line	Group		Form	nat	
CM (4)	00	0	1	En.	4>	4 beats CM
F6 (4)	01	0	4	En.	4>	4 beats F6
CM (4)	02	0	1	En.	4 >	4 beats CM
G7 (4)	03	0	5	En.	4>	4 beats G7
SEG.	04	0	0	En.	S.>	Set repeat from next line
CM (2)	05	0	1	En.	2>	2 beats CM
Am (2)	06	0	6	En.	2>	2 beats Am
Dm7 (2)	07	0	2	En.	2>	2 beats Dm7
G7 (2)	08	0	5	En.	2>	2 beats G7
CM (2)	09	0	1	En.	2>	2 beats CM
G7S (2)	10	0	d.	En.	2>	2 beats G7 susp 4th
GP.5	11	0	0	En.	5>	Change to chord group 5
FM7 (2)	12	5	J	En.	2>	2 beats FM7 (major 7th)
GP.0	13	5	0	En.	0>	Change to chord group 0
G7 (2)	14	0	5	En.	2>	2 beats G7
D.S.	15	0	0	En.	d. >	Repeat from SEG
CM (4)	16	0	1	En.	4>	4 beats CM
F6 (4)	17	0	4	En.	4>	4 beats F6
G7 (4)	18	0	5	En.	4>	4 beats G7
CM (3)	19	0	1	En.	3>	3 beats CM
FIN	20	0	0	En.	F. >	End

Fig. 17. Keying procedure for an example chord sequence. The two control keys used in composition are shown next to the instruction format and do not appear in the display



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# Digital Design Techniques...

Tom Gaskell B.A. (HONS) ELEC. ENG.

## **Part 6 Numerical Systems**

N the series so far we have touched briefly on codes and numbers; decimal, binary and 7-segment. This month we are going to look in more depth at numerical representation and manipulation within logic circuits.

### BASICS

The logic systems that we have been discussing are all based on a 'binary' concept; 'binary' meaning two-level. All normal conditions in a logic circuit can be represented by a logic 0 or a logic 1. 0's and 1's can be used to represent any two-state condition: On or off, open or closed, in or out, high voltage or low voltage, etc. Combinations of 0's and 1's can also be used to represent numbers. To see how this is done we must first look at our 'conventional' decimal numbering system.

Consider the number three thousand five hundred and twenty one:

THOUSANDS	HUNDREDS	TENS	UNITS
3	5	2	1

You will probably remember the units, tens, hundreds and thousands columns from schooldays! The number is made up by taking one from the units column, two from the tens column, five from the hundreds column, and three from the thousands column, then adding them all together:

1 + 20 + 500 + 3,000 = 3,521

This numerical system is based on tens; the columns are 'powers' of 10:

 $1 = 10^{\circ}$  (any number to the power 0 = 1)

- $10 = 10^{1}$
- $100 = 10^{2}$
- $1000 = 10^3$

So, our original 3,521 number can be drawn in columns marked as shown:

Obviously, as you move further to the left in the sequence of columns, the powers go higher; to the left of  $10^3$  comes  $10^4$ , then  $10^5$  etc., as the number becomes larger. To change the 'base' of 10 to binary, which has a base of 2, simply replace all the tens by twos:

etc., etc.,  $|2^3|2^2|2^1|2^0$  ( $2^0 = 1, 2^1 = 2, 2^2 = 4, 2^3 = 8$ , etc.)

The least significant digit (column 2°) is in units, the next digit (21) is in twos, the next is in fours, the next is in eights, and so on in multiples of two. In each column there can only

be a 0 or a 1, NOT a two; that would be a 1 in the next column to the left. So the binary number:

is equal to the decimal number eleven: One one, plus one two, plus no fours, plus one eight; total, eleven. The first sixteen numbers (including zero) of this binary code are shown in Table 1.

The binary table, of course, is one that we've come across before. You will remember that binary (and decimal) counters and dividers are readily available in CMOS and TTL integrated circuits. Binary, and as we shall see shortly 'BCD' code inputs and outputs from i.c.s are frequently labelled QA, QB, QC and QD, or even just A, B, C and D. A or QA is the least significant bit, i.e. 'units', and D or QD is the most significant bit, i.e. 'eights'.

Di	CIMAL		BIN	AR	1		GR	AY	
1		P	С	B	A	D	С	B	A
	0	0	0	0	0	0	0	0	0
	1	0	0	0	11	0	0	0	1
1	2	0	0	1	0	0	0	1	1
	3	0	0	1	1	0	0	1	0
	4	0	1	0	0	0	1	1	0
	5	0	1	0	1	0	1	1	1
1	6	0	1	1	0	0	1	0	1
	7	0	1	1	1	0	1	.0	0
1	8	1	0	0	0	1	1	0	0
	9	1	.0	0	1	1	1	0	1
1	10	1	0	1	0	1	1	1	1
	11	1	0	1	.1	1	1	1	0
	12	1	1	0	0	1	0	1	0
	13	1	1	0	1	1	0	1	1
	14	1	1	1	0	1	0	0	1
	15	1	1	1	1	1	0	0	0

TABLE 1 The binary code

Note that each vertical column of Table 1 changes state regularly as we count through the table. The 1's column changes alternately between 0 and 1, i.e. it changes every one step. The 2's column changes every two steps, the 4's column every 4, and the 8's column every eight. If this principle is remembered, it becomes very easy to draw up binary tables of any size, or any number of bits, when required; the starting point is always zero, since all binary bits are 0 for a count of zero.

### BCD

You may have noticed that there is a slight anomaly in this table, in the decimal column; it changes part way down the column from being a single digit number to being a two digit number (9 to 10). This can cause problems in circuit design. If three binary counters are being used to represent a three digit decimal number, for example, then each one should represent the numbers 0 to 9. This corresponds to binary numbers 0000 to 1001. After binary number 1001 has been reached, the next count should cause that counter to start again at 0000, and the next counter in the chain should then increase by one; in other words, a 'carry one' operation.

Self-resetting circuitry can be added to implement this return-to-zero effect, but in most cases it is easier to use an i.c. with the circuitry already built in. This is known as a 'BCD' device, which stands for Binary Coded Decimal. Each BCD number is a direct equivalent to a single digit decimal number. The maximum count for a BCD counter is 1001. There are no BCD numbers higher than this; they would be invalid, and have no meaning.

### **HEXADEÇIMAL**

To represent the decimal numbers 0 to 9, four bits of binary information are needed, as can be seen from Table 1. Imagine a circuit used to route, store and control these four bit numbers. There would have to be four bit latches, groups of four flip-flops, four bit registers, groups of four gates, etc., for each decimal number to be represented. Unfortunately, there is a great inefficiency in our system, because we are only using ten combinations of 0's and 1's, whereas there are a possible sixteen! We are 'Not Allowed' to use binary numbers:

because they all correspond to a two digit decimal number, and the second digit must be represented by a different four bits of binary information. So, these six extra codes are wasted.

To optimise the use of the circuitry, a new code must be introduced which allows the use of these extra six numbers. This code is known as "HEXADECIMAL", coming from Hex (six) plus decimal (ten), i.e. a numerical system with a base of 16, rather than 10 or 2. To represent 'Hex' (as it is shortened to) numbers in a fairly familiar way, we use decimal numbers for the first 10 states, then letters A to F for the last

			BIN	AR	(		B,C	C,D,			7-SEGMENT BARS
DECIMA	L	D	С	B	Α	D	C	B	A	HEX.	LIT (SEE LATER)
0		0	0	0	0	.0	0	0	0	0	a,b,c,d,e,f
1 1		0	0	0	1	0	0	0	1	1	b,c
2		0	0	1	0	0	0	1	0	2	a,b,d,e,g
3		0	0	1	1	0	0	1	1	3	a,b,c,d,g
4		0	1	0	0	0	1	0	0	4	b,c,f,g
5	- 9	0	1	0	1	0	1	0	1	5	a,c,d,f,g
6		0	1	1	0	0	1	1	0	6	a,c,d,e,f,g
7		0	1	1	1	0	1	1	1	7	a,b,c
8		1	0	0	0	1	0	0	0	8	a,b,c,d,e,f,g
9		1	0	0	1	1	0	0	1	9	a,b,c,d,f,g
10		1	0	1	0	-	-	-	-	A	a,b,c,e,f,g
11		1	0	1	1	-	-	-	-	B	c,d,e,f,g
12		1	1	0	0	-	-	-	-	C	a,d,e,f
13		1	1	0	1	-	-	-	-	D	b,c,d,e,g dp.
14		1	1	1	0	-	-	-	-	E	a,d,e,f,g
15		1	1	1	1	-	-	-		F	a,e,f,g DISPLAY BARS

TABLE 2 Comparison of numerical systems and the 7- segment display

six. A is equivalent to ten, B to eleven, C to twelve, etc. The full table of decimal, binary BCD, and Hex numbers is shown in Table 2.

It should be remembered that Hex is only a notation for 4 bit binary numbers. It does not have patterns of 0's and 1's exclusive to itself, but merely represents binary numbers in a 'short hand' way. There are other similar systems (the 'Octal' code represents 3-bit binary numbers, for example) but the 4 bit binary numbers of Hex are the most useful to represent, since groups of 4 bits, and groups containing multiples of 4 bits, occur very frequently in logic circuits and systems.

The Hex system can consist of multi-digit numbers of course, in the same way that binary and decimal systems can, and again these are arranged in powers of 16. Comparing Hex with decimal:

3A is equivalent to  $(3 \times 16) + (10 \times 1) = 58$ 

B9 is equivalent to  $(11 \times 16) + (9 \times 1) = 185$ 

2AF is equivalent to  $(2 \times 16^2) + (10 \times 16) + (15 \times 1) = 687$ 

Hex numbers are most usually seen these days in computer programs. When writing programs, it is ridiculous to use long lists of 0's and 1's to represent data and instructions, so the simplest abbreviation of this is to represent 8 bit binary numbers by 2 digit Hex numbers. (Most modern microcomputer systems use 8 bit logic circuitry). Conveniently, an 8 bit binary code can represent 256 different numbers; exactly the same as a 2 digit Hex code can! These 2 digit Hex numbers are known in computer jargon as 'object code', and so this explains the reason for those long lists of numbers such as 3A, OE, 3F, A2, B2, 23, 23, 47, etc., etc., that you may have seen written down in computer program listings.

### **7-SEGMENT DISPLAYS**

The principles of the 7-segment display are quite straightforward and were covered earlier in the series. Briefly, seven bars of light can be used to represent all the numbers 0 to 9, and many stylised letters of the alphabet, including the letters A, b, C, d, E and F, with upper and lower cases as shown. (This is convenient for Hex number representation.) The 7 segments of the display are lettered a,b,c,d,e,f and g (there is usually a decimal point, 'd,p.', provided too,) and are arranged in the layout shown in Table 2.

Integrated circuits are readily available to convert binary codes into 7-segment codes. Usually, these i.c.s have extra functions built into them, such as latches, or output driver

stages to directly drive the display segments without extra buffers or amplifiers. The Dual-Digi-Dice 'miniproject' this month uses two of these i.c.s built into them, while the other i.c.s are able to drive several displays, in a multi-digit arrangement, simultaneously. We shall look closer at display driving techniques, and alternative display technologies, next month.

### THE GRAY CODE

Although binary, BCD, Hex and 7-segment are the most regularly used electronic codes, there are many others of which the majority are very specialised indeed. The 'Gray Code' is one of the more widely used of these alternative codes. To be completely accurate, the term Gray code, sometimes known as a 'reflected' code, can encompass a range of different codes, but we shall only look at the most common one.

The Gray code is primarily used in positional encoders. These are electromechanical devices which convert a physical movement or rotation into an electronic code which has a value proportional to that movement or rotation. In essence, they are multi-way switches giving a coded output, and are used to feed positional information from machinery or instruments, into logic circuits. The switching action can be by electrical contacts, or by optical means using marked discs passing between light sources and detectors.

Binary codes could be used in these applications, but would suffer from race hazards; if the shaft was just on the point of changing from one binary number to another, it may be that some bits had changed state, while others were about to change. This would give a completely false binary number. For example, if 0111 was to count by one to 1000, the three least significant bits may change state fractionally before the most significant bit does. The sequence would then go: 0111, 0000, 1000. If the shaft was resting in the 0000 position, this would be a completely false and erroneous reading: in fact, it should read 'half way' between 0111 and 1000!

To get round this problem, the Gray code only changes one bit at a time, so each count only differs from the previous and succeeding counts by the change of state of one bit. By this means, the greatest uncertainty of value that there can ever be is plus or minus  $\frac{1}{2}$ . The complete code, with binary shown alongside for reference, is given in Table 3. Although the Gray code might look rather complex, it is



**TABLE 3 The Gray Code** 



Fig. 6.1. Gray to/from binary conversion

very easy to convert from binary to Gray, and vice versa. The circuits to do this are shown in Fig. 6.1, and can be expanded for any number of bits from one upwards! In each case, the most significant bit is common to both codes, and the other bits follow the interconnection pattern shown.

### **OTHER CODES**

The only other code that you will be likely to come across regularly is the ASCII code: the American Standard Code for Information Interchange. This is a fairly universal code used for representing symbols, characters and control signals, and has wide application in the field of computing. ASCII is a 7 bit binary code, and is used in association with large keyboard assemblies to provide coding for upper and lower case letters of the alphabet, numerals, punctuation, and control functions such as reset, backspace, carriage return, etc. The same codes are also used for character generation: when fed into suitable 'character generator' i.c.s they cause a specifically encoded signal to be produced which is then suitably modulated and displayed on a video monitor or domestic television. In these instances, most of the keyboard control functions are replaced by fairly complex graphic symbols which allow the programmer greater flexibility in the symbols that he can write up onto the screen.

Since there are 128 different ASCII codes, we shall not list them all here; they are easily accessed via character generator, microcomputer, or keyboard encoder i.c. data sheets. The generation of these 7-bit codes from the contacts of the keyboard switches, together with contact debouncing and other basic timing and control functions, is normally carried out by i.c.s on the actual keyboard p.c.b., itself.

It should be noted that code converters can be easily designed using Boolean algebra, as discussed in the first part of the series. Each digit (or bit) of the 'new' code has its own Karnaugh/Veitch map, drawn up from all the digits (or bits) of the 'old' code. In most cases, of course, there are readily available i.c.s to perform the complete conversion so these techniques need only be employed for very specialised code conversions.

### **BINARY ARITHMETIC**

All digital calculators, microprocessors and (of course) computers, at their most fundamental level, use binary codes for their numerical representation. On these codes must be performed arithmetic operations, mostly additions, subtraction, multiplication and division. To see how this is done in practice it is first necessary to determine the rules of binary arithmetic, which follow 'conventional' arithmetic techniques to a large extent.

### ADDITION

This works in a very straightforward way, the only difference between binary and decimal addition being that we 'carryone' to the next most significant digit when the sum exceeds 9 in decimal, whereas in binary we carry one when the sum exceeds 1. So we can see that:

0 + 0 = 0	
0 + 1 = 1	
(Similarly, $1 + 0 = 1$ )	
1 + 1 = 0 carry 1	
Examples: a) 11010	b) 0100 c) 100101
plus 00010	plus 0001 plus 001101
equals 11100	equals 0101 equals 110010
•	
carry	carry
1	1

### SUBTRACTION

This is a rather more complicated procedure than addition, due to the requirement to 'borrow' digits. The way that most systems overcome this difficulty is to complement one number, then add the other number to this complement. 'Complementing' simply means turning a negative number into a positive one, or vice versa; i.e. in terms of decimal numbers,

16 - 4 = 12

is the same as,

16 + (-4) = 12

Using this method, we can use the same circuits as for the addition process, with only an extra complementing circuit needed. There are a number of different ways of deriving a negative binary number from a positive one. We shall look at the most common system, known as 'Two's Complement':

To form the two's complement of a number (i.e. turn a positive binary number into a negative one), perform the following steps:

- add an extra O on the left of the number (i.e. the most significant bit)
- 2) invert all the bits (1 becomes 0, 0 becomes 1)

3) add 1 to the resulting number.

Example: a) Turn 17 into -17	17 in binary is:	10001
	add 0 to the left:	010001
	invert all the bits:	101110
	add 1:	000001
	Result:	101111
Hence, 101111 is the two's	complement repre	esentation
of –17.		
Example: b) Turn 3 into -3	3 in binary is:	11
	add 0 to the left:	011
	invert all the bits:	100

add 1:	001
Result:	• 101

Hence, 101 is the two's complement representation of -3.

In both these examples it is necessary to know that two's complement arithmetic is being used, and hence the left band bit is an 'extra' one; in the latter case, for example, we know that the result 101 is not 5 in decimal because the original number was a 2 bit number, and our 2's complement method results in a 3 bit number.

When building subtraction systems, circuitry to perform the two's complementing of one number is followed by addition circuits, to add the result of the two's complement process to the other number. The final result is a subtraction! Note that if a positive and a negative number are added and if the most significant bit of the result 'carries by one', i.e. the result is one bit larger than either of the two original numbers, then this most significant bit can be discarded, i.e. ignored.

For example, take

ake +3-3=0 -3 in binary = 101 +3 in binary = 011 Adding together, the RESULT = 1000 Discard

Hence, the result = 000 (zero)

### HALF AND FULL ADDERS

Combinational circuitry is used to implement binary addition. The most basic addition circuit is known as a 'half adder', since it is used when two bits only are to be added; it has no 'carry in' facility. See Fig. 6.2. To include carry in requires two half adders to be connected together with an extra gate, as shown in Fig. 6.3. Note that in Fig. 6.2 and 6.3 the circuits are based on the use of NAND and NOR gates, since these are most usually found in discrete circuitry. To simplify the circuits still further, EX-OR gates are often used in place of several gate combinations.

A 4-bit CMOS full adder i.c. is readily available, the 4008, which has extra circuitry provided to give a 'fast' carry out signal. If this extra circuitry was not provided, the carry out



(E0747) Fig. 6.3. Block diagram of the full adder

signal would have to ripple through the adder stages, incurring considerable delay and potentially giving rise to hazards in the operation of the following circuitry. The majority of adder circuits today are, of course, buried deep within microprocessor and calculator i.c.s. The principles outlined here still apply, nonetheless. The circuitry for binary subtraction need not be shown, since it is merely a set of inverters with an extra half adder to add one to the complemented binary number, followed by a set of full adders as detailed above.

### **MULTIPLICATION AND DIVISION**

There are three different approaches which can be adopted when designing multiplication and division circuits. The first is to use a fairly complex but fast arrangement of full adders and control circuitry to perform 'long multiplication' and 'long division':

For example, mult	tiply 01101 by 1010:
MULTIPLY:	01101 (13 in decimal)
BY:	1010 (10 in decimal)
00000	'Multiply, the top number (01101) by
01101	each digit of the bottom number one at
00000	a' time and each time shifting the
01101	answer left by one position.

TOTAL: 10000010 (Adding all four multiplications together)

The result is 10000010, or 130 in decimal.

Since the process was simply a 'shift-and-add' routine, this is obviously implemented by using a large number of full adders, suitably interconnected, with inputs gated on and off by the bits of the second number (1010). Division is carried out by a similar process.

In many calculators and microprocessors, the rather complex and specialised circuitry needed for these operations is unnecessary; a far simpler, though slower, method can be used. It is analogous to the way a supermarket check-out operator totals up many identical purchases: 6 items, at 7 pence each, total: 7 + 7 + 7 + 7 + 7 + 7 = 42 pence.

So, in the above case of 13 multiplied by ten, the result is obtained by adding 13 to 13, then adding the result to 13, then adding that result to 13, and so on, for the required number of times. The speed of circuit operation is sufficient to obtain a result from this apparently clumsy multiplication process in only a few microseconds! Divison, of course, is a very similar process.

The final method of multiplication and division is actually rather a cheat! If the number that you are multiplying by or dividing by is an integer (a whole number) power of 2 i.e. 2, 4, 8, 16, 32, 64, etc., then no addition steps are necessary. The original number to be multiplied or divided is merely 'shifted' left or right respectively by the power of 2 concerned, filling in 0's where necessary. The best way to illustrate this is to show some examples: Example 1:

	1011	(eleven in decimal)
multiplied by	100	(four in decimal)
i.e.	1011	multiplied by 2 <sup>2</sup>
therefore, move	1011	left by two positions, and
		fill in the gaps with O's
the result is	101100	(i.e. 44 in decimal)
	2 extra	
	O's added	
Example 2:	0 3 00000	
Example 2.	11001	(twenty five in decimal)
multiplied by	1000	(eight in decimal)
ie	11001	multiplied by 2 <sup>3</sup>
therefore move	11001	left by three positions etc.
the result is	11001000	(i.e. 200 in decimal)
the result is	11001000	(1.0. 200 11 000110)
	3 extra 0's	
	added	

In the case of division, as many bits must be discarded as the number of places being shifted to the right: Example 3:

	10100 (twenty in decimal)
divided by	00100 (four in decimal)
i.e.	10100 divided by 2 <sup>2</sup>
therefore, move	10100 right by two positions
the result is	0010100 i.e. the result is 00101
	(five in decimal)

### discard these two bits.

This shifting procedure is tailor-made for the shift register! If the number that you are to multiply or divide by is an integer power of two (this is surprisingly a regular occurrance) then the shifting technique is by far and away the easiest way to implement this arithmetic operation.

### THE ARITHMETIC LOGIC UNIT

When several full adders and some control logic are added together in one circuit, in combination with some external parallel in-parallel out registers, we end up with a very flexible and versatile multi-function unit capable of performing a number of arithmetic and logic operations on sets of binary information. This is known as the ALU (Arithmetic Logic Unit) and is at the heart of every computer, microprocessor and most calculators. The ALU is normally treated as a 'black box'; it's detailed internal operation need not be known, it is merely a block of circuitry capable of addition, subtraction, complementing, multiplications, division (these two functions sometimes requiring the use of extra external registers), logical operations such as AND-ing and OR-ing of binary numbers, and other similar operations.

To achieve this flexibility and versatility of operation, the interconnections between the various registers, control logic, full adders, etc., must be fairly complex. In order to vastly simplify these interconnections and to give some semblance of order to the internal structuring of microprocessor and calculator circuitry, the 'BUS' system is used.

### THE BUS

A bus is a set of common parallel interconnections between many different devices and circuit subsections in a system. It has a certain 'width' measured in binary bits, which usually corresponds to the maximum number of bits in the binary numbers to be manipulated within the equipment. The 8 bit bus is very common these days, so this bus physically consists of eight separate tracks on the p.c.b., or inside the i.c. chip. Sometimes extra bits for control purposes are added to the bus; the most regularly seen types being known as 'flags', which are used for control signalling between parts of the circuitry.

Some devices feed their outputs onto the bus, others take their inputs from the bus, and some even do both! Not all this happens simultaneously, though. Device A might feed onto the bus while device B takes it's input from the bus, in which case the output of A is fed to the input of B. These two then stop using the bus, while device C feeds onto the bus, and device D takes it's input from the bus; the output of C then feeds into the input of D, without affecting either A or B. The bus is often known as a 'data highway' with devices



Fig. 6.4. Simplified block diagram of bus-orientated ALU system

and circuit sections taking it in turn, two or more at a time, to be connected with each other via it.

To achieve all this without excessive loading or shorting out of gate outputs, a 'tri-state' (three state) logic system is employed on all device outputs feeding onto the bus. When the tri-state outputs are turned on, they can be at either logic O or logic 1 levels, as used in the rest of the system, but when the tri-state outputs are turned off they become very high impedence points, capable of floating in voltage, with negligible loading of other circuitry feeding the bus. Hence, only the device selected at any time by the controlling logic can feed onto the bus. Inputs to devices are enabled by the control logic in a fairly conventional way. The bus can therefore be considered 'bidirectional'; binary information flows up and down it in rapid succession as different parts of the circuit use it to perform their various interconnections.

It is usual for complex digital systems to have more than one bus. The other bus or buses can carry information to different areas, or carry other forms of information, for example 'addresses'. The address is a binary number which defines a particular place in the memory system of the device, rather like the use of Ordnance Survey grid reference numbers to define a particular place in the country. (We'll look at memories in more detail next month). A typical arrangement is to have an 8 bit data bus and a 16 bit address bus run adjacently in the circuit.

A simplified bus arrangement is shown in Fig. 6.4. The 'accumulator' is a register, similar to the B, C, and D registers, but dedicated to the ALU. It's input is always fed from the ALU, while its output always goes to one of the two main ALU inputs, and can also feed, via tri-state enable gates, onto the bus. By careful internal timing of the system, binary numbers can be fed out of any of the B, C or D registers into the ALU, mathematically processed with the output of the accumulator, and the accumulator can latch into itself the result of this mathematical process, which can then be fed onto the bus and either back into the ALU again, or into one of the registers. For example, the sequence of events to add the contents of register B to those of register C, and put the result in register D, would be:

- 1) Reset accumulator (output equals zero).
- 2) Feed output of register B onto the bus, and enable inputs to the ALU.
- Add ALU input 1 (i.e. the number in register B) to input 2 (zero).
- 4) Latch the output of the ALU into the accumulator.
- 5) Feed the output of register C onto the bus, and enable the inputs to the ALU.
- Add ALU input 1 (the contents of register C) to input 2 (the number which originally came from register B).
- 7) Latch the output of the ALU into the accumulator.
- Feed the output of the accumulator, via the tri-state enable gates, onto the bus and enable the inputs to register D.

Although this sounds a complex procedure most computers, calculators or microprocessors can carry out this series of steps in no more than a few microseconds; many in considerably less time than this! We're now starting to get an insight into the internal operation of microprocessor and computer central processing units, or 'CPUs'. We'll cover the subject more fully in the final article of the series. Before that, however, in next month's article we shall look at the use of analogue techniques within digital logic circuitry. This includes complex display driving, analogue transmission gates, conversion between analogue and digital signals, and (of course) those all-important memories!

### **DUAL-DIGI-DICE**

This project accurately simulates the rolling of a pair of dice, with fairly authentic l.e.d. dice face patterns. When the 'ROLL' switch is pressed, both dice faces start changing and flickering rapidly, then gradually slow down and stop at a final result. The changes are completely random; they do not simply 'count up' numerically. Approximately 10 seconds after the final result has been obtained, the two l.e.d. dice displays switch off to conserve battery power.

### PRINCIPLES

The circuit design is based upon the fact that dice face patterns change in a way that is approximately a binary count. Referring to the pattern of I.e.d.s, we can compare the dice face I.e.d.s lit with a sequence of binary numbers, as shown in Fig. 6.5. There is obviously a direct correlation between the I.e.d.s lit and the binary count; the 'A' I.e.d. is lit by the least significant binary digit, the 'B' I.e.d.s by the middle digit and the 'C' I.e.d.s by the most significant digit. Hence a binary counter can be used to provide the correct code for the dice face, as long as the invalid 0 and 7 states, which never occur on a dice, can be avoided.



Fig. 6.5. Dice face l.e.d. patterns

### **CIRCUIT DESCRIPTION**

IC4 is a dual binary counter; two separate synchronous counters in one i.c. package. The 'C' output of the first counter is used to clock the second counter, so the two halves are 'cascaded' asynchronously. The slight race between the two counter halves is unimportant in this case; any static hazards caused are irrelevant, as the counters are clocked randomly by a noise source anyway! The output of each counter is fed into a Quad D-type latch, the Q outputs of which drive the I.e.d.s via conventional transistor buffers. (Note that the 'B' and 'C' l.e.d.s are arranged in series pairs to cut down current consumption, although it would be unwise to put all four 'C' l.e.d.s in series since the total voltage drop across them could be as high as 10 volts, which would not allow for sufficient variation of power supply voltage.) TR3, TR4, R16 and R17, and TR5, TR6, R15 and R18 form Darlington driver configurations, each to supply current to all the l.e.d.s. in a dice face display. These configurations are fed in turn from IC7a. The inputs of IC7a are connected to a very long time period pulse stretcher — D4, C8 and R14. When S3 is pressed and released, IC7a pins 1 and 2 are held at logic 1 for approximately 15 seconds, causing the output pin 3 to go to logic 0, turning on the two Darlington drivers and hence the displays. After the time period of 15 seconds has ended, pin 3 goes high again and the Darlington drivers turn off, so the l.e.d. displays also turn off, conserving valuable battery power! This time period can obviously be varied by charging R14 or C8. S2 is provided to permanently switch off the 'A' dice display when only one dice is being used.

The random changes of dice face display are obtained by



EG723

Fig. 6.6. Circuit diagram of the Dual-Digi-Dice

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using a random noise source to clock the counters, then latching the counter outputs in IC5 and IC6 as frequently as required to give the effect of the dice rolling. The clocking of the counter occurs very rapidly and the latching fairly slowly, so for each latch pulse, IC4 will have counted through its entire range of numbers many times. Each successive dice face shown, therefore, will be chosen completely at random.

The noise source is formed by reverse biasing the baseemitter junction of TR1, with the collector left disconnected. This is buffered and amplified by TR2 (with R2, R3 and C1) then amplified again by IC1. R4 and R7 set the gain of IC1, while R5, R6 and C3 set the non-inverting input to a reference point of approximately +5.8 volts. The noise output of IC1 swings almost to each supply rail, and needs only to be fed through a Schmitt trigger gate, IC7d, to clean up the waveform into correct logic levels, before feeding into the clock input of the counter, IC4 pin 1. Note that a high battery voltage for the unit is needed (12 volts) in order that a satisfactory noise voltage can be obtained from the reverse biasing of the TR1 junction. Although a 9V battery could be used, it would require special selection of TR1 to find a transistor with a particularly high noise voltage. Diode D1 is provided to protect the unit against accidental reverse connection of the batteries with C9 and C10 decoupling the power supply rails.

IC2 and IC3 are used to generate the latch pulses, starting off with very rapidly occurring latch pulses, then slowing down to a final halt. CMOS timer IC2 is connected as a fairly conventional oscillator, but with an extra resistor R11 taken to the output of IC3. This op-amp is connected as a voltage follower; the voltage on pin 6 will be exactly the same as that on non-inverting input pin 3, but at a very low impedance. When S3 is pressed, C7 charges up to +11 volts via D3, hence the output pin 6 of IC3 also goes to +11 volts and IC2 oscillates at a relatively high frequency. As soon as S3 is released, C7 begins to discharge in a manner defined by R12 and R13, to a final voltage of approximately 3.7 volts. The output of IC3, via R11, then causes the oscillations of IC2 to decrease in frequency and finally stop. (Note that pin 5 of a 555 timer i.c. would normally be used to alter frequency, but in this case it was unsuitable as the oscillations could not be completely stopped.)

The output of IC2, pin 3, has short pulses derived from it by C4, D2 and R8. These are then used to latch IC5 via IC7b and IC8a, and IC6 via IC7c and IC8c. The invalid conditions of the counter IC4, 000 and 111 (as shown in Fig. 6.5.) are prevented from occurring by IC9c, IC9d and IC8b (for the 'B' dice) and IC9a, IC9b and IC8d (for the 'A' dice). In each of these networks, the outputs of the two EX-OR gates are both at logic 0 if their respective binary counts are 000 or 111. This, via the following NOR gate, disables any latch pulses passing to IC5 or IC6 (as appropriate), thereby preventing an invalid code from ever being latched and displayed.

### CONSTRUCTION

The matchboards should be built up as shown in Fig. 6.7, leaving the l.e.d.s OFF the boards for the moment and fitting the wire links on each board after all the components have been added. The l.e.d.s are then soldered to the REVERSE side of the board (i.e. the copper foil side) with their tops protruding approximately 10mm above the surface of the p.c.b. Note that the 'a' (centre) l.e.d. has to have its leads bent outwards to fit the holes in the board. Ensure that these bends are made well away from the l.e.d. body.

The case can then be drilled to take the switches and the two match-boards' support pillars, and rectangular cutouts should be made to allow viewing of the l.e.d. displays; these cutouts can then have a piece of red tinted transparent perspex or plastic glued behind them to improve visibility and contrast of the l.e.d.s. The interwiring between boards, and to the switches, can now be added in flexible (multistrand) wire, and the boards can be screwed to the front panel of the case using suitable spacers:  $\frac{1}{2}$  inch x 6 BA

COMPON	OMPONENTS Semiconductors		rs		
001111 0111			D1		1N4002
			D2. D3	3. D4	1N4148 (3 off)
Resistors			D5 to	D18	red Le.d. (14 off)
R1		2701	TR1		BC109
R2 R7 R14		1M (3 off)	TR2.T	R7 to	
R3 R8		247 (2 044)	TR12		BC548 (7 off)
R4		212	TR3.T	B5	BC558 (2 off)
B5 B6		174 (2 off)	TR4.T	B6	BEX30 (2 off)
B9 B15 B16		4/K (2 011)	IC1 IC	3	7418 pin i c (2 off)
R10		212	102		ICM 7555 CMOS timer
D11		282	104		4520 CMOS Dual binany counter
D12		02R	105 10	6	4042 CMOS Quad lateb
D12		470K	100,10	.0	4092 CMOS Quad Schmitt MAND ante
D17 D10 D10	0.000.001.000	220k	107		4053 CMOS Quad Schmitt NAND gate
n 17, n 10, n 13	9, NZU, NZT, NZO,	001 (0 10	100		4001 CIVIOS QUAD NOR GATE
K27, K28		33k (8 off)	109		4070 (or 4030) CIVIOS Quad EX-OR
H22, H29		1k8 (2 off)			gate
R23, R24, R29	5, R30, R31, R32	1k2 (6 off)	Miscoll	200000	
R33, R34, R35	5, R36, R37, R38	10k (6 off)	2 off	Matchb	and Eve 200PC Clobal Crasialtian
All resistors $\frac{1}{3}$ of	or $\frac{1}{2}$ W 5% carbon		2 011	Daubla	oard Exp-300PC Global Specialties
			01	Circle	pole miniature toggie switch
Compaitana			52	Single p	ole of double pole miniature
Capacitors	10	- 57)	<b>C</b> 0	toggi	e switch
CT, C5	T Un polyester (2)	(110	53	Nomen	tary push button switch
62	100n polyester		2 off	Battery	holders, with connecting leads
C3, C7	10µ 25V elect. (2	off)		(each	4 x HP7)
C4	330p ceramic pla	te	1 off	Design	Mate Case DMC2, Global Specialties
C6	2µ2 35V tant.		8 off	$\frac{1}{2}$ in. x 6	BA threaded spacer
C8	22µ 25V elect.		6BA s	crews and	d wire to suit
C9, C10	100µ 25V elect. (	2 off)	2 smal	Il pieces c	of red gelatine, perspex, etc (see text)



Fig. 6.7. Matchboard layout for the Dual-Digi-Dice

tapped pillars are ideal. The battery holders can be fixed to the case baseplate using 'sticky fixers', and the batteries should then be added.

The project is now finished! If it seems that the noise source is not operational (no logic changes at IC7d pin 11) try changing TR1, or lowering the value of R4 to 1kO, and make sure that the supply voltage is high enough. The rate at which the dice face displays slow down their changes can be varied by altering the values of R11, R12 and R13, but note that too slow a 'slowing down' may result in the dice face never actually finally stopping before the displays switch themselves off! For testing purposes, removing C2 and C4



Internal view



will enable you to use a logic pulser (on IC7d pin 11 and IC8a pin 1), to simulate the action of both the random noise generator and the latching pulse generator, and thereby debug any faults that there might be. Finally, there is plenty of room in the case to add a simple mains power supply if preferred, as shown in Part 2 of this series; any voltage from 12 to 15V will work very well indeed.

### **NEXT MONTH**

Multiplexing of displays, complex displays (liquid crystal, gas discharge and similar). Analogue switches, transmission gates etc, A to D and D to A converters.



### *Tel: 0322 863494 Hillcroft House Station Road Eynsford Kent DA4 0EJ*

CM05         4016         30         4031         170         4053         60         4077           4000         14         4017         4034         170         4054         110         4034           4000         14         4017         4036         285         4055         480         4063         95           4000         14         4019         35         4026         285         4066         95         4066         95         4064         95         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4066         95         4034         4036         40465         18         4098         4041         14         4025         18         4098         4037         18         4036         4037         18         4036         4037         18         4036         4037         150         4037         160         110	25       40175       100       4527       90         26       40193       120       4528       750         20       4502       70       4528       750         65       4503       50       +4532       95         140       4508       200       4534       495         140       4508       200       4534       495         38       4510       65       4543       110         140       *4511       50       4543       110         390       4512       70       4553       295         360       4516       75       *4559       300         90       4520       70       4584       456         91       100       4521       200       4584       59         3100       4526       80       4724       140         0       NE566       150       *TDA1024       125         0       NE566       150       *TL082       70         0       NE567       150       *TL082       70         0       NE567       150       *TL081       30         0       NK76477	COMPONENT KITS           W. Resistor KIL Contains 10 of each value fr % types (total of 650 resistors) Geramic Capacitor KIL Contains 5 of each v 001µF (total 135 caps.)           Polyester Capacitor KIL Contains 5 of each v 001µF (total 135 caps.)           Polyester Capacitor KIL Contains 5 of each v 01µF (total 85 caps.)           Polyester Radial (eads. 250V C280 type. 0 0.033 (69; 0.047, 0.068; 0.1, 7p; 0.22, 9r 13p; 0.68, 20p; 1.0µF, 23p.           Electrolytic, Radial or axial. Please' specif (183y, 2.2/63V, 4.7/63V, 10/25V, 7p; 22, 100/25V, 20p; 15/16V, 30p; 22/16V, 2 47/6V, 27p; 4.27/16V, 70p.           Polystyrene, 5% tolerance. 10p-1000p 6p; 8800p-0.012µF 10p.           Sterness Miniature Polyester. PCB mounting. 6n8, 10n, 15n, 7p; 22n, 33n, 47n, 68n, 69 11p; 220n, 13p; 330n, 20p; 47n, 25r, 58c 47.200 Represents exceptional value/H tideal Christmas gift. Expires 13th Jan. 82.           CORS 0005 010         Sterness Miniature Polyester. PCB mounting 6n8, 10n, 15n, 7p; 22n, 33n, 47n, 68n, 69 11p; 220, 13p; 330n, 20p; 47no, 25r, 968           CORS 0205 021 Jack Plug Sockt 99 120 Sizero 24p 225 03112         Sterness exceptional value/H tideal Christmas gift. Expires 13th Jan. 82.           CORS 041 Jack Plug Sockt 99 120 Sizero 24p 225 070 Jack Plug Sockt 99 120 Sizero 24p 255 070 Jack Plug Sockt 90 PD 1000; PD 2 120 Sizero 24p 255 070 Jack Plug Sockt 90 PD 1000; PD 2 120 Sizero 24p 255 070 Jack Plug Sockt 90 PD 1000;	OPTO         3mm red:#**7p         alue from 22pF to 370 peach (structure)         alue from 22pF to 370 peach (structure)         (structure)         (str
<text><text><text></text></text></text>	N THE ELECT           Inversion           Inversion           Buy Ambit's           vouchers. U           without this           fast service           Base Service           ZBOA DIG 1400           ZBOASIO/1 1400           ZBOASIO/2 1400           ZBOASIO/1 1400           ZBOASIO/1 1400           ZBOASIO/2	RONICS BUSINE ST GOP A ST GOP A ST GOP A CONTROL ST COMPANY ST GOP A ST GOP A S	A details on application, and a stretch it to your order. EBOEL

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**FITTING** the flow and speed sensors, installation, calibration and a description of the program controlling the computer.

There is the option of a combination lock facility which could be used to operate an ignition cutout or alarm. The combination consists of three digits and can be changed up to seven times without erasing the PROM.

### **FITTING THE FLOW SENSOR**

Because of the wide variety of fuel systems, precise instructions cannot be given to cover every make of car, but fitting the flow sensor is in fact an easier job than many would imagine, providing the following guide lines are observed.

The flow sensor is fitted in the petrol pipeline between pump and carburettor, and after the fuel filter. The arrow on the side must point in the direction of flow as seen in Fig. 11.

The fuel line, which may be of metal, plastic, rubber or a combination of all three, must be cut or otherwise parted in order to insert the flow meter. In the case of a flexible hose, it is simply necessary to slacken the hose clamp, preferably at the fuel pump end (assuming it is a mechanical engine driven pump), pull off the hose and join the flow meter to the pump outlet pipe by means of the polyurethane pipe supplied. In the case of a metal fuel line, it is better to remove the pipe from the engine before cutting, to enable the ends to be satisfactorily deburred and avoid swarf entering the fuel system.

The usual plastic fuel line is made of nylon and is best cut with a sharp knife. It is normally rigid enough to accept a flexible pipe and clamp without collapsing.

The flow sensor is not normally affected by vibration and because of its light weight, it may be mounted so that it is suspended in the fuel line. It should however not be positioned so that it is subjected to direct radiant heat from the exhaust manifold. If close proximity (less than 6in) is unavoidable, a metal heat shield should be fitted between.

It should ideally be mounted vertically, but if it has to be mounted horizontally or close to the horizontal, it should be fitted with the fuel passage above the detector housing. It is not susceptible to electrical interference but it is advisable to avoid close proximity to high tension leads.

If no filter is present, we recommend that a standard type of in-line paper element fuel filter be fitted just ahead of the flow sensor, taking care to note the direction of flow.

So far so good. Now for the snags. Some of you might have a car in which the fuel is not only pumped up to the carburettor, but is pumped back to the tank as well; in fact it hurtles around the fuel system at a much faster rate than your engine could ever use it. Obviously, if you stick your flow sensor in line with that lot, the answer is going to be rather wrong.

The presence of a second fuel pipe connected to the carburettor, which disappears back to the fuel tank means that you have a recirculating fuel system. It is standard on the Range Rover, Rover 3500 (pre SD1), Fiat, Audi and many other types. If you are in any doubt, your local agent will enlighten you.



Showing an in-line fuel filter and the flow sensor

This little problem is easily overcome by fitting the Tjunction supplied with each kit into the fuel line between the pump and the flow meter. Make sure that the return to the tank is fitted to the leg of the tee, since this contains the restricting orifice which limits the return flow.



On some cars, a T-junction is already fitted between pump and carburettor, in which case the flow meter and filter should be fitted after the Tjunction.

It is important to check all fuel lines and joints for leaks immediately the engine is first started and again after a short running interval. The possibility of fuel spillage will be reduced if the fuel tank is reasonably empty before the fuel line is disconnected or cut.

The present flow measuring system cannot be used with a fuel injection engine, since the fuel may not be diverted or shut off.

### Fig. 11 The flow sensor fitted

### **FITTING THE SPEED SENSOR**

Locate a suitable part of the speedometer cable to fit the sensor, probably fairly near the bulkhead in the engine compartment, and draw four lines on it as in Fig. 12. Detach the cable from the back of the speedometer and withdraw the inner cable. Using a hacksaw cut the outer cable at the two inner lines and throw away the section. Slide a jubilee clip along each part of the outer cable.

Refit the inner cable and re-connect to the speedometer, leaving the inner cable hanging from the cut end of the outer.

Push the inner cable through the sensor, the side of the sensor with the screw heads being nearer the speedometer.

This will take a certain amount of force as there is a friction grip on the cable.

Keep feeding the cable through until the outer cable is entering the slotted end tube of the sensor, pulling the inner cable at this point rather than pushing the outer. Allow the outer cable to enter the tube until the drawn line is level with the end of the tube, then release the inner cable and pull back the outer cable about 0.5 mm/0.02in. Drop the jubilee clip over the slotted tube and do it up to clamp the cable.

Check that the inner cable is free to rotate when pushed towards the sensor and pulled from it. Thread the inner cable through the rest of the outer, feeding the outer cable so that its line is level with the end of its slotted tube. Fit the jubilee clip as before.

TB	NTEL NTEL NTEL 18
EP732	14mm
	35.5mm
	49-5 mm

Fig. 12 Speedometer cable marking

Showing the speed sensor

### MAIN UNIT

Fit the main unit in a suitable position. If screws are to pass through the plastic box make sure that they will not foul anything inside. Double sided adhesive pads can be used to mount the flat side of the box to a flat surface or to a bracket, and these hold well. Thread the cable to a suitable point and connect up as follows. This is for -ve earth vehicles.

Black wire	—Terminal block 1, also to chassis.
Red wire	Unswitched battery power (+ 12 volts)
Yellow wire	Power switched by ignition switch
	(or auxiliary).
Orange wire	-Power switched by light switch
	(e.g. a panel light).
Brown wire	—Terminal block 2 (+5 volts).
Violet wire	—Terminal block 3 (Speed signal).
Green wire	Terminal block 4 (Flow signal)

Connect to the sensors as follows.

Terminal	block	1—Blue wires from both sensors.
Terminal	block	2—Brown wires from both sensors.
Terminal	block	3-Green/Yellow wire from speed sensor
Terminal	block	4-Green/Yellow wire from flow sensor.

### CALIBRATION

The flow sensor will be supplied with two numbers, one for litres, one for gallons. This number is the number of pulses per litre or gallon divided by 256.

With the unit installed in the car and the ignition switched on, press 'Enter', 'F.Cal', the number, 'End'. Note that if this number is greater than 128, fuel use will read distance/fuel (i.e. miles per gallon), if less than 128 it will read 100 x fuel/distance (i.e. litres per 100 kilometres). The distance calibration is calculated in a similar manner. It will be about 25 for miles, 16 for kilometres; the exact number depends on gear ratio and wheel sizes. Calibration is achieved by entering an approximate number (25 or 16), driving a known distance, pressing 'Reset' at the beginning of the distance and 'Hold' at the end. The true calibration number is obtained by multiplying the entered calibration number by the recorded distance and dividing by the actual distance.

The marker posts on motorways are exactly 100 metres apart (100 yards on some of the older ones) and can be used for calibration as follows. Press 'Enter', 'D.Cal', 1, 6, 'End', 'Distance'. Drive along the motorway and as a post is passed press 'Reset'. Count 20 posts (2 kilometres) and press 'Hold'. The kilometre calibration figure is 16 x reading /2. The calibration for miles is obtained by multiplying the kilometre calibration by 1.61. For greater accuracy travel a greater distance.

When reading speed it is likely that the computer reading will be considerably different from the car speedometer, and the distance could also be different. It is the car's own speedometer that is wrong, usually reading high. This makes the car appear to be faster and do more miles per gallon than is actually the case, so be prepared for a disappointment.

It is possible to calibrate the flow sensor for optimum accuracy in the same manner as for the speed sensor, by filling the tank up, driving a distance then refilling the tank. It is easy to trap air in the tank or spill a small amount of petrol, so this should be done over at least 10 gallons. Garage petrol pumps are usually very accurate.



The new calibration constant is:

Old calibration constant x Computer reading Actual quantity

The reading for the display is obtained by dividing the number of pulses counted by (256 x calibration constant). The litres figure is 0.219 x the gallons figure. The fuel could therefore be expressed in any form, for example cost, giving a readout of pounds, miles per pound and so on. The totals are not affected by changes in the calibration number.

The calibration numbers, entered numbers, entered start and stop values and totals will be held in the unit's memory until changed from the keyboard as long as the unit is connected to power. If the battery is disconnected or allowed to go flat the calibration numbers will have to be re-entered. The 1000 $\mu$  capacitor, C9, will hold the power for a very short while.

The unit is intended to work on negative earth vehicles, but can be adapted for positive earth by connecting the light return wire to the +ve supply wire and constructing a simple transistor invertor for the ignition wire as shown in Fig. 14.



Fig. 14 Transistor invertor for the ignition wire

### **COMBINATION LOCK**

Pin 35 of IC1 is an output of a combination lock facility incorporated in the computer. This could be used to operate an alarm or ignition cutout, and the operation is as follows. If 'Enter', 'Average/Low' is pressed, the display will show an 'L' in the function digit and blank the rest of the display. If the ignition is now switched off, when it comes on again pin 35 of IC1 will be high. To cause it to go low again a three digit combination must be entered, then 'End' pressed. The number is held in the 2716 program memory, and will normally be supplied with a random number, though a specific number could be supplied.

The signal on pin 35 will not go from low to high (locked) with the ignition switched on. This is needed with an ignition cutout as a safety measure as otherwise it could switch off the engine going along, but in addition to this, some sort of 'fail safe' circuit should be used, such as that shown in Fig. 15. R1 should be mounted inside the box, the other components outside. The reason for this is to keep interference noise out of the unit. The relay disables the ignition by shorting out the contact breakers, or inductive or photocell pick up. If the wire to the computer is cut with this circuit, the ignition will be disabled.



### **CHANGING THE COMBINATION**

The three digit combination number is held in the 2716 program memory and can be changed up to seven times without erasing the PROM, making use of the fact that individual memory locations in a 2716 can be programmed. The initial number is held in locations 7FE and 7FF (hex), but the unit will search for a number from locations 7FO upwards (in pairs), and use the first that it finds that is not FF. The first new number can thus be entered by programming into locations 7FC and 7FD, the next to 7FA and 7FB and so on. The low order location contains the two low digits of the combination, the high order location a hex 'D' (Binary 1101) and the high order digit of the combination.

So to change the number to 123, location 7FC is programmed with 23 hex and 7FD with D1 hex. To change again to 456, program location 7FA with 56 and location 7FB with D4.

### PROGRAM

The operation of the car computer is controlled by the program contained in IC2. It is not practical to describe in full this program, or even to list it, but what follows is a brief description of how it operates.

The various sections of the program are:

Interrupt—Every 500µs approximately, the program receives a time interrupt.

*Keyboard*—When a key has been pressed, this routine carries out the appropriate action.

*Calculation*—This routine takes the information from the appropriate stores, under the control of the function selected, and performs the necessary arithmetic on it, for feeding to the display.

Sample—Every second, or eighth of a second, this routine updates the instantaneous stores.

Start-Stop—If the start-stop mode is active, this routine compares the result from the calculation routine with the start stop information and carries out a reset, or sets the hold condition as required.



The two board component assembly

### INTERRUPT

The interrupt routine (a) updates the timebase and time store (b) samples the speed and flow inputs (c) every fourth interrupt it samples the keyboard, and advances the strobe and multiplexed data output.

The main stores for holding total time, fuel and distance are each three bytes long. The low order time byte is also the high order timebase byte, the timebase being two bytes long, and each interrupt the combined 4 byte store is incremented.

The speed and flow inputs are compared with their states at the previous interrupt. If one is now high and was low, the corresponding total store is incremented. A separate one byte store used for the instantaneous signal is also incremented, and the timebase copied to a start store if this is the first count in a sample, or to a stop store otherwise. This is done to improve resolution as there could be only a small number of counts in a sample.

Every fourth interrupt the keyboard lines are sampled. If one is high, the line and timebase information is copied into a current key store. The strobe lines are all made low, the data lines set to the next digit and the new strobe information sent, derived from the timebase.

Note that the timebase information that was copied into the current key store corresponded to the next strobe line. This is why the keyboard is scanned one strobe early. At the end of four strobes the current key store is compared with a previous key store, then copied to that. If the two were different, then a new key has been pressed and a flag is set to instruct the keyboard routine to action the key.

### **KEYBOARD**

If the new key flag is set, it is cleared, and the keyboard routine run. The key is first checked for nothing—releasing a key also sets the new key flag. There is a main flag with eight states which controls the keyboard.

If the key is 'End' the flag is set to zero. In this state with the key 1 to 5, the function store is set to the key number and the average/low and remainder flags are cleared.

When the key is 6, the average/low flag is set. Depressing 7, the total stores are set to zero. If the start-stop mode is active, then the Start flag is set. With 8, the hold flag is set and the start-stop flags cleared. At 9, the hold flag is cleared. If the start-stop request flag is set, the start-stop active flag is set, the start number subtracted from the stop number and the sign of the result copied to the start-stop direction flag.

With 0 keyed, the keyboard flag is set to 1. If the key is 'Enter', the keyboard flag is set to 3.

If the keyboard flag is 1 then if the key is 1 to 5 it is copied to the function store and to a remainder function store. The flag is set to 2 and the remainder flag set. Any other key, the keyboard flag is set to 0 and the key handled as for a flag of 0.

If the keyboard flag is 2 and if the key is 1 to 3 it is copied to the remainder function store. The keyboard flag is set to 0. any other key, the keyboard flag is set to zero and the key handled as for a keyboard flag of 0.

If the keyboard flag is 3 and if the key is 6 then the lock routine is executed. If 8 the start-stop request flag is set. If 4, 5 or . it is ignored. Otherwise the key is copied to a destination store, the keyboard flag is set to 4 and the contents addressed by the destination store are sent to the display.

If the keyboard flag is 4 then the results digits are cleared, the key added to the display store and this copied to the address pointed to by the destination store.

If the keyboard flag is set to 5 then as before except that the results digits are not cleared. Whenever the keyboard flag is set to a number other than zero, a countdown flag is set to 7.

### CALCULATION

There are five basic calculations, as follows *Total*—function 1 to 3 *Instantaneous*—function 4 or 5 *Average*—function 4 or 5 *Remaining total*—function 1 to 3 *Remaining average*—function 4 or 5

The total is calculated by taking the contents of the total store addressed by the function store and dividing by the corresponding calibration number. The calibration number for time is generated internally. If the low flag is set, this is now divided by .001 (the same as multiplying by 1000). If this causes an overflow, the low flag is cleared.

Instantaneous functions are obtained by taking the instantaneous distance divided by its calibration and dividing this by either instantaneous time (a constant) for speed, or instantaneous fuel for fuel use, again divided by its calibration number. If fuel use and the calibration number is less than 128 then 100 is divided by the result obtained (for litres per 100 kilometres).

Average functions are the same except that totals rather than instantaneous quantities are used.

It starts to get complicated for remaining totals. In the description following total function refers to the total addressed by the function store, entered remainder refers to the entered quantity addressed by the remaining function store, and so on.

The total remainder is divided by its calibration, copied to a temporary store then divided by the total function divided by its calibration. The result is stored in a second temporary location. The contents of the first temporary location are now subtracted from the entered remainder, and this divided by the contents of the second temporary store to give the result.

Remaining averages are obtained as for averages except that the difference between the entered stores and the totals are used rather than the total stores.

### SAMPLE

When the timebase low byte becomes zero in the interrupt routine, the sample routine is entered. This is approximately eight times per second. If the lower three bits of the timebase high byte are also zero, i.e. every second, then the countdown flag is decremented. If it is now zero, the keyboard flag is set to zero. The main sample routine is entered. If the lower three bits are not zero, then if the startstop active flag is not set, the sample routine is skipped. If either the flow or distance counts (instantaneous) is less than 2 the sample routine is skipped, otherwise the main sample routine is executed, as follows.

The instantaneous count for flow is divided by the difference between the start and stop numbers, the result being instantaneous flow. This is repeated for distance. The instantaneous counts are cleared. The input signals are thus measured for both frequency and period giving good resolution even at low frequencies.

### START-STOP

There are three flags controlling start-stop operation, start-stop request/started, start-stop active and start-stop direction. The latter is set if the start number is greater than the stop number. The request flag is set when 'Enter', 'Stop' is entered from the keyboard and the active flag when 'Run' is pressed and the request flag is set.

If the active flag is clear, the start-stop routine is ignored.

If the request/started flag is set then the start store is used for the following comparison, otherwise the stop store.

The result is subtracted from the start or stop store and the sign of the result exclusive ORed with the start-stop direction flag. If the result of this is a '1' no further action is taken, otherwise:

If the request/started flag is set, it is cleared and the main totals reset to zero. The timebase and instantaneous counts are also reset.

If the request/started flag was clear, the hold flag is set and all start-stop flags are cleared.

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6



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is heard. Conventionally there is a switched choice between slow pulsato, at 0.7 Hz, and fast pulsato, at 7 Hz.

Figure 2 shows the CBS electronic equivalent to this well known mechanical arrangement. A musical tone at input 20 is fed to variable delay device 22, which can be a bucket brigade delay line. The delay is driven by clock 24 which is under the control of a sine wave generator. This is adjustable between 1Hz and 7Hz for slow and fast operation. The control waveform is shown at Figure 3A.

The output of delay 22 is filtered at 28, to remove any clock pulses impressed on the signal, and the filtered output is shown at Figure 3B. As the delay 22 is modulated by the clock signal it causes the phase of the musical tone to advance and retard, making it sharp or flat with respect to the input. This mimics the Doppler effect created by a rotary speaker.

The frequency modulated signal (B) at the output of filter 28 is applied to the input of amplitude modulator 40. This is also controlled by sine wave oscillator 26. Modulator 40 provides 80% amplitude modulation of the frequency modulated signal B to produce a composite signal. As shown at Figure 3C the amplitude of this composite signal is maximum at the transitions from sharp to flat pitch and minimum





at the transitions from flat to sharp pitch. The modulator 40 inverts the phase of the signal, and high frequency components are removed by filter 42. The filtered, amplitude-modulated signal (C) is summed at 46 with the constant amplitude, frequency-modulated signal B. Capacitor 54 transmits only the high frequencies of signal B for summing at 46 with the phase inverted signal C. So only high frequencies are amplitude modulated in summing circuit 46 by the AM, FM signals from modulator 40. This produces a composite envelope as shown in Figure 3D. Amplitude modulation of the higher frequencies is in opposite phase relative to that of the lower frequencies, and the percentage of modulation varies with frequency. This complex, amplitude-modulated, AM signal is reproduced by stationary speaker 50 and the less complex, FM signal is reproduced by stationary speaker 34. The overall effect, with acoustic summing of the two sound signals, is said to resemble that produced by a rotary Leslie system.

### **BUY BRITISH!**

The Director of Leeds Library reminds us that British, American, European and PCT patents can be inspected free of charge at public libraries in Birmingham, Glasgow, Leeds, Liverpool, Manchester and Newcastle, as well as in the libraries attached to the London Patent Office. This reminder follows our warning that it can be very expensive to purchase a copy of a lengthy foreign application: up to £20 each in the case of the two very bulky PCT patents applications on Robert Carver's power amplifier and sonic holography circuits. There is also another way of avoiding the high cost of purchasing foreign applications.

When the British equivalent application to a foreign patent is published it is of course possible to obtain a full copy of the British version for the standard price of just £1.45. The trick is to wait for the British version of lengthy foreigns. Take for instance the example of PCT 80/02219, the Robert Carver patent on sonic holography. The equivalent British patent application has now been published as no. 2 058 524. Although the British document is only a single printed page synopsis which cross references with the lengthy PCT case, the British Patent Office is obliged to provide a full photocopy of the PCT case for the all-in price of £1.45, instead of the £20 or so it would cost to obtain the same patent by ordering it under the PCT number!

LESLIE SIMULATION

sound of an electronic organ.

CBS of New York has filed a string of

European patent applications (under num-

ber 0 031 692) on an electronic circuit

for simulating the sound of a Leslie

loudspeaker, as often used to reproduce the

Figure 1 shows a loudspeaker of the

Leslie type. The drive unit 10 rotates about

a vertical axis while reproducing the sound.

In the speaker position shown in Figure 1,

no direct sound reaches the listener L; only

sound reflected from the walls of the

cabinet. As the speaker rotates towards

position 2, the reproduced sound rises in

pitch due to Doppler effect. At the same

time direct sound starts to reach the

listener so there is an increase in amplitude along with increase in perceived frequency.

This continues up to a maximum at position 3. Further rotation away from position 3

towards position 4 produces a perceived

decrease in frequency and amplitude. The

term "pulsato" conveniently describes the

combination of tremulo and vibrato which



HE MOTOR control boards which fit on the wheel base of the M101, control the two 1/10 h.p. 12 V motors which provide its motive power. The controls implemented by these boards are forward, reverse, stop and variable speed. .

### **DISPLAY BOARD**

This gives a visual indication of the microprocessor's control mode and an audible indication of incoming data.

### **POSITION DETECTOR BOARD**

This is the only electronics inside the bases of the Genesis S101, P101 robots. It is used to suppress mains hum and generally improve the signal from the position detector coils sending it at low impedance to the interface board.

### MANUAL CONTROL KEYBOARD

All the position control and programming switches fit to this unit. Information from it is either by infra-red link for the M101 or by wire link for the S101 and P101.

### RECEIVER

This detects the infra-red transmissions sent to the M101 mobile machine.





FORWARD

REVERSE

STOP

STOP



EG 740



### **DISPLAY BOARD**

Refer to Fig. 3.1. The seven segment display which indicates the memory page number (0 to 7) is driven by decoder IC1. An open collector TTL buffer drives l.e.d.s indicating such functions as RECORD, PLAY, MEMORY ( $\frac{3}{4}$  full) and motor control. The bleeper announces the arrival of data.

### **MOTOR CONTROL**

Refer to Fig 3.2. The wheel base is driven by two independently operated 1/10 h.p. motors equipped with special shafts in direct contact with the rubber wheels. The gearing is by virtue of the difference in diameter of the shaft and the wheel. Steering is accomplished by operating the motors at different rates. The castors are not driven and are for stabilisation of the wheel base only.

There is one control board per motor, which is a 12V d.c. 90 watt device. When the motor is turned on, the initial current is 15 amps, falling to 7.5 amps after 0.3 seconds. This hefty current is handled by power transistors TR3 and TR4. On/off control is a TTL signal (A). Motor reversal is produced with a changeover relay, again under TTL control (B). The motor speed is controlled using mark/space modulation with a period of about 40ms. The control box has two motor controls. One is forward-stop-reverse, and the other is steering. Both controls are specified with a three bit code. The software decodes these two parameters and generates the appropriate B signals and mark/space modulated A signals. When no control data is received by the interface board a stop signal is generated which turns off both motors. This prevents the mobile unit travelling beyond its reception range.



### COMPONENTS ...

### MOTOR CONTROL (2 off on mobile unit only)

### Resistors

R1, R7 1k (2 off) R2, R8, R122k7.(3 off) R3 47k R4, R5 100m 2W (2 off) R6 100 R9 560 R10, R1110k (2 off)

### **Transistors and Diodes**

D1, D2, D4, D51N4002 (4 off) D3, D6-81N4148 (4 off) TR1, TR5TIP29 (2 off) TR2 BC182L TR3, TR4TIP 3055 (2 off) TR6, TR7BC212L (2 off)

### Miscellaneous

Printed circuit board RMBa p.c.b. mounting fuse holder 8A fuse 4-way screw terminal Relay 2p-2w, 10A contacts 5-way Molex p.c. terminals Heatsink bar (special product) 6BA pan head  $\frac{1}{2}$ in. plus nut (4 off) Insulating kit for TIP 3055 (2 off) Insulating kit for TIP29 (2 off)

### DISPLAY BOARD

### Resistors R1-10 220 (10 off)

R11 47

Capacitors C1 220µ/10V

### **Diodes and Displays**

D1, D3 Red I.e.d. 0.2in. (2 off)

- D2 Green I.e.d. 0.2in.
- X1 Common anode 7-segment

### **Integrated Circuits**

C1	74LS47
C2	7407

### Miscellaneous

Printed circuit board WD 1 audible warning device PB2130 10-way lead (10 x 7/0·2) 10-way Molex shell 6471-10 Crimp terminals 4809TL 7/0·2 wires 6mm p.v.c. sleeving

		ON
S4	NORMAL	TEST E19-TRISTATE
S3	NORMAL	TEST E20-TRISTATE
S2	NORMAL	Т
	S+SOLENOIDS ON	S+SOLENOIDS OFF
\$1	NORMAL	T
	T S- SOLENOIDS ON	S- SOLENOIDS OFF

Fig. 3.8. Signal and axis directory

0	1	2	3	4	5
DO	DI	DC2	003	DC4	
M8-8	M8-5	M8-4	M8-10	M8-1	
13	14	15	12	1	
ROTATE * LEFT	RETRACT *	DOWN *	WRIST * CCW	HAND OPEN	NONE
ROTATE * RIGHT	EXTEND *	UP *	WRIST * CW	HAND CLOSED	NONE
ROTATE * LEFT	RETRACT *	DOWN *	WRIST * CCW	HAND OPEN	NONE
ROTATE * RIGHT	EXTEND *	UP *	WRIST * CW	HAND CLOSED	NONE
ROTATE * LEFT	SHOULDER *	ELBOW *	WRIST *	WRIST * CCW	HAND OPEN
ROTATE * RIGHT	SHOULDER *	ELBOW *	WRIST*	WRIST * CW	HAND CLOSED
S0+ M3-8	S1+ M3-10	\$2+ M2-8	S3+ M2-1	S4+ M2-3	S5+ M2-5
S0 M3-9	S1- M2-7	S2- M2-9	S3- M2-2	S4	S5- M2-6
	0 DCO DO M8-8 13 ROTATE * LEFT ROTATE * RIGHT ROTATE * LEFT ROTATE * RIGHT ROTATE * LEFT ROTATE * RIGHT SO+ M3-8 SO- M3-9	0       1         DC0       DC1         D0       D1         M8–8       M8–5         13       14         ROTATE*       RETRACT*         ROTATE*       EXTEND*         ROTATE*       EXTEND*         ROTATE*       RETRACT*         ROTATE*       RETRACT*         ROTATE*       EXTEND*         ROTATE*       EXTEND*         ROTATE*       SHOULDER*         ROTATE*       SHOULDER*         ROTATE*       SO+         SO+       S1-         M3–9       M2–7	0       1       2         DC0       DC1       DC2         D0       D1       D2         M8-8       M8-5       M8-4         13       14       15         ROTATE*       RETRACT*       DOWN*         ROTATE*       EXTEND*       UP*         ROTATE*       RETRACT*       DOWN*         ROTATE*       SHOULDER*       ELBOW*         RIGHT       ↑       ↑         SO+       S1+       S2+         M3-8       M3-10       M2-8         SO-       S1-       S2-         M3-9       M2-7       M2-9	0       1       2       3         DC0       DC1       DC2       DC3         D0       D1       D2       D3         M8-8       M8-5       M8-4       M8-10         13       14       15       12         ROTATE*       RETRACT*       DOWN*       WRIST*         ROTATE*       RETRACT*       DOWN*       WRIST*         ROTATE*       EXTEND*       UP*       WRIST*         ROTATE*       RETRACT*       DOWN*       WRIST*         ROTATE*       RETRACT*       DOWN*       WRIST*         ROTATE*       RETRACT*       DOWN*       WRIST*         ROTATE*       SHOULDER*       ELBOW*       WRIST*         ROTATE*       SHOULDER*       ELBOW*       WRIST*         SO+       S1+       S2+       S3+         M3-8       M3-10       M2-8       M2-1         SO-       S1-       S2-       S3-         M3-9       M2-7       M2-9       M2-2	0       1       2       3       4         DC0       DC1       DC2       DC3       DC4         D0       D1       D2       D3       D4         M8-8       M8-5       M8-4       M8-10       M8-1         13       14       15       12       1         ROTATE*       RETRACT*       DOWN*       VRIST*       HAND OPEN         ROTATE*       RETRACT*       DOWN*       WRIST*       HAND OPEN         ROTATE*       EXTEND*       UP*       WRIST*       HAND OPEN         ROTATE*       RETRACT*       DOWN*       WRIST*       CLOSED         ROTATE*       RETRACT*       DOWN*       WRIST*       CLOSED         ROTATE*       RETRACT*       DOWN*       WRIST*       CLOSED         ROTATE*       EXTEND*       UP*       WRIST*       CLOSED         ROTATE*       SHOULDER*       ELBOW*       WRIST*       WRIST*         ROTATE*       SHOULDER*       ELBOW*       WRIST*       WRIST*         S0+       S1+       S2+       S3+       S4+         M3-8       M3-10       M2-9       M2-4       M2-4

### **POSITION DETECTOR BOARD**

The first stage of the position detector electronics provides a signal gain of 20dB (times 10). The second stage is a 100Hz high pass filter that suppresses any picked-up mains hum (50Hz) by 12dB (a factor of 4) relative to the 100Hz feedback signal. The output signal is typically a sinewave that varies in amplitude from 160mV p.p. to 1.6V p.p. as the hydraulic actuators move over their full range. This signal is then fed to the interface board where it is turned into a d.c. voltage. See Figs. 3.9, 3.10 and 3.12.

### MANUAL CONTROL KEYBOARD

This hand-held controller provides up to 27 push-button commands plus two analogue channels for use with the mobile M101 to which data is transmitted by infra-red whilst the S101 and P101 use a 4-core cable link. See Figs. 3.13 and 3.14.

E10 is an oscillator set to run at 2KHz. E4 divides this frequency by two and generates complementary squarewave outputs. These are differentiated and then squared up by the Schmitt triggers E5 thus producing complementary marker and data pulses. The data pluses drive a binary counter E3, which then drives row and column multiplexers (E2 and E1). This causes the keyboard switches to be sequentially scanned. When a key is pressed data pertaining to that key will appear at E2 pin 3. The timing diagram shows that there are 64 data pulses per complete scan of the unit. The first 40 are reserved for push buttons (only 27 are used), next come two blocks of 8 that are used for analogue to digital conversion and the last block of 8 is a sync period. The ADC units operate by comparing a ramp waveform with the d.c. voltage that is to be digitized. When the ramp voltage exceeds the d.c. voltage the output of the comparator E8 goes high and appears at the Q output of E9 on the next marker clock pulse. As the d.c. voltage is varied the ADC will produce zero to 8 data pulses in its respective time slot. E6 and E7 combine the keyboard and ADC data with the marker pulses and the sync period. The M101 unit uses infra red diodes to transmit the data. The diodes need current pulses of about 1 to 2 amps and so a Darlington transistor pair had been used (TR1, TR2).

Two component layouts are given (next month), one for the M101 unit and one for the S101 and P101 units neither of which have ADCs or an infra-red link.





Fig. 3.9. Position Detector board circuit diagram

Axis	M101, S101	P101
DO	$\begin{array}{c} C23 = 3n3 \\ R4 = 220k \end{array}$	C23 = 3n3 R4 = 220k
D1	C24 = 4n7 R10 = 220k	C24 = 3n3 R10 = 130k
D2	C25 = 2n2 R16 = 220k	C25 = 3n3 R16 = 130k
D3	C26 = 1n5 R22 = 220k	C26 = 1n5 F22 = 22.0k
D4		C27 = 1n5 R28 = 220k

### Fig. 3.10. Component values for Position Detector









### Practical Electronics January 1982





Fig. 3.14. Timing diagram for manual control keyboard

### **DIRECT SOLENOID CONTROLLER**

It is possible to control the robots directly without any electronics by use of this board. Switches S1–S12 are used to turn on the Darlington drivers E1–E4 which supply the power to the solenoid operated valves.

### **INFRA-RED RECEIVER (next month)**

The infra-red pulses are detected by two reverse biased diodes (D3, D4). The signal from these diodes is amplified by E1, and level shifted by the first part of E2. The second part of E2 is a voltage comparator which is used to recover the original pulses from somewhat noisy infra-red signal. The data is then sent to the decoding section on the interface board. TR4 and TR5 turn on the l.e.d. (D5) when data is being received.

### MICROPROCESSOR SECTION: HOW THE CON-TROLS WORK (next month)

On power up the bleeper sounds five times, the play and record l.e.d.s go off, and the sequence number indicates zero. The controller is now in reset mode. Only from this mode can the sequence number be changed or can the current sequence be cleared.

Reset mode can always be reached by pressing the RESET button.

## CHANGING THE SEQUENCE NUMBER (RESET MODE ONLY)

Pressing the SEQ+ button will advance the indicated sequence number (up to 7). Pressing the SEQ- button will decrement the indicated sequence number (down to  $\emptyset$ ).

For correct functioning of a new unit (or one in which the CMOS memory had been powered down) it is necessary to clear each sequence memory before you attempt to record a sequence.



To clear a sequence, enter reset mode (if not already in this mode), and select the required sequence number. Hold down the CLEAR button; whilst holding this button down, momentarily press the EDIT button, then release the CLEAR button. The unit responds by bleeping and flashing the display.

### COMPONENTS **POSITION DETECTOR BOARD** Resistors R1, R7, R13, R19, R25 1k (5 off) R2,R3,R8,R9,R14,R15,R20,R21,R26,R27 10k (10 off) R5,R11,R17,R23,R29 13k (5 off) R6,R12,R18,R24,R30 91k (5 off) R31-35 15 (5 off) For R4,R10,R16,R22 and R28 see Fig. 3.10 All resistors 1W 5% Capacitors C1-C3, C5-C7, C9-C11, C13-C15, C17-19 47n (15 off) (Siemens B32560 type) C4,C8,C12,C16,C20 10u elect. (7 off) C21,C22 For C23-27 see Fig. 3.10 (should be Siemens B32560) **Integrated circuits** IC1-3 LM324 (3 off) **Miscellaneous** Printed circuit board RPD 14 pin sockets for i.c.s (3 off)

**NEXT MONTH:** Manual control and manual control board electronics

MICRO-EUS

## Compiled by DJD.

Appearing every two months, Micro-Bus presents ideas, applications, and programs for the most popular microprocessors; ones that you are unlikely to find in the manufacturers' data. The most original ideas often come from readers working on their own systems; payment will be made for any contribution featured.

IN THIS month's Micro-Bus the emphasis is on graphics. Two novel hardware modifications for the Acorn Atom provide noise-free graphics, with virtually no loss in speed, and a previously inaccessible eight-colour graphics mode. Also featured are two graphics programs for the ZX81.

### NOISE-FREE ATOM GRAPHICS

When the Atom's 6502 processor accesses the video memory, screen interference is produced in the form of white specks. The following simple hardware modification completely removes this problem. producing interference-free video-RAM access. It was discovered by *Chris Dunning* of Bristol who writes:

"The 6502 processor used in the Acorn Atom only accesses memory when its  $\emptyset 0$  clock is high, so if the video output circuit is arranged so that it only accesses memory

when  $\emptyset 0$  is low the micro and the video controller will never clash for memory access and no interference will be produced on the screen.

"To achieve this on the Atom carry out the following modifications, shown in Fig. 1:

1) Solder a wire link across C10.

2) Remove that 4MHz crystal.

3) Connect a wire from pin 8 to IC9, the inverted 3.58MHz clock used by the 6847 CRT controller.

4) Connect the other end of this wire to the hole left by removing the crystal, which is connected to pin 3 of IC45.

The effect of these modifications can be shown most strikingly by the following test. Before making them, assemble and execute the following machine code in the screen memory by typing:

> P = # 8000 |JMP P LINK P-3



Fig. 1. Modifications to the Acorn Atom give noise-free graphics



Fig. 2. Modification to the Atom switches between noise-free graphics and full-speed operation

The text will become completely masked by screen noise produced by accessing the graphics memory. Repeating the test after having made the modifications gives a perfectly clear screen, with no noise.

"Since the micro's ØO clock is now 0.895MHz instead of the previous 1MHz the cassette interface is no longer CUTS standard; this is presumably why the Atom uses independent crystals for the display and the processor. You could of course re-record all your programs at the new frequency, or the modification can be made switchable. The best way to do this is probably to cut the track to pin 13 of IC44 and connect this via a changeover, switch to pin 2 of IC45 (with C10 and the crystal still in place) and pin 8 of IC9; see Fig. 2. The switch can now be used to change the clock to 1MHz for reading tapes. or 0.895MHz for clean graphics. Note, however, that you should always hold down the break key when operating the switch or the contents of memory may become corrupted."

### NEW ATOM GRAPHICS MODE

The second of this month's hardware modifications to the Acorn Atom computer shows how to plot in an eight-colour graphics mode. This mode is provided by the 6847 Video Display Generator chip, and is called the "Semigraphics Four" mode; it is not directly accessible on the Atom, but can be obtained with two simple circuit modifications.



Fig. 3. Eight-colour graphics mode: (a) character-cell pixel arrangement; and (b) corresponding memory byte

The mode, which will be referred to as "0a". uses the same arrangement of character cells as the text mode. The character cell is divided into four elements, as in Fig. 3(a), and each of the four elements can be "on" or "off", as determined by the states of the lower four bits of the corresponding byte in memory. The next three bits determine the colour of the elements that are "on", as shown in Fig. 3(b). All the elements, in one character cell have to be the same colour, or black, and with this restriction mode 0a provides nine-colour graphics (counting black) with a resolution of  $64 \times 32$ .

### HARDWARE MODIFICATION

To access the eight-colour graphics mode the following modifications should be made to the Atom circuit board:

1) Remove the 6847, IC31, from its socket.

2) If the i.c. socket gives access to the printed circuit board beneath it, locate the track which links pins 31 and 34 to pin 2 (on the component side of the board). Make two cuts in this track, as close as possible to pins 31 and 34, to isolate these pins from the circuit. Replace IC31.

3) Alternatively, bend pins 31 and 34 of the 6847 outwards, and replace the i.c. into its socket with these pins sticking out sideways.

4) Connect a wire from pin 31 if the i.c. (INT/EXT) to pin 1 (earth).

5) Connect a wire from pin 34 of the i.c. (A/S) to pin 39 (CSS)

The normal switch-on mode will be text, as before; to obtain the new graphics mode type: ? #B002 = 8

This takes pin 34 on the 6847 VDG high and gives the new mode. The low-resolution graphes characters are now not accessible; however, if required, a switch could be inserted to give a choice of either mode.

### **PLOTTING POINTS**

A BASIC routine 'p' to plot points in mode Oa is shown in Fig. 4, lines 100 to 110. The coordinates are passed to the routine in variables X and Y, where X = 0, Y = 0corresponds to the bottom left-hand corner of the screen, and X = 63, Y = 31 is the top right-hand corner of the screen. The value of C determines the colour in which the point is plotted, and this can have the values 0 to 7 as shown in Fig. 3(b). Note that plotting a point of one colour in the same character cell as another point will set both points to the last colour.

10 DIM(W3) 20 IW-#4080102 30 CLEAR 0, 78B002=8. 40 FOR U=-31 TO 31, FOR V=-15 TO 15 50 C=((U\*U+V\*V)/80)&7; X=U+31; Y=V+15 60 GOSUB p, NEXT; NEXT; DO UNTIL 0 100pP=X/2+(31-Y)/2\*32+88000, 7P=(?P&#F)+C\*16 110 ?P=7P|W?(X&1+Y&1\*2); RETURN

Fig. 4. Atom program plots circles in the eight-colour graphics mode

To clear to mode 0a the statements: CLEAR 0; ? # B002 = 8

can be used. Note that pressing escape will not reset the graphics mode to the text mode; to do this it is necessary either to type: ? # B002 = 0

or to press BREAK followed by typing OLD to retrieve the program.

The BASIC plotting routine could be converted into machine-code and patched into the Atom's graphics to give fast line drawing in the new mode.

### **COLOURED CIRCLES**

As an example of the use of this routine the program in Fig. 4, lines 40 to 60, draws a series of concentric coloured circles. The equation in line 50 gives a number whose value depends on the distance from the centre of the screen; this value is then used to select the colour for plotting. The resulting display, shown in Fig. 5, is very colourful, although the black-and-white photograph does not really do justice to it.



Fig. 5. Display produced by the program of Fig. 4

### **LINES ON ZX81**

Drawing a straight line between any two points is one of the fundamental graphics operations. The subroutine of Fig. 6 devised by S. J. Duggins of Birmingham performs this operation in just seven statements, improving over the routine given in the ZX81 manual (on page 121) which takes 26 statements.

> 9000 LET L=INT(0.5+(SQR(ABS(X2-X 1)\*\*2+ABS(Y2-Y1)\*\*2))) 9005 FOR A=0 TO L 9010 LET X=(X2-X1)\*A/L 9020 LET Y=(Y2-Y1)\*A/L 9030 PLOT X1+X,Y1+Y 9040 NEXT A 9050 RETURN

Fig. 6. ZX81 routine plots a line between X1, Y1 and X2, Y2

The two points between which the line is to be drawn are supplied to the program as variables X1, Y1 and X2, Y2, where X1>=0, X2<=63, Y1>=0 and Y2<=43. The number of plotting points between the end-points is first calculated as L (line 9000). To plot the points two separate linear equations are used. one for the X direction and one for the Y direction.

### **ZX81 ETCH-A-SKETCH**

The program shown in Fig. 7 was submitted by G. Wheaton of Bolton, and turns the 1K ZX81 into an etch-a-sketch machine so that designs can be drawn using the cursormovement keys 5, 6, 7 and 8. Mistakes can be rectified by pressing the 0 (rubout) key, and plotting resumed by pressing the 1 key. Diagonal lines are possible by careful use of the 0 and 1 keys between plotting.

10	LET N=10
20	LET X=0
30	PLOT N,X
40	IF INKEYS="8" THEN LET N=N+1
50	IF INKEYS="5" THEN LET N=N-1
60	IF INKEYS="6" THEN LET X=X-1
70	IF INKEYS="7" THEN LET X=X+1
71	IF INKEY\$="0" THEN GO TO 90
72	IF N>30 THEN LET N=30
74	IF N <o let="" n="0&lt;/td" then=""></o>
76	IF X>30 THEN LET X=30
78	IF X <o let="" then="" x="0&lt;/td"></o>
78 79	IF X<0 THEN LET X=0 UNPLOT N,X
78 79 80	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30
78 79 80 90	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X
78 79 80 90 95	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X
78 79 80 90 95 100	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X IF INKEX\$="1" THEN GO TO 30
78 79 80 90 95 100 110	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X IF INKEYS="1" THEN GO TO 30 IF INKEYS="8" THEN LET N=N+1
78 79 80 90 95 100 110 120	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X IF INKEYS="1" THEN GO TO 30 IF INKEYS="8" THEN LET N=N+1 IF INKEYS="5" THEN LET N=N-1
78 79 80 90 95 100 110 120 130	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X IF INKEYS="1" THEN GO TO 30 IF INKEYS="8" THEN LET N=N+1 IF INKEYS="5" THEN LET N=N-1 IF INKEYS="6" THEN LET x=x-1
78 79 80 90 95 100 110 120 130 140	IF X<0 THEN LET X=0 UNPLOT N,X GO TO 30 PLOT N,X UNPLOT N,X IF INKEYS="1" THEN GO TO 30 IF INKEYS="8" THEN LET N=N+1 IF INKEYS="5" THEN LET X=X-1 IF INKEYS="6" THEN LET X=X+1

## Fig. 7. Etch-a-sketch program for the ZX81 uses the cursor controls to draw pictures

The program should be fairly selfexplanatory. Plotting is performed by lines 30 to 80, and unplotting by lines 90 to 150. When unplotting the cursor flashes (lines 90 and 95) to indicate its position.

### **HORSE-RACE ADDENDA**

"The Horse-Race program for the Mk14 VDU in August's Micro-Bus is excellent, once it works!" writes W. R. Osborne of Tyne and Wear, who has pointed out some misprints. Three jump offsets are incorrect, and should be altered as follows:

AD2 should read E1 not D1 AD8 should read 0D not 0C

ADE should read 07 not 0C

Also, the XPPC 3 at the end, of the program does not return to the monitor, since P3 is altered in the program. The code shown in Fig. 8. added to the end of the program, overcomes this problem and also displays the number of the winning horse; hitting any key (except ABORT) then runs the game again. To run the modified game GO to 0B4B.

0B39	C4	0F	36	C4	00	32	C4	
0840	01	37	C4	3F	33	40	CA	0D
<b>0B48</b>	3F	80	08	C4	00	36	C4	0F
0B50	36	32	C4	00	CE	01	32	9C
0B58	F8	C4	0A	35	C4	AO	31	3D

Fig. 8. Addition to the Mk14 Horse-Race game displays the number of the winning horse

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1 TO 22

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1/250       10       3/3/100       22       2 CLIPS       5p         IMMED Sizes in mm       4/7100       27       TIL111       57p         POLYESTER AXL 10%       TANT BEAD 5mm 20%       Val       Val       Val       Val       Val       Val       Yal       Yal<	.033/250v 10 .047/100v 10 .068/100v 10	1.5/10 2.2/10 All at	0 22 0 22	.2 RE Red	СТ	17p
POLVESTER AXL 5p         TANT BEAD 5mm 20% Val         Val         <	.1/250v 10 In MFDS sizes in m	3.3/10 4.7/10	0v 22 0v 27	2 CL TIL11	IPS 1	57p
01/400, 33/100, 1       1       50       7p       4,7       25       15p         02/250, 68/400, 47       35       12p       22       35       31p         01/60, 10/250, 1.0/250, 1.0       35       12p       150       16       90p         DIODES by ITT. MULL SESCOSEM. TEXAS (CODED)         8AX13/16       3p       0A47       5p       IN400/12       21p         BAY71       9p       0A90/91       6p       IN400/12       21p         BAY71       9p       0A90/91       6p       IN400/12       21p         BY127       7p       0A20/21       1p       IN400/12       21p         BY10       3op IN814/6       2p       IN4148       1p       3p         S01 (8)       22p       555 (8)       19p       1458 (8)       27p         301 (8)       22p       555 (8)       19p       300 (14)       3p         311 (8)       42p       556 (8)       19p       1458 (8)       3p         324 (14)       45p       70 (40)       3p       3900 (14)       3p         324 (14)       45p       714 (8)       7p       74-7       N         324 (14)       45p       7	POLYESTER AX 10%	L TANT B	EAD 5mm V	20% Vat	v	
10.7.17.15.00         1.00         35         13p         100         6         4.44p           12/4000         1.5/1000         2.2         20         12p         150         16         90p           BAX13/16         3p         0.A47         5p         IN4001/2         21p           BAY17         3p         0.A47         5p         IN4001/2         21p           BY127         7p         0.A200/21         1p         IN4001/2         21p           BY126         3pp         PR108         3p         IN4418         1p           BY206         1p         IN5401/4         1p         State         State         State           BY10         30p         IN914/6         2p         State	.01/400v .33/1 .022/250v .47/2 .047/250v .68/4	00v .1 50v .22 .47	50 7p 35 9p 35 12p	4.7 10 22	25 25 35	16p 18p 31p
DIODES by ITT, MULL, SECOSEM, TEXAS (CODED)           BAX13/16         3p         OA47         5p         IN400/12         2p           BAX13/16         3p         OA90/31         6p         IN400/12         2p           BY127         7p         OA200/2         1p         IN400/12         2p           BY205         3p         RFG10B         3p         IN4148         1p           BY205         Sp         INS10/6         2p         INS408         13p           ICa DIL PINNING (-) LM ANO CA         747 (14)         37p         304614         5p           301 (8)         42p         555 (8)         13p         1460(8)         3pp           311 (8)         44p         71 (14)         37p         304614         5p         1460(8)         3pp           326 (14)         45p         70 (14)         32p         3914 (18) (23)         390 (16)         3pp           326 (14)         45p         725 (8)         212 (7)         44p         40         40         19p           14 35p         74 (8)         12p (7)         44p         40         19p         174           74 (8)         74p         14p         13p         13p	.1/160v 1.0/2 .22/400v 1.5/1	50v 1.0 00v 2.2	35 <b>13</b> p 20 <b>12</b> p	100 150	6 16	44p 90p
EV127         75         CA200/2         115         INACO7         25           EV206         139         PR6106         29         INAC07         25           EV206         139         PR6106         29         IN5401/4         119           EV210         309         IN914/6         29         IN5408         139           Ca DLE PINNING (-) LM ANO CA         747(114)         379         301 (8)         227           301 (8)         429         555 (8)         199         314 (8)         239         300 (8)         839           311 (3)         449         700 (14)         329         3900 (14)         439         348 (14)         559         741 (8)         159         3900 (14)         439           314 (14)         559         741 (8)         159         3900 (14)         439         3900 (14)         439           148 (14)         559         741 (8)         159         3900 (14)         439         3900 (14)         439           148 (14)         569         717         746         747         747         747         747         747         747         747         747         747         747         747         747	DIODES by ITT, N 8AX13/16 3p 8AX71 90	OA47	SEM, TEXA	S (CODI IN40	ED) 01/2	21p
BYX10         30p         IN210/6         Bp         IN240/4         11p           BYX10         30p         IN314/6         2p         INS400         13p           BYX10         30p         IN314/6         2p         INS400         13p           G08 (8)         42p         555 (8)         13p         146 (14)         5p           301 (8)         42p         555 (8)         13p         3046 (14)         5p           314 (14)         35p         709 (14)         21p         390 (14)         3p           318 (14)         55p         741 (8)         15p         13600 (16)         3p           162 LPS         CMOS BUFF         TL         TL         74N         00 to 40         00 to 40         00 to 40         15p         13600 (16)         3p           174 18p         05 40p         01 2p         18 48 41         15p         00 to 40         01 15p         15kp         00 5p         17p         14sp         15kp         13kp         132 (5p         17p         17p         14sp         134 45p         13kp         134 (5p         13kp         134 (5p<	BY127 7p BY206 19p	OA200/2 RPG10B	11p 3p	IN40 IN41	07 48	6p
Lab Dit PinNung, (=) Eth Anto LA         -147 [18]         37p           3001 [8]         42p         556 [14]         42p         3046 [14]         59p           3001 [8]         42p         556 [14]         42p         3046 [14]         59p           314 [14]         44p         557 [8]         610 [32]         3140 [14]         59p           314 [14]         44p         557 [8]         610 [32]         3140 [14]         52p           314 [14]         45p         705 [8]         610 [32]         3140 [14]         52p           314 [14]         45p         705 [8]         610 [32]         3140 [14]         707 [32]           314 [14]         45p         716 [8]         610 [32]         15p         13600 [16]         93p           314 [14]         45p         716 [12]         74 [48]         20         34p         136         910 [12]         74 [48]         700 [16]         74 [13]         136         747 [13]         136         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16]         747 [16] <td>BYX10 30p</td> <td>IN914/6</td> <td>2p</td> <td>IN54 IN54</td> <td>08</td> <td>13p</td>	BYX10 30p	IN914/6	2p	IN54 IN54	08	13p
311 [3]       41p       557 [8]       95p       3080 [8]       83p         324 [14]       44p       710 [14]       32p       3900 [14]       43p         338 [14]       44p       710 [14]       32p       3900 [14]       43p         338 [14]       44p       710 [14]       32p       3910 [14]       43p         338 [14]       44p       710 [14]       32p       3900 [14]       43p         338 [14]       45p       741 [8]       75p       741 [8]       74p         74 [56]       741 [8p       00p [63 a2p       23 4p4 [16]       74p       74p         74 [56]       95 10p       95 10p       94 4p       92 4p       94 p       70       74p         74 [56]       95 12p       94 4p       15p       95p       65 7p       74       38p         74 [16]       14p       132 50p       08 52p 22 54p       42 24p       66 34p       132 65p       717       716p 23 66 77p       714 48p 28 28 48p 93 38p       132 65p       717       74 6p 33 16p 367 48p 11 44p 22 42p 64 33p       1199 42p       338p       132 65p         71 [16]       97p 11 5 48p 28 28 48p 93 38p       1372 65 33p       1197 42p       338p       132 65p       132 65p	301 (8) 22p 308 (8) 42p	555 (8) 556 (14)	19p 42p	1458 3046	14) [8) (14)	37p 27p 59p
348 (114)       55p       726 (8)       £1.92       3914 (18) (22.35         380 (14)       55p       714 (8)       15p       1360 (016)       74p         744.5       74       74       74       74       74         70 140       740 (212)       740 (212)       740 (212)       740 (212)       740 (212)       740 (212)         744 70 (212)       740 (212)       740 (212)       740 (212)       740 (212)       741 (212)       740 (212)         745 745 (212)       740 (212)       740 (212)       740 (212)       740 (212)       741 (212)       742       742       742 <t< td=""><td>311 (8) 41p 324 (14) 35p 339 (14) 44p</td><td>567 (8) 709 (14) 710 (14)</td><td>95p 21p 32p</td><td>3080 3140 3900</td><td>(8) (8) (14)</td><td>83p 92p 43p</td></t<>	311 (8) 41p 324 (14) 35p 339 (14) 44p	567 (8) 709 (14) 710 (14)	95p 21p 32p	3080 3140 3900	(8) (8) (14)	83p 92p 43p
ICG LPS         CMOS BUFF         TTL           74LSN         400-N         74-S         74-S           00 10p         74 18p         00 10p         16 34p         29 48p         00 to 40           01 14p         13p         17 48p         00 10p         12p         17 48p         00 10p           01 14p         132 50p         06 82p         25 4p         92 8p         10 11p         13p           11 14p         12p         12 48p         00 12p         15p         10 12p         11 14p         13p           11 14p         13dp         13b 40p         11 16p         23 15p         05 52p         25 54p         13 9p         13p         15p         13p	348 (14) 56p 380 (14) 55p	725 (8) 741 (8)	£1.92 15p	3914 1360	(18) (16)	2.35 99p
02       14p       85       74p       01       12p       18       48p       152p       42       24p         05       16p       109       32p       06       48p       20       54p       42       52p       47       38p         06       16p       12p       16       44p       68       00       51       19p         10       14p       132       50p       08       52p       22       54p       43       28p       70       77p         13       16p       244       84p       152p       66       7p       71       74       64p       74k       85p       14       44p       22       74p       15       35p       13p       74p       35p       13p       74p       35p       13p       13p       35p       13p       13p       35p       13p       14p       35p       13p       13p       13p       12p       13p       13p       13p	ICs LPS 74LS N 00 10p 74 18	p 00 10p	40N 16 34p 2	9 48p	74- 00 to	N 40
08       16p       123       40p       10       14p       15p       10       14p       15p       10       14p       15p       10       14p       15p       10       15p       12p	02 14p 85 74 04 16p 90 40 05 16p 109 32	p 01 12p p 02 12p p 06 48p 2	17 48p 4 18 48p 4 20 54p 4	0 48p 1 52p 2 52p	42 47	19p 24p 38p
27       16p       240       940       12       20p       24       42p       66s       34p       132       65p       36p       134       95p       134p       25p       69p       16p       193       96p       134       48p       21p       24p       66s       34p       134p       25p       142p       34p       24p       144p       34p       25p       142p       34p       24p       144p       34p       25p       145p       34p       145p       34p       145p       34p       145p       34p       145p       34p       145p       34p       142p	08 16p 123 40 10 14p 132 50 14 36p 138 40	p 07 16p 2 p 08 52p 2 p 11 16p 2	21 54p 4 22 54p 4 23 16p 6	6 80p 9 28p 0 52p	51 70 86	19p 17p
51         186         374         976         15         486         28         487         93         386         197         425           SOCKETS GOLD T.I. 8 PIN 8p. 16 PIN 7p. 16 PIN 8p.           LOW PROF. SILV. STANFORD 24 PIN 17p. 40 PIN 27p.           RESISTORS E12 ‡W: C. FILM 5% 1p. M. FILM 2% 4p.           TRANSISTORS           AC127/8         25p         B0537/8         23p         TIS90/91         8p.           AC108/6         17p         TIS180/91         15p.           AC127/8         16p         B12/87         24p.         2N1711         19p.           BC137/8         12p         M13026/6         12p.         N2026/7         13p.           BC1213/8         5p.         M2206/7         13p.	27 16p 240 94 30 16p 244 86 33 16p 367 48	p 12 20p p 13 34p	24 42p 6 25 12p 6 27 38p 8	6 34p 9 16p	132 193 194	69p 96p 38p
SOCKETS GOLD T.I. 8 PIN 6p. 16 PIN 7p. 16 PIN 8p. LOW PROF. SILV. STANFORD 24 PIN 17p. 40 PIN 27p.           RESISTORS E12 ‡W: C. FILM 5% 1p. M. FILM 2% 4p.           TRANSISTORS           AC127/8 25p         BD537/8 33p           AC127/8 25p         BD537/8 33p           TISB079         BD75A 49p           TRANSISTORS         BD232/6/8 25p           AC127/8 25p         BD537/8 33p           TISB079         Bp           AD149 69p         BD575A 49p           DTK12 28p         BF2448 25p           DK196/8 12p         BF726/9           BC108/8 12p         BF726/9           BC109/1 12p         BF737/8 21p           BC109/1 2p         BFY50/51 119p           BC107/8 12p         BFY50/51 119p           BC107/8 12p         BFY50/51 12p           BC1182/8/1 6p         BU208/6 35p           BC182/8/1 6p         BU208/6 35p           BC182/8/1 6p         BU208           BC212/8 6p         MU208 16p           BC237/8 9p         MP5A6/21 12p           BC337/8 9p	51 18p 374 97	p 15 48p	28 <b>48</b> p 9	3 <b>3</b> 8p	197	42p
RESISTORS E12 ‡W: C. FILM 5% 1p. M. FILM 2% 4p.           TRANSISTORS         BD232/6/8 2pp         TIP3055         39p           AC127/8 25p         BD537/8 32p         TIS309/1 8p         500           AD161/2 28p         BP244B         25p         TIS306/8 19p         500           AU113 £1.69         BF268/9 24p         ZN918/30         500         500           BC107/6 10p         BF337/8 21p         ZN16113         25p         BK506/6 19p           BC107/6 10p         BF337/8 21p         ZN16113         25p         ZN2646/8 19p           BC103/6 12p         BFX26/88 17p         ZN1711         19p         DS205/7 19p         ZN2222A 12p           BC112/6 2p         BFX26/88 12p         ZN2646         49p         ZN2646         49p           BC113/6/L 6p         BU205/6 9p         ZN2646         49p         ZN3053 22p         ZN2646         49p           BC13/78 6p         MU205/1 61.09         ZN3702/4 7p         ZN3065 33p         BC237/8 3p         MPSA04/14 4p         ZN3771/2 8p         Bap           BC237/8 3p         MPSA64/23 12p         ZN3064 3p         ZN3064 3p         BC237/8 3p         BPSA6/14 4p         ZN3771/2 8p         Bap         ZN306/10 3p         SN66/10 3p         SN66/10 3p         SN	SOCKETS GOLD	V. STANFORD	16 PIN 7 24 PIN 17	. 16 PIN p. 40 Pi	18p. IN 27p	
TRANSISTORS         BD232/6/8         29p         TIP3055         39p           AC127/8         25p         BD537/8         33p         TIS90/91         8p           AD149         69p         BD57/8         33p         TIS90/91         8p           AD149         69p         BD75/8         49p         ZTX212A         6p           AD161/2         28p         BF2448         25p         ZN696/8         19p           AU113         E1.69         BF256/9         21p         ZN1613         28p           BC108/18         12.p         BF2048         25p         ZN218/19         15p           BC108/18         12.p         BF750/51         13p         ZN218/19         15p           BC107/18         12.p         BFY30/51         12p         ZN218/19         15p           BC177/19         12p         BSV67/68         23p         ZN23894         16p           BC177/19         12p         BV206/64         35p         ZN3055         35p           BC177/18         12p         MU201         1109         ZN302/4         7p           BC217/8         5p         MPSA42/3         12p         ZN306/10         3p	RESISTORS E1	2 ₩: C. FILM	1 5% 1 p. M.	FILM 2	%4p.	
BD232/6/8         29p         TIP3055         39p           AC127/8         25p         BD57/8         33p         TIS3091         Bp           AD161/2         28p         BD675A         49p         TIX212A         6p           AD161/2         28p         BF2488         25p         2N656/8         19p           AU113         £1.69         BF258/9         24p         2N918/30         15p           BC107/8         10p         BF337/8         21p         2N16113         25p           BC109         10p         BFY50/51         11p         2N2222A         12p           BC141/2/3         12p         BV126/6         2p         2N2363         12p           BC141/2/3         12p         BU126         £1.02         2N2646         49p           BC13/8         6p         BU205/6         5p         2N3053         22p           BC13/8         6p         BU205/6         5p         2N3053         32p           BC213/8         6p         MU2501         £1.09         2N342/2         63p           BC237/8         9p         ME340         30p         2N3702/4         7p           BC337/8         9p	TRANSISTOR	5	- 20			
AD16 1/2         256         DE744B         256         214696/8         157           AU113         E1.659         BE744B         256         214696/8         159           BC108/6         109         BF337/8         219         21416/3         256           BC108/6         129         BFY50/51         139         2111         159           BC108/16         129         BFY50/51         139         212218/19         159           BC107/6         129         BFY50/51         139         212218/19         159           BC177/8/9         129         BSW67/68         29         212369A         159           BC177/8/9         128         B1206/6         559         213055         339           BC183/8/1.         69         BU208         89         213055         359           BC137/8         59         BU208         89         213055         359           BC214/8/18         59         BU208         309         2133054         259           BC237/8         59         MU2501         E1.09         213442         39           BC337/8         59         MP5A42/3         129         245474         39	AC127/8 25	BD232/6 BD537/8 BD675A	/8 29p 33p	TIP305 TIS90/ ZTX21	91 24	39p 8p
Bc101//6         100//6         100         Bc33/3/8         210         211111         100           Bc103/2         120         Bc733/3/8         210         211111         100           Bc103/2         120         Bc753/3/8         240         211211         100           Bc103/2         120         Bc753/3/8         240         21222A         120           Bc113/2         120         Bc753/3/8         240         21222A         120           Bc113/3         120         Bc753/3/8         240         21222A         120           Bc113/3/4         140         Bc713/3         120         850         21266/4         490           Bc113/3/4         Bp         Bl206/8         890         213055         320         820           Bc213/8         Bp         MJ2501         11.09         213024/4         79         80           Bc237/8         Sp         MJ2501         11.09         213732         813         89           Bc237/8         Sp         MJ5201         12.09         213737         81.39         80           Bc337/8         Sp         MJ5300         12.0         213428         329           Bc337/8	AD161/2 28 AU113 £1.6	BF244B BF258/9	25p 24p	2N696 2N918	/8	19p 15p
BC 103C.3 12p         BK1337,88 23p         Ext2456.4 12p           BC 14172,3 12p         BK1567,88 23p         Ext2456.4 12p           BC 18176/L 5p         BU126 £1.02         Ext2456.4 12p           BC 18276, 5p         BU268 £1.02         Ext2456.4 12p           BC 18376/L 5p         BU268 £1.02         Ext2456.4 12p           BC 18376/L 5p         BU268 £1.02         Ext2456.4 12p           BC 18476/L 5p         BU268 £1.02         Ext2456.4 12p           BC 18476/L 5p         BU268 £1.02         Ext2456.4 12p           BC 21476 5p         BU250 £1.03         Ext2457.4 12p           BC 2386/98 5p         MUE520 42p         Ext3771712 85p           BC 23778 5p         MPSA67/14 14p.         Ext3778 13p           BC 247778 13p         MPSA56/92 21p         Ext387.2 2p           BC 24778 5p         C28/71 8p         Ext2477           Stock 48 5p         TIF23/30 17p         Ext447 5p           BC 25478 5p         TIF214/32A 37p         Ext530 22p           BC 25478 5p         TIF214/32A 37p         Ext530 23p           BC 25478 5p         TIF214/32A 37p         Ext530 23p           BC 25478 5p         TIF214/32A 37p         Ext550 12p           BC 25478 5p         TIF14/32A 37p         Ext55	BC108/B 12 BC109 10	BFX29/8 BFY50/5	8 17p 1 19p	2N171 2N221	1 8/19	19p 15p
BC 182/8/L         6.p         BU208         8.pc         24/305/1         1.pc           BC 183/8/L         6.p         BU208         8.pc         24/3053         22p           BC 184/8/L         6.p         BU208         8.pc         24/3053         22p           BC 183/8/L         6.p         BU208         8.pc         24/3053         23p           BC 21/8         6.p         BU208         8.pc         24/3053         23p           BC 21/8         6.p         MU2501         6.10         3p         24/376         3p           BC 237/8         3p         MPSA06/14         4.p         24/373         61.38         3p           BC 237/8         3p         MPSA62/12         24/43/88         3p         EC 337/8         3p           BC 547/8         3p         MPSA62/31         2p         24/348         3p           BC 5478         3p         C28/71         8p         24/557         3p           BC 5478         3p         T1721/322         2p         24/547/7         3p           BC 5478         3p         T162/116         63p         T167/9         3p           BC 5478         3p         T162/37	BC109C 12 BC141/2/3 19 BC177/8/9 12	BSW67/0 BU126	68 29p £1.02	2N236 2N264	9A	16p 49p
BC2112/B         6p         BUY87         63p         2N3055         33p           BC213/B         6p         MU2501         £1.09         2N3424         65p           BC213/B         6p         MU3201         £1.09         2N3702/4         7p           BC237/B         9p         MU340         30p         2N3702/4         7p           BC237/B         9p         MPSA06/14         Ap         2N3771/2         Bap           BC327/B         9p         MPSA06/14         Ap         2N3773         £1.38           BC337/B         9p         MPSA06/14         Ap         2N3773         £1.38           BC477/B         19p         MPSA66/12         1p         2N43368         3p           BC547/B         3p         DC28/71         Bsp         2N5307         3p           BC557/B/3         9         T121/12A2A         2p         2N5507         3p <t< td=""><td>BC182/B/L 6 BC183/B/L 6 BC184/B/LB 9</td><td>P BU205/6 P BU208 P BU326A</td><td>95p 89p £1.02</td><td>2N290 2N305 2N305</td><td>3</td><td>19p 22p 29p</td></t<>	BC182/B/L 6 BC183/B/L 6 BC184/B/LB 9	P BU205/6 P BU208 P BU326A	95p 89p £1.02	2N290 2N305 2N305	3	19p 22p 29p
BC2387/B         9p         MJE340         30p         2N3706/10         9p           BC2388/98         9p         MJE520         42p         2N3771/12         8pp           BC327/8         9p         MPSA06/14         14p.         2N3773         £1.39           BC327/8         9p         MPSA02/12         2N3703         £1.39           BC37/8         9p         MPSA42/3         2P         2N3706/10         9p           BC477/8         9p         MPSA56/92         21p         2N4358         39p           BC5478         9p         C28/71         8p         2N5320         29p           BC5488         9p         TIP29/30         17p         2N5437         9p           BC559/8/9         9p         TIP314/32A         37p         2N5550         12p           BC770/71         14p         TIP47         29p         2N6028         12p           BD135/6         22p         TIP121/216         2N6356         2N6356         2N4356           BD135/6         22p         TIP121/216         2N6107/9         37p         2N404/8         18p           COK of all items         SCRS         ZN6107/9         37p         2N444 <t< td=""><td>BC212/B 6 BC213/B 6 BC214/B/LB 9</td><td>p BUY87 p MJ2501 p MJ3001</td><td>69p £1.09 £1.09</td><td>2N305 2N344 2N370</td><td>2</td><td>39p 69p 7p</td></t<>	BC212/B 6 BC213/B 6 BC214/B/LB 9	p BUY87 p MJ2501 p MJ3001	69p £1.09 £1.09	2N305 2N344 2N370	2	39p 69p 7p
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#### NICKEL CADMIUM BATTERIES AA (HP7) SUB 'C' 'C' (HP11) 'C' (HP11) 'D' (HP2) PP3

	U.BAHr	1.ZAHF	1.00AHr	2.UAM	4.UAMP	U.TAHP
1-24 25-49 50-99 100 up	£0.85 £0.75 £0.65 £0.59	£1.38 £1.28 £1.24 £1.15	£1.69 £1.58 £1.52 £1.41	£2,25 £2,10 £2.02 £1.87	£2.97 £2.77 £2.67 £2.47	£3.79

All cells are brand new full spec devices from reputable mnfrs. All Nickel Cadmlum cells (except PP3) are supplied complete with solder tags and are 'VENTED' devices suitable for fast charge. CHARGERS – single or dual O/P to charge PP3. AA or SUB 'C' cells in 12-14 hrs (chargers will charge 'C' and 'D' cells but with longer charging time). Units supplied complete in plug top case with flying leads. Number of cells (10 max) in series and type <u>must</u> be specified for each re-quired O/P when ordering.

SINGLE O/P CHARGER £5.04 DUAL O/P CHARGER £5.72

TRANSFORMERS – as used in chargers, 2 x 12 volt 0.25 amp secondarys 240v primary, tag connections £1.57 each.

Cheques, P.O.'s Mail Order to:-Data and charging circuits free with orders over £10 otherwise 30p post. P&P 10% if order less than £10, 5% if order over £10. Prices DO NOT INCLUDE VAT and this should be added to the total order. Please allow 28 days for delivery. SOLID STATE SECURITY, Dept. (PE), Bradshaw Lane, Parbold, Wigan, Lancs Telephone 02576-3018.

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T SPECIAL C	DFFER	*		
	1-24	25-99	100+	
2114 Low Power 200nS	1.00	0.90	0.85	
4116-200nS	0.75	0.70	0.65	
2716 (+5V)	2.25	2.10	2.0 <b>0</b>	
2732 (+5V)	4.50	4.25	4.00	
6116	9.00	8.00	7.50	

C	ONNECTOR	SYSTE	M	
COCE CONNECTOR	EUDO CONMECTOR	s I.O.	CONNECT	ORS
EDGE CUNNECTUR	EUNU CUMMECTUM	HEAD	ER RECEP. F	DEE CABLES
0.1" 0.156"	DIM SID PLUG SKI	DI 10	C TACLE CI	INN
2×18 Way — 150p	4161731 Way 200p 200	P 10 W/m 220	ALLE CL	650
2×22 Way 310p '170p	41612 2x32 Way 300p 350	P 10 Way 220	p 100p -	160-
2 x 23 Way 335p	2x32 Way (angled) 400	p 26 Way 350	p 265p 33	iup toup
2×25 Way 350p 200p	ttor 2 x 32 Way specify a+b o	34 Way 450	p 325p 4	50p 240p
1 x 43 Way 260p -	3+0	40 Way 525	p 370p -	— 280p
2×43 Way 450n -		50 Way 575	p 450p -	— 330p
2 x 50 Way 400n *	DIL HEADER PLUG	S	LINADED I	CADO
1 v 77 Way 700m	Solder IDC	J	UWPENLI	LAUS
TATT Hay Toop	type type	24	NIDCON LADIE W	ith headers
	14 oin 50n 130n		14 nin 16 nin	24 nin 40 nin
BOOKS	16 pin 60p 140p	Single and	145. 155.	2400 2800
DOONO	24 sig 100s 200s	Double and	210- 220-	2400 5000
(no VAT)	24 pm 100p 200p	WOLLINE EITO	Ziup Ziup	avab avoh
	40 pm 2/30 2030		Lable with Soc	Rets
The Brood March and Co on other			ZUpin 26 pin	34 pm 40 pm
The Pascal Mandbook LU.UU, LI	KI Lantroller Manabook 15.95	Single end (18")	160p 210p	270p 300p
Understanding Microprocessors	£3.50, Understanding Solid	Clouble end (18")	290p 385p	490p 270p
State Electronics £3.50. MICH	10S — Intertacing Techniques			
£12.10, Introduction to Microco	mputers £10.95, TTL Cookbook	OF MAN AN	NI D CON	NECTORE
£7.15, CMOS Cookbook £7.95.	, Video Cookbook £5.15, Z-80	ZO WAT WI	NI D-CON	NECIUNS
Interfacing (1) £7.95, Z-80 Inter	facing (2) £9.75, Programming		Solder Sol	ider Salder
the Z80 £11.50, Programmin	a the 6502 £10.25, 6502		Bucket P	in Pin
Applications Book £10.25 65	02 Assy, Lan, Prog £12.10.			(angled)
8080A/8085 Assy, Lang, Prog. 6	11.60	Male	200p 21	50 2650
		Female	245n 25	50 3100
PLEASE ADD P&P	£1.00 PER BOOK -	Ton Entry/Side Entry	v Hood 100n Lou	king Lever 100n
		sob curly and curly	t upon toob. rot	and reach toop.

#### ADD SOUND, RELAY CONTROL, LIGHT DETECTION ★ ZX80/81. USER PORT ★ (As described in Oct/Nov PCW)

Port module plugs directly into ZX80/81 to provide 8 input and 8 output lines. These allow input of data from switches, photocells, sensors, joysticks etc and control of 8 relays. Also 7 segment displays and LED may be used — "VARIABLE TONE AUDIO OUTPUT CAN PRODUCE YOUR OWN SOUND EFFECTS." Port access is by simple PEEK & POKE COMMANDS.

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accessible from BASIC or MACHINE CODE. 1) DECODING MODULE: Providing a dual 5v supply, 16 bit programmable i/o port, plus ex-tensive address decoding for a wide variety of interfaces, including full decoding for a pro-grammable sound generator, and also a 40 pin skt for further expansion. 21 ANALOGUE BOARD: Plugs into the decoding module to provide D/A converter, 8 channel mul-tiplexed A/D converter with 20NS conversion time. AY3-8910 SOUND GENERATOR plus 6522 VIA provide complex timing & counting functions and additional 16 bit port. DECODING MODULE KIT C27.50 ANALOGUE BOARD £39.95 P&P 0.75p/Kit

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TTLs           7400         11p           7402         12p           7402         12p           7402         12p           7404         12p           7405         18p           7406         25p           7407         25p           7408         14p           7409         15p           7411         20p           7412         20p           7413         25p           7414         35p           7416         25p           7422         22p           7423         30p           7422         32p           7433         27p           7432         30p           7433         30p           7433         30p           7434         60p           7435         17p           7436         17p           7437         30p           7444         70p           7450         17p           7453         17p           7454         17p           7455         17p           7456         17p <t< td=""><td>74393         100p           74490         120p           744500         120p           74LSSERIES         140p       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74S133         350p           74S183         350p           74S183         350p           74S241         300p           74S257</td><td>4040         60p           4041         70p           4042         55p           4043         60p           4044         70p           4045         512p           4046         80p           4047         75p           4048         55p           4055         60p           4055         130p           4055         130p           4055         130p           4055         130p           4055         130p           4055         500p           4056         120p           4056         120p           4057         4000           4056         720p           4070         20p           4071         20p           4072         20p           4073         20p           4074         150p           4075         30p           4076         60p           4077         4008           209         4007           4073         20p           4074         150p           4075         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745371         650p           745373         950p           745373         950p           745373         950p           745373         350p           745373         950p     <td>INTERFACE ICs           AD558CJ         775p           AD556CJ         715p           AD556CJ         514           AM265IS32         190p           DA8131         375p           D58602         250p           D58803         250p           D68803         250p           D78304         400p           D78304         250p           D78304         250p           MC1488         60p           MC1488         60p           MC3486         500p           MC44412         £9           MM58174         £12           ULN2003A         100p           75110         160p           75111         160p           75112         160p           75113         160p           75114         160p           75153         150p           754517/2         <td< td=""><td>NTS           32.768KHz         100p           100KHz         320p           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  100KHz         320p           100KHz         350p           200KHz         250p           200KHz         250p           200KHz         250p           200KHz         250p           200MHz         150p           35795MHz         150p           4.194MHz         150p           4.194MHz         250p           6.0MHz         175p           6.0MHz         150p           7.168MHz         200p           8.00MHz         175p           8.00MHz         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           7.168MHz         300p           26.600MHz         300p           27.145MHz         30p           26.60MHz         300p           26.60MHz         300p           26.60MHz         300p           <td< td=""></td<></td></td<></td>	INTERFACE ICs           AD558CJ         775p           AD556CJ         715p           AD556CJ         514           AM265IS32         190p           DA8131         375p           D58602         250p           D58803         250p           D68803         250p           D78304         400p           D78304         250p           D78304         250p           MC1488         60p           MC1488         60p           MC3486         500p           MC44412         £9           MM58174         £12           ULN2003A         100p           75110         160p           75111         160p           75112         160p           75113         160p           75114         160p           75153         150p           754517/2 <td< td=""><td>NTS           32.768KHz         100p           100KHz         320p           200KHz         320p           100KHz         320p           100KHz         350p           200KHz         250p           200KHz         250p           200KHz         250p           200KHz         250p           200MHz         150p           35795MHz         150p           4.194MHz         150p           4.194MHz         250p           6.0MHz         175p           6.0MHz         150p           7.168MHz         200p           8.00MHz         175p           8.00MHz         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           7.168MHz         300p           26.600MHz         300p           27.145MHz         30p           26.60MHz         300p           26.60MHz         300p           26.60MHz         300p           <td< td=""></td<></td></td<>	NTS           32.768KHz         100p           100KHz         320p           200KHz         320p           100KHz         320p           100KHz         350p           200KHz         250p           200KHz         250p           200KHz         250p           200KHz         250p           200MHz         150p           35795MHz         150p           4.194MHz         150p           4.194MHz         250p           6.0MHz         175p           6.0MHz         150p           7.168MHz         200p           8.00MHz         175p           8.00MHz         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           10.7MHz         250p           13.0MHz         350p           14.3168M         150p           7.168MHz         300p           26.600MHz         300p           27.145MHz         30p           26.60MHz         300p           26.60MHz         300p           26.60MHz         300p <td< td=""></td<>
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WUS C

# What CB? Guide to Legal Rigs

This guide covers all the currently available legal rigs we could find. Please note that the prices quoted are only intended as a guide and will vary depending upon the supplier.



The DNT range of rigs which are marketed by Radiotechnic Ltd., Bel Royal, St Lawrence, Jersey C.I. include the B40 FM (top left), the F40 FM (top right), the HF13/40 (bottom left) and the HF12/3 (bottom right)





The Cybernet Beta 1000, 2000, 3000. Goodmans Loudspeakers Ltd., Downley Road, Havant, Hants



The Radiomobile 201 and 202. Radiomobile Ltd., Goodwood Works, North Circular Road, London NW2 7JS



The M2 mobile and the Diplomat 40 base station. John Woolfe Racing, Electronics Division, Woolfe House, Norse Road, Bedford

## What CB? Guide to Legal Rigs



The CB900 and CB901. Amstrad Consumer Electronics Ltd., 1–7 Garman Road, Tottenham, London



The JCB 863 from York which is marketed by Sulkin (UK) Ltd., 73 Grosvenor Street, London W1X 9DD



The Midland 2001 (top), the 3001 (left), and the 4001. Midland Telecom, 133 Flaxley Road, Stechford, Birmingham B33.9HQ



The Reftec 934 is a 20 channel for 934 MHz. RF Technology Ltd., Leyton Avenue Industrial Estate, Mildenhall, Suffolk



The Realistic TRC-2000



The Realistic TRC-1001



The Realistic TRC-2001. The Realistic range of rigs is available from all Tandy stores

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	LONG RANGER 12	69 · 95	27	12	•		•					•				•	•	
	SPEEDWAY	79 95	27	40	•											•	•	
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TRANSCOM	GBX 2000	69 95	27	40	•						-					•	•	
	GBX 4000	89 95	27	40	•					•	•	•	•	•	•	•	•	
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## What CB? Guide to Legal Rigs



The Grandstand base station. Beeware Ltd., Ripon Way, Harrogate, North Yorkshire



The Harrier CBX from Dixons, Dept. DS33, Camera House, Cartwright Road, Stevenage





The Binatone Phone Breaker (top) and the 5-star. Binatone House, Beresford Avenue, Wembley, Middlesex



The Voxson Tenvox. Voxson Audio Ltd., Nuffield Way, Abingdon, Oxfordshire



The CB1000. Fidelity Radio, Victoria Road, London



The Grandstand Buzzing Bee





The Transcom GBX 2000 (top) and the GBX 4000. Transcom International, 1–12 Market Street, Bracknell, Berks