

CHOOSE ATOM POWER

At work or play-everything you need in a personal computer

The Atom is a machine to be used. Every day, day after day. It's a full function machine-check the specification against others. It's rugged, easy to operate built to last and features a full-size typewriter kevboard.

Just look at some of the features!

- More hardware support than any other microcomputer Superfast BASIC - can be updated to BBC BASIC if required
- High resolution and comprehensive graphics ideal for games programmers and players* Integral printer connection*
- Software available for games, education, maths, graphs, business, word processing, etc.
- Other languages: Pascal FORTH LISP
- I/O port for control of external devices
- Built-in loudspeaker Cassette interface
- Full service/repair facility Users club
- * Expanded version only

Optional Extras

- Network facility with Econet
- Disk PAL UHF colour encoder
- Add-on cards include 32K memory. analogue to digital, viewdata VDU, disk controller, daisywheel printer, plus many, many more! Power supply

FREE MANUAL

The Atom's highly acclaimed manual comes free with every Atom and leaves nothing out. In just a while you'll be completely at ease with your new machine! Within hours you'll be writing your own programs



Computer stores are stocking Atoms — there's a list below, but if you have any problems getting hold of one just fill in the coupon and we'll rush one to you within 28 days. If the machine isn't all you expected, or all we've told you, just return it within 14 days for a full refund.

or all we've told you, just return it withln 14 days for a full refund.

MOM, Aberdeen 22863. Broadway Elect, Bedford 213639. Owl Computers, Bishops Stortford, 52682. Eltec Services, Bradford 491371. Gamer, Brighton 698424. Electronic Information Systems, Bristol 774564. Cambridge Comp Store, Cambridge 65334. Cardiff Micros, Cardiff 373072. Bellard Elect. Chester 380123. Emprise, Colchester 865926. Silicon Centre, Edinburgh 332 5277. Esco Computing, Glasgow 204 1811. Control Universal, Harlow 31604. Unitron Elect, Haslington. Castle Elect, Hastings 437875. Currys Micro Systems, High Wycombe. Customised Electronics, Leeds 792332. D.A. Computers Leicester 708402. Microdigital, Liverpool 236 0707. Barrie Elect, London 488 3316. Eurocalc London 729 4555. Microage London 959 7119. Sinclair Equip. Int (Export) London 235 9649. Off Records, SW12, 674 1205. Technomatic, NW10 452 1500. NSC Comp Shops, Manchester 832 2269. Customised Electronics, Middlesborough 247727. Compshop, New Barnet 441 2922. H.C.C.S., Newcastle. 821924. Newcastle Comp Services, Newcastle 615325. Anglia Comp Centre, Norwich 29652. Leasalink Viewdata, Nottingham 396976. R.D.S. Electrical Portsmouth 812478. Computers for All, Romford 751906. Intelligent Artefacts, Royston, Arrington 689. Computer Facilities Scunthorpe. Datron Micro Centre Sheffield 885490. Superior Systems, Sheffield 77824. Q-TEC Systems, Stevenage 65385. 3D Computers, Surbiton (01) 337 4317. Abacus Micro Comp, Tonbridge, Paddock Wood 3861. Northern Comp, Warrington 601683. Compass Design Wigan Standish 426252.

YOU AND YOUR CHILDREN

More and more schools are buying Atoms. More and more children will learn on an Atom. You can give them that extra familiarity with an Atom in the home.



When you order your Atom we will include full details of all-software packs and the optional hardware.

To: Acom Computer Limited, 4A Market Hill, Cambridge CB23NJ.

I enclose a cheque/postal order for £.

Please debit my Access/

Barclaycard No.....

Signature

Name (please print)....

Address . . .

Telephone Number

Registered No. 1403810 VAT No. 215 400 220

Quantity	Item	Item price inc. VAT + P&P	Totals
	Atom Kit 8K ROM+ 2K RAM	@£140 00	
	Atom Assembled 8K ROM+2K RAM	@£174 50	
	Atom Assembled 12K ROM+12K RAM	@ £289 50	
	Power Supply	@£ 1020	
		TOTAL	

PRACTICAL

ELECTRONICS

VOLUME 17

No. 12

DECEMBER 1981

CONSTRUCTIONAL PROJECTS SPACE EVADERS by Tom Gaskell B.A. . 22 Galactic battle between aliens and humans PE CAR COMPUTER Part 1 by P. MacFarlane 30 Modes of operation, boards and construction PE ROBOTS Part 2 by Richard Becker and Tim Orr ... 42 The interface board PE BANDBOX Part 2 by Alan Boothman B:Sc 52 . . . Principles and construction of the peripheral system PERANGER Part 4 by Michael Tooley B.A. and David Whitfield M.A., M.Sc 60 Alignment and fault finding **GENERAL FEATURES** DIGITAL DESIGN TECHNIQUES Part 5 by Tom Gaskell B.A. 38 Advanced sequential circuits SEMICONDUCTOR UPDATE by R. W. Coles 48 LM1894 ICL 7663 Z800 MICROPROMPT ... 66 Hardware and software exchange point for PE computer projects **NEWS AND COMMENT** EDITORIAL 17 NEWS & MARKET PLACE 18 Including Countdown and Points Arising INDUSTRY NOTEBOOK by Nexus ... 21 What's going on behind the scenes SPECIAL OFFER-HI-FI 29 A passport to listening pleasure SPACEWATCH by Frank W. Hyde ... 51 News of the second shuttle mission SPECIAL OFFER—DIGITAL MULTIMETER 59 A six function meter kit with I.c.d. for under £20!

OUR JANUARY ISSUE WILL BE ON SALE FRIDAY, 11 DECEMBER 1981

68

(for details of contents see page 37)

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INDEX FOR VOLUME 17 ...

CARDIFF ROAD, WATFORD, HERTS, ENGLAND Tel. Watford (0923) 40588. Telex: 8956095

POLYESTER RADIAL LEAD CAPACITORS: 250V; 10n, 15n, 22n, 27n 8p; 33n, 47n, 68n, 100n 7p; 150n, 220n 10p; 330n, 470n 13p; 680n 19p; 1µ 23p; 1µ5 40p; 2µ2 46p. FEED THROUGH CAPACITORS 1000pF 350V 8p

S301. 470n 139; 680n 139; 19 349; 19 340; 29 459. 1000pr 350V 8p ELECTROLYTIC CAPACITORS (Values in µF). 500V: 10 52p; 47 78p; 63V: 0. 47. 10. 1-5. 2 2, 3 3, 8p; 4-7 9p; 6-8, 10 10p; 15. 22 12p; 33 15p; 47 12p; 100 19p; 1000 70p; 50V: 47 12p; 68 20p; 220 24p; 470 32p; 220 90p; 40V: 4-7, 15, 22, 9p; 330 90p; 4700 120p; 25V: 1-5, 6-8, 10, 22 8p; 33 9p; 47 8p; 100 11p; 150 12p; 220 15p; 330 22p; 470 25p; 680, 1000 34p; 2200 50p; 3300 74p; 4700 92p; 18V: 40, 47, 100 9p; 125 12p; 220 13p; 470 20p; 680 34p; 1000 27p; 1500 31p; 2500 36p; 3300 36p; 3300 36p; 3300 36p; 3300 35p; 200 130p; 4700 92p; 125 120 13p; 4700 9245p; 64V: 3300 198p; 2200 130p; 50V: 3300 154p; 2200 110p; 40V: 4700 160p; 25V: 10,000 320p; 15,000 345p.

TANTALUM BEAD CAPACITORS: 35V: 0.1, 0.22, 0.33 15p; 0.47, 0.68, 1.0, 1.5 16p; 2.2, 3.3 18p; 4.7, 6.8, 22p; 10.28p; 18V: 2.2, 3.3, 16p; 4.7, 6.8, 10.18p; 15.36p; 22.30p; 33, 47, 40p; 100.75p; 220.88p; 10V: 15, 22.26p; 33, 47.35p; 100.55p.

MYLAR FILM CAPACITORS: 100V: 1nF, 2n, 4n, 4n7, 10 6p; 15nF, 22n, 30n, 40, 47 7p; 56, 100n, 200 9p; 470n/50V: 12p.

CERAMIC CAPACITORS: (50V)
Range: 0-5pf to 10nF
15nF, 22nF, 33nF, 47nF
100nF/30V 7p; 220nF/6V

POLYSTYRENE CAPACITORS: 10pF to 1nF 8p 1.5nF to 12nF 10p.

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0.1W 50Ω-2-2M Mini Vert. & Horiz.
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Precision Cermet 1W 100Ω-100K

SIEMENS multilayer miniature capacitors. 250V: 1nF, 1n5, 2n2, 3n3, 4n7, 6n8, 8n2, 10n, 12n, 15n, 22n, 7p, 18n, 27n, 33n, 47n, 8p, 3p, 56n, 68n, 9p, 100V: 100n, 120n, 10p; 150n, 11p; 220n, 13p; 330n, 18p; 470n, 23p; 680n, 30p; 1µF, 34p; 2µZ 50p. SILVER MICA (pF)
2, 3:3, 4:7, 6:8, 8:2, 10,
12: 18, 2:2, 27, 33,
47, 50, 56, 68, 75, 82,
85, 100, 120, 150, 180,
220, 250, 270, 330,
360, 390, 470, 600,
800, 8:20pF, 21p each
1000, 1200, 1800
30p each 30p each 3300, 4700 60p each LINEAR IC's 555 CMOS 80 702 75 709C 8 pin 35 710 48 741 1 74 747C 14 pin 70 748C 8 pin 185 810 159 94400CJ 350 AY-1-0212 675 AY-1-1313A 660

790 MK50398 635 T
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MM53087A 475 T
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80 M543 T
80 M544 9400CJ 350 AV-1-0212 675 AV-1-1313A 660 AV-1-1392 225 AV-1-5050 94 AV-1-5050 160 AV-1-6720 210 AV-3-1270 840 AV-3-8500 390 AV-3-8910 620 AV-3-8910 620 AV-3-8910 620 AV-3-1230 AV-5-1230 A .M348 .M349 .M358 .M377 .LM725CN .LM733 .LM380 .LM381 N .LM381 N .LM384 .LM384 .LM386 .LM386 .LM387

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TAA661 A 185
TAA700 250
TAA900 00
TAA900 00
TAA960 00
TBA120S 70
TBA5500 370
TBA5500 370
TBA641-A12/
BX1 or 8X11275
TBA651 190
TBA800 80
TBA820 70
TBA820 70
TBA820 260
TBA950 300
TCA220 350 CA3160 CA3161E CA3162E CA3189 MC1494 MC1495 MC1496L MC1596 MC1648 70 225 290 90 79 150 120 750 975 MC1709G MC1710 L7107 L8038CC L8211 M7204 M7205 M7207 M7215 300 450 550 150 T8A950 120 TCA220 120 TCA940 52 TCA965 110 TDA1004 150 TDA1008 97 TDA1022

TRANSISTORS BFX29/84
BFX85/86
BFX87/8B
BFY50/51
BFY52
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BFY64
BFY81
BFY90
BRY39
BSX29
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8C184 BC182L BC183L BC184L BC187 8C212/3 BC212L BC213L BC214 BC214L BC237/8

27, 74170
177, 74170
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186, 74173
38, 74173
90, 74176
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168 LS40
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96 LS53
140 LS75
99 LS76
290 LS83
150 LS112
150 LS114
150 LS115



105 105 790 790 725 695 800 480 280 770

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SPEAKERS 80. 0.3W. 2"3" 800 0.3W. 2 .5" 800 0.	80p BRIDGE RECTIFIERS (plastic case) 1A/50V 20 1A/100V 22 1A/400V 29 1A/50V 34 2A/50V 35 2A/200V 40 2A/400V 49 5A/600V 125 6A/600V 34 2X/500V 34 2X/500V 34 2X/500V 35 6A/600V 125 6A/600V 125 6A/600V 125 6A/600V 35 6A/600V 3	LIQUID CRYSTAL DISPLAYS 3\(\frac{1}{2}\) digit 750 6 digit 850 NEONS Red or Amber Round or Square 30p each OPTO SWITCH TILI 39 170 6542\(\frac{1}{2}\) x2 x1 105 5x4 x2 1 120 6x4 x2 1 120	OPTO ELECTR LEDS including of TIL209 Red 125 TIL211 Green 1: TIL212 Yellow TIL220 2.º Red 0.2º Yel, Gm, A Red, Green and Rectangular LEDs 0.2º Risolour Red/Green Green/Yellow 0.2º Tri colour Red/Green Yellow 0.2º Tri Colour Triangle T	Cips 3, 25° 18 18 18 18 15 18 18 15 15 15 15 15 11 15
SCR's Thyristors 0 8A-100V 32 1A/200V 58	881058 40 BB106 40 Noise Diode Z5J 195	31x5" 95p	79p PROTO-Di 211p Veroblock S-Dec 50p Eurobreadb 118p Bimboard 1 162p Superstrip S	375 350 oard 520 695
1A/400V 70 5A/300V 38 5A/400V 40 5A/600V 48 8A/300V 60	TRIACS 3A/100V 48 3A/400V 56	Spare Wire (Spo	PEN and Spool	310p nbs 6p ea.
8A/300V 60 3A/800V 85 12A/100V 78 8A/400V 69 12A/300V 95 8A/400V 69 12A/800V 188 8A/800V 115 8T106 150 12A/100V 78 8T106 150 12A/400V 82	8A/100V 60 8A/400V 69 8A/800V 115 12A/100V 78 12A/400V 82 12A/800V 135	Pen + FERI	Spare tip 90p RIC CHLORIDI ag Anhydrous + 50p p&p	
TIC44 24 TIC45 29 TIC47 35 2N5062 32 2N5064 38 2N4444 130	16A/100V103 16A/400V105 16A/800V220 25A/400V185 25A/800V295 25A/1000V 480 30A/400V525	COPPER CLAI Fibre Singl Glass sided 6"x6" 90p 6"x12" 150p	BOARDS le- Oouble- l sided 9	SRBP -5"x8-5" 95p
DIAC ST2 25	T2800D 120	SOLDERCON 100 pins 70p	PINS 500 p	ins 325p
	COMPUT	ER CORNER		VOLTA 1A 5v 12v 15v 18v

VIC 20 Microcomputer

Microcomputer

At last it is here, the long awaited
Commodore's VIC 20
Microcomputer. Connects directly to
any colour TV set. Sound reproduced
through TV Speaker (Music,
Language & other Sound effects).
Has PET type Graphics. Standard
PET Basic. Full size Keyboard, 5K
memory expandable to 32K.
Attractively cased memory expanuos.

Attractively cased.

Price: Only £165

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SPECIAL OFFER

		1+	50+
i	741	14p	12p
	NE555	16p	14p
ı	2114-450n	90p	85p
	2114L-3	99p	85p
	2114L-2	130p	115p
i	2708	200p	185p
1	2532	525p	499p
1	2716	230p	199p
	2732	455p	390p
	4027	240p	215p
	4116	85p	70p
	4334-3	325p	290p
	6116-3	855p	835p
	7805 1A/5V	45p	35p

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C15W CX17W CCN15W X25W 425 425 440 Spare bits Elements 200

Iron stands 160 **Heat Shunt**

GAS & SMOKE DETECTORS TGS812 & 813 575

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through, we

do the rest. ULTRASONIC Transducers 40KHz 395p pr. DIL SOCKETS (TEXAS)

25p 35p 42p 52p 60p 70p 70p 80p 105p 99p 8 pin 14 pin 16 pin 18 pin 20 pin 22 pin 24 pin 28 pin 36 pin 40 pin 10p 16p 22p 25p 25p 28p 30p

PLUGS (Headers) 14 pin 44 16 pin 49 24 pin 88 40 pin 255 Ribbon Cable 10 way 22p 20 way 40p

SPEAKING CLOCK

MON

WATFORD'S Ultimate Monitor IC.

A 4K Monitor Chip specially designed to produce the best from your: Superboard Series I & II, Enhanced Superboard & UK101. As reviewed by Dr. A. A. Berk In Practical Electronics, June 1981. Price only £19.95 + 50p P&P.

EPSON MX Series PRINTERS

Now available from stock at very competitive prices.

• MX80T 10" Tractor Feed, 9x 9 matrix, 80 column Speed 80 CPS bi-directional

Centronics Interface, Baud rate 110-9600 (RS232) £355 MX80FT Has Friction & Tractor Feed

plus all the MX80T's facilities £395 MX80FT2 Has high resolution
Graphics option plus all the
MX80FT's facilities £435

'D' CONNECTORS (Cannon type) Covers						
	Plugs	Sockets	plastic			
9 way	95p	125p	145p			
15 way	135p	198p	150p			
25 way	170p	250p	130p			
37 way	290p	398p	185p			

3				_
	JUMPER LEA Single ended DI	DS Ribbon P. 24" leng	th Cable	
	14 pin 145p		16	pin 165p;
	24 pin 240p;		40	pin 380p.
	Double ended D	IP		
	14 pir	16 pin	24 pin	40 pin
	6" 185p	205p	300p	465p
	12" 198p	215p	3150	490p
	24" 210p	2350	345p	540p

ETI/WATFORD'S MICRO EXPANSION SYSTEM. This versatile economical Expansion System as published in ETI provides a Cheap but reliable Expansion possibility for most of the popular Micro-Computers. Send SAE for details.

VIC 20 MICROCOMPUTER (see above SPECIAL OFFER section).

EPSON MX SERIES PRINTER: see top right

hand side of this advertisement.

SEIKOSHA GP80A — Unihammer Printer, gives normal and double width characters as well as dot resolution graphics 8" Tractor feed. Parallel

resolution graphics 8" fractor feed. Faranter Interface standard. £195
SOFTY-2. As reviewed in PE September 1981.
The complete microprocessor development system for Engineers & Beginners. New powerful instruction. Accepts any 24 pin 5V single rail EPROM. Supplied fully built, tested & enclosed in a black ABS case. Price incl. encapsulated plug in 1970/1978 (1986).

nower supply VIDEO MONITOR 9" fully cased. B&W. Fully guaranteed. Excellent value for money at only £69 TEX EPROM ERASER. Erases up to 32 ICs in

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5V/5A PSU Ready built and tested £25 Attractive Beige/Brown ABS CASE Superboard/UK101 or Home Brew for £26 Extra 4K of RAM (8 off 2114L-300nS) £7.95

Space Invaders for Superboard £6 Full ASC11 coded keyboard type '756' £39 4 x 4 matrix keypad (reed switch assembly) £4 40p £6

C12 Cassettes in Library Cases
8½ Fan fold paper (500 sheets) (no VAT)
9½" Fan fold paper (500 sheets) (no VAT)
Teleprinter Roll (no VAT) £3.50

(P&P on most of the above items is extra)

Call in at our shop for demonstration of any of the above items. Be satisfied before you buy.

		Suckers	40	
1	VOL	TAGE RE	GULATO	ORS
ł	1.A		-ve	
ı	5V		p 7905	220p
Ĭ	12V	7812 145	p 7912	220p
	15V	7815 145	p 7915	220p
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ĺ			CHES	
		250V DT 14	TOGGLE	2A 250V
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		on/on/on 40		
			4 pole on	011 34
		BUTTON	SUB-MI	N
	Spring	loaded	TOGGLE	

SWIT	CHES
SLIDE 250V 1A DPDT 14 1A DPDT C/OFF15 1A DP on/on/on40	TOGGLE 2A 250 SPST 3 DPDT 4 4 pole on off 5
PUSH BUTTON Spring loaded Latching or Momentary 6A SPDT c/over 99 DPDT c/over 145 MINIATURE Non Locking Push to make 15p Push break 25p	SUB-MIN TOGGLE SP changeover 6 SPST on off 5 SPDT c/off 8 SPDT Biased 10 DPDT 6 tags 7 DPDT C/OFF 8 DPDT Biased 14 3 pole c/over 20

ROCKER: 5A, 250V, SPST 28p ROCKER: (white) 10A 250V SPDT

ROCKER: With neon lights red when on.
10A250V, DPST
ROCKER: (White I 10A/250V DPDT 72p
ROCKER: (White I 10A/250V DPDT 72p
ROTARY: Make your own Multiway
Switch. Shafting Assembly -accommodates up to 6 wafers.
90p
Break before make Wafers. Silver contacts.
1 pole/12 way: 2 pole/6 way; 3 pole/
4 way; 4 pole/3 way; 6 pole/2 way
Mains DPST Switch to fit
Screen & Spacers
6p
ROTARY: (Adjustable Space Type)

ROTARY: (Adjustable Stop Type)
1 pole/2 to 12 way, 2p/2 to 6 way, 3 pole/
2 to 4 way, 4 pole/2 to 3 way
45p 45p ROTARY: Mains 250V AC, 4 Amp 56p DIL Switches: (SPST) 4 way 70p; 6 way 85p; 8 way 90p; 10 way 145p; (SPDT) 4 way 190p.

7	CRYSTALS	
-11	32-768KHz	200
	100KHz	270
	200KHz	295
	455KHz	370
	1MHz	295
	1-28MHz	392
	1-6MHz	323
	1-8MHz	323
	1-008M	290
	1º 8432M	220
	2-OMHz	240
	2-4576M	220
	3.2768M	220
	3-2768M 3-57954M	150
	3-6864M	300
	4-OMHz	200
	4-032MHz	290
	4-80MHz	200
	4·194304M	200
	4-433619M	120
	5-OMHz	200
	5-185MHz	300
	5-24288M	390
	6-0MHz	220
	6-144MHz	240
	6-5536MHz	200
	7-OMHz	200
	7-168MHz	250
	7-68MHz	200
ı	8-0MHz	200
	8-867237M	240
1	9-00MHz	200
	10-0MHz	200
	10 7MHz	220
	10-24MHz	200
	12-0MHz	290
	14-31818M	320 275
	16-0MHz	275
	18-0MHz	240
	18-432M	240
	19-968MHz	
	24-0MHz	200
)	26-69M	290
	27-648M	330
	27-145M	240
	38 66667M	290
	48 OMHz	270

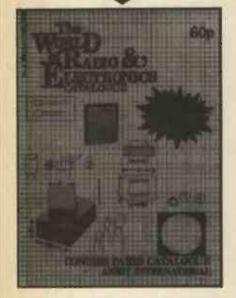
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contact resistance		BC308 8	p 2SA872	A 18p	3SK51	54p
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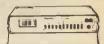
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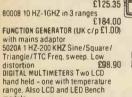


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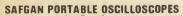
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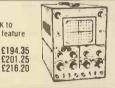
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effective fully built module which contains both ultrasonic transmitter and receiver, together with the necessary circultry for providing the appropriate delays and false alarm suppression. Using this module with a suitable 12V power supply and relay unit such as that shown, a really effective though inexpensive intruder alarm may be constructed. The module, which is supplied with a comprehensive data sheet, is easily mounted in a wide range of enclosures. A ready drilled case, together with all the necessary hardware, is available below.

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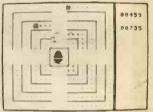
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GAMES PACK 1
Asteroids Shoot them before they crash into you, Lists ten best scores, Program 4K, graphics 6K.
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Breakout Score points knocking bricks from wall. Ball has two changes of angle and speed. Program 3K, graphics

1-2K, COLOUR **GAMES PACK 3** Rat Trap Move your rats with-out colliding with the trails left. Entangle your opponent before he entangles you! High speed rat action replay. Program 4K, graphics 6K. Lunar Lander Land a spacescraft on a lunar crater; altitude velocity, fuel and drift. Program 1K, graphics ¼K. Black Box Deduce the position of four invisible objects in the

Black Box by firing rays at them. Program 4K, graphics ½K. GAMES PACK 4
Star Trek Classic computer
game; rid the universe of Klingons.
Short and long-range scans, galactic map, phasers, photon torpedoes, shields etc. Program 5K, graphics 2K. Four Row Take turns in placing marbles on the board; the first to get a line of four wins. Program 5K, graphics 6K. COLOUR Space Attack Repel the invasions of earth and avoid being hit by

the gunner ships. Becomes pro-gressively harder with each invasion. Program 3K, graphics

GAMES PACK 7 Green Things An alien life-form has invaded your space-craft; dis-cover a way of destroying it with the weapons available on the ship Program 5K, graphics 2K, COLOUR Ballistics Take turns in firing shells at the other player, taking into account the wind and shape of the hill, Program 3K, graphics 6K, needs floating-point.
Snake Grow yourself a snake by

guiding it towards digits which it eats. Program 2K, graphics ½K.

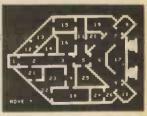
GAMES PACK 2 Dogfight Two-player game; each player controls a plane and tries to shoot down his opponent without crashing. Program 4K, graphics 6K.

Mastermind Guess the computer's code before the computer guesses yours; program 3K, graphics ½K. Zombie Land on Zombie island; try to lure all the zombies into the swamp. In desperation jump into hyper-space! Program 3K, graphics ½K. COLOUR



GAMES PACK 5 Invaders The most popular video game, with invaders, flying saucers, shelters, and full sound effects. Program 5K, graphics

Wumpus Wander in caves inhabited by the Wumpus. Find and shoot him before he eats you. Pits and bats make things harder. Program 2K, graphics ½K. Reversi Reversi, or Othello played with counters that are black one side and white on the other; Program 3K, graphics 1/2K. COLOUR



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Automatically gives wah or swell sounds with each guitar note played.

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Kit order code --SET-119 645 36

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Very effective stereo circuit for reducing noise found in most tape recordings.

Kit order code = SET-97 £14.57

ENVELOPE SHAPER

With integral voltage controlled amplifier and full manual control over the A.D.S.R.

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EXPOSURE TIMER

Controls up to 750 watts in 0.5sec steps up to 10 mlnutes, with built-in audio alarm.

SET-93 £38.03 Kit order code ==

FORMANT SYNTHESISER

For the more advanced constructor who puts performance first, this is a very sophisticated 3-octave synthesiser with a wealth of facilities, including 6 oscillators, 3 waveform converters, voltage controlled filter, 2 envelope shapers and voltage con-trolled amplifier. Case and hardware not included - see our lists for further details. Kit plus Keyboard & Contacts

SET-66 £330.24

FREQUENCY COUNTER

A 4-digit counter for 1Hz to 99KHz with 1 Hz sampling rate. SFT-79 £44 27 Kit order code =

FUNNY TALKER

Incorporates a ring modulator, chopper & frequency modulator to produce fascinating sounds when used with speech Kit order code ==

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GUITAR EFFECTS UNIT

Modulates the attack, decay and filter characteristics of a signal from most audio sources, producing 8 different switchable sounds that can be further modified by manual controls.

Kit order code =

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GUITAR FREQUENCY DOUBLER

Produces an output one octave higher than the input. Inputs and outputs may be mixed to give greater depth. SFT-98 £10.62

GUITAR MULTIPROCESSOR

An extremely versatile sound processing unit capable of producing, for example, flanging, vibrato, reverb, fuzz and tremolo as well as other fascinating sounds. May be used with most electronic instruments. Meter & some SW's not included in kit see list for selection.

SET-85 £74.03 Kit order code =

GUITAR OVERDRIVE

Sophisticated versatile fuzz unit incl. variable controls affecting the fuzz quality whilst retaining the attack and decay, and also providing filtering.

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For use with magnetic, ceramic or crystal pick-ups, tapedeck or tuner, and for most headphones. Designed with RIAA equalisation.

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Kit order code = SET-118 £9.51

P.E. STRING ENSEMBLE

four-octave multi-voiced polyphonic string-chorus synthesiser that also has switchable wind instrument voices. See our lists for details.

P.E. MINISONIC SYNTHESISER

A very versatile 3-octave portable mains operated synthesiser with 2 oscillators, voltage controlled filter, 2 envelope shapers, ring modulator, noise generator, mixer, power supply and sub-min toggle switches to select the functions. A case is excluded, but the text gives comprehensive constructional details. Kit plus Keyboard & Contacts

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An automatically controlled 6 stage phasing unit with internal oscillator. Depth can be increased with extension.

Main kit code =

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SET-70 £42 85 Kit order code =

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SIGNAL TRACER & GENERATOR

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Kit order code =

SET-109 £16.26

SIMPLE WAVEFORM CONVERTER

Modifies a sawtooth waveform to produce triangle and sine outputs. SET-96 68 25 Kit order code =

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Order code =

SET-91 £12.45

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SYNTHESISER INTERFACE

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SFT-81 £8.40

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Suitable for use with most electric guitars, organs and other similar instruments. Includes speed, depth & by-pass controls.

Kit order code = SET-116 £11.91

TUNING FORK

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TUNING INDICATOR

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Requires a 12V PSU at about 30mA. SET-117 £20.37 Kit order code =

WAVEFORM CONVERTER

Converts saw-tooth waveform into sinewave, mark-space sawtooth, regular converted or squarewave with variable triangle, or squarewave with variable mark-space. Ideally one should be used with each synthesiser oscillator.

Kit order code = SET-67 £20.34

WAVEFORM GENERATOR

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SET-112 621.58

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As the name says! Kit order code =

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CASED AUTO TRANSFORMERS Price 6.55 8.50 11.00 0.95 1.20 1.44 1.44 13.39 2.04 2.20 O.A.

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Voltages stated are on full load **Continuous Ratings**

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	Am	DS		
Ref	60V		Price	P&P
124	-5	1	4.27	1.20
126	1	2	6.50	1.20
127	2	4	8.36	1.60
125	3	6	12.10	1.72
123	4	8	13.77	1.96
40	5	10	17.42	1.84
120	6	12	19.87	2.04
121	8	16	27.92	O.A.
122	10	20	32.51	0,A.
189	12	24	37.47	O.A.

	Р	ri 220-24	12-0-12\ D volts	'
	12V	1 24V	Price	P&P
	0.5	0 25	2.42	0.95
9	10	0.5	2.90	1.00
	2	1	3.86	1.00
3	0 5	2 5	4-46 6-16	1.20
,	6	2 5	6.99	1.20
3	8	4	8.16	1.44
	10	5	8.93	1.60
5	12	6	9.89	1.60
	16	8	11.79	1.72

Т		ENED MI			;
lef	mA	Volts		£	P&P
38	200	3-0-3		2.83	0.50
12	200 1A, 1A	0-6, 0-6		3.14	1.00
13	100	9-0-9		2.35	0.50
235	330, 330	0-9, 0-9		2-19	0.60
207	500,500	0-8-9.0-	8-9	3.05	0.95
808	1A, 1A	0-8-9.0-	8-9	3.88	1.20
36	200, 200	0-15, 0-1	15	2.19	0.60
114	300,300	0-20, 0-2	20	3.08	1.00
21	700 (DC)	20-12-0-	12-20	3.75	1.00
	1A, 1A	0-15-20	(x2)	5.09	1.20
203	500, 500	0-15-27			1.20
04	1A, 1A	0-15-27			1.20
39	50				0.50
34	500	6-0-6		2.19	0.44

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Voltages available: 105, 115, 190, 200, 210, 220, 230, 240, for step up and step down.

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Ref.	(Watts)	£	P&P
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Ref			P&P
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172	1 A	3.26	1.00
173	2A	3.95	1.00
174	3A	4-13	1.20
175	4A°	6.30	1.20
9	6/48/36V	RANGI	
	Pri 0-120		
Sec 2 wi	ndings 0-	-36-48V	to give
36-0-36V d	r 48-0-48	N or 721	/ or 96V

72v/96v 36v/48v Ref. Price P&	
	P
1 2 431 8.12 1.4 2 4 432 13.35 1.3 3 6 433 16.17 13.4 4 8 434 20.65 2.4 5 10 435 29.30 2.6 6 12 436 36.69 0.8 16 437 40.03 0.8	22 96 04 20 A.

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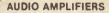
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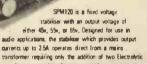
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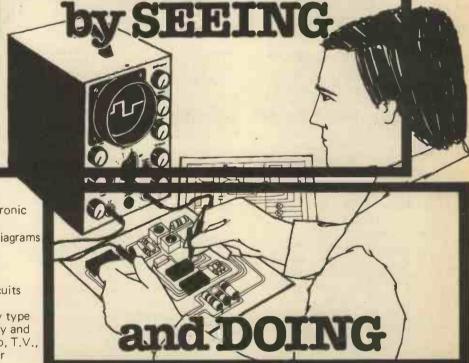
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You learn the practical way in easy steps mastering all the essentials of your hobby or to start or further a career in electronics or as a self-employed servicing engineer.

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You will do the following:

- Build a modern oscilloscope
- Recognise and handle current electronic components
- Read, draw and understand circuit diagrams
- Carry out 40 experiments on basic electronic circuits used in modern equipment
- Build and use digital electronic circuits and current solid state 'chips'
- Learn how to test and service every type of electronic device used in industry and commerce today. Servicing of radio, T.V., Hi-Fi and microprocessor/computer equipment.



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Sinclair ZX81 Personal Compathe heart of a system that grows with you.

1980 saw a genuine breakthrough – the Sinclair ZX80, world's first complete personal computer for under £100. Not surprisingly, over 50,000 were sold.

In March 1981, the Sinclair lead increased dramatically. For just £69.95 the Sinclair ZX81 offers even more advanced facilities at an even lower price. Initially, even we were surprised by the demand – over 50,000 in the first 3 months!

Today, the Sinclair ZX81 is the heart of a computer system. You can add 16-times more memory with the ZX RAM pack. The ZX Printer offers an unbeatable combination of performance and price. And the ZX Software library is growing every day.

Lower price: higher capability
With the ZX81, it's still very simple to
teach yourself computing, but the
ZX81 packs even greater working
capability than the ZX80.

It uses the same micro-processor, but incorporates a new, more powerful 8K BASIC ROM – the 'trained intelligence' of the computer. This chip works in decimals, handles logs and trig, allows you to plot graphs, and builds up animated displays.

And the ZX81 incorporates other operation refinements – the facility to load and save named programs on cassette, for example, and to drive the new ZX Printer.



Every ZX81 comes with a comprehensive, specially-written manual – a complete course in BASIC programming, from first principles to complex programs.

Kit: £49.95

Higher specification, lower price – how's it done?

Quite simply, by design. The ZX80 reduced the chips in a working computer from 40 or so, to 21. The ZX81 reduces the 21 to 4!

The secret lies in a totally new master chip. Designed by Sinclair and custom-built in Britain, this unique chip replaces 18 chips from the ZX80!

New, improved specification

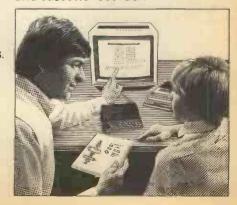
- Z80A micro-processor new faster version of the famous Z80 chip, widely recognised as the best ever made.
- Unique 'one-touch' key word entry: the ZX81 eliminates a great deal of tiresome typing. Key words (RUN, LIST, PRINT, etc.) have their own single-key entry.
- Unique syntax-check and report codes identify programming errors immediately.
- Full range of mathematical and scientific functions accurate to eight decimal places.
- Graph-drawing and animateddisplay facilities.
- Multi-dimensional string and numerical arrays.
- Up to 26 FOR/NEXT loops.
- Randomise function useful for games as well as serious applications.
- Cassette LOAD and SAVE with named programs.
- 1K-byte RAM expandable to 16K bytes with Sinclair RAM pack.
- Able to drive the new Sinclair printer
- Advanced 4-chip design: microprocessor, ROM, RAM, plus master chip – unique, custom-built chip replacing 18 ZX80 chips.



Kit or built - it's up to you!

You'll be surprised how easy the ZX81 kit is to build: just four chips to assemble (plus, of course the other discrete components) – a few hours' work with a fine-tipped soldering iron. And you may already have a suitable mains adaptor – 600 mA at 9 V DC nominal unregulated (supplied with built version).

Kit and built versions come complete with all leads to connect to your TV (colour or black and white) and cassette recorder.





16K-byte RAM pack for massive add-on memory.

Designed as a complete module to fit your Sinclair ZX80 or ZX81, the RAM pack simply plugs into the existing expansion port at the rear of the computer to multiply your data/program storage by 16!

Use it for long and complex programs or as a personal database. Yet it costs as little as half the price of competitive additional memory.

With the RAM pack, you can also run some of the more sophisticated ZX Software - the Business & Household management systems for example.

simcleir

6 Kings Parade, Cambridge, Cambs., CB2 1SN. Tel: (0276) 66104 & 21282.

numerics and highly sophisticated graphics.

A special feature is COPY, which prints out exactly what is on the whole TV screen without the need for further intructions.

At last you can have a hard copy of your program listings-particularly

How to order your ZX81

BY PHONE - Access, Barclaycard or Trustcard holders can call 01-200 0200 for personal attention 24 hours a day, every day. BY FREEPOST - use the no-stampneeded coupon below. You can pay

per second, with 32 characters per line and 9 lines per vertical inch.

The ZX Printer connects to the rear of your computer - using a stackable connector so you can plug in a RAM pack as well. A roll of paper (65 ft long x 4 in wide) is supplied, along with full instructions.

by cheque, postal order, Access, Barclaycard or Trustcard. EITHER WAY - please allow up to 28 days for delivery. And there's a 14-day money-back option. We want you to be satisfied beyond doubt and we have no doubt that you will be.

o: Si	nclair Research Ltd, FREEPOST 7, Cambridge, CB2 1YY. Item	Code	Item price	Order Total £
-	Sinclair ZX81 Personal Computer kit(s). Price includes ZX81 BASIC manual, excludes mains adaptor.	12	49.95	
	Ready-assembled Sinclair ZX81 Personal Computer(s). Price includes ZX81 BASIC manual and mains adaptor.	11	69.95	
	Mains Adaptor(s) (600 mA at 9 V DC nominal unregulated).	10	8.95	
	16K-BYTE RAM pack	18	49.95	
	Sinclair ZX Printer.	27	49.95	
	8K BASIC ROM to fit ZX80.	17	19.95	
	Post and Packing.			2.95
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Pleas	e delete/complete as applicable.		F	Please print.
Nam	e: Mr/Mrs/Miss			
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	POST - no stamp needed. Offer applies to UK only			PRE 12

PRACTICAL ELECTRONICS - STEREO This easy to build 3 band stereo AM/FM tuner kit is designed in conjunction with Practical Electronics (July issue). For ease of construction and alignment it incorporates three Mullard modules and an I.C. IF. System. FEATURES: VHF, MW, LW Bands, interstation muting and AFC on VHF. Tuning meter. Two back printed PCB's. Ready made chassis and scale. Aerial: AM - ferrite rod, FM - 75 or 300 ohms. Stabilised power supply with 'C' core mains transformer. All components supplied are to P.E. strict specification. Front scale size 10½"x 2½" approx. Complete with diagrams and instructions. SPECIAL OFFER! TUNER KIT PLUS Matching I.C. 10+10 Stereo Pov · Matching set of 4 slider controls complete amplifier kit (usually £3.95 + £1.15 p&p) with knobs for bass, trable and volumes (usually £1.70 + 80p p&p) ONLY Mullard LP1183 built preamp, suitable for ceramic and auxiliary 217.95 inputs (usually £1.95 + 70p p&p) plus £3.80 p&p. • Matching power supply kit with transformer (usually £3.00 + £1.95 p&p)

RTVE

• Featuring latest SGS/ATES TDA 2006 10 watt output IC's

• reaturing lates/363/AFES 1DA 2006 to wait output it s with in-built thermal and short circuit protection. • Mullard Stereo Preamplifier Module. • Attractive black vinyl finish cabinet, 9"x8%"x3%"(approx) • 10+10 Stereo converts to a 20 watt Disco amplifier.

To complete you just supply connecting wire and solder. Features include din input sockets for ceramic cartridge, mlc-rophone, tape or tuner. Outputs - tape, speakers and head-phones. By the press of a button it transforms into a 20 watt mono disco amplifier with twin deck mixing. The kit incorp-orates a Mullard LP1183 pre-amp module, plus power amp assembly kit and mains power supply. Also features 4 slider level controls, rotary bass and treble controls and 6 push button switches. Silver finish fascia with matching knobs and contrasting cabinet. Instructions available, price 50p. Supptied FREE with the kit. FLEE with the kit.

SPECIFICATIONS: Suitable for 4 to 8 ohm speakers.
Frequency response Input sensitivity P.U. 150mV, Aux, 200mV,

Tone controls

P.U. 150mV. Aux. 200m Mic. 1.5mV. Bass ±12db @ 60Hz Treble ±12db @ 10KHz 0.1% typically @ 8 watts 220 - 250 volts 50Hz.

Mains supply

STEREO MAGNETIC PRE-AMP CONVERSION KIT Includes FREE Magnetic cartridge with diamond styli. All components including p.c.b. to convert your ceramic

put on the 10+10 to magnetic.
Only available with 10+10 amp. £2.00 includes p&p. 8" SPEAKER KIT Two 8" twin cone domestic speakers. £4.75 per stereo pair plus £1.70 p&p. when purchased with amplifier. Available separately £6.75 plus £1.70 p&p.

PRACTICAL ELECTRONICS SERIES II CAR RADIO KIT

2 WAVE BAND MW -- LW

• Easy to build

• 5 push button tuning • Modern design • 6 watt output • Ready etched and punched PCB • Incorporates suppression circuits.

and punched PCB • Incorporates suppression circuits.

All the electronic components to build the radio, you supply only the wire and the solder, featured in Practical Electronics March issue. Features: pre-set tuning with 5 push button options, black illuminated tuning scale. The P.E. Traveller has a 6 watt output neg, ground and incorporates an integrated circuit output stage, a Mullard IF Module LP181 ceramic filter type pre-aligned and seembled and a State Dec. £10.50

assembled, and a Bird pre-aligned push button tuning unit.

Plus £2.00 p&p. Suitable stainless steel fully retractable aerial (locking) and

speaker (6"x 4"app.), available as a kit complete

£1.95/pack. Plus £1.15 p&p.

HIGH POWER

READY BUILT OR IN KIT FORM

125 WATT MODEL £10.50

Plus £1.15 p&p 200 WATT MODEL

SPECIFICATIONS:

Operating voltage (DC)

Frequency response

£14 95 Plus £1.15 p&p

125 W Model Max. output power (RMS) 125 watts 50 - 80 max 4 - 16 ohms

measured @ 100 watts Sensitivity for 100 watts Typical T.H.D. @

50 watts, 4 ohms 0.1% 0.1% Dimensions (both models) 205 x 90 and 190 x 36mm.

The power amp kit is a module for high power applications — disco units, guitar amplifiers, public address systems and even high power domestic systems. The unit is protected against short circulting of the load and is safe in an open

circuit condition. A large safety margin exists by use of

200 watts 70 - 95 max, 4 - 16 ohms 25Hz - 20KHz 25Hz - 20KHz 400mV @ 47K 400mV @ 47K

BUILT

£14.25

Plus £1.15 p&p £18.95

Plus £1,15 p&p.

200 W Model

Sultable LS coupling electrolytic for 200W model

unit for 125W model supply for 200W model

generously rated components, result, a high powered rugged unit. The PC Board is back printed, etched and ready to drill for ease of construction and the aluminium chassis is preformed and ready to use. Supplied with all parts, circuit diagrams and instructions

ACCESSORIES:

Suitable LS coupling electrolytic for 125W model

Suitable mains power supply

Suitable Twin transformer power

£1.00 plus 25p p&p.

£1.25 plus 250 p&p.

£7.50 plus £3.15 p&p

£13.95 plus £4.00 p&p.



30+30 WATT STEREO AMPLIFIER

Viscount IV unit in teak simulate cabinet, silver finished rotary controls and pushbuttons with matching fascia, mains indicator and stereo jack socket. Functions switch for mic magnetic and crystal pickups, tape and auxiliary. Rear panel features fuse holder. DIN speaker and input socket 30+30 watts RMS, 60+60 watts peak. For use with 4 to 8 ohm speakers.

BUILT AND TESTED

Size 14 %"x 10" approx.

£32.90 Plus £3.80 p&p

PHILIPS BELT DRIVE RECORD PLAYER

DECK GC037 (Size: 154"x 124"approx.) HiFi record player deck, 2 speed, damped cueing, auto shut-off, belt drive with

floating sub chassis to minimise acoustic feedback. Complete with GP401 stereo magnetic cartridge

LIMITED STOCK UNBEATABLE OFFER AT £27.50

COMPLETE Plus €3.16 p&p



MONO MIXER AMPLIFIERS



50 WATT Six individually mixed inputs for two pick ups (Cer. or Mag.), two moving coil microphones and two auxiliary for tape, tuner, organs, etc. Eight slider controls -six for level and two for master bass and treele, four extra treble cour extra treble courtors for mic and aux inputs. Size: 134"x56"x33"app. Power output 50 watts R.M.S. (continuous) for use with 4 to 80 hm speakers. Attractive black virty case with matching for the control of the court of the

fascia and knobs. Ready to use

Plus £3.70 p&p



100 WATT

Aluminium ary controls

Size: approx. 14"x4"x10%".

Five vertical slider controls, master volume, tape level, mic level, deck level, PLUS INTERDECK FADER for perfect graduated change from record deck No. 1 to No. 2, or vice versa. Pre lade level controls (PFL) lets YOU hear the next disc before fading it in. £76.00 VU meter monitors output.

100w RMS output (200w peak)

Plus £4.60 p&p



CALLERS ONLY

323 Edgware Rd, London W2. Tel: 01-723 8432. Open 9.30am - 5.30pm. Closed all day Thursday. Persons under 16 not served without parents authorisation

ALL PRICES INCLUDE VAT AT 15%

MAIL ORDER ONLY

21B HIGH STREET, ACTON, W3 6NG

Note: Goods despatched to UK-postal addresses only. For further information send for instructions 20p plus stamped addressed envelope.

All items subject to availability. Prices correct at 1/10/80 and subject to change without notice, RTVC Limited reserve the right to update their products without notice.



VOLUME 17 No. 12 DECEMBER 1981

THE CB INDUSTRY

By the time this is published the UK will have its very own CB band. It is, of course, quite impossible for us to foresee just how things will turn out and just how useful the band will be. What is perhaps significent at this time is the lack of British made rigs. Although manufacturers are expecting to sell many tens of thousands of rigs each, before Christmas, almost without exception all the 27MHz rigs will be imported. The exception is of course our own PE Ranger 27FM designed for PE by Mike Tooley and David Whitfield and for which Autumn Products (Modus Systems) are the appointed kit supplier; Autumn are now also manufacturing PE Rangers.

There are a number of obvious reasons why British manufacturers are unable to build competitively priced rigs in the UK but one restriction, that is not so well known outside the industry, is that import duty has to be paid on components but is not levied on complete equipment. However, it is very pleasing to note that at least two UK manufacturers are proposing to make rigs for the 934MHz band. This shows that UK companies can be competitive

on products where the market is smaller and where the retail unit cost is higher, giving more flexibility on profit margins.

Some of the bigger companies are claiming that they will totally dominate the UK CB market but we doubt that this will happen within the first year, if ever. There are many manufacturers investing large amounts and none of them will be prepared to back out quickly.

QUALITY.

What we must also wait and see is the technical quality of the products. Because it was first, and a kit, the PE Ranger has attracted much attention on its ability to meet the Home Office specification and of course our designers have been fully aware of this; hence the additional information in the article this month. What we will soon know is just how much attention manufacturers have paid to ensuring all their rigs meet the Home Office performance specification.

We also await with interest the fate of those who continue to use illegal a.m. or a.m./f.m. CB rigs; it is rumoured that the radio regulatory department

are planning to clamp down heavily on illegal use. This would seem sensible as they have been avoiding the protests on interference by advising injured parties that everything will change when the legal system is introduced; it certainly won't unless they prevent the continued use of a.m. equipment.

NEWS

No doubt many readers will be looking for their first rig, be it for 27 or 934MHz; with this in mind we are putting together a four page guide to available legal rigs, which will be published as a pull-out next month. Once again this does not mean PE will be going over to CB, but the new rigs are news and we hope the pull-out will interest most readers.

As we mentioned in *News and Market Place* last month, the PE Ranger was shown on TV and the *PE Bandbox* has also attracted media interest, being put through its paces for many radio programmes by the designer Alan Boothman.

Mike Kenward

EDITOR

Mike Kenward

Gordon Godbold ASSISTANT EDITOR

Mike Abbott TECHNICAL EDITOR

David Shortland PROJECTS EDITOR

Jasper Scott PRODUCTION EDITOR

ADVERTISEMENT MANAGER

SECRETARY
AD. SALES EXEC.

CLASSIFIED SUPERVISOR

Jack Pountney ART EDITOR

Keith Woodruff ASSISTANT ART EDITOR

John Pickering SEN. TECH. ILLUSTRATOR

Isabelle Greenaway TECH. ILLUSTRATOR

Colette McKenzie SECRETARY

D. W. B. Tilleard
Christine Pocknell

Alfred Tonge 01-261 6819

OR Barbara Blake 01-261 5897

Editorial Offices: Practical Electronics, Westover House, West Quay Road, Poole, Dorset BH15 1JG Phone: Editorial Poole 71191

We regret that lengthy technical enquiries cannot be answered over the telephone (see below).

Advertising Offices:
Practical Electronics Advertisements,
King's Reach Tower,
King's Reach, Stamford Street, SE1 9LS
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Technical Queries

We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in Practical Electronics.

All letters requiring a reply should be accompanied by a stamped, self addressed envelope and each letter should relate to one published project only.

Components and p.c.b.s are usually available from advertisers; where we anticipate difficulties a source will be suggested.

Back Numbers

Copies of some of our recent issues are available from: Post Sales Department (Practical Electronics), IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SE1 OPF, at 95p each including Inland/Overseas p&p.

Binders

Binders for PE are available from the same address as back numbers at £4-30 each to UK or overseas addresses, including

postage and packing, and VAT where appropriate. Orders should state the year and volume required.

Subscriptions

Copies of PE are available by post, inland or overseas, for £13.00 per 12 issues, from: Practical Electronics, Subscription Department, Oakfield House, Perrymount Road, Haywards Heath, West Sussex RH16 3DH. Cheques and postal orders should be made payable to IPC Magazines Limited.

... HEUS &

Edited by David Shortland & Jasper Scott

BBC: far from disappointed

Following our headline in the October issue of News and Market Place concerning the BBC disappointment over the response to their Electronics and Microelectronics School Radio Course we were very pleased to hear from Mr Arthur Vialls, the producer of School Broadcasting Radio.

Mr Vialls tells us that since our headline the response has been overwhelming with the teachers' notes (supplied on the basis of one set per applicant) now in their third reprint and to date 2500 copies have been dispatched. This means that the penetration into the secondary school market has been between 30 and 40 per cent.

Orders for the kits which accompany the programmes are now coming in at the

rate of 60 a day and with the film strips also having to be reprinted it would appear that many schools have taken up the challenge.

Full details of how to obtain the kits and film strips are included in the teachers' notes which are available from Electronics and Microelectronics BBC School Radio, 1 Portland Place, London W1A 1AA on receipt of a s.a.e. with a 20p stamp.

ULTIMATE TIMER...

The CT5000 from TK Electronics is a timer kit which has 18 programmable time sets and 4 independent 2A mains outputs with zero voltage switching.

The 12hr 0.5in display will give day of the week, a.m./p.m. and output status indication. A battery back-up facility saves stored programmes and continues time



keeping during power failures.

The unit is programmed by a 20 function keypad which includes programme verification button.

The timer kit is priced at £45.00 plus VAT and is available from TK Electronics, 11 Boston Road, London W7 3SJ (01-579 9794).

SOLAR CAMERA

The Ricoh camera company have recently announced the XR-S Solar powered camera which utilises two solar panels containing 20 solar cells mounted on each side of the pentaprism housing. These solar cells extend the life of a 31V silver oxide battery developed by Ricoh, for an estimated five years.

When first shown at Photokina '80, it was questioned as to whether it would need to be exposed to sunlight to charge the camera's battery, however, it can be effectively charged under white light as well as sunlight and if the light level is too low for the solar cells to operate the battery will work for up to four hours when fully charged.

CHEAP TWEET!

Any reader who is planning to build a pair of speaker units or update old speakers will be interested to learn that RT-VC are offering Goodmans soft-dome tweeters (shown below) at a very competitive price. The tweeters can be used in systems handling up to 40 watts and are priced at £3.50 each plus £1.75 p&p, or £5.95 per pair plus £2.40 p&p. Filter components are included in the prices. RT-VC, 21b High Street, Acton, London W3 6NG.



POINTS ARISING . . .

INGENUITY UNLIMITED (Nov. '81)

In the circuit diagram of the 0-99s photographic timer, the centre tap of the mains transformer is shown connected to the positive supply rail. It should be connected to the negative.

HOROLOGICUM (Oct. '81)

There is an error in Table 1, the MM5309 pin-outs. The pin-out functions of 15–28 should be reversed. I.e. Pin 15—Positive Supply and Pin 28—Display Enable.

Also, on page 22, column 2, 4th line from bottom should read: 1MHz instead of 1.03MHz.

MICROPROMPT (July '81)

Line 60 in Noel Caffrey's "HECDEC" program should read: S\$ = MID\$ (A\$, I, 1) etc...

BIGGE STATES OF Prices corre

Items mentioned are available through normal retail outlets unless otherwise specified.

Prices correct at time of going to press.

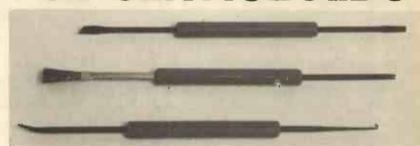
Briefly...

Bernard Babani, publishers of technical books on most subjects in the field of electronics inform us that their 1982 catalogue is now available. The 32 page catalogue lists books for both beginners and those with a wide knowledge of the subject. It is available free to readers who send a large s.a.e. to: Bernard Babani (Publishing) Ltd,, The Grampians, Shepherds Bush Road, London W6 7NF. (01-603 2581).

The business of A. Marshall (London) Ltd. at 40 Cricklewood Broadway, London NW2 has now changed hands and will be trading in the future as Cricklewood Electronics Ltd. Customers are invited to phone 01-452 0161 for details of stock and prices as a catalogue has yet to be printed. A. Marshall continue to trade from Kingsgate House and Edgware Road.

The latest 64 page Bi-Pak catalogue is now available from Bi-Pak Semiconductors, The Maltings, 63A High Street, Ware, Herts., on receipt of £1.00 including p&p.

PCB SERVICE AIDS



New from Tele-Production Tools are six aids specially designed for use on printed circuit boards. The set comprises three double ended tools which includes a stainless steel brush for removing resin and oxidization etc. from p.c.b.s, a fork for forming component leads and wire wrapping, a knife for cutting tracks and a scraper for cleaning leads. Also included is a hook for removing components after de-soldering and a reamer for opening p.c.b. holes.

The tools which are fully insulated are housed in a plastic case and cost £3.74 including VAT and p&p.

Tele-Production Tools Ltd., Stirton House, Electric Avenue, Westcliff-on-Sea, Essex. (0702 35219).

MODULAR LIGHTING SYSTEM

L&B Electronic have introduced a versatile range of modular power dimmers for use in portable and fixed lighting rigs. Four types of module are available to suit particular systems, with facilities to preset channels, on multi core lines, master dim, "kill" and provide logic compatibility with other lighting units. A single supply/reference board can supply up to 50 various units making it feasible to construct a lighting board with just the required number of channels, saving considerable cost over complete manufactured systems. Prices range from around £7 to £20, with substantial discounts on quantity orders.

A product sheet with prices is available on request. L&B Electronic, 45 Wortley Road, West Croydon, Surrey. Tel: 01-689 4138



Gountdown.

Please check dates before setting out, as we cannot guarantee the accuracy of the information presented below.

IFSEC (Fire & Sec.) Nov. 17–19. RDS Dublin. V Compec Nov. 17–20. Olympia London. Z1 BEX Plymouth Nov. 18–19. Holiday Inn. K Intron Nov. 24–26. RDS Dublin. V Continuous events at Nat. Micro & Elect. Centre. L1

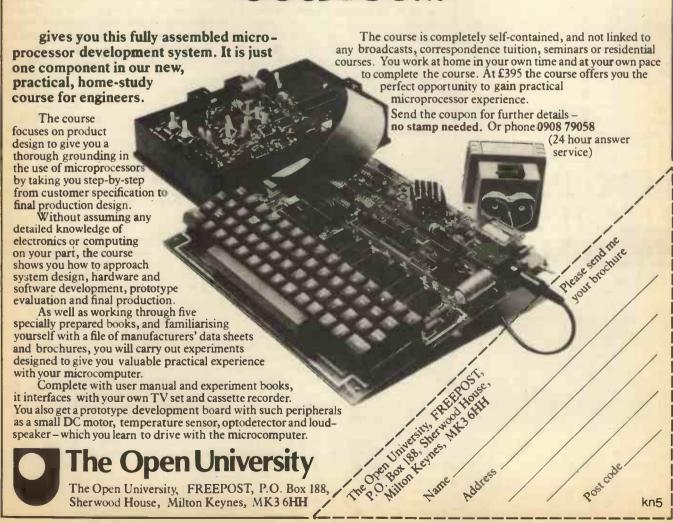
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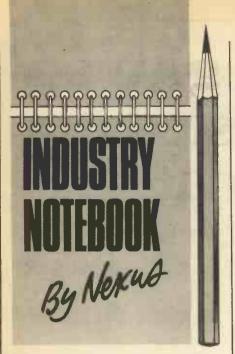
IDEA (Domestic appliances) Jan. 12–14. Birmingham. B6
OEM Assemblies Feb. 2–4. Royal Hort. Halls London. T
BEX Bristol Feb. 3–4. K
BEX Bournemouth Feb. 17–18. K
Microsystems Feb. 24–26. West Centre Hotel London. Z1
Seminex Mar. 29–Apr. 2. Imperial College London. H1

- B6 Andry Montgomery Ltd. & 01-486 1951
- E Evan Steadman, Saffron Walden & 0799 22612
- H1 Seminex Ltd., Tunbridge Wells & 0892 39664
- K Douglas Temple, Bournemouth & 0202 20533
- L1 World Trade Centre @ 01-488 2400
- T Trident, Tavistock & 0822 4671
- V SDL & Dublin 763871
- Z1 IPC Exhibitions, Sutton & 01-643 8040

What would you give an Engineer interested in learning about microprocessors?

Our microprocessor course...





Pioneers

Viewed in the context of the general economic climate, electronics has enjoyed a remarkable year. But 1981 also saw the passing of two pioneers who in different ways furthered the growth of the industry.

P. A. G. H. Voigt did as much as any one man could to popularise the concept of high-fidelity sound reproduction in the 1930s. The Voigt corner horn loudspeaker system was an outstanding achievement and was not only uncanny in its sound realism but had remarkable acoustic efficiency. So much so that at a Radiolympia exhibition of the period, when each exhibitor was allowed 100 milliwatts of audio from a central amplifier, the sound volume from Paul Voigt's exhibit was so markedly louder that neighbouring stands complained he must be cheating. In fact for domestic use only three watts, obtained from a single triode output valve, was more than enough to deafen the listener.

Of course the Voigt corner horn would hardly do in today's small box-like rooms. It was designed in and for a more spacious age. The speaker itself was a massive unit with a huge electromagnet which was powered from the a.c. supply. And there was a heavy concrete block at the base of the horn unit. He also developed a moving coil pick-up, probably the first ever, which gave us, even with those old 78 r.p.m. discs, a glimpse of future fidelity yet to come.

Paul Voigt emigrated to Canada in 1950 where he died earlier this year at the age of 80. He started his career in audio in 1922 and retired in 1970.

Dr Trevor Lloyd Wadley, a South African scientist and inventor, died in Natal at the early age of 61. It was he who came to England in the early 1950s canvassing a new idea for a very stable communications receiver. He visited all the major companies and was told it was too complicated to manufacture. Scraping the bottom of the barrel he was introduced to a tiny company, almost a back-street concern, who not only showed interest but bought the licence to

manufacture. The company was Racal Engineering and the end result was the world-beating RA 17 communications receiver incorporating the principle of the Wadley-Loop drift-cancelling circuit.

The rest is history. The RA 17 was the breakthrough for the company which we now know as the Racal Electronics Group. But Dr Wadley did more than help put Racal on the ladder to success. He also invented the Tellurometer, a radio-based distance measuring system for survey work. Unlike the Wadley Loop which has now been superseded by frequency synthesis, the Tellurometer is still in production and has earned a great deal of money over the years for Plessey, the British licensee.

Wadley and Voigt both left their mark in the history of electronics. Their passing, one in South Africa, the other in Canada, reminds us that electronics as an applied science knows no boundaries and grows inexorably through cross-fertilisation of brilliant minds.

Neston

Nothing more clearly showed up the chaotic structural state of British trade unionism than the establishment of Marconi's new factory at Neston. Looking at industries like printing and automobile manufacture in which a multiplicity of separate, often competing, unions are involved, with consequent demarcation disputes and frequent walk-outs, it seems sensible to have all workers in a plant in a common union.

In the case of Neston, which manufactures torpedoes calling for a variety of engineering disciplines, the workers could not agree that any single existing union could adequately represent the totality of the workforce.

So, by popular vote, there will be no union at all. Instead a works council of workers and management.

This simple, almost obvious, solution has been described as a new shop-floor industrial revolution. Or is it plain common sense opting for concord rather than discord? This happened before Marconi was awarded, subject to price negotiations, the contract for the heavyweight torpedo for the Royal Navy said to be worth £500 million.

Mercifully, the electronics industry has always escaped the worst excesses of industrial relations. Of course it has every advantage. Continued growth generates none of the bitterness of workers in stagnating or declining industries. A forced redundancy in electronics is not automatic relegation to the 'scrap heap'. Unlike static industries such as ship building or mining it is not geographically limited and mobility is accepted as natural by the ambitious either through promotion within a company or change of employer.

In short, there is none of the rigidity of the older industries. And there are almost daily start-ups by small entrepreneurial teams, fired by enthusiasm and who constantly refresh the industry while, at the same time, the pace of technological advance maintains interest and presents evernew challenges. The industrial climate of

electronics enjoying rude health encourages constructive co-operation towards further success and this contagious enthusiasm appears to work at all levels.

Expansion

1982 already looks a bumper year for expansion. Plessey, whose first quarter results suggest £100 million pretax profit in the year ahead, are to build a new Telecommunications Engineering Centre on their site at Edge Lane, Liverpool. Six storeys high, it will accommodate 700 engineering personnel and will cost £2.5 million. The production plant is also to receive investment in redesign and new equipment to fit it to changing technology. All this confirms Plessey commitment to Merseyside. As a auid pro auo the workforce of some 4,000 is being asked to accept what Plessey describes as 'major changes in working practices and productivity'.

Then there's Mitel who, before their 100,000sq. ft plant at Caldicot, South Wales, was completed, have already decided to triple the size and could be employing 2,000 people by the end of next year. Mitel will be making ICs and thick film microcircuits as well as assembling PABX microprocessor controlled telephone exchanges one of which, re-engineered to UK standards, is being promoted by British Telecom under the name Regent.

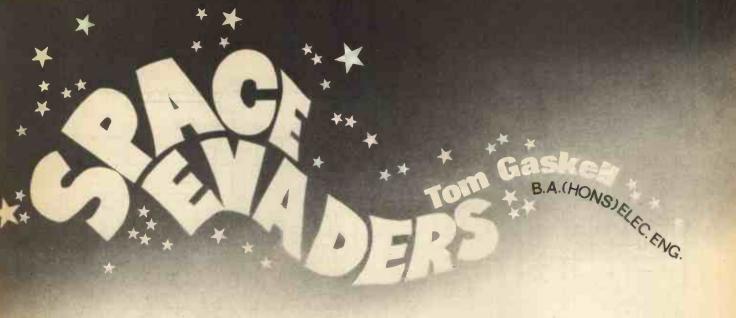
Systime, making microcomputers on a site on the outskirts of Leeds, has a planned £46 million investment which initially should create some 450 jobs with further phases over three years taking the total up to a possible 1,000.

These investments top up Motorola's £60m, NEC's £40 million and H-P's £12 million mentioned in this column last month. Not a bad score in just two months and reflecting confidence in the future.

Exports, too, remain lively. Marconi Marine, in a depressed shipping market, are to provide all the communications equipment for eight deep sea cargo vessels for Pakistan being built at the Gdansk yards in Poland. And Thorn Consumer Electronics has won a £2 million order for Ferguson TX colour TV kits for assembly in Greece. Cable & Wireless has also been active in Greece, winning a £4 million contract for communications equipment.

The defence market continues buoyant both at home and overseas. Thus we note an order for £2.5 million worth of electronic warfare equipment from the Argentine Navy for Racal-Decca Defence Systems, while sister company Racal-Decca Marine Radar has contracted for £0.5 million worth of radar to the Swedish Navy. And Racal-Decca Survey has a £5 million order from the Royal Navy for electronic positioning systems for mine-hunting operations.

In avionics, companies such as Plessey, Ferranti and Smiths should have a good share in the big British Aerospace/McDonnell Douglas deal for the Anglo-US AV-8B advanced Harrier jump-jet which in total could top £1 billion over five years. Good news, too, from British Aerospace in its first year of de-nationalisation achieving record turnover and profits.



THE problem with many small self-contained electronic games is that they are too easily beaten, or their interest and entertainment value seems to diminish rather rapidly after an initial enthusiasm! Computer based games gain in sophistication, but are often too large, complex or expensive to be a realistic proposition. However, in between these two extremes comes SPACE EVADERS! Simple enough to be built with standard CMOS logic devices in a small portable case, but difficult and frustrating enough when played to keep you occupied for hours.

As in the case of many other 'Space games', EVADERS is a battle between alien and human, the latter using missiles to defend himself against the former. The alien flies along from left to right, over and over again, dipping downwards towards the missile base on each pass. The missile is launched initially from a position very close to the alien's path. After launching, the missile moves slowly upwards with a 'bleep' noise. If it hits the alien, there is an explosion sound effect, and the missile jumps back one position, i.e. one step further away from the alien. 'Hitting' is achieved by having the missile arrive in the way of the alien's flight path just as the alien gets there. If the missile misses, it returns to its previous starting position. Gradually, as more and more hits are scored on the alien, the missile moves further away from the alien's path until a successful hit from the most distant missile base wins the game; the alien stops dead and a dull drone can be heard. The number of missiles fired is counted and displayed on a numerical readout in order that scores can be readily compared.



DIFFICULTIES!

On every fourth pass of the alien over the missile base it drops a bomb (which makes a 'dull bleep' sound) on the missile. This terminates any missile attack that is currently under way, and moves the missile one position back towards the alien flight path, i.e. away from that final winning position! The alien and missile speeds are independently adjustable; the missile normally travels fairly slowly, so the game becomes more difficult the further its starting position is from the alien. As if that wasn't enough, the really nasty part of the game, and the reason why it's a space EVADER, is that the alien doesn't fly at a constant rate, but at random! There is a subtle amount of speeding up and slowing down (even stopping dead sometimes) from the normal set speed. Hence the 'frustration' aspect of the game, because just as a missile is right on course for him, he can jump past it, or even stop in his tracks, and it misses again!

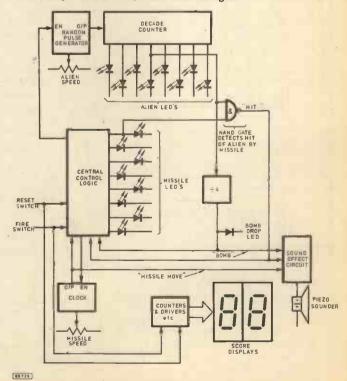


Fig. 1. Block diagram

Fig. 2. Complete circuit diagram

Two 7-segment I.e.d. displays show the score, with the alien and missiles being indicated by discrete I.e.d.s; mostly red, but with the centre alien and top missile I.e.d.s being green. (These are the ones that must be lit together at the same time to score a hit.) The design principles for this project have or will be covered in our Digital Design Techniques series, so for greater detail on the design of the circuitry refer to these articles. However, we can look now at the design of the circuit in fairly general terms, with the assistance of the block diagram shown in Fig. 1 and the circuit diagram shown in Fig. 2.

CIRCUIT DESCRIPTION

The circuitry can be divided up into three different areas: the control circuit, the scoring circuit, and the sound effects circuit. Let's start by considering the control circuit, as this comprises the main part of the project.

The alien's path is indicated by a row of I.e.d.s, D5 to D13, which are lit sequentially by IC3, a decade counter which is connected to reset itself at the end of each sequence. Hence, the I.e.d.s give the impression of a continuously moving light. The common cathodes of the I.e.d.s are taken to 0 volts via TR3, which is turned on and off by the clock input signal to IC3. Each I.e.d. is turned on for a short period, then off again, and then a short time elapses before the next I.e.d. lights, giving the impression of the alien 'jumping along'.

TR1, TR2 and IC1 form a random noise generator, fully described in this month's Digital Design Techniques. The random noise signal feeds the 'D' input of D-type flip-flop IC2a, which is clocked by the very low frequency oscillator formed by IC13c and IC13d. At every positive clock transition the logic state of the random noise signal is fed to the Q output, and depending on its state the Q output may, or may not, change state. Because the Q output clocks IC3, this gives the pseudo-randomness to the alien I.e.d. movement. Some control of the alien's speed can be given by VR1 (setting the oscillator frequency), since this determines the rate at which the random noise is sampled.

The illumination of the missile l.e.d.s is considerably more complicated than of the alien's! Basically, they are controlled by IC6, an up/down presettable binary counter, which has its binary outputs decoded into decimal by IC7, and then uses the inverter drivers of IC9 and IC10 to sink current from, and illuminate, the I.e.d.s. IC6 is clocked by IC8 a 7555 CMOS timer connected as a very low frequency oscillator, with frequency controlled by VR2. At each clock pulse of IC8, the counter IC6 (which is hard wired to always count DOWN) counts down by one, and hence illuminates the l.e.d.s in the sequence, one at a time. Note that the highest count corresponds to D25, i.e. the lowest l.e.d. in the missile chain. So, as the counter counts down, the missile appears to move upwards towards the alien l.e.d.s. TR4 gates the l.e.d.s on and off in a similar way to TR3 and the alien l.e.d.s. This not only gives the impression of jumping of the l.e.d., but also (more importantly) makes it much more difficult to hit the alien!

The alien is 'hit' when output pin 10 of IC3 (the alien's green I.e.d.) is at logic 1 at the same time as D18 is lit (the missile's green I.e.d.). This condition is detected by IC14c, the output of which is then enabled by IC12b from IC8; this ensures that the alien can only be hit when the missile I.e.d. is actually illuminated. The output of IC12b is inverted, then passes through IC14b and is inverted again by IC10d (we'll look at the action of IC14b pin 6 fater), which provides the clock to IC5, another presettable up/down counter. IC5 is used to determine the starting position for the missile which, of course, advances by one for every alien hit, but decreases by one for every bomb dropped. For the moment let's assume that the up/down control pin 10 is at logic 1 (i.e.

count up) and hence as soon as an alien is hit, IC5 counts up by one. The outputs of IC5 feed into the preset inputs of IC6, and the IC6 ouputs are forced to this preset condition when the missile 'run' is completed; IC7 pin 3 (the last decoded output of IC6) goes to logic 1, which feeds via IC12c to the pulse deriving network of C7, D14 and R25, then via IC14d to the pulse stretching network of D17, C14 and R30, and then to the preset enable pin 1 of IC6. So, when the missile has finished moving towards the alien, it moves to a position determined by IC5. For every alien hit the IC5 count increases by one, so each time the missile hits the alien it jumps back one position before starting its next run.

A missile is fired at the alien by pressing S2, a simple push-to-make switch, contact de-bounced by R26 and C9. This logic signal passes to the pulse deriving network of R24, D15 and C8 via IC11b, and then to the cross-coupled latch (or 'bistable') formed by IC11c and IC11d. This causes IC11c pin 10 to go to logic 1, forcing IC8 pin 4 (the reset pin) to go high, which enables IC8 and allows the missile to start 'moving'. As mentioned above, at the end of the missile's run IC7 pin 3 goes to logic 1 which then passes via IC12c to the pulse deriving network of C7, D14 and R25. This causes the latch to be set into the opposite state, i.e. IC11 pin 10 goes to logic 0, which resets IC8 and stops the missile from moving forwards towards the alien.

THE BOMB

At every fourth pass of the alien over the missile base it drops the bomb. IC3 pin 10 is the output which is at logic 1 when the alien is in its central position, so this is passed through IC4b and IC4a, both connected as divide-by-two circuits. Hence, pin 1 of IC4a goes to logic 1 for every four output pulses from IC3 pin 10. C5, D3 and R20 form a pulse deriving network to illuminate the bomb I.e.d., D4, via IC10e and R21. C4, R19 and D2 form a similar network with a much shorter time period, the pulse from which passes to IC12c and IC13b. Via IC12c it stops the missile and presets IC6 in exactly the same way as IC7 pin 3 going to logic 1 does. Via IC13b it passes to IC14b, IC10d and then to the clock of IC5 in the same way as a 'hit signal' does. Also, it causes the up/down control to IC5 (pin 10) to go to logic 0 via the pulse stretching network of D16, C10 and R27. Since the pulse derived by C4, R19 and D2 is much shorter in duration than the pulse stretched period of D16, C10 and R27, the up/down control is still at logic 0 (count down) when the clock input goes back to logic 1, so IC5 counts DOWN by one, moving the missile one position nearer the alien because of the preset enable pulse applied to IC6. Note that these timing delays have to be introduced between the up/down control and the clock to avoid hazards being



COMPONENTS ... D5, D6, D7, D8, D10, D11, D12, D13, D19, Resistors D20, D21, D22, D23, R1, R44 270k (2 off) D24, D25 red l.e.d. (0.125in) (15 off) R2 2k7 D9 D18 green I.e.d. (0-125in) (2 off) R3, R5, R20, R22, R26, R48 1MO (6 off) Displays X1, X2 common cathode red l.e.d. 7-3k3 segment display (2 off) 0.3in R6, R7, R8, R40 33k (4 off) Maplin type 4 (FR38R) R9, R19, R29, R39, R42, R47 10k (6 off) TR1 BC109 R10, R11, R12, R13, R15, R16, R17, TR2. TR3 BC184L (2 off) R18, R32, R33, R34, R35, R36, TR4 BC214L R37, R38 1k0 (15 off) 741 (8 pin) IC1 680 (3 off) R14, R21, R31 IC2, IC4 4013 CMOS dual D-type flip-R23, R25, R28, R41, R45 100k (5 off) flop (2 off) 150k (3 off) R24, R27, R30 IC3 4017 CMOS decade counter 47k R43 4029 CMOS presettable IC5, IC6 680k **B**46 binaryup/downcounter(2 off) R49, R50, R51, R52, R53, R54, R55, 4028 CMOS BCD to decimal **IC7** R56, R57, R58, R59, R60, R61, R62 1k8 (14 off) decoder All resistors $\frac{1}{3}$ or $\frac{1}{4}$ watt 5% carbon IC8 ICM 7555 CMOS timer IC9, IC10 4049 CMOS HEX inverter/ buffer (2 off) **Potentiometers** IC11, IC13, IC14, IC18 4011 CMOS quad NAND VR1 1M0 lin gate (4 off) VR2 100k lin IC12, IC20 4001 CMOS quad NOR gate (2 off) IC15 4518 CMOS dual BCD Capacitors counter C1, C4, C10, C14, C15 10n polyester (5 off) IC16, IC17 4511 CMOS 7-segment latch/ C2, C5, C6, C8, C9, C20, decoder/driver (2 off) C21, C22 100n polyester (8 off) IC19 4093 CMOS guad Schmitt C3, C13 10μ 25V electrolytic (2 off) NAND gate **C7** 3n3 ceramic C11 100n 30V disc ceramic C12 680µ 16V electrolytic Miscellaneous C16 2µ2 35V tantalum bead 1 off TOKO PB 2720 PIEZO SOUNDER (available from C17 1n ceramic AMBIT) C18 22µ 25V electrolytic 1 off each p.c.b. C19 1μ 35V tantalum bead S1—Double pole miniature toggle switch 330n polyester C23 S2, S3-Momentary pushbutton switches (2 off) 2 off battery holders with connecting leads (each 4 x Semiconductors 1 off Global Specialties 'DESIGN MATE' case DMC-1 D1 1N4002 1 piece of red tinted gelatine or perspex (see text) D2, D3, D14, D15, D16, 2 off suitable knobs for the two potentiometers, mounting D17, D26, D27, D28, pillars, screws, etc., for the p.c.b.s. 1N4148 (10 off) D29 (6BA threaded pillars are suggested) D4 vellow I.e.d. (0.125in) Sticky fixers.

created; this was covered extensively in this month's Digital Design Techniques article.

If the missile is already at the nearest starting position it can get to the alien (D19 is lit) then we don't want to move it at all when a bomb is dropped. IC12d detects this condition, which corresponds to a binary count of 0011. The output of IC12d is inverted, and then disables IC13b, preventing any counting down of IC5.

WINNING AND RESETTING

When you've finally moved the missile to its bottom l.e.d. position, D25, and then subsequently hit the alien from this position, the game is won. The output of IC5 becomes binary number 1010, which is detected by IC11a. The output of IC11a disables the "FIRE" switch S2 via IC11b and stops the alien by disabling the oscillator formed by IC13d and IC13c.

Power-on reset is provided by C13, R29 and IC12a. Manual reset, via S3, can also be applied in the same way. The reset pulse out of IC12a pin 3 is inverted by IC10a and used to reset the two flip-flops of IC4, and to preset enable

IC5, the preset inputs of which are connected to give a binary count of 0011; this corresponds to the missile I.e.d. D19 being lit, and is the starting condition for the game. The reset pulse also passes through IC14d and the pulse stretching network, then to the preset enable pin of IC6, and hence the whole system is set up to start a new game.

SCORING

The scoring circuit comprises the two 7-segment displays, IC15, IC16, IC17, IC18c and IC18d. IC15 is a dual BCD counter, with asynchronous cascading between the two halves being arranged by detecting the 1001 output of the first counter (i.e. the number 9) with IC18c, then when this count changes to 1010, pin 10 of IC18c goes back to logic 1, so via the pulse deriving network of C17, D26 and R43 and inverter IC18d, this is used to clock the second counter which is arranged to trigger a negative edge. IC16 and IC17 are 7-segment latch/decoder/drivers, although in this application the latching facility is not used. The BCD inputs are

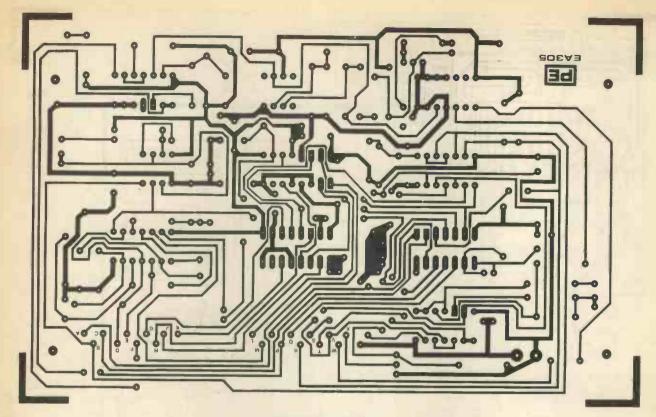


Fig. 3. P.c.b. design

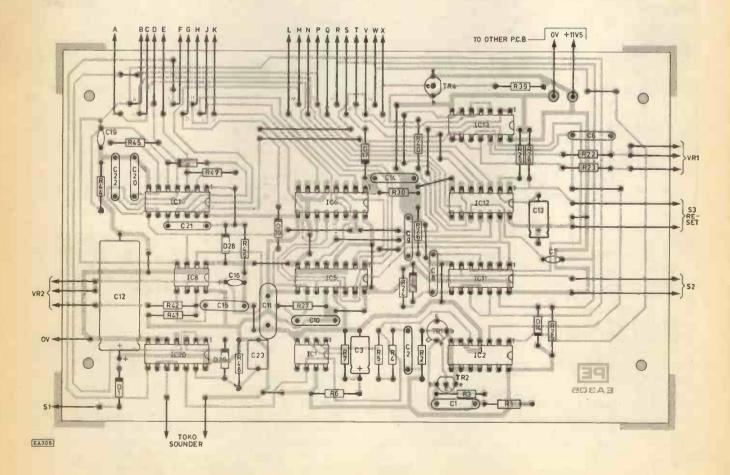


Fig. 4. Component layout

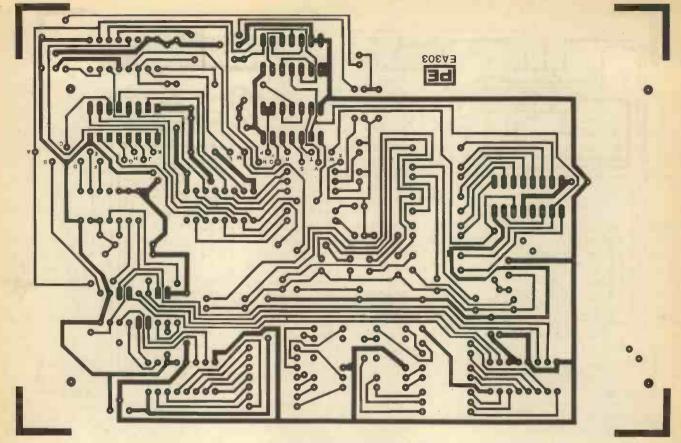


Fig. 5. P.c.b. design

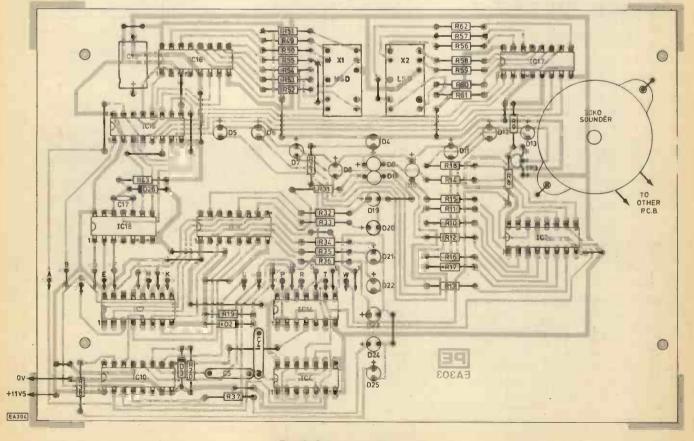


Fig. 6. Component layout

converted to 7-segment output codes (on pins 9 to 15) which drive the displays via current limiting resistors. The two counters are reset by the control circuit's reset pulse, and the first counter is clocked by the output of the latch formed by IC11c and IC11d. (Because of the latching action this point is completely bounce free; the output of IC11b pin 4 would not be as bounce free, and could give false counting, so was not used even though it might seem to be a more likely point to take the scoring from.)

SOUND EFFECTS

The raucous noises generated by this game can all be blamed on the TOKO sounder used! This is a cheap piezo loudspeaker which can be driven directly from a CMOS gate (it has a very high impedance) yet gives relatively high sound levels. The alien hit explosion sound is generated by gating on the noise output of IC1 by the 'hit signal' pulse stretched by D29, R48 and C23. (IC20a is included to prevent any problems caused by feeding the 'hit signal' directly into the pulse stretching network.)

Every movement of the missile is indicated by deriving a short pulse from the output of IC8 using the network of C21, D28 and R46 (and via IC19c), and using this to gate on and off a medium frequency audio oscillator formed by IC19a, R47 and C22. This gives out a short 'squeak' for each missile movement. The circuit is disabled, though, whenever the bomb is dropped or an overall win occurs, to allow these other sounds to take precedence, via IC19c pin 9.

The bomb and overall win sounds are both the same low frequency audio tone generated by IC19b, with R45 and C20. The oscillator is gated on and off via a pulse stretcher circuit formed by D27, C19 and R44. In the case of a bomb dropping only a short tone is heard, but in the case of an overall win the tone is continuous until the game is reset or switched off. IC18a and IC20c serve merely to combine the various effects and feed them to the sounder.

Finally, D1 provides protection against incorrect connection of the batteries, and C11, C12 and C18 decouple the power supplies to help stop transients from affecting circuit operation. Note that a high battery voltage is needed for the unit (12 volts) in order that a satisfactory noise voltage can be obtained from the reverse biasing of the TR1 junction. Although a 9 volt battery could be used, it would require special selection of TR1 to find a transistor with a particularly high noise voltage. If preferred, a simple mains power supply could be added, either squeezed directly into the unit or as a separate 'battery eliminator'. Part 2 of the Digital Design Techniques series (September '81) showed a suitable circuit; any voltage from 12 to 15 volts will work very well indeed. Whatever power supply is being used, make sure that it never exceeds 15 volts under any circumstances, or change C12 to a 25 volt type and make sure that the supply never exceeds 18 volts.

CONSTRUCTION

The components should be soldered to the p.c.b.s as shown in Figs. 4 and 6. Leave the integrated circuits until last, don't touch their pins with your hands, and use an earthed soldering iron. The l.e.d.s and displays sit well up off the p.c.b. surface, not flat down, and should be adjusted in height off the board to suit the spacers and fixings used. The displays will be visible through a 'window' in the case, and the l.e.d.s will fit directly through holes in the front panel. The piezo sounder should be fixed to its p.c.b. using short wire links fixed through its mounting holes. (Flying leads connect it to the other p.c.b.)

The two p.c.b.s should be wired together using ribbon cable, jumper strip, or just discrete wire links as preferred. Join point A to A, B to B etc. The battery holder connectors, potentiometers, and switches can also be wired up at this

point using adequate lengths of flexible stranded wire. (A spare pin on the power switch S1 can be used to anchor this joint between the two battery holder connector wires.) The case should be drilled to take the switches, pots, mounting pillars for the p.c.b., the l.e.d.s and a rectangular cutout should be made to allow viewing of the 'score' displays. This cutout should have a piece of red tinted plastic or perspex glued behind it to improve visibility and contrast of the 7-segment displays. Note that the l.e.d. holes must be AC-CURATELY drilled to match the positioning of the l.e.d.s on the p.c.b.; it is easy to damage l.e.d.s by too much bending of their legs, so be careful!

Lettering and symbols should then be applied to the case. The p.c.b.s, joined together and with pillars fitted, should be offered up to the case, screwed in, and the switches and pots added. The battery holders should be fixed to a suitable position in the case using 'sticky fixers'.

The SPACE EVADERS game is now finished. If it seems that the noise source is not operational (no logic level changes at IC2a pin 5) try changing TR1, or lowering the value of R4 to 1k0, and make sure that the supply voltage is as high as it is supposed to be. If no l.e.d.s come on at all, it is possible that they've all been inserted into the p.c.b. the wrong way round, so check this before proceeding. To test the action of the game, it is usually best to set the alien speed to minimum, and the missile speed to the maximum.

There are many potential changes, modifications, and variations possible on the basic theme of this game. Naturally the more complex it becomes, the more likely it is that a dedicated microprocessor would prove easier to use than discrete logic. However, at the level shown it is fairly easy and economical to build, and the alien seems to be sufficiently difficult to beat that even die-hard enthusiasts of that other alien space game find it an absorbing deviation!





INCLUDING V.A.T. POSTAGE & PACKING

The DP2010 is a development of the DP200 Multimeter (featured in PE May 1981) aimed at giving reasonable specification at remarkable value for money. The instrument is available ready-assembled and calibrated, or in kit form for home assembly. All parts (except PP3 battery and test leads) are supplied including clear assembly and calibration details. The DP2010 features 6 functions and 21 measurement ranges, with a high contrast 12-5mm l.c.d. readout for extended battery life.

SPECIFICATION

FUNCTIONS: Volts (d.c.) 1mV–500V, 4 ranges; accuracy $1\% \pm 1$ digit. Current (d.c.) 1μA–1000mA, 4 ranges; accuracy $1\% \pm 1$ digit—5% ± 1 digit @ 1000mA. Volts (a.c.) 1mV–500V, 4 ranges; accuracy $2\% \pm 5$ digit. Current (a.c.) 1μA–1000mA, 4 ranges; accuracy $2\% \pm 5$ digit—7% ± 5 digit @ 1000mA. Resistance 1R–2000k, 4 ranges; accuracy $1\% \pm 1$ digit. Diode Test 2V range; accuracy $1\% \pm 1$ digit. DISPLAY: 12.5 l.c.d.INPUT IMPEDANCE: $10M\Omega$. BATTERY TYPE: PP3, 2mA typical consumption. POLARITY INDICATION: Automatic. LOW BATTERY INDICATION: Automatic. OVERRANGE INDICATION "1" at most significant digit with other digits suppressed. INPUT TERMINALS: Standard 4mm.

IMPORTANT NOTE: This is a genuine Special Offer, and it is essential that all orders are accompanied by a coupon (or a copy) appearing on this page. This offer ends strictly on 1.2.1982 (UK orders) and 1.3.1982 (Overseas). The Special Offer prices including VAT & p&p give big savings on the normal 1-off Lascar prices.

For overseas orders £2.00 (Europe) or £5.00 (all other areas) must be added to cover Air Mail Delivery.

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Car Computer... P. MacFarlane Part 1

THIS computer (shown actual size below) is able to provide the following digital information in either imperial or metric units.

Current or average speed and m.p.g., distance travelled, remaining distance possible on fuel left, average speed needed for remaining distance to meet an arrival time, acceleration and deceleration times between any two preset speeds, fuel used during acceleration, average m.p.g. during acceleration, distance covered including standard distance times and fuel used and provides instant and average fuel costs.

MODES OF OPERATION

There are three basic modes of operation:

Indicating mode. This is used to display such things as current or average speed, or distance travelled.

Remainder mode. The unit can perform simple calculations such as remaining distance, remaining distance possible on the amount of fuel left, average speed required for the remainder of a journey in order to arrive in the time allowed, and so on.

Start-stop or programmed mode. This is used for acceleration timing and the like.

There is also provision to drive a combination lock ignition cut out which will be described later,

INDICATING MODE

To cause the unit to display one of the functions listed above the buttons 1 to 5, simply press that button. 'Time' is 'elapsed time' not time of day, the latter would require the unit to be on power even with the car engine off. This will read out in hours and minutes up to 19 hours 59 minutes and in decimal hours for longer times. The other functions read over three ranges depending on the value. 'Fuel Use' is either miles per gallon or litres per 100 kilometres.

Pressing 'Average/Low' causes the unit to read average rather than instantaneous values if the function selected is 'Speed' or 'Fuel Use', and to read 1000 times the value if the function is 'Fuel' or 'Distance'. When the function selected is 'Time', the unit reads in minutes and seconds or seconds and tenths. The main use for the low operation is for reading the

low values obtained during start-stop operation. To indicate low or average, a colon shows by the 'function' digit.

'Reset' sets the time, fuel and distance stores to zero and introduces the averages.

'Hold' causes the total and instantaneous stores to be held at their current values and an 'H' to show in the function digit. It is still possible to change functions using keys 1 to 6 so the conditions at the moment the 'Hold' button was pressed can be examined. 'Run' cancels 'Hold' and returns to normal operation.

REMAINDER MODE

The 'Remaining' key can be used to calculate values based on previously entered information as follows.

'Remaining' followed by 'Time', 'Fuel' or 'Distance' causes the value digits to read the entered quantity minus the measured quantity. If a further key 1 to 3 is pressed, the digits show the value of the first function based on the remaining second and the ratio of the two. For example, pressing 'Remaining', 'Fuel', 'Distance' gives the amount of fuel required for the remaining distance assuming the same average fuel use. 'Remaining', 'Distance', 'Fuel' is the distance possible with the remaining fuel and so on. The function digit always shows the units of the value digits. There are nine such remaining calculations possible.

'Remaining', 'Speed' calculates remaining distance divided by remaining time, which is the average speed now required to complete the distance in the time allowed. Similarly with remaining fuel use.

To revert to normal use, press 'End' followed by the function now required, or the function twice. The unit will perform the equivalent of pressing the 'End' button eight seconds after the last button was pressed, unless in the hold condition.

The quantities required for a journey are entered into the unit by pressing 'Enter', followed by the key for the function to be entered (1 to 3), followed by the number, followed by 'End'. Thus to enter $25\frac{1}{2}$ miles, press 'Enter', 'Distance', 2, 5, ., 5, 'End'. The 'End' is needed to indicate that a subsequent key is not a number, but see previously about automatic 'End'.



Numbers entered this way are not affected by 'Reset' or changes in the totalising stores, so if the same journey is to be done, the numbers need not be re-entered. To examine a number press 'Enter', followed by the key for the quantity to be examined. The previously entered number will now appear in the result digits and can be modified as before using the number digits, or left the same by pressing 'End'.

START-STOP MODE

The unit can be programmed to reset its stores when the display reads a certain number and to go to the hold condition at another number. Either number can be zero in which case the unit will not reset until the display reads just above zero or will not hold until the display becomes zero.

To enter the number the unit is required to reset at, press 'Enter', 'Start' (same as 'Reset'), followed by the number, followed by 'End'. Similarly the stop number can be entered using 'Stop' ('Hold'). 'Enter', 'Stop' sets a start-stop request flag in the unit, but the start-stop mode does not become active until 'Run' is pressed, indicated by a 'P' in the function digit. During start-stop operation the sample rate for instantaneous values is increased up to eight times per second.

Suppose it is required to test the acceleration of the car from 0 to 50 miles per hour, the sequence is as follows. Press 'Enter', 'Start', 0, 'End', 'Enter', 'Stop', 5, 0, 'End'. The unit is then set to read 'Speed', the car driven to where the test is to be carried out, stopped (for long enough for the display to go to zero), then 'Run' pressed. As soon as the car starts moving the total stores will be reset to zero, and when the speed exceeds 50 miles per hour, the unit will go to the hold condition. Press 'Time', 'Low' to see the acceleration time, 'Fuel', 'Low' the amount of fuel used and so on. 'Speed'

shows the speed at which the unit stopped, which will be just over 50. 'Speed', 'Average' the average speed, and so on.

This is a very good way to tune a car as the best compromise between performance and economy can be found quite quickly. The setting for ignition timing that gives the best performance also gives best economy, and up to a point the richer the mixture the better the performance at the expense of economy and vice-versa.

It is possible to start and stop on any function, thus a standing $\frac{1}{4}$ mile test can be done starting at zero and stopping at $\cdot 25$, reading 'Distance' (or stopping at 250 reading 'Distance' and 'Low'). Make sure though that 'Reset' is pressed before 'Run' when stationary. If starting and stopping on a time, this time must be expressed in decimal hours.

Pressing 'Reset' when in the start-stop mode will start the unit without waiting for the start condition. Similarly the unit can be stopped early by pressing 'Hold'. Going to the hold condition, manually or automatically, clears the start-stop mode. To repeat a test using the same figures press 'Enter', 'Stop, 'End, 'Run'.

The start number can be greater than the stop number, used, for example, for braking tests. The display must be reading more than the start number when the 'Run' button is pressed, and obviously the wheels must not skid.

It is interesting to repeat the distance tests accelerating both fiercely and gently, work out the difference in time taken and the difference in cost of fuel used, to obtain cost/time and compare that with how much you feel your time is worth to you.

'F.Cal' and 'D.Cal' are used for calibrating the unit for metric or imperial use.

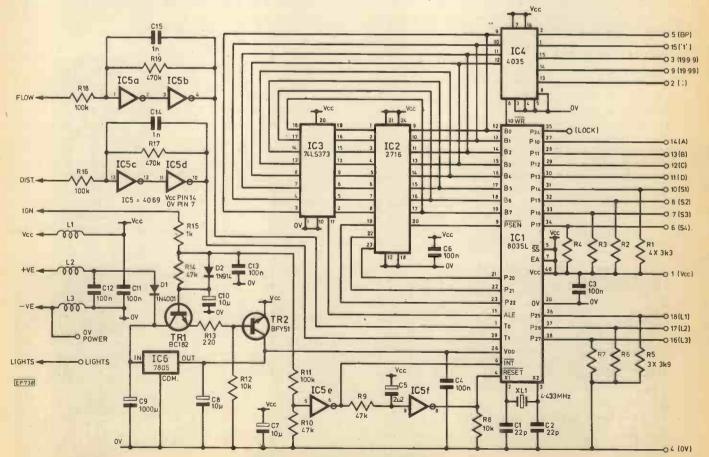


Fig. 1. Circuit diagram of main board

MAIN BOARD

The circuit for this is based around a 8035L microprocessor with a 2716 program store (Fig. 1). A 74LS373, IC3 is used to latch low order program store addresses for the ROM which on the 8035L are multiplexed with the data bus. B0 to B3 are also latched under the control of the WR signal in IC4, a 4035. This has exclusive OR outputs for driving the l.c.d. direct.

The digit outputs for the display are in BCD form on lines ABCD, multiplexed under the control of strobes S1 to S4. The strobes also drive the keyboard, organised as a 3 x 4 matrix, the sense lines being L1 to L3. Pull up resistors on the strobes are necessary because the 8035L port lines have a high impedance (50k) pull up on both inputs and outputs. Without the resistors there would be insufficient current to drive the keyboard lines.

The 8035L has an internal 64 x 8 data RAM which is used for holding the various quantities which must not be lost when the ignition is switched off. V_{dd} line drives only this RAM and is connected directly to the 5 volt regulator, IC6. This regulator is powered from the car battery directly. The rest of the circuit is powered via TR2 which is only on when the ignition switch is on.

IC5 pins 9 and 8 and 5 and 6 ensure that a RESET signal is applied to IC1 when the power is applied or removed. Pin 6 of IC1 is used to sense when power is about to fail.

The remainder of IC5 is organised as two Schmitt triggers for the inputs from the sensors. C14 and C15 ensure that there is maximum noise immunity when a threshold has just been crossed.

DISPLAY BOARD

The display board circuit is shown in Fig. 2. Here the 7211A is a 4 digit l.c.d. driver and is fed with the multiplexed data ABCD and the strobes directly. The l.c.d. decimal points, results digits '1' and function colon are driven directly from the 4035.

The keyboard is organised as a 3 x 4 matrix but is laid out on the board as 2 x 6. It is also driven one strobe early because the lines L1 to L3 are sampled at the end of a strobe when the internal strobe information is for the next strobe,

There are two bulbs for backlighting the display on this board.

The basic microcomputer comprises IC1, IC2 and IC3, and could be used to drive other display/keyboard arrangements, and with a different power supply/input arrangement.

POWER SUPPLY AND INPUT

Pin 26 of IC1 must be supplied with 5 ± 0.5 volts continuously to retain the information in the RAM, at a current of about 10 mA. When power is being applied or removed, pin 4 of IC1 (RESET) should be low and preferably when power is about to be removed pin 6 (INT) should be high. This puts the processor in a known state, and ensures that power will not fail half way through an addition for example.

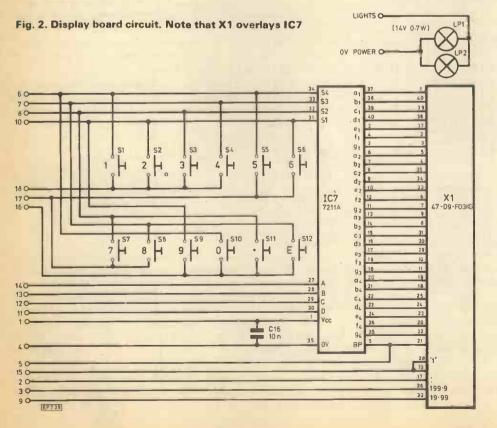
The flow pulses are applied to pin 1 and the distance pulses to pin 39, IC1. A 4-433619MHz crystal is needed as in Fig. 1 for the timebase for the correct timing.

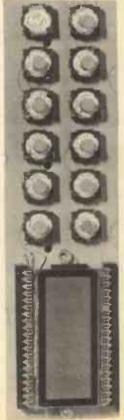
KEYBOARD/DISPLAY

The keyboard is matrix driven between strobe lines S1 to S4 and keyboard input lines L1 to L3. Note that the strobe lines are only capable of sourcing about 100µA without pull up resistors.

In order to get correct letter outputs on the function digits a code 'B' type decoder is needed for lines ABCD. There is a small amount of interdigit blanking on the strobe lines which will prevent ghosting when multiplex driving l.e.d. or fluorescent displays, but which may not be sufficient when driving gas discharge displays. Strobe S1 is high when lines ABCD have information for the least significant digit, through to strobe S4, high for the function digit.

Other output information is present on lines B0 to B7 when pin 10 of IC1 (WR) is low, as follows.





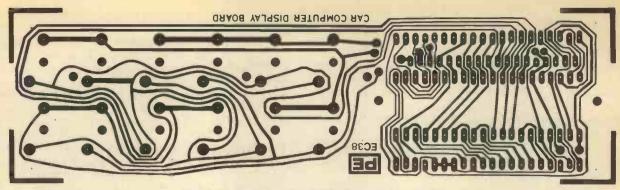


Fig. 3. P.c.b. design for the display and keyboard

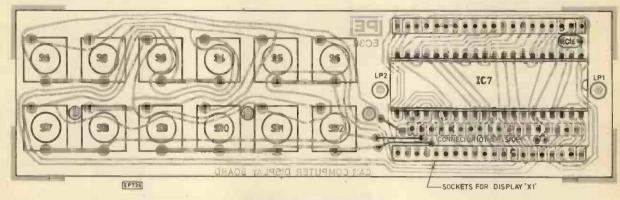


Fig. 4. Component layout

ВО	Most	significant	111	digit	on	display
----	------	-------------	-----	-------	----	---------

B1 199.9 decimal point

B2 19-99 decimal point

B3 Low/Average flag

B4 Remainder flag

B5 Start-stop information

B6 J

B7 Start-stop direction

These are active low. The start-stop direction flag is low if the start number is greater than the stop number. Bits B5 and B6 operate as follows.

B5 = 1, B6 = 1Not programmed mode.

B5 = 0, B6 = 1 Start-stop request—'Enter', 'Stop' pressed, but not 'Run' yet.

B5 = 0, B6 = 0Start-stop active-'Run' pressed from start-stop request.

Started-unit has performed reset, dis-B5 = 1, B6 = 0play value is between start number and stop number.

FLOW SENSOR

The unit requires approximately 20,000 pulses per litre of fuel. The flow range in a motor car can be from 1 to 50 litres per hour, and it is difficult to make a flow sensor to handle this range. There are problems too with pulsations in the flow.

The recommended sensor has been designed specifically for this application and should be accurate to within 2 per cent. It uses a small helical turbine running in precision bearings, which is rotated by the flow, the rotation being sensed optically. It is mounted together with its pulse reducing valve in the fuel line between the fuel pump and the carburettor. This sensor cannot be used with fuel injection or diesel engines.

DISTANCE SENSOR

The unit needs one pulse every 200mm or so, a sensor giving one pulse per revolution of the prop shaft could be used, or one giving 4 pulses per revolution of a half shaft. The recommended sensor fits into the speedometer cable giving 6 pulses per revolution. This is because of the difficulty of making a universal fitting for a drive shaft sensor, and one fitting on the end of a speedometer cable could necessitate a tight bend in the cable.

There are at least six different end formats for speedometer cables as well, but I have only come across two different inner cable sizes so far, both of which the sensor will accommodate. To fit it requires that the outer cable be cut at some point. A slotted disc inside the sensor rotates with the inner cable, the rotation being sensed optically.

HOUSING

In order to keep the outside dimensions of the unit as small as possible and to give a professional appearance to the finished unit, a custom box has been designed.

CONSTRUCTION

The p.c. board layout for the display and keyboard is shown in Fig. 3. Fig. 4 shows the component layout, the display is mounted above IC7, in the soldercon connectors.

Fit the three wire links first, then the switches, making sure that they are close against the board. Now fit IC7 as in Fig. 4, again pressed close to the board, and without a socket. Cut the bottom row of legs of this i.c. close to the board. Fit and solder the capacitor C16 and the soldercon sockets for the display. Break off the backing strip from the sockets. A small piece of white paper about 25mm/lin square stuck to the middle of IC7 will improve the appearance of the display when back lit. Press the display into

the sockets so that the front of it is 11mm from the front of the board. Fit and solder the connecting wires on the copper side of the board. These can be $40\text{mm}/1\frac{1}{2}\text{in}$ lengths of 10/0.1 wire, or a 40mm length of 18 way 0.1 in pitch ribbon cable.

Figs. 5 and 6 show the printed circuit layouts for the main board. Fit a through pin in all top side pads and solder both sides. The position of the components is shown in Fig. 7; it is recommended that IC1 and IC2 be mounted in sockets. Solder in the sockets, but do not fit IC1 or IC2 yet.

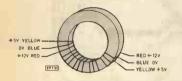
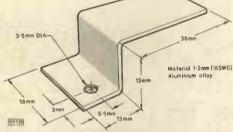


Fig. 8. Coils L1 to L3 consist of five turns each of kynar wire

L1 to L3 consists of five turns each of three lengths of .25mm kynar wire wound around the ferrite core as shown in Fig. 8. The core is held onto the board using a cable tie.

Fit IC6 and its heatsink (Fig. 9) last, an M3 × 8mm bolt holding them to the board, with the nut and washer on the component side. Fit the crystal (as far as the wires permit) lying over IC1.

Fig. 9. Heatsink for IC6. It is only shown in outline in Fig. 7



Join the two boards together, again soldering the connecting wires to the copper side of the board. The wires connect without crossing over. Fit the seven wires for the power and sensors, thread through the sleeving, and if the unit is to be mounted above the car dashboard, i.e. with the sloping part of the box at the top, thread the cable through the big hole in the board. Fit a cable tie approximately 75mm/3in from the board.

The two bulbs are fitted through the holes in the display board, from the component side, making sure that the metal parts of them are not touching the metal bar passing through IC7, and held in place with adhesive (e.g. 'Superglue'). Connect these to the main board with a twisted pair of wires kept away from the main components.

TESTING

Apply power between the —ve and +ve wires (black and red on board), 9 to 15 volts, and check that there is 5 volts ± 25 between 0 volts (heatsink) and pin 26 of IC1 socket. Now connect IGN (yellow) to the +ve supply and check that there is 5 volts between 0 volts and pin 40 IC1 socket. Remove the power, wait a minute or so for the capacitors to discharge and plug in IC1 and IC2.

Apply power between -ve and +ve/IGN again, press the

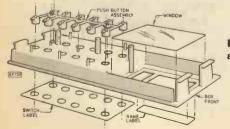


Fig. 10. Box/front assembly

'End' button (bottom right) on the keyboard, the 'Run' button, then some of the buttons 1 to 5 on the keyboard top row and check that the function (leftmost) digit of the display shows the key number 1 to 5.

MAIN ASSEMBLY

Refer to Fig. 10 for the positioning of the various components that make up the box front. Remove the backing paper from the switch label and fit it to the box front, making sure that the holes line up and that it is the right way up. Use the two plastic sprues from the box mouldings through button 6 and 7 holes to align the label, and once pressed down do not attempt to move it. Fit the name label similarly.

The transparent window is fitted to the inside of the box front using polystyrene cement or 'Superglue'. Do not get adhesive on the visible part of the window, it does not come off! Fit the switch button assembly over its studs and melt the studs down to hold it in place.

Thread the cable from the main p.c.b. through the hole in the back of the box cover and pull through, sliding the p.c.b. along the appropriate set of runners. These will grip the board fairly tightly. Press the board home, making sure that the board engages the slots at the back of the box cover, and that the capacitor C9 is not pulled from the board by the edge of the box.

Locate the holes in the display p.c.b. over the long studs in the box front, and tucking the connecting wires under the main p.c.b., hook the keyboard side lugs on the box front into the slots in the cover. Press the display side lugs inwards to fit into the cover and press the front into the cover so that the lugs snap home.

400	450	M 25	-	-		ACCRECATE VALUE OF	50 S	-	
u		IVA		8	1.31	844	133	HU	
		11/1	2		144	E.	18.5	TS	

Resistors			
R1-R4	3k3 (4 off)	R11, R16, R18	100k (3 off)
R5R7	3k9 (3 off)	R13	220
R8, R12	10k (2 off)	R15	1k
R9, R10, R14	47k (3 off)	R17, R19	470k (2 off)
All resistors 1 W	5% carbon f	ilm	

Capacitors

C1, C2	22p ceramic (2 off)
C16	10n ceramic
C5	2μ2 electrolytic 16V
C7, C8, C10	10µ electrolytic 16V (3 off)
C9	1000μ electrolytic 16V
C3, C4, C6, C11, C12, C13	100n ceramic (6 off)
C14, C15	1n ceramic (2 off)

Semiconductors

D1	1N4001	1C3	SN74LS37
D2	1N914	IC4	4035
TR1	BC182	IC5	4069
TR2	BFY51	1C6	7805
IC1	8035L	1C7	7211A
IC2	2716		

Miscellaneous

L1-L3 3 x 5 turns on T68-40 core, XL1 4-433619MHz crystal, S1 to S12 AKS switch (12 off), X1 47-D9-F03KG display, LP1-LP2 14V 0-7W LES bulbs (2 off), 40 pin DIL socket, 24 pin DIL socket, soldercon pins (40 off), heatsink, p.c.b.s, through pins (34 off), wire, cables etc., plastic box kit, speed sensor, flow sensor and valve, connector block, cable ties, various 'plumbing' parts as required.

A complete kit of parts is available from Pimac Systems
Ltd., 20 Bloomfield Road, Birmingham B13 9BY.
Price £89.50 which includes VAT plus postage and packing.

Next Month: Fitting the flow and speed sensors. Installation, calibration and a description of the program controlling the computer.

3

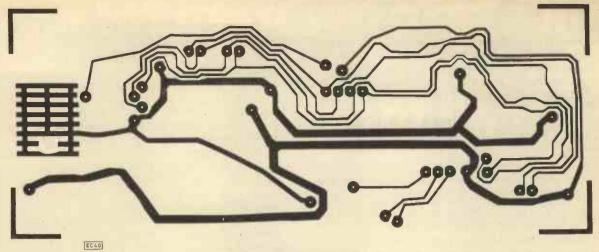


Fig. 5. Topside of p.c.b.

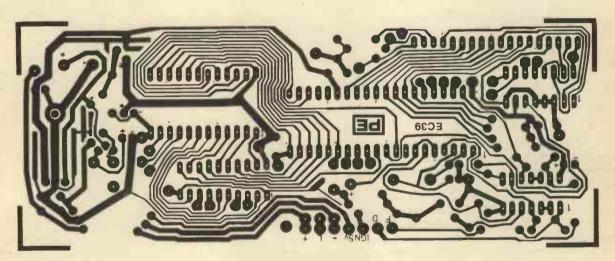


Fig. 6. Underside of p.c.b.

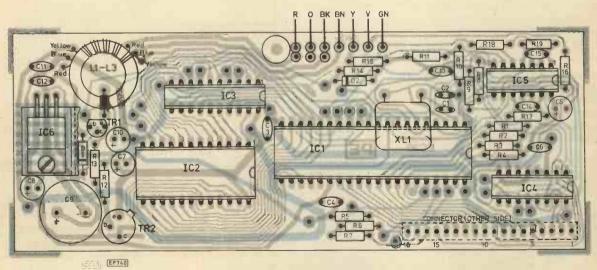


Fig. 7. Component overlay

Don't take our word for it... the reviews say it all for us

We at Videotone are very confident that the Seoum range offers excellent value, as equipment of such high specification would normally be in a much higher price bracket. This is basically what you might expect us to say about our products, but don't just take our word for it, read on.

The name of Videotone certainly needs no introduction, as their popular range of loudspeakers, and especially the remarkable showstopper in the shape of the Minimax 2, well and truly secured them an established position in the hi-fi scene a good many years back. Since then a lot has happened and today they offer a very wide range of products among which are the brand names of Coral and Seoum.

Coral is now a widely known and respected name but Seoum, the relative newcomer to the Videotone fold, has yet to fully make its mark, but if the value-for-money quality apparent in these three products is anything to go by it won't be long before it's an established brand name too!

Normally, any equipment submitted for review is used thoroughly for a reasonable period, but in the case of the Seoum SA-4120 amplifier, ST-4120 tuner and SC-4200 cassette deck, it has been in constant daily use in excess of four months and therefore well and truly exposed to the glaring light of day.

I've used it, my wife uses it almost all day long, a friend borrowed the amplifier for a disco one night, the children inadvertently abused it, and it's driven all manner of different speakers, but never for a second has the system gone wrong or given anything but pleasure.

The SA-4160 amplifier produces sixty watts per channel into eightohm speakers, both channels driven, and boasts a generous range of facilities. There are switchable outputs for two pairs of speakers. inputs for two cassette or tape decks, microphone input, switching for tone defeat, 20dB muting, loudness, mono/stereo, subsonic filter and the usual bass, treble, balance, volume and selector con-trols. Also, there's the surprising addition of a built-in head amplifier for moving-coil cartridges, peak level led power meters and a green led which lights up when the unit is switched on but which changes to red in event of a fault condition at the output stage such as ashort circuit or the use of loudspeakers of exceptionally low impedance.

It's certainly a very comprehensive amplifier and omlts nothing worthwhile that I can think of at the same time as it includes a lot I hadn't even thought about. And the price . . . just £95 including VAT!

Matching in style is the ST-4120 three-waveband tuner (vhf/fm, long and medium wave) and this again, proved very good as Gordon King also found in his Audiolab report last September. Reception on the two am bands is quite

adequate using the unit's ferriterod aerial but stereo transmissions
on the vhf/fm band were judged
as being especially good and
decidedly better than that I've had
from so-called up-market tuners
with a much higher price tag.
Particularly noticeable, using the
indoor ribbon aerial supplied, is the
very low level of background noise
which brought about that startling
effect of a voice or music emerging
out of a near-inky black silence!

Facilities are adequate with precise tuning on the vhf/fm band being made particularly easy by use of the clear led signal strength and tuning indicators.

Aerial connections at the rear of the unit make provision for an external am aerial, which must prove superior, if long enough, to the ferrite rod, and 300-ohm balanced and 75-ohm unbalanced co-axial — the latter being favoured in this country. Excellent value at £69 including VATI®

The Dolby-equipped SC-4200 cassette deck, replacing the earlier SC-3200, again proved a rather surprising package for just £85 inclusive of VAT, boasts features which normally call for a higher price tag.

Firstly, the deck is metal-tapecompatible. Secondly, it incorporates peak reading led level meters, green up to OdB and red thereafter. Thirdly, it utilises one-touch logic controls instead of the mechanical type commonly found on most lower priced decks and fourthly, the output is variable as opposed to being at a fixed nominal level.

A wide variety of quality cassettes all worked well with the deck but BASF's latest chrome formulation, Chrome II, proved particularly noteworthy.

There is never enough space to write all one wants to about a system, and especially so if such has generated genuine enthusiasm as this one has so easily done, but suffice it to say that in all respects, I just cannot fault it, Highly recommended.

Geoff Giles



This review by Geoff Giles, has been presented in full, as it appeared in the July '81 issue of Practical HiFi.

The system reviewed consisted of: the SA4160, 60 watt stereo amplifier, ST4120 stereo tuner, and the SC4200 stereo cassette recorder.

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With CB now legal we thought it would be a good idea to let you know just what rigs are available so we have collected details and photos of every legal rig we can find, for both 27MHz and 934MHz, and put them all in this pull-out guide.

PE RANGER 27FM CB BASE STATION Part1

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ELECTRONICS

JANUARY ISSUE ON SALE FRIDAY DECEMBER 11th

Digital Design Techniques...

Tom Gaskell B.A. (HONS) ELEC. ENG.

Part 5 Advanced Sequential Circuits

AST month we looked at the principles of flip-flops, dividing, counting and sequencing. This month we shall take the principles further and look at more complex devices and more sophisticated circuitry.

SHIFT REGISTERS

A 'register' is the term used for a collection of flip-flops, often with associated gates, which can be used to store and 'move around' combinations of logic O's and logic 1's. They are used extensively in calculators, computers and microprocessors where each piece of binary information (logic O or logic 1) is known as a 'BIT' (which stands for Blnary Digit). Hence, a piece of circuitry consisting of 8 flip-flops, and capable of giving eight logic outputs, might be called an 8 bit register.

The most basic form of register is known as a 'shift register'; it has a clock input, a data input and a data output, with logic 0's and 1's being 'shifted' through the flip-flops of the register, one position per clock pulse, from the input to the output pin. A basic 4 bit shift register based on D-type

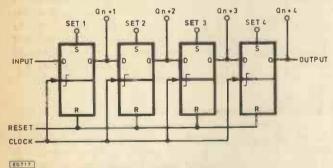


Fig. 5.1. Basic 4 bit shift register

flip-flops is shown in Fig. 5.1. Whatever the logic state of the input, after one clock pulse point Qn+1 will be at that state, then after 2 clock pulses Qn+2 will be at that state, after 3 clock pulses Qn+3 will be at that state, then after 4 clock pulses the output (Qn+4) will be at that state; the logic state of the input is being 'shifted' along the register. Hence, whatever the state of the input, the output will go to that state 4 clock pulses later. By changing the logic state of the input prior to each clock pulse, the 'contents' of the register can be adjusted at will. Note that the flip-flops must be edge triggered, otherwise input changes would be transferred directly to the output whenever the clock was at logic 1.

Fig. 5.1 represents a 'Serial In-Serial Out' device; 'data' is represented by a series of logic changes at the input, one per clock pulse and emerges as a series of logic changes at the output 4 clock cycles later. This format of register is abbreviated to SISO. It may be more convenient to us,

however, to 'fill up' the register as required with 1's and 0's, then to look at the points Qn+1, Qn+2, Qn+3, and Qn+4 simultaneously. These give us a parallel set of data, because although the 1's and 0's were loaded into the register one at a time, they can be fed out of the register simultaneously. This is now a Serial In-Parallel Out register, or a SIPO.

Taking the concept even further, it is possible to use the 'set' inputs of the flip-flops to 'preset' a certain number of 1's and 0's into the register simultaneously; applying clock pulses would then shift the 1's and 0's out of the register output pin one by one. It doesn't require a lot of imagination to see that this is a PISO: Parallel In-Serial Out. The parallel outputs Qn+1, Qn+2, Qn+3 and Qn+4 can also be used in this instance, of course, in which case the device acts as a 4 bit or 'Quad' latch. For consistency we can refer to this as a PIPO register: Parallel In-Parallel Out.

Other far more complex registers are available which are often used in calculator and computer circuitry. For example, the First In-Last Out register, in which the first bit coming out of the register is the last bit that went in and vice-versa. The more usual type of course is a 'FIFO': first in, first out. Many shift registers are of considerable length (i.e. many bits); up to 1024 is typical of the larger types. (These, for obvious reasons, are only available in SISO form.) More generally available types such as the CMOS 4006 have several stages of shift register in them, mostly SISO but with several parallel 'tappings'. Mention should also be made of the CMOS 4035. This is a 4 bit serial or parallel in, serial or parallel out device of great versatility and low cost, and can perform many complex functions. One of its most notable features is that the first stage is a JK flip-flop, with the K input inverted. Hence, all the input functions of a JK flip-flop can be utilised, or the J and K inputs can be connected together and it will behave as a normal D-type device.

COMPLEX COUNTERS

Last month we looked at basic counters, both decade and binary. There are, of course, much more advanced types of counter, with many extra facilities and functions. Many have decoding logic provided to count in special codes; often these are used to count and directly drive numerical displays such as the '7-segment' l.e.d. displays used in the Disco Hat project last month, or 'liquid crystal' displays. (We'll look at the 7-segment code next month.) Others have built-in latches, oscillators (less the capacitors and resistors) or even two independent counters in the same i.c. package. The 4018 is a presettable divide-by-N counter, and is in essence a cross between a shift register and a counter. It is a five stage counter, with counting stages that can be preset by parallel logic inputs, often known as 'JAM' inputs. The first counter stage has a 'data' input, which can be fed from any

of the outputs, via combinational gates if necessary, to set the values of 'N'. By this means, the output counts up to 'N', then starts again from zero as a continuous process, with no 'glitches' or transients that might be produced when using a simple self-resetting circuit.

Finally, probably the most useful counter in the CMOS range is the 4029; a presettable 4 stage up/down counter. Presetting occurs in parallel, in a similar way to the 4018 mentioned above, and from this preset value the 4029 can count upwards or downwards, and in one of two possible codes, binary or BCD (binary coded decimal), which we shall be looking at later in the series. Two of these counters are used in the project this month, so a greater understanding of the workings of this i.c. will be possible now. There are, of course, even more complex counters than these; a frequency counter instrument, for example, is often based on a single i.c. for the majority of its functions, but these are far more specialised in use and are usually fairly expensive when compared with the normal 4000 series of CMOS i.c.'s and most TTL series.

SYNCHRONOUS/ASYNCHRONOUS OPERATION

The term 'synchronous' means that changes in a device or circuit occur at the same time as the clock pulse. Asynchronous is the opposite of this; changes in the circuit can occur at any time, not necessarily related to the state of the clock. In the case of the 4029 up/down counter, for example, the preset 'JAM' inputs can be fed into the counter asynchronously, it doesn't matter what state the clock is in — the moment that the 'preset enable' pin is taken to logic 1, the counter outputs immediately change to these new preset states.

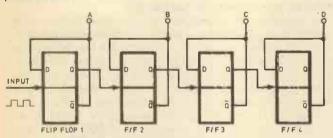


Fig. 5.2. Cascaded divide by two circuit

To see how this affects the design and the use of counters, let us look at the basic binary counter, as discussed last month. In Fig. 5.2, when the clock input goes to logic 1 it causes flip-flop 1 to change state. The change of state of this flip-flop then clocks flip-flop 2. Due to propagation delays, there is a finite time interval between the counter clock input changing, and the clock input of flip-flop 2 changing. When the state of the whole counter is such that the 'D' output state changes, this change takes place a considerable time after the counter clock input edge occurs: flip-flop 1 changes, which causes flip-flop 2 to change, which in turn

causes flip-flop 3 to change, finally causing flip-flop 4 to change. These changes seem to 'ripple' through the counter and since output 'D' changes some time after 'A' changes and hence is not synchronous with the clock, this counter is known as a ripple or asynchronous counter. Although the delay is very short (normally less than 1µs) it does result in invalid information being present at the counter output for a short while, which potentially could be latched into registers or other devices and result in serious mis-operation of the system. We'll look in greater depth at this type of error a little later on.

To avoid this ripple effect it is necessary to change the state of all flip-flops simultaneously rather than in sequence. Fig. 5.3 shows a simple synchronous counter. You will remember that if the J and K inputs of a flip-flop are both held at logic 0, no change in the output state occurs when the clock goes to logic 1. If J and K are both at logic 1, the outputs reverse state when the clock goes to logic 1. Hence, flip-flop 1 always reverses state; it is a simple divide-by-two circuit. Assuming that the other outputs start from an initial zero condition, then if the state of output A is 1 before a clock pulse occurs, flip-flop 2 will change state as soon as the clock pulse occurs, in turn changing output B. When both A and B are 1 before a clock pulse, i.e. binary count 0011, then as soon as the clock pulse occurs, flip-flop 3 changes state, so output C goes to logic 1; but, A and B also both change state (they go back to logic 0) so the count becomes 0100, and so on.

The extra complexity and greater cost of synchronous counters is not always needed although both asynchronous and synchronous devices are readily available in both CMOS and TTL families. Practically the only operational disadvantage of synchronous circuits is that they tend to be less tolerant of slowly changing or noisy clock inputs. In a ripple counter all flip-flop clocks are derived successively from the previous flip-flop, and this relatively slow propagation of changes ensures that the correct logic changes all occur in the correct order, whereas in the synchronous counter a very slow or noisy clock edge may cause some flip-flops in the device to be clocked before others, (due to manufacturing tolerances in the CMOS threshold points) and this could lead to incorrect operation. Hence, it is important to ensure fast clean clock edges when driving synchronous circuitry.

CASCADING COUNTERS

The use of synchronous or asynchronous circuit designs should also be considered when cascading counters. 'Cascading' is the term used to describe the joining together of two or more counters to make a much larger counter in total. This can sometimes be as simple as taking the last output of one counter and feeding it into the clock input of another counter, but is considerably complicated when up/down counters are involved. Often, these i.c.'s have 'carry out' and 'carry in' pins, to permit easy cascading.

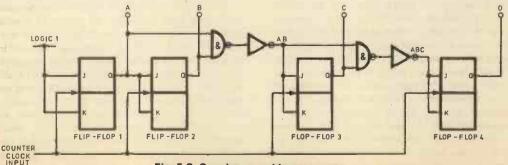


Fig. 5.3. Synchronous binary counter

Once again, data sheets and books should be consulted for detailed applications of counters including asynchronous and synchronous cascading where appropriate: there are far too many different possibilities and circuits to go into here. The discussion of synchronous versus asynchronous working, and the potential errors introduced into logic systems, leads us naturally on to look at the whole aspect of errors in sequential logic circuitry, these errors being collectively known as 'hazards'.

HAZARDS

Due to manufacturing variations and tolerances and to physical limits on the speed of propagation of electric signals, various delays and timing differences are always present in logic circuitry. Outputs of gates change a finite time after the input has changed; counter or latch outputs do not all change simultaneously, etc. We can classify the effects and types of errors in circuit operation due to these timing differences. A 'hazard' is an incorrect or invalid state of a logic output and a 'race' is the time difference between the changes of various outputs within the circuitry; hence, some races cause hazards.

Transient Hazards' are caused by, and occur for the duration of, the delay between the input of a circuit changing and its output changing. The error in the circuit operation is only present during this propagation delay period, and hence it is a short duration, or transient effect. However, if this transient hazard results in a permanent error in the following circuitry's state (a flip-flop is reset, for example, when it should not have been) then the error is termed a 'Static Hazard'. Timing differences between supposedly simultaneously changing outputs of a circuit are known as 'races'; the output changes of a ripple counter are a good example of a race, because, as already discussed, output changes do not occur simultaneously, but in succession. If a race gives rise to a permanent error (i.e. a static hazard) in the state of the following circuitry, then it is known as a 'Critical Race', Any other types of hazard all fall under the overall category heading of 'Race Hazards'.

There are forms of mathematics which can be used to describe and manipulate the design of sequential circuits, using similar (but more complex) types of 'maps' to those used in 'Boolean Algebra' as discussed in Part 1 of this series. Unfortunately, they are very complicated indeed and far too involved for us to look at even briefly. These mathematical techniques have procedures for removing all the above mentioned hazards or at worse reducing them to transient effects only, but to avoid going deeply into the mathematics we shall have to be content with a rather more intuitive, common sense approach to the problem.

TYPICAL PROBLEMS

Hazards are typically caused by trying to have too many sequential circuit elements fed from the same single logic change. A frequently seen design error is to have the same control used to reset a flip-flop (or counter) as is used to latch the output of that flip-flop (or counter); in this case, correct operation of the circuit is obviously dependent on the latch being operated before the device is reset, not the other way round. Similar problems can be encountered with up/down counters; see Fig. 5.4. If the up/down control is at logic 1 when the clock input goes to 1, the device counts up; if the up/down is at logic 0 when the clock input goes to 1, it counts down. If the same pulse is used for both the up/down control and the clock, then depending on which delays in the i.c. are greater, the counter could count down as shown in Fig. 5.4a or up as in Fig. 5.4b. In practice it is usually found

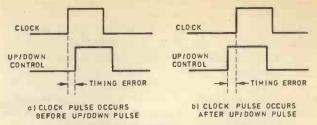


Fig. 5.4. Race hazards in an up/down counter

that the circuit will sometimes count up and sometimes down. The effects are unpredictable. Note also that these delays (and hence the timing errors) can be both internal to the i.c. and external to it in the control logic and other surrounding circuitry.

Another often seen design error is to have the input to a flip-flop or other sequential i.c., dependent on the output of the same i.c., often via an indirect path of combinational gates. In some instances this is allowable; self re-setting of counters, for example, and divide-by-two circuits as discussed last month. However, it is frequently the cause of race hazards, and should be avoided in most cases. Finally, be aware that some counters, shift registers, etc., give out short transients on their outputs when in the process of being reset. Although these are only transient hazards, they could become static if the following circuitry is not carefully designed to allow for this effect. This is not usually a problem, as most devices do not exhibit this fault, but it is worth bearing in mind. TTL seems worse than CMOS in this respect.

THE ELIMINATION OF HAZARDS

Generally speaking, the best way to design out hazards is to ensure that only one change in a logic signal path happens at a time, or, if several supposedly simultaneous and related changes occur, to arrange that the following circuitry is not dependent on receiving all its inputs at exactly the same time. Often, extra gates or sequential devices have to be added to isolate or avoid potential hazard situations.

There are several techniques employed to space apart 'events', or critical logic changes, in time. Fixed time delays can be added to the relevant parts of the circuitry using monostable timer i.c.s or simple resistor/capacitor networks as already discussed in this series. The time delays necessary are usually very short indeed, so small values of capacitor and large values of resistor can be used (usually a few hundred picofarads and a few hundred kilohms) which places negligible loading on the outputs of CMOS logic, causing no 'glitch' or transient problems. In fairly simple circuits, sufficient time delay can be added by passing the offending logic signal through a few spare gates in series, acting as inverters or buffers as required. The propagation delay of most CMOS gates is between 20 and 100ns and this is often sufficient to compensate for a slight timing error elsewhere. The addition of gates in the signal path is the technique usually employed within i.c.s to correct for time delay errors and races, and remove potential hazards. In fact, the only way in which some flip-flops, counters, and shift registers can actually work at all is by utilising propagation delays. In the basic binary counter shown in Fig. 5.2, the circuit action is based on there being a finite propagation delay between each flip-flop's D input state being clocked in, and that flip-flop's Q and Q outputs changing. If this delay did not occur, the Q output could never be used to feed back to the D input; the device would just sit in an indeterminate state. The synchronous counter, Fig. 5.3, is similarly dependent on propagation delays for its correct operation.

It may be possible to avoid hazards by using both edges of a pulse, instead of just one edge, often necessitating the inversion of the edge of the incorrect polarity. Hence, one event may be caused to occur by the 0 to 1 transition, and the other event by the 1 to 0 transition, spacing them apart by the width of the clock pulse, in a similar way to the two pulses derived in the "PE Pulser" described earlier in the series. Beware, though, that it is often bad practice to use edges for implementing several functions simultaneously, because (as mentioned in the case of synchronous counters) different logic inputs can trigger at different points on the waveform edge. In these instances it is wise to keep the edge as fast and 'clean' (i.e. noise free) as possible.

If a number of events must be made to happen in a fixed sequence, it may be necessary to add some form of sequencer to control them. For example, a shift register or decimal counter could be used, with each parallel output connected to the relevant circuit to be controlled. Finally, one should remember that most transient hazards are only a few nanoseconds long, which is often too short to trigger logic

RANDOM PULSE GENERATION

We've been concerned until now with the removal of noise, transients, and other spurious effects from our logic circuitry. There are, however, situations which actually require this type of logic change: random number generators! These are often used in statistical work, for testing purposes, or more usually in games and gambling machines!

In its simplest form, the generator can be a very fast running oscillator (10 or 100kHz typically) gated on and off by a pushbutton switch, and feeding a counter, as shown in Fig. 5.5. The frequency should be selected such that many com-

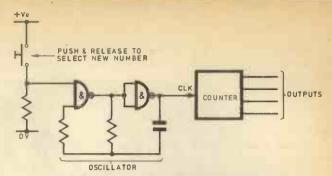


Fig. 5.5. Simple random number generator

plete counter cycles occur in 0.1 seconds. By this means, there is no way in which the person pressing the button can pre-judge the value of the counter output. However, if a second oscillator is being used to gate the first on and off, patterns of output numbers will be seen to occur after a time. A 'pseudo-random' number sequence can be generated by feeding the counter from a clocked shift register with various combinations of gates taking parallel outputs of the register and feeding them back, suitably combined, into the serial input. This technique will still cause repeats of patterns, although it can take many thousands of counts before any repetitions are obvious. Finally, a 'pure' noise source can be used, such as the amplified noise generated by the reverse biasing of a transistor junction.

Our project this month is Space Evaders on page 22.

NEXT MONTH

Next month we cover Numerical Systems — the various codes and arithmetic used in digital electronics, and the driving of numerical displays.



Cobbies Ltd. London. 01-699-2282 Microdigital Ltd.

Liverpool. 051-227-2535 Electronico CG Ltd Monchester, 061-788-0656 DUTTON LANE EASTLEIGH SO5 4AA TEL(0703)610944/5

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London, 01-402-8137

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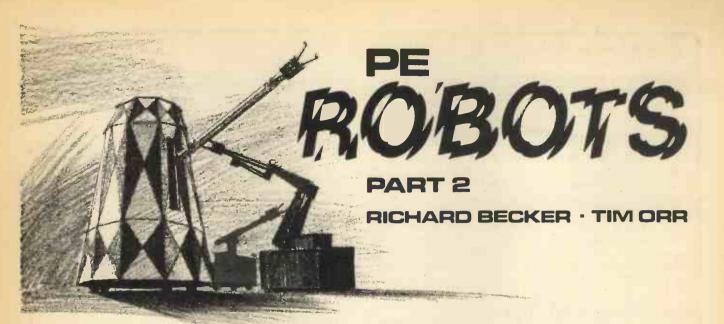
Interface Components Ltd. Amersham, 02403-22307

New Bear Computing Store Ltd., Mewbury, 0635-30505

Spectron Electronics (Monchester) Ltd., Solford, 061-834-4583

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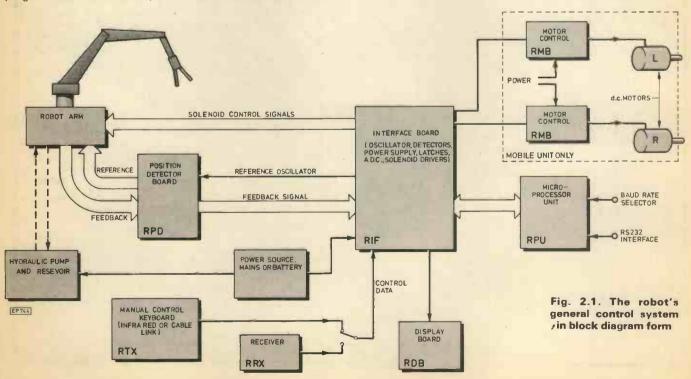


ALTHOUGH the Genesis series of hydraulically powered robots can be controlled by a simple bank of pushbutton switches energising solenoid operated valves, it is necessary to use an electronic control system with feedback to take full advantage of the power and the versatility of these machines. Feedback of positional information is a feature included in these machines, which, although essential for reliable repeatability, is absent in the majority of all other low cost machines, severely restricting their use as emulators of the mainstream industrial robots. The block diagram of the microprocessor based control system is shown in Fig. 2.1.

MICROPROCESSOR UNIT

The heart of the system is the microprocessor unit which performs the task of overseeing the complete system and issuing control signals in response to arm positional data, programmed-in commands, commands from the control box

or information from an external computer. With its CMOS memory it stores the positions of the arm and by using servo techniques will instruct the robot to repeat the sequence of tasks programmed in. Each of the arm and joint positions is defined by an 8 bit word giving a resolution of 1 part in 256 (0·4%). Data is transferred to and from the interface board by 8 bits in parallel whilst data is received from the control box serially, permitting the use of a simple infra-red link to be used for controlling the Genesis M101 mobile machine. In the interests of standardisation, interfacing with an external computer is via an RS232C serial interface with a range of baud rates between 300 and 9600. Although this facility is included, an external computer is by no means necessary as the system's own processor and operating system provides all the functions normally required.



COMPONENTS . . .

INTERFACE BOARD

R	esistors	
	R1	47
	R2	1k
	R3, R4, R7, R9, R12,	10k (5 off)
	R5, R8, R10, R11, R21	100k (5 off)
	R6	110k
	R13	15k
	R14	100
	R15, R18	220 (2 off)
	R16	2Ω2
	R17	2k2
	R19	2k7
	R20	1k2
	R22	10
	R23, R24	4k7 (2 off)
	R25	4Ω7
	R26	330
	R27	1Ω5 2 Watt
	R28	18k
	R29	47k
	R30 A-E	10k 1% (5 off)
	R31 A-E	20k 1% (5 off)
	R32, R33 A-E	100k 1% (10 off
	R34 A-E	68k (5 off)
	R35, R36 A-E	47k (10 off)
	R37 A-E	24k (5 off)
	R38, R39, R42,	
	,R43 A-E	100k (20 off)
	R40 A-E	390k (5 off)
	R41 A-E	150k (5 off)

Integrated Circuits

1C1	74LS123
IC2	74LS132
IC3	74LS00
IC4	74LS175
IC5, IC7 a-e	LM324N (6 off)
1C6	LM383
IC8	ICL 7660 CPA
IC9, IC10	7805 (1 Amp TO220) (2 off)
IC11-14	ULN2003 (4 off)
IC15-19	74LS373 (5 off)
IC20	ADC 0804
IC21	4051 B

N	liscellaneous
	Printed circuit board RIFb
	S1 SPST switch
	Link-through 0-84mm dia. (approx. 200 off)
	TV5 heatsink
	1-25 QY heatsink (drilled for two TO220)
	TB1 4-way screw terminal
	PL2-PL6, PL8 10-way Molex p.c. pins (6 off)
	PL7, PL9, PL10 5-way Molex p.c. pins (3 off)
	SIP1, SIP2 SIP 1k networks (2 off)
	SIP3 SIP 4k7 network
	20-pin i.c. sockets (6 off)
	16-pin i.c. sockets (3 off)
	14-pin i.c. sockets (8 off)
	8-pin i.c. socket
	20mm p.c. mounting fuse holder
	20mm 1-5A fuse

Potentiometers

VR1 A-E	470k preset (5 off)
VR2 A-E	100k preset (5 off)

All resistors 1W 5% unless otherwise stated

22k (5 off) 1 2W

15n (2 off)

Capacitors C1, C2

R44 A-E R45

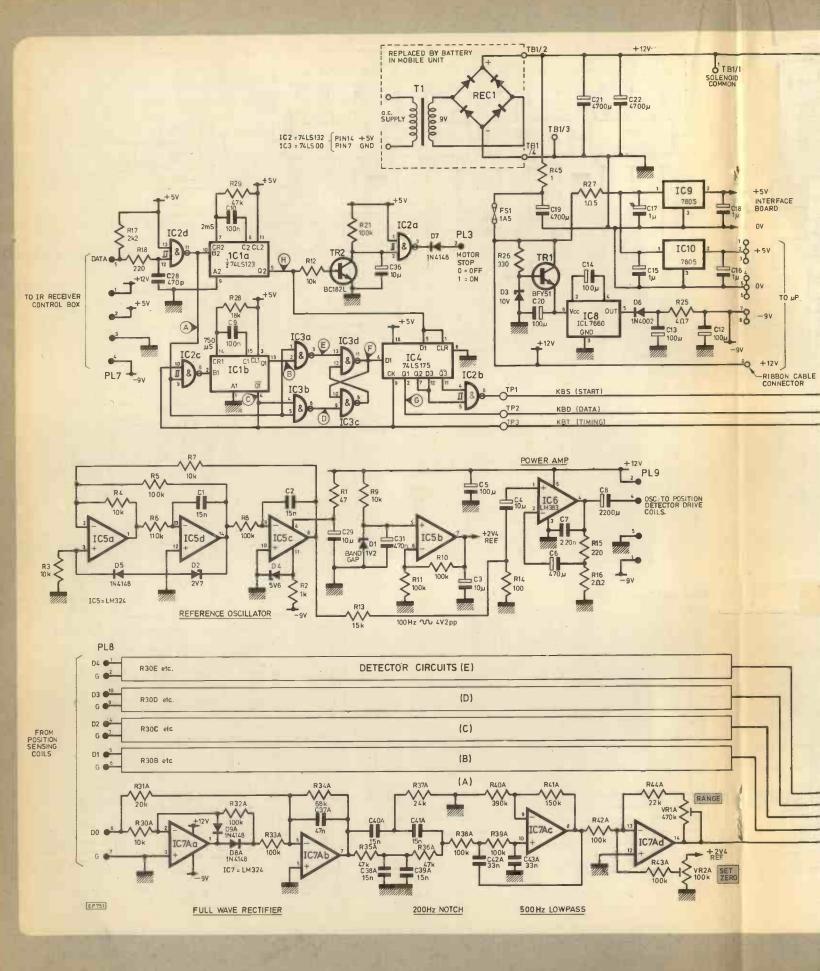
C3, C4, C11, C29, C30,	
C36	10μ/16V tant. (6 off)
C6	470μ/10V elect.
C7	220n Siemens B32560
C8	2200µ/10V elect.
C9	100n
C10	100n
C5, C12-14, C20	100µ/16V elect. (5 off)
C15-18	1μ/16V tant. (4 off)
C19, C21, C22	4700µ/16V elect. (3 off)
C23-27	47n/50V disc cer. (5 off)
C28	470p Piher ceramic
C31	470n/35V
C32-35	680p Piher ceramic (4 off)
C37 A-E	47n B32560 (5 off)
C38-41 A-E	15μ B32560 (20 off)
C42, C43 A-E	33n B32560 (10 off)

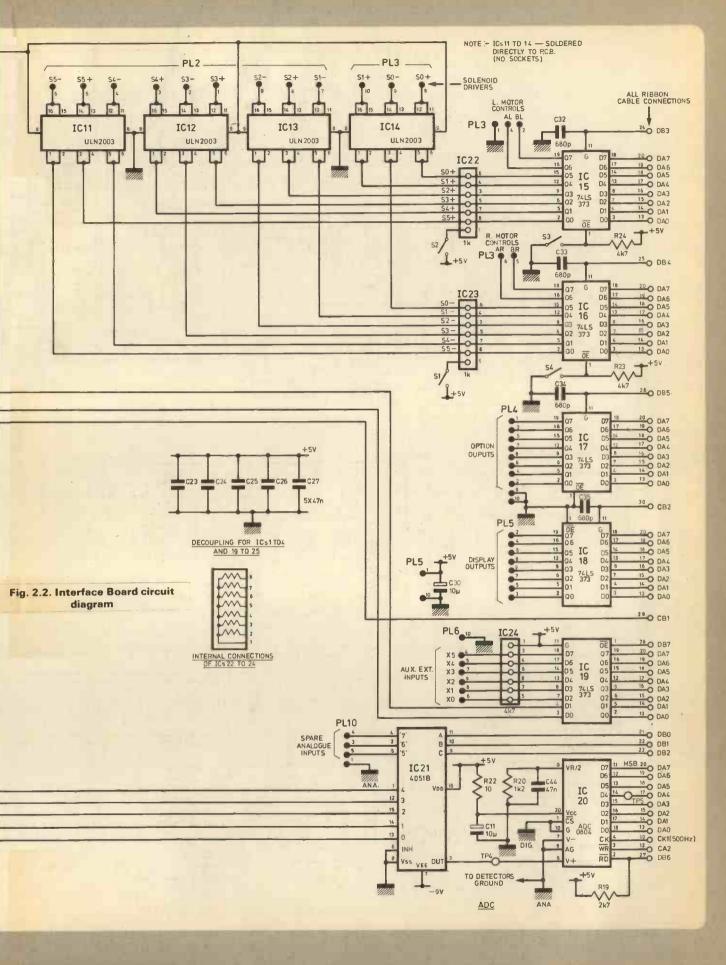
Constructor's Note

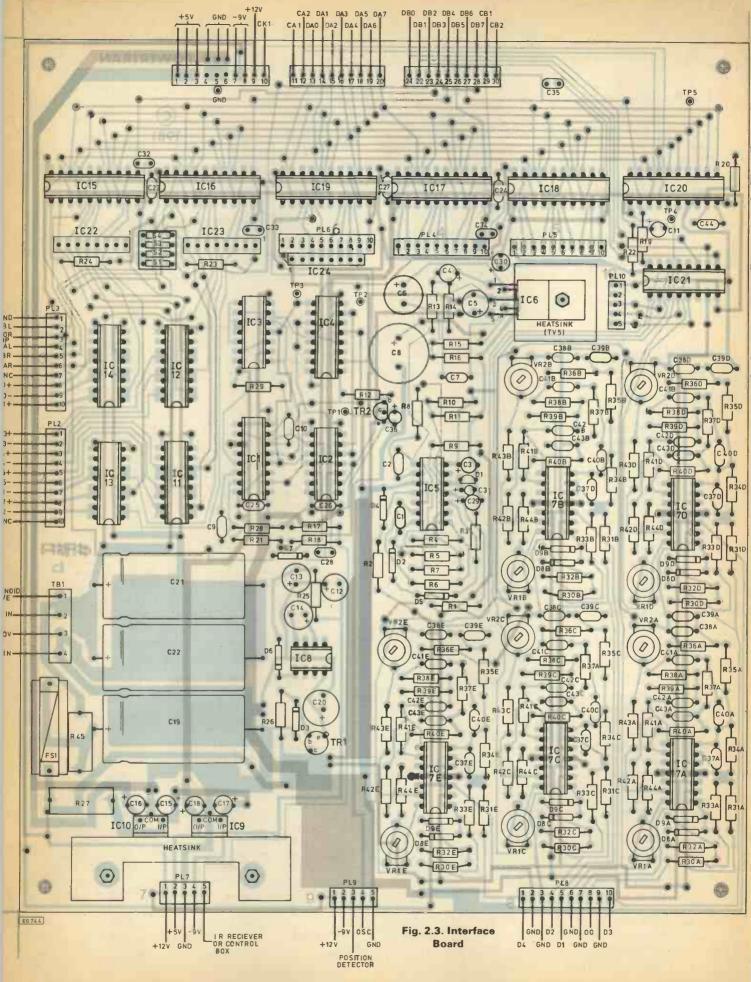
Complete kit of parts for this project can be obtained from Powertran Cybernetics, Portway Industrial Estate, Andover, Hants SP10 3WN. & Andover (0264) 64455.

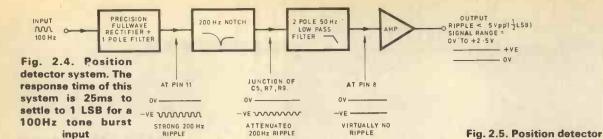
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Prices are as follows	
Genesis M101 4 axis model (excluding wheel base)	£295.00
Genesis M101 5 axis model (excluding wheel base)	£345.00
Genesis M101 wheel base	£79.00
Genesis P101 4 axis model	£450.00
Genesis P101 6 axis model	£545.00
Genesis S101 4 axis model	£355.00
Genesis S101 5 axis model	£405.00
Position detector coil set for M101, S101 4 axis models	£15.00
Position detector coil set for M101, S101 5 axis models	£19.00
Position detector coll set for P101 4 axis model	£15.00
Position detector coil set for P101 6 axis model	£24.00
Position detector board for M101, S101 4, 5 axis model	s £6.50
Position detector board for P101 4, 6 axis models	£7.50
Motor drive board for M101 wheel base	
(2 required per machine)	£11.50
Control electronics for M101 (microprocessor board,	
interface board, display board and mounting	
bracket)	£135.00
Processor box for \$101, P101 (microprocessor board,	
interface board, display board, power supply,	
interface cables, conduit, cabinet)	£175.00
Parts for RS232C interface (fits on microprocessor	
board)	£14.50
Hand held controller box for M101 (includes	
infra red transmitter and rechargeable battery)	£47.00
Hand held controller box for \$101	£33.00
Hand held controller box for P101	£33.50
All prices subject to 15% V.A.T.	

I MITOLOGO IO MITOLOGO	
D1	1V2 Zener BAND GAP
D2	2V7 BZY 88
D3	10V BZY 88
D4	5V6 BZY 88
D5, D7, D8A-E, D9A-E	1N4148 (12 off)
D6	1N4002
TR1	BFY 51 (or 2N699)
TDO	DC1921









INTERFACE BOARD

This board produces the power supply rails for all the electronic units; it also generates a precision reference sinewave for the position detector units. It detects the position feedback data and converts it to a d.c. voltage which is then multiplexed into an ADC. This then delivers its data to the microprocessor. All data into and out of the microprocessor is held in latches on this board. The solenoid valves are driven by Darlington power drivers which are themselves driven by the latched data. This board together with the microprocessor board fits on the rotating platform of the M101, but for the bench mounting S101 and P101 there is a separate cabinet which encloses the pair of them.

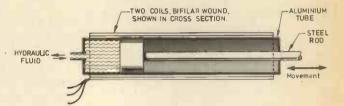
CIRCUIT OPERATION

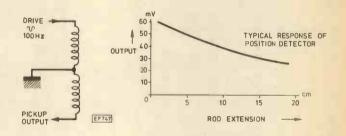
Refer to interface board (Figs. 2.2 & 2.3). The power supply is relatively simple. Most of the electronics runs from +5V which is obtained from two regulators IC13 and IC14. The positive unregulated rail is used to power op. amps. and some other devices, and is nominally +10 to +12V. A negative rail is needed to power the op. amps. and so a d.c. to d.c. converter is used (IC12). This delivers -7 to -9V at about 40mA. This is quite a low current and so it is necessary to use low power op. amps. (LM324 quad devices).

Latches IC19 to IC23 latch data into and out of the microprocessor section. Latches IC19 and IC20 send data to the Darlington drivers which drive the solenoid valves (Fig. 2.6). The solenoids need 600mA of current drive and so two drivers are paralleled to provide enough drive. Because of the high current, i.c. sockets are not used for these drivers. The ULN2003 also has internal protection diodes to prevent damage from back-e.m.f.s from the solenoids. The latches can be turned on and off (tri-state outputs) so that the solenoid drivers can be manually tested, i.e. tested without the microprocessor.

The position detectors use a magnetic coupling principle (Fig. 2.5) which avoids any electrical contacts on moving parts. Two coils are wound on a tube that fits on the outside of the non-ferrous hydraulic cylinder. A sine wave is fed to the drive coil and it is detected on the pickup coil. The size of the pickup signal depends on the magnetic coupling between the coils, which is dependent upon the position of the steel actuator rod (see graph). The pickup signal is attenuated by about 30dB relative to the drive signal and changes by about 6dB at the operating frequency of 100Hz. Fast response in the system is ensured by the carefully designed detector electronics which has a very short settling time.

The reference oscillator is a state variable filter with positive feedback (IC5). A low distortion sinewave (total harmonic distortion of about 1%) is fed into a power amplifier (IC6), which is used to drive the position detector coils. The position detector electronics consist of a precision full-wave rectifer, a notch filter and a lowpass filter, see Fig. 2.4. Half. wave rectification can be seen at the junction of D2/R3. Full-wave rectification occurs by mixing this signal with the original. The smoothed full wave rectified signal has a strong





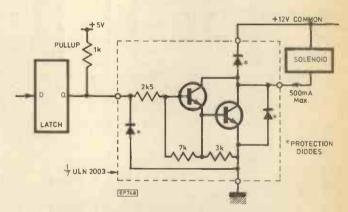


Fig. 2.6. Darlington drivers for solenoids

200Hz component and theoretically no 100Hz component. The 200Hz component is attenuated by the notch filter and it is then further filtered by a 50Hz lowpass filter. The last stage of the detector is a variable gain stage with a variable d.c. offset. The output voltage should be 0V with the actuator extended, and +2·5V with it retracted. There are 5 detector circuits on the interface board, although only 4 are used on the M101 and S101 robots, i.e. one for each movement except the gripper where there is little advantage in knowing how *open* its fingers are. The five detector output voltages are multiplexed (IC25) into the micro-processor controlled ADC (Analog to Digital Converter), IC24. Conversion time is about 20 microseconds. IC1, 2, 3, 4 are used to detect the incoming data from the manual control unit which will be discussed in a future part.

NEXT MONTH: Display and Motor boards, plus mobile wheel base.

Semiconductor UPDATE...

FEATURING

LM1894 ICL7663 Z800

R.W. Coles

NOISES OFF

The trouble with noise reduction systems like Dolby is that audio signals have to be encoded before transmission or recording for the technique to work. The principle employed relies on the pre-emphasis of high frequency sounds before recording, and the de-emphasis of those portions of the audio spectrum during replay. Troublesome noise sources introduced during the recording and replay phase such as tape "hiss" are not boosted during pre-emphasis but are attenuated during de-emphasis with the result that the high frequency noise "floor" is dropped by up to 10dB for the Dolby B system. All this is fine if signals are properly encoded by means of pre-emphasis before reproduction, but it cannot help at all in the case of un-encoded signals such as most radio broadcasts, records, and old tape recordings.

A new technique called Dynamic Noise Reduction (DNR), while probably not replacing Dolby where it can be most effectively employed, in cassette systems for example, offers the major advantage of being completely independent of any sort of preemphasis or encoding and therefore usable with any kind of audio source—even old 78 records! This technique relies on the phenomenon of auditory masking exploited by the use of signal controlled bandwidth reduction in the replay or receiving equipment only.

Taking advantage of the fact that aggregate noise output is proportional to the bandwidth of the system, DNR acts to reduce bandwidth or frequency response when only low audio signal levels are present, while increasing it to the maximum possible during louder passages. While the signal level is high, then no noise reduction takes place in the DNR circuitry, but the human ear can happily take care of these situations unaided thanks to the auditory masking of the lower level high frequency noise components during loud passages.

Electronically, the audio signal is fed through a low-pass filter with a variable cut-off frequency which is "tuned" dynamically by the incoming signal level. To ensure a well balanced response, it is necessary to pass the incoming signal through a high-pass filter with a 6dB per octave slope and a —3dB point at about 6kHz before using this to generate the filter control signal. This ensures that a loud low frequency signal opens up the bandwidth less than a loud high frequency signal.

National Semiconductor have produced one chip that does the whole job, coded LM1894. It contains two variable low-pass filter sections (for stereo applications), a

summing amplifier to combine the two input signal channels, and a detector and voltage to current converter to drive the filters. All you need in addition to the LM1894 and its 14 pin plastic package are a few resistors and capacitors, and maybe a 19kHz filter coil if the circuit must handle stereo broadcast signals.

(At the time of going to press, the LM1894 is not available, as National are considering the introduction of a licensing system, similar to that presently being operated by Dolby.—*Ed.*)

CMOS REGS

Those Intersil whizz-kids who showed us that CMOS wasn't just a logic technology have done it again. Their latest CMOS chip is going to be very useful in very many applications, in fact now that I have seen the spec. I can't help wondering how we managed to get by without it all these years!

On offer are their ICL7663 and 7664 voltage regulators, but these regulators are unique in: a) being made in CMOS, and b) having a standby current consumption of just 4 microamps—maximum! The new chips are ideal for battery powered instruments that need stable supply rails, and they can handle supply currents from only 1 microamp to a respectable 40 milliamps over a voltage range adjustable from 1.6 to 16 volts with an input-output voltage differential of only 50 millivolts.

You can even use these babies as switches to save even more precious juice during your system's idle moments, a simple logic command is all you need to use them as an on/off switch, just like on those fancy l.c.d. calculators which turn themselves off when you ignore them for long enough.

The ICL7663 handles positive voltages and the 7664 negative voltages, and in their 8 pin mini-dip packages they will only set you back a pound or two, a bargain if ever I saw one!

CORPORAL ZILOG?

You may remember me mentioning the "Captain Zilog" comic strip character and his heroic deeds among the index registers of the Z8000 sixteen bit microprocessor. Well the 16 bit race is now old hat, and Zilog seem to have retired their hero on full pay. Not that he was a complete success, because it now seems that the Intel 8086 and the Motorola 68000 are selling rather better than the Z8000, probably because Intel were first and Motorola have (arguably) the better chip.

Another possible reason for the lacklustre sales performance of the Z8000, despite the tremendous success of the eight bit Z80, could be the lack of an "in between" processor to bridge the gap from eight to full sixteen bit performance. Intel have their 8088 which is really an 8086 inside working with a multiplexed eight bit bus outside to keep system costs low, and Motorola have the 6809 which is rather like a souped up 6800 with a lot of internal sixteen bit operations which the earlier 6800 lacked.

Now Zilog are not an outfit to let the grass grow under their feet, and to meet the challenge they are about to introduce a new device which could prove more popular than either the 8088 or the 6809 since it will build directly on their acknowledged success with the Z80. The new chip will be known as the Z800 (surprise surprise!) and it will offer complete code compatibility with the Z80 so that system upgrade will be possible without rewriting software, something which neither of the competitors can claim. Despite its ability to run Z80 programs, the Z80 is a true 16 bit machine internally with a very advanced performance which will allow even old Z80 stuff to run three to five times faster, with even better results available when new programs are written to take full advantage of the 16 bit architecture and new opcodes.

The Z800 will also be available in two basic versions. The first of these uses a non-multiplexed eight bit bus compatible with existing Z80 systems which offers a very simple performance increase to established Z80 users. When users are ready for yet more power, they can switch to the second version which has an 8088 type multiplexed bus to make better use of its sixteen bit architecture. After that gentle rise in capability, Z800 users are expected to find the switch to full Z8000 sixteen bit designs a painless procedure!

All this is interesting of course, but all I am waiting for is the new comic—featuring Corporal Zilog no doubt!

WESTERN SUPPLIER

In last October's Semiconductor Update, one of the featured devices was the WD55 from Western Digital. I mentioned that it was available in this country from Jee Distribution of Hayes, Middlesex. Now I am informed that Jee do not stock the WD55, but it is available from Pronto Electronic Systems of Ilford, who are distributors for Western Digital.

Step-by-step fully illustrated assembly and fitting instructions are included together with circuit descriptions. Highest quality components are used throughout.

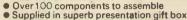
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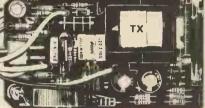
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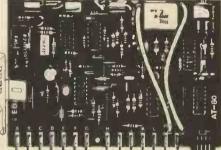
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FRANK W. HYDE

THE SECOND SHUTTLE MISSION

After the first and historic mission of the spacecraft Columbia there was widespread excitement, and relief that the mission had achieved its object. Naturally there was the enthusiastic reaction and speculation that there would be an immediate turnround programme for a second mission. However, after the first inspections and examination of various items of a physical nature, some of the data concerning the aerodynamics, coupled with the new programme for the second and perhaps vital trial, more time has been needed before orders to proceed were forthcoming. By the time you read this, history will have determined the way in which the programme will proceed after the second mission.

The sophistication of the electronics involved in space programmes is extensive and much of the practice is well known to those familiar with the art. Perhaps in some ways many people have become somewhat blasé about the whole business. Yet those engaged directly or indirectly with the space programme are still very conscious of the implications for the future. The public generally have come to regard even Columbia as a secondary matter to pop stars and football. While many of those close to the matter do regard such an attitude as heresy, the fact is that the very attitude is in a way complimentary. The main theme of any activity seems to contain a large element of gladiatorial stimulus. The fact that space activities are commonplace and do not have great disasters, takes from the matter the excitement required. It is significant perhaps that this really is an admission that modern technology is reliable.

The second mission of Columbia, with USAF Colonel Joe H. Engle and Navy Captain Richard H. Truly, will be carrying out an extensive survey of the many unknowns which became evident in the first flight. There were some unusual events noted on the first mission with regard to aerodynamics. The two Astronauts have been preparing certain plan-

ned manoeuvres by using the Singer Link simulator. These relate to some complicated supersonic and hypersonic techniques for reentry plans. The data obtained will provide information about the vehicle's centre of gravity, angles of attack and flight stability and performance required by the differing characteristics of the various payloads. It is clear already that an important point in re-entry is manual control. Both Astronauts will be very busy on this trip for there are 19 groups of manoeuvres to be made. Some of them allow intervals of only 20 seconds between sequences.

On the first mission the vehicle was quite stable. Bank reversals were manually flown at Mach 5 and Mach 2.8 but did not use manual control: apart from that, until reaching an altitude of 35,000 ft no manual control was exercised. One of the reasons for this was that at Mach 5 and Mach 2.8 there were stability questions in those two areas. Mission 2 will operate manual control slightly differently. The Mach 5 and Mach 2.8 manoeuvres will be flown automatically because they were found to be stable in practice. However the initial bank manoeuvre will be initiated somewhere about Mach 24 to 25 with manual control and reach up to 80° bank at an altitude of 260,000 ft. In this area more precise control can be maintained manually than could be achieved by automatic control. It was in fact at this point on Mission 1 that a roll oscillation that exceeded the expected sideslip occurred.

The importance of the simulator in the whole scheme of vehicle control is exemplified in the remarks made by the first of the Shuttle crews. Both Young and Crippen have already made reference to the fact by such remarks which indicated that actual experience of reentry was helped enormously by the time spent with the simulator. It is here perhaps that tribute should be paid to such men as Major Steven R. Nagel of the USAF who specialises in shuttle re-entry issues. During re-entry the incoming data is mainly from dynamic pressure and velocity. Dynamic pressure is most active above velocities of Mach 22 at an altitude about 238,000 ft. Below this the velocity is the more useful parameter from which to initiate the next manoeuvres. Great use is made of 'cue' cards in much the same way that aerobatic teams operate. With the Shuttle the cues relate for example to the dynamic pressure or the velocity at which to initiate action and a minimum level of control system propellant below which any manoeuvre would not be continued. There will be a time at re-entry when communications after the radio blackout period will be kept to a minimum. This is to allow the astronauts maximum concentration at about Mach 10.

THE NEW INDUSTRIALISATION

The success of the first Shuttle mission and the soon to be realised hopes of the second, is already accelerating the vanguard of *new* things and the rapid development of those begun. Robotics has an important part to play here. The success of the USSR in their unmanned automatic missions to embryo space stations gives new impetus to the extension of man's peaceful pursuits in recovering the fruits of the Earth and its environs. The reduction of

certain activities required in any task has been established for more than a century. Now though the mechanical precision is still very much required, it is the control which needs the most attention. There is an opinion among a large group who consider that all things should be done by remote operation. The argument usually is that this reduces mortality. Some reason quite well and plausibly. There is good reason to suspect fear is a motivation for some of this. Perhaps it will be possible to be free from this naïve approach when the forecasting aids such as simulators, which can give real time information leaving practically nothing to chance, are used. Though in some instances continuing communication in real time becomes difficult when extensive journeys from the Earth are undertaken, nevertheless this has been shown with the Voyagers to be surmountable.

In the specific instance Marshall Space Flight Center in Alabama have tasks which are being examined by the Massachusetts Institute of Technology under a one year contract to investigate systems of robotics for working in outer space. When work is required to be done in geostationary orbits or others at high altitudes protection for astronauts would have to be extensive. This is due to the high level of radiation and also the conditions of magnetic fields holding charged particles inimical to the biological machineman. The robotic units would of course be immune. It is to be hoped that the robots of film fame have finally disappeared when these matters are discussed and a great deal of engineering skill will not be squandered in making man in his own image. Indeed one robotic unit with which the writer is associated puts all three of the parameters (seeing, feeling and hearing) into one box manipulator. It is perhaps pertinent to point out here that there is always a revival of the 'making machines think' approach. It is on some occasions perhaps worth while asking why people approach the subject from the wrong end, that is thinking of emulation instead of analysing the problem in its reality. The designing of Automatics (as the writer would prefer to call them) has been around for a long time and many people do not seem to realise that these form quite a large part of the engineering scene. It is only the advent of some of the new requirements that focuses attention and ideas run riot. Also it has to be remembered that these highly sophisticated units would be in many cases directed at a particular task. If one encountered a space unit not of the kind for which the 'robot' was designed, it would be useless.

This leads to another important point: standardisation. It will be necessary for the builders of new satellites to adopt some common agreement as to configuration. This does not necessarily mean overall but rather the standard units could be those which have a finite lifetime and could have a configuration which was standard including fixing with a homing signal to which it could call the repair unit. This would apply to much of the operating equipment including the replacement of manoeuvring propellants etc.

Whatever may be the progress in this area the final and human way will be to take a look see! Nothing in the foreseeable future will take the adventure from the human temperament.



'HIS month the principles and construction of the peripheral system which operates in conjunction with the Microcontroller will be described.

The Band-Box provides a full trio backing of musicians in a single unit and may be programmed by the vocal or instrumental soloist to give automatic accompaniment for some 50 to 100 tunes dependent on the complexity of each score.

THE MICROCONTROLLER

Fig. 3 shows the track layout and component overlay for the p.c.b. and it is advisable to closely inspect this before proceeding, particularly ensuring that bridges are not present between any of the tracks.

The first operation is to insert the track linking pins from the front of the board. Track pins are easily pushed into the holes and broken off from the stick, using a metal implement e.g. screwdriver, the pins can be pushed home with the thumb and then soldered on top of the board. Note that some of the pins are positioned underneath i.c. sockets so that it is essential they are inserted first.

All i.c.s should be mounted in sockets which can be soldered to the board in the same operation as the resistors. In order to avoid confusion later it should be noted that all i.c. sockets have some indication to fix the position of pin 1 and this should be followed at this stage to ensure that orientation of the i.c.s is not confused at a later date. IC1 has pin 1 to the bottom of the board, IC2 to the right, IC3-7 to the left. Memories IC8-11 have pin 1 to the top, IC12-15 to the bottom, monitor IC16 is to the top whilst the spare EPROM position is to the bottom. At this stage the larger components should be soldered to the board. Some difficulty may be experienced in soldering with a 1mm bit on the larger components and a $\frac{3}{39}$ in bit may be necessary.

A further inspection of all tracks and connections should be made after assembly of the board including meter checks on adjacent tracks to detect shorts, and then the i.c.s can be inserted into the sockets. IC9, 10, 11, 13, 14, and 15 should be omitted until the complete Band-Box has been tested since they are unnecessary until the first five pages of the score store have been used and there is no point in risking damage to them unnecessarily. When any i.c.s are inserted care should be taken to ensure that all pins correctly enter the socket and do not bend under the integrated circuit.

Apart from checking for track shorts and missing solder connections this is the third most common cause of malfunction in high density i.c. based systems.

SYSTEM BOARD

The majority of the electronic hardware, apart from the Microcontroller, is mounted on a single p.c.b. which will be referred to as the system board. A second board contains the displays and a number of keyswitches. The circuitry is best understood if it is split into four parts, music generation, input controls, display and keypads, and the power supply.

MUSIC GENERATION

Before describing the music generation circuitry the method used to generate the audio tones is illustrated in Fig. 4. A set of waveforms is stored in the monitor memory in a similar manner to that shown in Fig. 4(a). In the illustration a complete cycle is divided into sixteen parts and values between 0 and 15 given to approximate the amplitude of the waveform in a stepped manner. In the Band-Box the chord instrument waveforms are split into 64 segments and the bass into 128 segments, both using amplitude values between 0 and 63; however, for diagrammatic simplicity sixteen segments are used in the illustration without change to the principles involved.

In order to produce a sound from the sequence of stored values it is necessary to convert each number into an analog voltage which is achieved using a digital to analog converter (DAC), and provided that an interval of time is allowed to lapse between the moment at which each value is converted by the DAC a stepped waveform will appear at the DAC output. Consequently a method of changing the fundamental frequency of the waveform is apparent by changing the interval of time between samples which if reduced will result in an increase in frequency.

This assumes that each and every stored value requires to be read at varying time intervals, but it is also possible to use a fixed time interval and either skip some values to increase frequency or read some values more than once to reduce frequency. The Band-Box uses this second principle and has a set of increment figures stored in the monitor which accurately correspond to the required amount of movement through the memory, after each fixed time interval, which will produce the required range of fundamental frequencies.

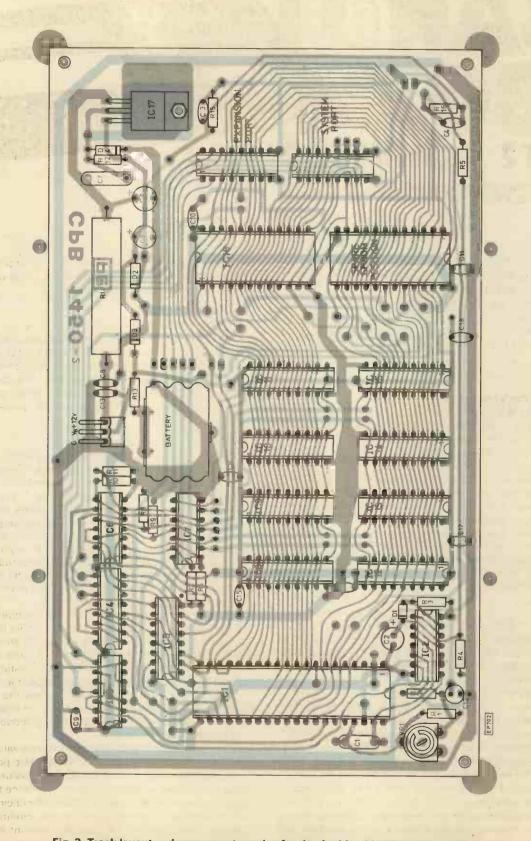


Fig. 3. Track layout and component overlay for the double-side Microcontroller

The effect of different increments is shown in Figs. 4(b-d). Whereas in Fig. 4(a) the X-axis simply represents the memory positions in which the sixteen numbers are stored (0-15), Figs. 4(b-d) are shown in real time at intervals of 0.2ms which roughly corresponds to that used in the Band-Box. The microprocessor is programmed to accumulate one relevant increment per time interval so that for an increment of 0.67 it will count 0, 0.67, 1.33, 2.0 etc., and it looks at the value stored in the memory position given by dropping the following fraction—i.e. position 0, 0, 1, 2 etc. As shown in Fig. 4(b), 24 time intervals are required to complete a cycle using an increment of 0.67, such that the overall period with an 0.2ms interval is 4.8ms, giving a frequency of 208-33Hz. An increment of 1.0 is shown in Fig. 4(c) and since it causes longer jumps through the memory the higher frequency of 312.5Hz results. Fig. 4(d) shows an octave increase in frequency to 416.67Hz due to the corresponding doubling of the increment to 1.33.

Very accurate tuning can be realised using this technique, a factor which has considerable importance in the Band-Box application where the four note combinations used for the chord instrument, which must have high relative accuracy, are produced by programming the microprocessor to separately accumulate increments corresponding to each of the four notes and then add the four waveforms internally to give a composite waveform at the output of the DAC. It will be noticed that the example waveforms shown in Fig. 4 are not of simple sine, square, or triangular variety, but are in fact computed from elements of the fundamental plus overtones at factors of 2, 2.75, 3, 4 and 5.4, a freedom in waveform construction not easily available by conventional synthesizer techniques.

MUSIC FROM THE MEMORY

In Part 1 the score store was described in that it contains numbers corresponding to the next action required of the Band-Box. Most instructions give the chord required plus its duration, and when detected by the micro it converts half the number to represent a particular type of chord the notes of which are tabled in the monitor. To cope with the adjustable playback key and movements in chord group which will have occurred during composition, the micro transposes the notes (frequency) of the chord and then checks the monitor for the increments corresponding to each chord note plus the increments corresponding to four bass notes one of which will be playing at a particular time. The increments are translated into waveforms as described above and thus is music produced.

MUSIC GENERATION CIRCUITS

The complete circuitry for music generation is shown in Fig. 5. The bass waveform first appears at the output of IC22d and is produced by DAC IC20. At intervals of 0.2ms a binary number between 0 and 63 is taken from the data bus into the latches which comprise Output Port 1. The moment at which the number is placed in the latches is determined by the 0.5µs positive chip select pulse CS1 produced by the decoding circuits which operate on the address and write signals generated by the micro. The number remains on the outputs of the port after the chip select pulse is over, and is converted by IC20 to a corresponding analog voltage level.

BASS ENVELOPES

It will be noted that 0 to 63 uses the bottom six lines of the data bus (D0-D5) leaving D6 and D7 spare. D6 is normally at logic zero except when a pulse is required to switch off the Master Rhythm in the auto-stop mode which occurs

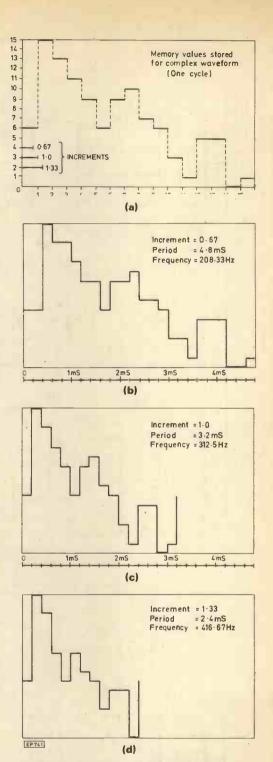
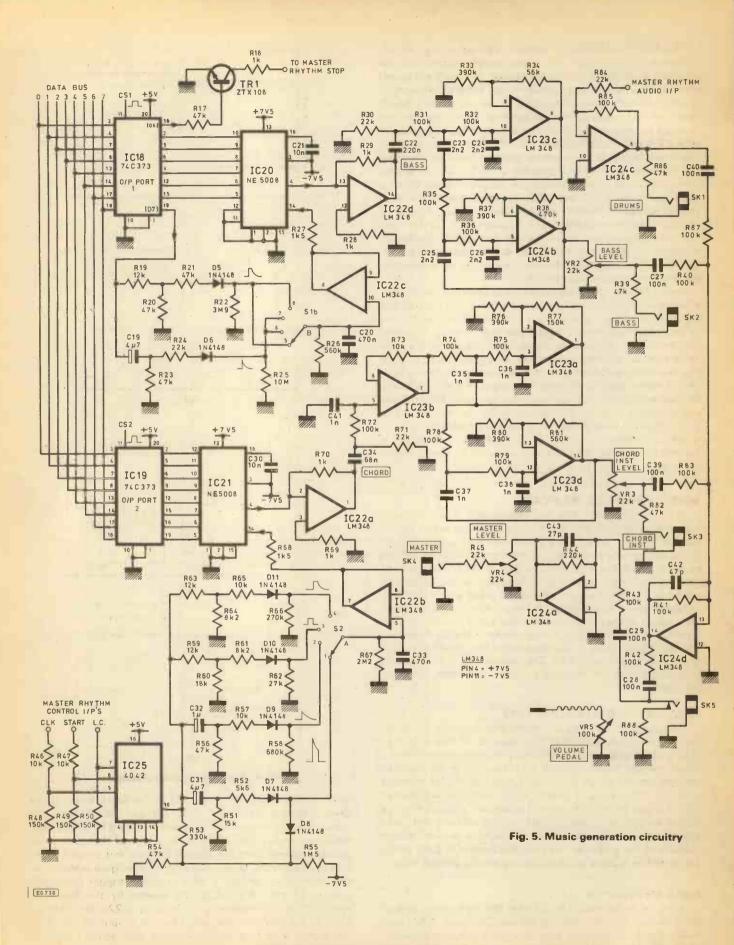


Fig. 4. Music generation principles

after a 'Fine' instruction is found in the score store. D7 is used to provide a positive pulse to trigger the two bass envelope circuits, one of which is selected by S1b at any particular moment. The micro is programmed to count clock pulses received from the Master Rhythm and convert them into musical beats. Depending on the bass figure selected on the appropriate control a bass envelope trigger pulse will appear on pin 19 of IC18, be shaped by the following resistor/capacitor networks, buffered by IC22c, and presented at the DAC control pin 14. The output at pin 4 is modulated from zero by the envelope input at pin 14 and is buffered by IC22d.



LOW PASS FILTERS

The waveform at the output of IC22d contains 0.2ms steps, as shown earlier, corresponding to a frequency of 5kHz. IC23c and IC24b comprise a four pole low pass filter with a 24dB per octave roll off and cut off frequency of 700Hz to remove the steps from the bass waveform.

CHORD INSTRUMENT WAVEFORM

Four notes are summed inside the micro to produce the chord instrument waveform which first appears at the output of IC22a. The operation is similar to the bass generator except that all eight data lines are used to give numbers between 0 and 252 (4×63) at the latched outputs of Output Port 2. The filter comprising IC23b, 23a and 23d is of five pole type with a cut off of 1.6 kHz and roll off of 30dB per octave. The higher quality filter is necessary since the fundamental and harmonic frequency range of the chord instrument more nearly approach the theoretical limit of $\frac{1}{2}$ the sampling frequency which is placed on sample waveform generation techniques.

CHORD INSTRUMENT ENVELOPES

Master Rhythm inputs are shown entering pins 5, 6 and 7 of IC25 which generates the envelope trigger at its output pin 10. When pin 6 is held at zero by an off condition in the Master Rhythm, pin 10 is also held at zero. When the Master Rhythm is playing the "Start" signal rises to +5 volts, and for

each positive clock pulse on pin 5 of IC25 the output takes up the state of pin 7 at that moment, latching the state until the next clock pulse occurs. Pin 7 is connected to the Master Rhythm control line which previously operated the long cymbal circuit and now becomes the programmable rhythmic pattern for the chord instrument. Due to the latching action of IC25 a continuous program of play pulses from the Master Rhythm will result in an uninterrupted high output at pin 10 which can give a sustained organ type output for example. However, the envelope trigger will drop when a programmed rest appears at pin 7 giving a large number of possible rhythmic permutations which may be stored in the Master Rhythm.

Four chord instrument envelope shapers are present on the board. In position one a percussive attack is followed by a damper action when pin 10 of IC25 drops to zero, whilst in position two the damper circuit is omitted. Positions three and four give sustained envelopes with different rates of attack and decay.

MIXER AND PREAMPS

IC24c amplifies the output from the Master Rhythm before entering the mixer IC24d together with the bass and chord instruments. A volume pedal fits between IC24d and IC24a which is the final preamplifier giving a mixed signal in phase with the individual instrument outputs.

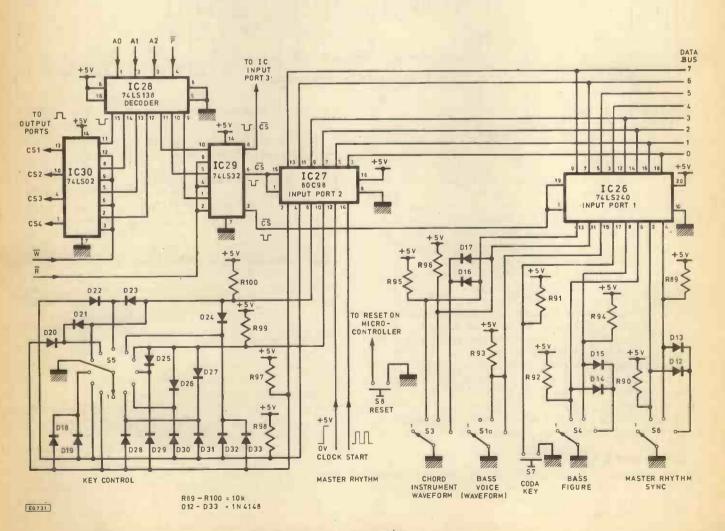


Fig. 6. Input control circuits for Band-box

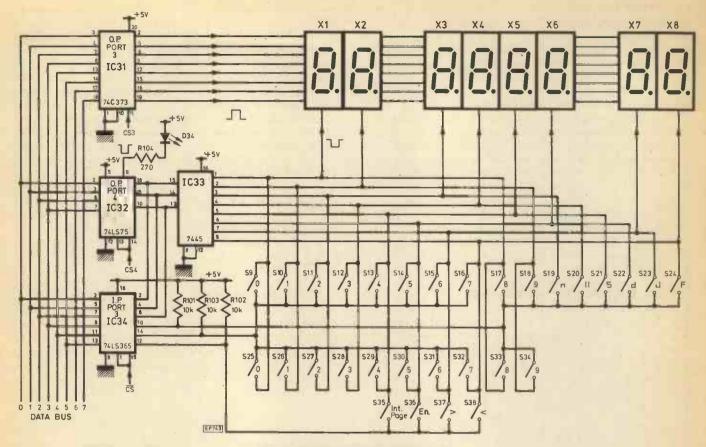


Fig. 7. Display and keyswitch circuits

INPUT CONTROLS

The input controls cover the multi-position rotary switches and the full circuitry is given in Fig. 6. As shown last month a four position switch can be represented by numbers 0,1,2,3. Referring to S6 in Fig. 6, resistors R89 and R90 hold the two inputs to IC26 at +5 volts when the switch is in position 1. IC26 is an inverter used as an input port so that when it receives an 0.5µs chip select pulse from the decoding circuits it puts the complement of the binary number seen at its inputs onto the data bus. Thus in positions 1,2,3 and 4 the binary numbers 00,01,10 and 11 are put onto bits D1 and D0 of the data bus respectively.

Considering S4 it will place similar binary numbers onto bits D3 and D2, which in decimal terms are equivalent to 0, 4,8 and 12. The remaining switches on Input Port 1 act in a similar manner on data lines D4–D7 equivalent to higher decimal numbers and it can be seen that a single number between 0 and 255 can represent the sum of a unique combination of positions for all switches on the Port.

The chip select pins 1 and 19 on IC26 are normally at +5 volts but the micro is programmed by the monitor to pulse them to ground for 0.5µs every 0.2ms to pass the number corresponding to the switches onto the data bus. The Band-Box program in the monitor determines what procedures to carry out based on the switch position combination.

INPUT PORT 2

This port passes the information relating to the playback key onto the data bus in the same manner as above and it will be noticed that data lines D4 and D5 are not used. D6 and D7 enable the micro to look at the Master Rhythm. Pin 12 rises to +5 volts when the Master Rhythm is running which after inversion sends a zero to the Microcontroller.

The Master Rhythm clock pulse is being checked every 0.2ms by the input port and the micro is programmed to detect each rising edge through D7 and use this for the purpose of timing chord duration and bass trigger.

CHIP SELECT PULSES

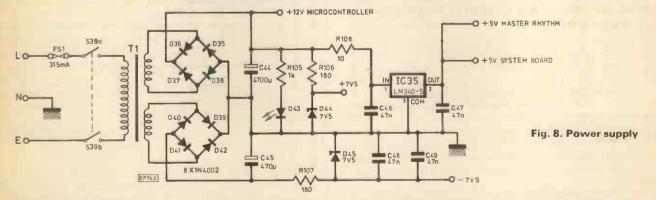
A total of seven ports are involved in the Band-Box system, three are inputs to and four are outputs from the Microcontroller and each is selected at the appropriate moment by a chip select pulse generated by IC28–30. A combination of A0, A1, A2 and \vec{P} decides which port is required using the decoder IC28, and the 0.5µs period of selection is determined by \vec{R} or \vec{W} gated with the negative outputs of IC28. OR gates in IC29 and NOR gates in IC30 ensure that input ports receive negative chip select pulses and output ports receive positive chip select pulses.

DISPLAY AND KEYING

A common circuit handles both the eight segment l.e.d. displays and the two sets of input keyswitches. This part of the system is inoperative whilst music is being generated but is used for playback score selection and composition. The system is multiplexed which means that the eight displays are each scanned in turn for approximately 1ms and the micro puts the information required for a particular display into the output port latches of IC31 for the period concerned. The monitor contains a set of codes which are converted to messages on the displays (Fig. 7).

The Output Port 4 comprising IC32 has three input lines to produce a sequence of eight numbers 0 to 7 which are decoded from their binary form by IC33, the outputs of which fall to zero as selected. The display segments which require to be lit at a particular moment receive a positive

COMPONENTS		Capacitors		Semiconductors			
CUIVIPUI	ACIAIS .	* *		C19	4μ7 35V elect.	D5-33	1N4148
				C20	0.47μ	D34	l.e.d.
				C21	10n ceramic	D35-42	1N4002
SYSTEM	BOARD/D	ISPLAY BOAR	D	C22	0.22μ polyester	D43	l.e.d.
				C23-26	2n2 ± 2%	D44-45	7V5 Zener 400mW
				C27-29	0.1 µ polyester	TR1	ZTX108
Mariana.				C30	10n ceramic	IC18-19	74C373
Resistors	740			C31	4µ7 35V elect.	IC20-21	NE5008
R17	47k	R59	12k	C32	1µ0 63V elect.	IC22-24	LM348
R18	1kO	R60	18k	C33	0.47µ	IC25	4042
R19	12k	R61	8k2	C34	68n polyester	IC26	74LS240
R20-21	47k	R62	27k	C35-38	1n0 ± 2%	1C27	80098
R22	3M9	R63	12k	C39-40	0.1μ polyester	IC28	74LS138
R23	47k	R64	8k2	C41	1n0 ± 2%	IC29	74LS32
R24	22k	R65	10k	C42	47p ceramic	IC30	74LS02
R25	10M	R66	270k	C43	27p ceramic	IC31	74C373
R26	560k	R67	2M2	C44	4k7µ 16V elect.	IC32	74LS75
R27	1k5	R68	1k5	C45	470µ 16V elect.	IC33	7445
R28-29	1kO	R69-70	1k0	C46-49	47n ceramic	IC34	74LS365
R30	22k	R71	22k	Miscella		IC35	LM340-5
R31-32	100k*	R72	100k*	VR24		clog	
R33	390k*	R73	10k	VR24 VR5		Ok log	
R34	56k*	R74-75	100k*	VH5 S1			
R35-36	100k*	R76	390k*	\$2-4		ole 4 way rotar	
R37	390k*	R77	150k*			ole 4 way rotar	
R38	470k*	R78-79	100k*	S5		ole 12 way rota	
R39	47k	R80	390k*	\$6		ole 4 way rotar kevswitches	У
R4043	100k	R81	560k*	S7-38			
R44	220k	R82	47k	000		6 key caps	
R45	22k	R83	100k	\$39		ins 2 pole ins transformer	
R46-47	10k	R84	22k	T1		9V, 20VA	
R48-50	150k	R85	100k	FS1		5mA slow blow	
R51	15k	R86	47k			DL-500	
R52	5k6	R87-88	100k	Display			n la m ol
R53	330k	R89-103	10k	SK1-4		no jack unswite	
R54	47k	R104	270	SK5		no jack switche	eu
R55	1M5	R105	1kO		D.I.L. sockets 2		
R56	47k	R106-107	180		D.I.L. SOUNCE		
R57	10k	R108	10,7W	Track p		9	
R58	680k				D.I.L. sockets 5		
All resistors	0.25W 5% carb	on film except * which	ch are 2%				
VII 163131013	0 2011 0/0 Card	TOTAL THE CHOOPS		20 pin	D.I.L. sockets 4		



voltage from IC31 and the current in the segments is selflimited by the voltage drop across this integrated circuit.

IC34 decodes keying activity in parallel with the display operation in that data lines D0, D1 and D2 are connected to the latched outputs of IC32 which indicate which display and key column (0–7) is currently being accessed, and depression of a key in this column will pull the voltage down on either pin 14, 10 or 12 depending on which key group is being used (0–7), (8–F.), or (Int. Page, En, >, or <). Keys 0–9 appear twice in parallel to give operator convenience. The key group selected is transmitted on data lines D3, 4 and 5 to the micro which is programmed to note when a key is pressed and take the necessary action.

Data line D3 on IC32 is controlled by the micro to give a negative pulse on each beat to flash the i.e.d.

POWER SUPPLIES

A mains transformer is mounted in the base of the Band-Box and the remainder of the circuitry as shown in Fig. 8 is integrated into the system board. An unregulated supply of approximately +12 volts feeds the microcontroller and Zener diodes are used to obtain $\pm 7 \lor 5$ for the audio circuits. The remainder of the system operates on +5 volts from the regulator IC35.

Next Month: Construction of the system and display boards together with details of assembly and memory map.



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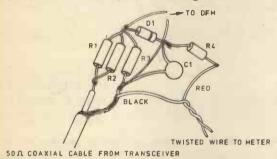
27FM CB PART FOUR

MICHAEL TOOLEY B.A.

DAVID WHITFIELD M.A.M.Sc

CONSTRUCTORS who own or have access to more sophisticated test gear may wish to carry out additional tests on the Ranger in order to confirm that it is operating in accordance with the design specification. The most useful items of equipment are summarised in the list below. It is not, however, expected that all of these items will be to hand and the actual measurements carried out by individual constructors will obviously be limited by the available test gear. For this reason the measurements will not be described in detail and only a brief outline of each will be given.

(i) A digital frequency meter which can provide a direct readout to 30MHz with a resolution of 1kHz or better. Alternatively a low frequency DFM and pre-scaler can be employed. See Fig. 4.1.



EG 200 EG716

Fig. 4.1. Transmitter Frequency adjustment using a DFM

Connect the DFM across the 10Ω resistor of the 50Ω load. This should produce an RF level of approx. 1V r.m.s. Operate the PTT switch on the microphone and adjust L1 to give the exact channel frequency required. Note that the reading on the output indicator will be slightly reduced when the DFM is connected.

(ii) An r.f. power meter with an internal load of 50Ω . Alternatively a combined power/SWR meter may be used together with a separate 50Ω load; the 50Ω load/indicator is quite suitable. See Fig. 4.2.

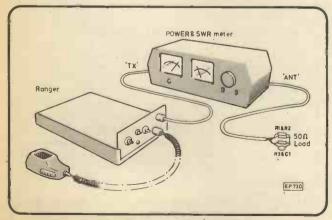


Fig. 4.2. Measurement of r.f. output power

Connect the Power and SWR Meter as shown above. Operate the PTT. Measure the SWR (follow the instructions supplied with the meter) and check that it is less than 1-25:1. Then measure the r.f. power. Note that, with most combined power and SWR meters, the power meter is a voltage sensing device and it is only accurate for a low value of SWR (ie: less than 1-25:1). If necessary make slight adjustments to VC1 and VC2 for maximum power output.

(iii) A stable and accurately calibrated r.f. signal generator with a 50Ω attenuated output. The generator should preferably also have an FM facility rather than the more usual AM. See Fig. 4.3.

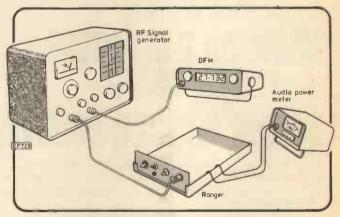


Fig. 4.3. Measurement of receiver sensitivity

Switch 'on' the signal generator and allow it to stabilise. Accurately set the output frequency to the channel in use with the aid of the DFM. Switch the internal modulation 'off' and adjust L104/5 so that the signal appears in the centre of the receiver passband. Switch off the squelch and progressively reduce the output of the generator and adjust L100/1, L102/3 and L106 for maximum quieting (reduction in background noise). Switch the modulation 'on' and adjust L108 for best audio quality.

Disconnect the r.f. generator and terminate the aerial socket with the 50Ω load. Connect the audio power meter in place of the internal loudspeaker and adjust the volume control to produce an output power of 100mW. Reconnect the generator and adjust its output level (using the attenuator) so that the output power meter reads 10mW. The output voltage from the generator is the receiver sensitivity for 10dB quieting.

(iv) An accurate and sensitive absorption wavemeter covering a frequency range of at least 27MHz to 150MHz. Alternatively an r.f. field strength meter covering the same range may be used.

Position the transceiver in the vertical plane and connect the helical aerial. Operate the PTT and hold the wavemeter in close proximity to the aerial. Check that a strong output is obtained at 27MHz and no other output is obtained at any other frequency. Particular frequencies to check are those that are harmonically related to the output, ie: 55MHz,

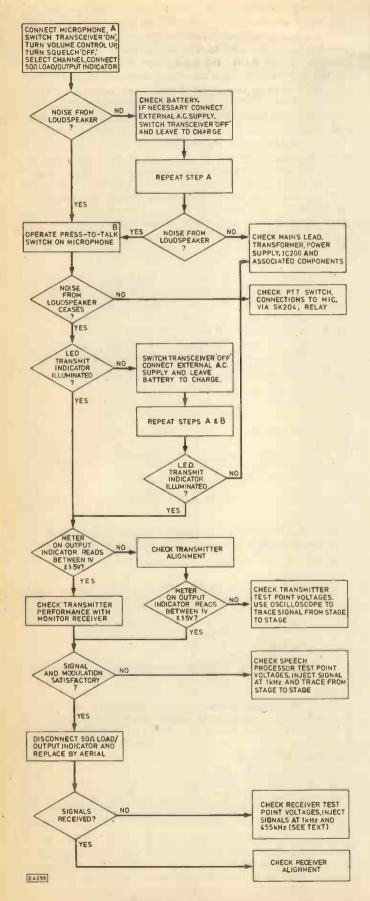


Fig. 4.4. Fault finding flowchart

83MHz, 111MHz, and 138MHz. With a very sensitive instrument (and particularly in the case of a field strength meter when used in place of the wavemeter) it may be possible to detect the presence of harmonics; however, their relative level should be very much less than that of the 27MHz output.

- (v) An FM deviation meter suitable for use at 27MHz.
 (An a.f. generator will also be needed for deviation measurements).
- (vi) An r.f. spectrum analyser covering the range 9MHz

The last two items are unlikely to be found in any other than the better equipped r.f. laboratories but they are included for the sake of completeness. Where such devices are available it is highly probable that constructors will already have the expertise required to use them and therefore instructions for carrying out measurements with these instruments are not given.

FAULT FINDING

By now most constructors will have completed the wiring and assembly of the Ranger and this would appear to be an appropriate moment to introduce the topic of fault finding. As with any piece of complex electronic equipment, a logical approach to fault tracing is required. The basic tools required for fault finding are a d.c. multi-range meter, an a.f./r.f. signal generator, and an oscilloscope. The last two items are useful but fortunately they are not always required. Indeed, it is generally possible to find even the most complex fault using no more than a basic multimeter and a lot of patience!

The first step is to ascertain the general area of the fault, i.e: transmitter, receiver, power supply, control circuitry. Then sub-divide the area you have identified into its constituent parts; oscillator, multiplier, driver and power amplifier, for example. Comparison of the test voltages with the actual circuit voltages can usually throw some immediate light on the subject and, having satisfied yourself that the d.c. supplies are correct the next step is to follow the signal through from stage to stage. When fault tracing on the transmitter it is important to note that most of the stages are operated under class C conditions and the d.c. voltages will vary widely with the level of r.f. Fortunately comparison between driven and non-driven conditions can be very easily made by simply switching the channel selector to a spare channel. This will provide an immediate indication as to whether, or not, r.f. is present in the stage concerned.

In the receiver section things are not quite so simple and it is unlikely that constructors will be able to glean a great deal of information from test voltages in, for example, the i.f. amplifier stage—unless they are lucky enough to have the internal circuit of IC100! This is where a signal generator can be useful. A 455kHz signal can be injected directly into IC100 (pin 3) and this will help decide whether the fault is in the front-end or i.f. stages of the receiver. Similarly an a.f. signal, at say 1kHz, can be injected across the volume control and this will provide some indication of whether the fault is in the i.f. or audio stage. To help with the initial stages of fault tracing, a flow chart has been provided in order to determine the area of any possible fault condition, see Fig. 4.4.

VOLTAGE TABLE

Test voltages were measured under the following conditions:

(i) 240V a.c. mains supply connected via SK201

- (ii) 50Ω load/output indicator connected (see Alignment section).
- (iii) VR2 adjusted for 500mW output (see Alignment section).

Test voltages were measured using a $20k\Omega/volt$ multi-range meter.

TEST POINT	TRANSMIT	RECEIVE
a b	2·1 0·8	0
c d	1·1 9·0	0
e f	0	6·0 5·7
g h	0	14·5 0
	5·7 5·7	0
k load output	9·3 1·2	0
"ext d.c." socket SK203	12.3	15.5

RF VOLTAGE TABLE

Transmit	SK200 TR1 emitter TR1 collector TR2 collector TR3 collector TR4 collector	5V 1.9V 2.0V 5.0V 9.0V 19.5V
Receive	ȚR 102 drain	1·0V

All voltages measured using external a.c. supply and 50Ω load/output indicator. Voltages are r.m.s. readings using a valve voltmeter and diode probe.

AERIALS

The aerial system used with any transceiver is a crucial factor in determining its overall performance both on transmit and on receive. The best possible aerial system should always be employed and money spent on improving aerials and feeders is always well invested.

Users should not expect too much from the Ranger when it is operated as a hand-held unit in conjunction with its matching helical aerial. This is due not only to the relatively low output power but also to the inefficiency of a miniature aerial system operating with a very inadequate earth plane. The result is a working range which is very much less than that which could be obtained with, say, a well sited full-size base station aerial system. A further point worth noting when using the helical aerial is that a significant amount of the r.f. energy radiated will be absorbed by the body of the user! For best results the transceiver should be held with the helical aerial in the vertical plane as high as possible and well away from the user. If a car roof, filing cabinet, water tank, or similar metal structure is available this can be used most effectively as a means of improving the efficiency of the aerial system. Simply place the transceiver upright as near as possible to the centre of its improvised ground plane and operate as far away from the transceiver as the microphone lead will allow.

The type of aerial used in any particular base station situation will depend on several factors, the most significant of which are the available space and, when dealing with omnidirectional vertically polarised aerials, the maximum height attainable. Whenever possible the Ranger should be used with a properly designed base station aerial rather than depend on its helical aerial which will certainly give disappointing results when used indoors. The most obvious choice of aerial will probably be a derivative of the vertical monopole. This is a very simple aerial consisting of a single radiating element and a ground plane to provide a reflected image of the monopole. The aerial performs in a manner similar to that of a free-space dipole and has basically similar radiation patterns modified, somewhat, by the effect of the imperfect ground plane. The major problem is that, with a discontinuous ground plane, an appreciable amount of radiation is directed upwards away from the surface of the earth and not concentrated in the most useful direction which is, of course, at right angles to the element. Typical radiation patterns for perfect and imperfect ground plane monopole aerials are shown in Figs. 4.5a and b and it is clear from these that the quality of the ground plane has a major effect on the performance of the aerial system. For this, and other reasons, there has been a move to free-space aerials, such as the vertical dipole, "J-antenna", and "Slim-Jim". All of these aerials operate without the need for a ground plane but they are generally a little more difficult to construct and adjust. Whatever type of aerial is selected most proprietary CB aerials will work well with the Ranger provided, of course, that the manufacturer's assembly and installation instructions are carefully followed.

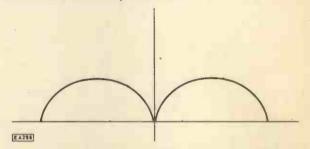


Fig. 4.5a. Vertical plane radiation pattern of a vertical quarter wave monopole aerial mounted above a perfect ground plane

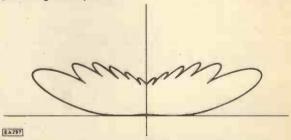


Fig. 4.5b. Vertical plane radiation pattern of a vertical quarter wave monopole aerial mounted above an imperfect ground plane

FEEDERS AND SWR

Coaxial feeders having a nominal impedance of 50Ω should be used for connecting the Ranger to an aerial system (do not use 75Ω TV aerial downlead!). Ideally the feeder used should be a "low-loss" type. However, where only a short length of feeder is used (say less than 20') the power lost in "standard" cables will be quite insignificant. The power lost depends on two factors; the type and quality of tt cable used and its length. A 25' length of UR-43, for example, will have a loss of approximately 0.5dB, doubling the length to 50' increases the loss to 1dB. In the first case approximately 10 per cent of the power is wasted in the cable

and 90 per cent arrives at the aerial (assuming, of course, that it is perfectly matched). In the second case approximately 20 per cent is lost in the cable and 80 per cent reaches the aerial. In practical terms this would make very little difference to the radiated signal. In general the feeder loss should not be allowed to exceed about 2dB since, at this point, only 63 per cent of the power appears at the aerial. The relationship between feeder loss and per cent power arriving at a perfectly matched aerial is shown in the table.

The characteristics of some commonly available coaxial cables are given in the table. Note that inferior cables of unknown origin and sometimes described as "UR-43 type" etc, should always be avoided as their performance is generally not as good as the cable which they purport to be!

A great deal of nonsense is often heard (and read!) concerning the topic of SWR (standing wave ratio). Far too many existing CB operators place too much emphasis on obtaining the lowest possible SWR from their aerial systems. Much of this effort is wasted since, in practical terms, an SWR of less than 2:1 is quite adequate and only represents an additional power loss of 0.2dB when present in, for example, a cable offering a matched loss of 1dB (ie: the total loss due to both feeder loss and SWR loss will only be 1.2dB). Furthermore a low SWR is not conclusive proof that an aerial is working efficiently. In fact a very low value of SWR can easily be obtained by using grossly inefficient aerial connected at the end of a very long length of lossy feeder!

In general, an SWR of 2:1 is nothing at all to worry about, 1.5:1 is good, and better than 1.3:1 is excellent. High values of SWR, say greater than 2:1 do not necessarily imply that an aerial system is poor. The solution is to add an aerial matching device in order to tune out the reactance associated with a non-resonant aerial and mismatched feeder. Aerial matching or tuning units are readily available as standard CB accessories however there is little point in purchasing one unless your SWR is greater than 2:1.

One final point worth mentioning is that, unlike some other CB transceivers, the power amplifier stage on the Ranger will tolerate excessively high values of SWR indefinitely. This means that, in the unfortunate event of your

FEEDER LOSS (dB)	PER CENT POWER AT PERFECTLY MATCHED AERIAL	PER CENT POWER WASTED IN FEEDER
0 0.5 1.0 2.0 3.0	100 90 80 63 50	0 10 20 37 50
6·0 α	25	75 100

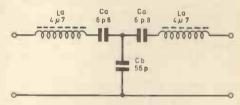
CABLE NUMBER	IMPE- DANCE (Ω)	OVER- ALL DIAM. (INCH)	INNER CONDUC- TOR (INCH)-	CAPACI- TANCE PER FOOT (pF)	APPROX. ATTENUA- TION PER 100' at 27MHz (dB)
UR 43	52	0·195	1/0.032	29	2·2
UR 67	50	0·405	7/0.029	30	1
UR 76	51	0·195	19/0.0066	29	2·6

aerial dropping-off or the feeder becoming short circuited, the Ranger will just sit and take it.

MODIFICATIONS

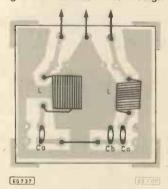
As a result of detailed tests and measurements made on a number of PE Ranger transceivers the following information has become available:

- 1. It is sometimes possible to set up the transmitter such that the level of spurious radiation is above the permitted level at particular frequencies. In order to substantially reduce the possibility of these harmonics being generated, the following changes to the original circuit must be made.
 - a) Replace R6 (100 ohms) by a $10\mu H$ inductor; the type used for L6 is suitable.
 - b) Add a 100pF disc ceramic capacitor between the primary winding terminals of L4. This is most conveniently soldered directly to the coil on the underside of the p.c.b.
 - c) Replace C13 by a shorting link.
 - d) Add a 15pF disc ceramic capacitor in parallel with L6, soldered on the underside of the p.c.b.
 - e) Reduce the value of R10 from 27Ω to 10Ω .
- 2. An improved filter response will be obtained from the original low pass filter design if the mutual coupling between the input and output coils is reduced by the addition of a tinplate screen between these coils. The screen should be earthed, and may even be extended to enclose the whole filter unit. A recommended alternative filter design, with improved stopband attenuation but slightly higher insertion loss (1–2dB max), is shown in Fig. 4.6a and b. This may be fabricated on the original filter p.c.b. with only slight changes (2 holes and 2 trackbreaks).



N.B.Lq = 18 TURNS OF 30 SWG WOUND ON A 10mm o.dio FORMER WITH FERRITE SLUG.CLOSE WOUND. Q = 120 APPROX.

Fig. 4.6a. Modified filter design



N.B. IT IS ESSENTIAL THAT THE TWO COIL ARE AT RIGHT ANGLES AS SHOWN.

Fig. 4.6b. Modified board design

3. If more output power is required from the transmitter, 2N2219 transistors may be used for TR3 and TR4. (You will have to make slight re-adjustments to L4/5 and L7/8 to compensate for the change.) The 2N2219 has a larger value of current gain and this will usually increase the output to 1W or so. Note that this modification will reduce the battery life (normally 2–3 hours of continuous operation).

- 4. If instability in the audio amplifier is observed this can easily be cured by the addition of a 1nF ceramic capacitor wired under the p.c.b. between pins 1 and 2 of IC101. To check whether instability is present move the loudspeaker connecting wires and listen to the noise produced by the loudspeaker. If there is a change in the noise produced as and when you move the connections or when they are touched this usually indicates high frequency instability in the audio stage and the signal produced may interact with the i.f./r.f. amplifier stages.
- 5. To improve front-end selectivity, image rejection, and simplify alignment the values of C121 and C122 should be changed to 22pF. It will then be harder to mis-align the r.f. tuned circuits.
- 6. For final alignment of the two r.f. tuned circuits, L100/101 and L102/103, it is recommended that all-plastic or all-nylon trimming tools are used. Tools fitted with phosphor bronze blades will de-tune the inductors whilst an adjustment is made. This is quite important if you want to get the "last ounce" of performance from your Ranger!
- 7. When aligning the receiver of the Ranger without the aid of test equipment it is important to note that, at present and until the new system comes into operation, signals likely to be heard will be AM and SSB and these will usually be off-frequency. These signals will sound very distorted and will certainly not be readable—they will at least give an indication that the receiver is functioning. Final adjustment of the r.f. tuned circuits should therefore be left until a proper FM signal is available.
- 8. There have been a number of cases where spurious output has been generated as a result of feedback by direct radiation of r.f. energy from the co-ax link (between the p.c.b. and the aerial socket) to the transmitter chain. It is essential that the front panel should exhibit a low impedance r.f. path to the earth plane on the p.c.b. A series tuned circuit is recommended for inclusion between the co-ax cable and the aerial socket, mounted immediately behind the aerial socket.

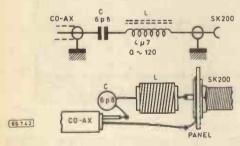


Fig. 4.7. Series tuned circuit

- 9. It is important that early issue p.c.b.s from Modus Systems are checked to ensure that the solder resist has been removed from around the slider pin of VR2 prior to soldering. Failure to do this will limit the maximum available output power to less than 5mW.
- 10. Should a 'chattering' squelch (i.e. where the squelch appears to open and close erratically in the absence of any discernible signal) be encountered, it is recommended that C102 be increased to $4.7\mu F$, or even $10\mu F$.
- 11. When tuning up the receiver, it is recommended that a matched 50Ω impedance be maintained. The receiver front end may show some sign of instability if not correctly terminated.
- 12. It is recommended that the exposed terminals of SK201 be protected with insulated sleeving.
- 13. A good earth bond between the front panel and the p.c.b. ground plane is essential. In some cases it has been

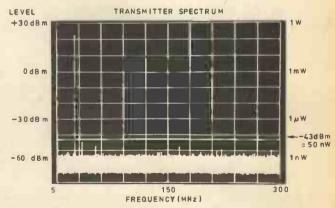
- found necessary to use a short length of braid to connect the body of SK204 to the earth tag on SK200. A short length of braid should also be fitted from the earth tag of SK200 to the can of L104.
- 14. Should the squelch appear to be non-operational, it is well worth checking the polarity and marking of D102 with an ohm meter; there have been cases where the polarity of the diodes has been incorrectly marked on the body of the diode.
- 15. Low profile d.i.l. sockets (N.B. no other type) have been found suitable for use with all of the i.c.s in the Ranger.
- 16. When mounting the NiCad batteries (B200 and B201) on the p.c.b., it is advisable to ensure that they are firmly seated; track damage may occur (especially in transit) if the tags are not pushed fully through the board. Similar considerations apply to the mains transformer, T200.
- 17. Apparent instability and lack of sensitivity in the receiver can be attributed to relatively high values of local oscillator injections in some sets. This can be cured by wiring a resistor of between 33Ω and 180Ω under the p.c.b. directly to the pins of L105. The value should not be so low as to stop the oscillator functioning and 100Ω makes a good starting point.

A further improvement may also be gained by wiring a $47k\Omega$ resistor under the p.c.b. between pin 1 of IC100 and OV. This increases the range of the local oscillator input swing.

After including the above mods (filter, series trap, R6, R10, C13, 2 extra capacitors) the alignment of the transmitter continues from step 17 as follows: using the load output indicator, adjust in the following sequence and repeat until maximum output is achieved: core in the series trap, cores in the filter, VC2, L8, VC1, L4, and L2.

TEST RESULTS

The following test results have been produced from a kitbuilt sample of the PE Ranger 27FM, which has been carefully aligned using techniques outlined in the series of articles. The set in question incorporated all of the suggested modifications in this part of the series, and was run from a mains supply.



RECEIVER SPURIOUS EMISSIONS

The maximum amplitude of emission from the receiver was measured as -48 dBm into 50Ω ; this is a power level of 15.8 nW.

RECEIVER SENSITIVITY

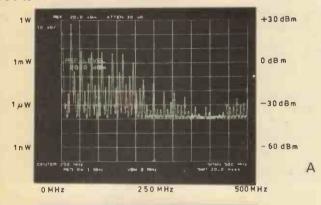
The measured usable sensitivity of the receiver on Channel 9 using 1kHz tone modulation and ± 2.5 kHz deviation was $0.43\mu V$. The measured sensitivity at 10dB signal-to-noise ratio was $0.80\mu V$. All measurements made in a constant 50Ω environment.

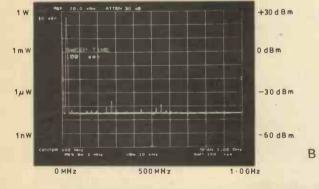
TRANSMITTER SPECTRUM ANALYSIS

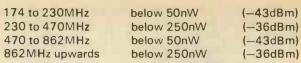
The detailed transmitter spectrum is shown for a Ranger transmitter which is fitted with the improved filter, the series trap in the co-ax link between p.c.b. and SK200, and has R6 and R10 replaced as described earlier.

The Home Office specification calls for the levels of spurious emissions to be as follows:

0 to 80MHz	below 250nW	(-36dBm)
80 to 85MHz	below 50nW	(-43dBm)
85 to 87.5MHz	below 250nW	(-36dBm)
87.5 to 104MHz	below 50nW	(-43dBm)
104 to 108MHz	below 250nW	(-36dBm)
108 to 118MHz	below 50nW	(-43dBm)
118 to 135MHz	below 250nW	(-36dBm)
135 to 136MHz	below 50nW	(-43dBm)
136 to 174MHz	below 250nW	(-36dBm)

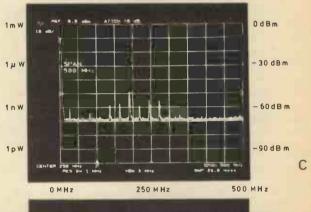


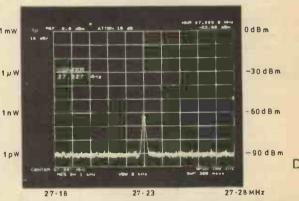




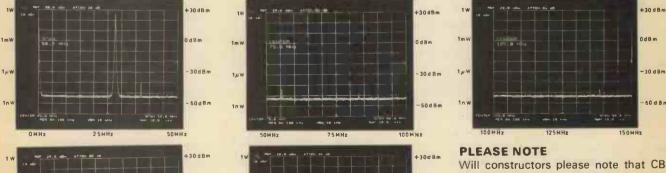
The traces below show the importance of correct alignment. The first trace (A) is without a filter, and the second (B) is with the transmitter above.

The results for a spectrum analysis of the Ranger receiver are shown below (C). The Home Office specification calls for a maximum allowable level of emission from the receiver of below 20nW (-47dBm) at any frequency. The Ranger receiver meets this specification. The tested sample is shown without modifications; the performance above 30MHz is improved when the series tuned circuit is added. A detailed examination of the local oscillator spectrum is shown (level = -63.6dBm or 0.5nW) in trace (D).

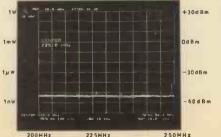




DETAILED TRANSMITTER SPECTRUM ANALYSIS - D.C. TO 250MHz IN 50MHz STEPS.



1 W +30 dBm OdBm OdBm -30 dBm -30 dBm -50 dBm



Will constructors please note that CB licences are now available from any Post Office for an annual fee of £10.00.

NEXT MONTH

The base mobile adaptor has been developed to complement the basic ranger transceiver, whilst retaining all of the original features.

The hardware and software exchange point for PE computer projects

M/C TO BASIC CONVERTER

Sir-I expect that all UK101 owners know how to put a decimal m/c program into HEX simply by using the READ, DATA and POKE commands in BASIC. But how do you convert a HEX m/c program into the above BASIC thus not needing checksum loaders etc.?

One slow way is to PEEK all the m/c program when in BASIC and enter the bytes into a program of DATA statements by hand, not much fun for a .200 byte program! Alternatively, you could write a program that would write its own BASIC lines into the memory, like Tony Walsh's "Graph Plot" (PE January 1981).

A far simpler way to achieve this would be to make use of the fact that everything printed on the screen of the UK101 when in Save mode also goes on to tape. The enclosed program writes a BASIC program to store anything stored anywhere in the memory as DATA using only the print command

The program starts at line 49999 so as not to overlap any program already in the computer. To start it just type "Run 50000"

Description:

Line 49999: prevents any other program running accidentally into it.

50040-50110: data entry from keyboard and validity checking.

50200: present memory address (decimal). 50240-50250: converts byte at present address to string of decimal value and remove preceding space caused by STR\$. Increments character count on line being printed.

50260: checks if line full, if not then 50330.

50270-50320: starts new line, sets L to next line No. and sets character count for new line to zero.

50330: checks if sufficient line numbers left to complete program.

50340: prints data byte in D\$ onto tape with preceding comma.

50360-50390: prints BASIC routine to poke data back in when loaded.

50400: resets Save flag and gives a small tape delay before instructing for the tape to be stopped.

Variables:

First and last memory addresses to be recorded.

L&IN Starting line number and line increment.

M & D\$ Present memory address and string of decimal data value at M

M/C TO BASIC 'DATA' STATEMENT PROGRAM WRITTEN BY A.U.H BRUCE 13-4-1981 TO OPERATE TYPE 'RUN SOOOO' AUTHOR'S ADDRESS :-10 NORTHEND, WARLEY BRENTWOOD, ESSEX . 8: BRENTWOOD, ESSEX.

10: ACTUAL LISTING FOR PRINT: 49999 TO 50440.

11: 49999 PRINT: PRINT"ETFOR...program over run.": PRINT: STOP
50000 PRINT: PRINT"ETFOR...program over run.": PRINT: STOP
50010 PRINT"HEMEDAY DATA RECORDING PROGRAM"
50010 PRINT"HEMEDAY DATA RECORDING PROGRAM"
50010 PRINT"HEMEDAY DATA RECORDING PROGRAM"
50030 PRINT"HEMEDAY DATA RECORDING PROGRAM"
50040 PRINT"HEMEDAY DATA RECORDING PROGRAM"
50050 LINEUT/MENORY AGRESS, SERTE';
50060 PRINTTAB(15): 11NPUT"Iniah";
50060 PRINTIAB(15): 11NPUT"Iniah";
50070 IFFSORPOS\$3509ACTHERRUISO000
50080 PRINT(1): 17L-00RD-53997HEMESOGRO
50100 PRINTTAB(15): 11NPUT"Incornenet"; IN
50110 IN-INITABS(18): 11PTM-OORIN-300THEM50100
50120 REM — RECORD HEM-DATA AS BASIC 'DATA' —
50130 PRINT: FORI-17039 PRINT""; INETTERINTERINT PRINT
50140 PRINT"BUH - 1703; 2533-RUSK[U]
50140 FORX-1703000·NEXTREH — TAPE-DELAT —
50170 PORKETS, 72: FORKET], 1: PRINTIT-PRINT
50180 REM — SET COUNT HIGH TO CAUSE NEW LINE —
50190 LL-90
50200 FORM-STOP 50190 LL-90
50200 FORM-STOP
50210 REH- CONVERT BYTE TO STRING 6 REMOVE —
50210 REH- PRECEDDING SPACE CAUSED BY STRS.—
50210 DS-RIGHTS (DS. DL): LL-LL+DL+1
50240 DS-RIGHTS (DS. DL): LL-LL+DL+1
50250 REH — NEU LINET 5
50250 FEHL-SOTHENSOSSO
50270 FRINT
50280 REH — START NEW LINE —
50290 FRINTL; "DATA "LDS.
50300 REH — NEW LINE No., LINE LENGTH TO 0 —
50310 L-L-HII: LL-0
50310 L-L-HII: LL-0
50310 CS. 50310 L-L-HII:LL-0
50320 COT050350
50330 IFL-14:P2-53995THENPRINT:PRINT"ERROR..line too big.":GOT050410
50340 PRINT","|DS;
50350 NEXT:PRINT'
50350 REM — DATA FINISHED & NOU TO POLE IT 4
50350 PRINTL;"FOR I ="15;"TO";F
50360 PRINTL+IN;"READ J:POKE I,J:HEXT I"
50360 PRINTL+IN;"READ J:POKE I,J:HEXT I"
50400 REM -- TIME DELAY & TURN OFF TAPE —
50410 FORT=INTOPORDS-107,0
50410 FORT=INTOPORDS-107,0
50420 PRINT:FORT=ITOPORDS-17,0
50420 PRINT:FORT=ITOJI:PRINT"\";:NEXT:PRINT
50430 PRINT" Recording finished, stop tape.":PRINT
50440 EDG

DL & LL Length of D\$ and No. of characters on line.

NOTE Line 50260 should read: IFLL<56 etc. . .

A. W. H. Bruce. Brentwood, Essex.

UN-NEW!

Here is a short machine-code program (38 bytes) for the UK101 which may come in useful. It will restore "lost" programs. By "lost" I mean programs which have been inadvertently NEWed. It must be entered as a m/c program since entering it as data statements will destroy the "lost" program pointers in basic.

It is relocatable and uses two subroutines in ROM. Once entered, it must be executed immediately after the NEW command, i.e. before any new line numbers are entered.

A0 04 LDY &04 C8 L1:INY B1 79 LDA (79), Y DO FB BNE L1 ; find end of C8 INV ; first line **C8** INY 8C 01 03 STY 0301 A5 7A LDA 7A 8D 02 03 STA 0302 A9 FF LDA &FF 85 12 STA 12 **JSR A432** 20 32 A4 ; find end of 86 7A STX 7A ; program A5 AA LDA AA 20 6D A4 JSR A46D : reset pointers A9 03 LDA &03 85 7A STA 7A 4C 74 A2 JMPA274 ; warm start

> Stuart Smith, Dumbarton.

MULTIPLE USER CALLS

Sir-If you wish to link more than one machine code routine to UK101 Basic, the restriction of a single 'USR' function can be a drawback. By POKING into locations 11 and 12 you can of course change the USR address but this leads to lengthy BASIC lines which don't make for easy reading. For example it's difficult to see that

POKE 11,227:POKE 12,168:X=USR(Y)

is calling the routine at 43235 decimal.

When the USR call is made from BASIC as above, the value of Y is placed into the floating accumulator at \$ AC to \$ BQ. It seems logical therefore to try to use this route to allow multiple USR calls with the address of the routine as the argument.

The technique is to vector USR calls to a machine code patch.containing the followina:

JSR \$B408 Convert floating accumulator contents to integer at \$11, \$12 JMP (11) Branch to routine whose address is in \$11, \$12

Including the following lines of BASIC in a program will set this patch up at \$0228 and link subsequent USR calls to it.

FOR I=565 TO 570 : READ X : POKE I, X: NEXT DATA 32, 8, 180, 108, 17, 0 : POKE 11, 53: POKE 12, 2

The patch used here is compatible with the original monitor, and the new monitor as well as CEGMON.

The calling routine mentioned above now reduces to:

X=USR (43235)

This saves memory, is more readable and the omission of the two POKEs also speeds up the calling procedure considerably. Enterprising EPROM programmers could fit the patch into the New Monitor or CEGMON by stealing some of the ROM used for message storage.

P. Beckett, Blackpool.

OUICK TIP

A short routine to print superscripts as required in A2 etc.

Example:

140 AC=32

150 ?:? "ACCELERATION="; AC; "FEET/SEC"; : POKE 54157 + POS (I), 50:?

How about an article on programming techniques with tips like this one and wider aspects such as how the interpreter works. For example, is it faster to put DATA statements early in the programme, or after the READ statement?

> S. Jeans. Cardiff.

FULL RENUMBER

Sir—Here is a full-function BASIC Renumberer program for the UK101 in reply to Mr. I. Pawson of Leicester (PE November 1980).

This program renumbers, with the same steps as the original, lines and statements GOTO, GOSUB, THEN, ONI GOTO and ONI GOSUB if they are in positions it is necessary to change, anywhere in the program.

The renumbering is successful in GOTO'S, THEN'S etc., even with new numbers having more or less digits than the old ones. If there is no space to enter the full number (in the case of a number having more digits), the program enters the most significant digits of the new number and reminds you to REWRITE line n. So, when writing BASIC programs it is better to leave one or two spaces in GOTO'S, THEN'S etc., before or after the number.

After renumbering, the lines remain at the same memory locations as before. Suppose you have a program with lines 100, 110, 120, 130.

- CASE 1 Renumber from 100 to 110 starting at 80; The new sequence of lines will be 80, 90, 120, 130.
- CASE 2 Renumber from 120 to 130 starting at 400; The new sequence of lines will be 100, 110,400,410.
- CASE 3 Renumber from 110 to 120 starting at 400; The new sequence of lines will be 100, 400, 410, 130. The new program will not work. You must SAVE it.in a spare cassette and LOAD back to COMPUKIT. The new sequence will become 100, 130, 400, 410.
- 44400 REM *** FULL-FUNCTION RENUMBERER FOR UK101
- 44405 REM **** A. GALLOPOULOS — GREECE ****
- 44410 INPUT "RENUMBER OLD LINES FROM"; X
- 44415 INPUT "THROUGH TO"; Y
- 44420 INPUT "AS NEW LINE NUMBERS FROM"; Z
- 44425 A = 768 : S = Z X
- 44430 A = A + 1 : L = PEEK (A+2)+ PEEK (A+3) * 256
- 44435 IF L => 44400 THEN 44475 44440 IF L < X OR L > Y THEN
- 44440 IF L < X OR L > Y THEN M = L : GOTO 44455
- 44445 M=L+S:POKE A + 2, M-INT(M/256) * 256
- 44450 POKE A + 3, INT (M/256): PRINT L, M
- 44455 A = A + 4
- 44460 C = PEEK(A): IF C = Ø THEN 44430
- 44465 IF C = 136 OR C = 140 OR C = 160 THEN 44500
- 44470 A = A + 1 : GOTO 44460
- 44475 PRINT "RENUMBERING COMPLETED" : END
- 44500 E\$ = "": B = A
- 44505 B = B + 1 : C = PEEK (B)

- 44510 IF C > 64 THEN A = A + 1: GOTO 44460
- 44515 IF C = Ø OR C = 44 OR C = 58 THEN 44525
- 44520 E\$ = E\$ + CHR\$(C) : GOTO 44505
- 44525 K = VAL(ES) : W = B A : A = A 1
- 44530 IF K < X OR K > Y THEN 44555
- 44535 E\$ = STR\$ (K + \$) : FOR J = 2TO W
- 44540 IF J <= LEN(E\$) THEN 44550
- 44545 POKE A + J, 32:NEXT:GOTO 44555
- 44550 V = ASC (MID\$(E\$, J, 1)) : POKE A + J, V : NEXT
- 44552 IF LEN (E\$) > W THEN PRINT "REWRITE LINE"; M
- 44555 A = B: IF C = 44 THEN 44500
- 44560 GOTO 44460

Antonios Gallopoulos, Greece.

CASSETTE FILE HANDLING

Sir—The basic UK101 has no built-in data file handling capability. The data file program published in your "Micro Prompt" of April 1980 suffers from the disadvantage that a program change is required to change:—

a) the name of the file;

b) the number of characters allowed in the file name:

c) the number of data "words" stored in the file

Also, the pause period in line 10 is running while the operator is actually reading the instruction and starting the tape. There is, therefore, no tape run-up time before the instruction "HIT ANY KEY" is given. The following modification of your program overcomes these limitations.

- 5 INPUT"NAME THE FILE";F\$
- 10 INPUT"HOW MANY ITEMS OF DATA";N
- 15 FOR A=0 TO N-1: PRINT"ENTER ITEM" A+1;:INPUTW\$(A): NEXT:PRINT:PRINT
- 20 PRINT"WIND TAPE TO RE-QUIRED POSITION & SET TO RECORD":PRINT:PRINT
- 25 PRINT"HAVE YOU STARTED THE TAPE?":PRINT:PRINT
- 27 INPUT"IF SO, ANSWER 'Y' ";-O\$:IF Q\$<>"Y" THEN 25
- 30 PRINT:PRINT"WAIT . . . ";:FOR A=1TO 8000:NEXT:PRINT"HIT ANY KEY"
- 35 POKE 11,0:POKE 12,253:X=USR (X):SAVE
- 40 PRINT FS:PRINT N
- 45 FOR A=0 TO N-1:PRINT WS(A):NEXT:PRINT
- 50 POKE517,0
- 55 PRINT""F\$" FILE IS NOW ON TAPE"
- 60 END

The program to retrieve the file from tape follows.

- 120 INPUT"WHICH FILE DO YOU REQUIRE";F\$:PRINT
- 125 PRINT"WIND TAPE TO FILE POSITION, SWITCH TO"
- 130 PRINT"REPLAY & HIT ANY KEY IMMEDIATELY."
- 135 POKE 11,0:POKE 12,253:X= USR(X):LOAD
- 140 INPUT T\$
- 145 IF RIGHTS(TS,LEN(FS))=FS
 THEN 155
- 150 GOTO 140
- 155 INPUT N
- 160 FOR A=0 TO N-1:INPUT W\$(A):NEXT
- 165 POKE515,0 : PRINT : PRINT : PRINT :
- 170 PRINT F\$:PRINT:FOR A=0 TO N-1:PRINTW\$(A):NEXT
- 175 END

Since the W\$(A) array has not been dimensioned, the maximum number of data "words" is 11. This is in any case the maximum number that will be displayed together at line 170.

D. R. Smith Coventry

LIKE V

Sir—This short program for the UK101 allows the programmer to view the contents of a tape without loading it into memory, a similar facility to the V command in the extended monitor.

The program is in BASIC, and uses a machine code routine as follows:

0229-character store

The FE80 is the address to a subroutine, that inputs a ASCII character from cassette, and stores the value in the accumulator. After re-entry, the value in A is stored at 0229, and the program returns to BASIC and prints out the character.

10 FOR A=546 TO 552 20 READ B:POKE A,B:NEXT 30 DATA 32,128,254,141,41,2,96 40 POKE 11,34:POKE 12,2:X=USR(X) 50 V=PEEK(553):PRINTCHR\$(V): 60 GOTO 40

> Hans Palm, Sweden.

CEGMON TRACE HELP

Sir—I noticed R. J. Newman's snags with "Trace". Having used it under CEGMON I can only suggest either (1) the "Trace" cannot be loaded at 546 as the window handler resides there; I relocated mine to 672 (\$02A0 hex), and (2) the control-C vector is mentioned twice in the program, being \$FB94 under CEGMON.

I have found the PE "Trace" one of the most useful utilities published. I hope this is helpful.

Robin H. Tracey, Coventry.

INDEX

JANUARY 1981 TO DECEMBER 1981

VOLUME 17

CONSTRUCTIONAL PROJECTS

Alarm, Ultrasonic Intruder	32
Bandbox, PE Nov. 32, Dec. Binaural, Headphones Oct.	52 36
Capacitance Meter by Martin Kent July Car Computer, PE by P. MacFarlane Dec. Chimer, Minute Aug. Clock, Horologicum Star Oct. Compukit, Interfacing Jan. 44, Feb. 54, March April 56, May 60, June 68, July Computer, Car Dec. Continuity Tester by Martin Kent June	30 52 22 52, 62 30 64
Controller, Train Aug. Converter, 27/28MHz March Counter Timer, Digital Feb. 32, March Courtesy Light Delay by Philip Dakin May Curve Tracer, Semiconductor June	30 66 54
Desk, Disco	48
Alex Kowalewski B.Sc. Feb. 32, March Digital Lock by Bart Trepak May Digital Multimeter by Martin Kent May Digital Thermometer by Martin Kent May Dimmer, Slave Light Feb. Disco Desk by Ben J. Duncan Jan. Disco Hat by Tom Gaskell B.A. Nov. Drill PSU by Chris Lare April Drum Synthesiser by P. Bailey Sept.	22 44 36 26 60 43 44 50
Echo Simulator by S. J. Musgrave Aug. Elapsed Hour Meter by Chris Lare Feb. EPROM Programmer by J. Dyson Aug. Evaders, Space Dec.	18 60
Frequency Meter, Analogue Sept.	32
Hat, Disco	36
Inductive Discharge Car Ignition System by K. Wevill	14, 50.
Keyboard Remote Sent	

Light Delay, Courtesy	Man	. 5/
Light Dimmas Claus	. IVIA	20
Light Dimmer, Slave	. reb.	. 20
Light Meter by Martin Kent	July	46
Linear Ohmmeter by D. H. E. King	Jan.	36
Lock, Digital		
200K, Digital	. IVILLY	6.6
54 54		
Magnum Modifications, PE	. Feb.	52
Meter, Analogue Frequency	Sept.	32
Meter, Capacitance	July	34
Meter, Elapsed Hour		
Motor Light	, I CD.	10
Meter, Light	July	40
Meter, Noise	July	50
Meter, pH	.June	40
Microphone Mixer by R. A. Penfold	March	46
Microtune, PE	lan	50
Minitune, PE	.June	34
Minute Chimer by James S. B. Dick	. Aug.	52
Mixer, Microphone	Narch	46
Monitor Change, UK101	Nov	5.9
Multimeter, Digital	A4	4 4
Withineter, Digital	. Iviay	44
and the second s		
Noise Meter by Martin Kent	July	50
Ohmmeter, Linear	lan	36
Oliminator, Elifour Transfer T	, .o a i i .	50
DE D		
PE Bandbox by Alan Boothman B.Sc Nov. 32	, Dec.	52
PE Digisounder by Brian Currie April 30	, May	48
PE Magnum Modifications by Andy Flind	Feb.	52
PE Master Rhythm by A. J. Boothman B.Sc	lan	22
PE Microtune by Martin Kent		
TE Microtule by Wartin Nent	Jan.	20
PE Minitune by Martin Kent	June	34
PE Pulser by Tom Gaskell B.A	.Oct.	62
PE Quasar	.July	26
PE Quasar PE Ranger 27FM by M. Tooley B.A. and D. Whitfie	ld	
M.A., M.Sc Sept. 40, Oct. 50, Nov. 48,	Dec	60
PE Robots by Richard Becker Nov. 24,	Dec.	40
TE HODOLS by Michard Becker	Dec.	42
PE Starspinner Jan. 30	, reb.	42
Period Power Tester by Chris Lare	/larch	38
pH Meter by Martin Kent	June	40
Processor, Speech	April	38
Programmer, EPROM	Aug	60
DCII Dell	Annil	11
PSÜ, Drill	April	44
Pulser, PE	.Oct.	62
Quasar, PE	July.	26
Ranger 27FM, PE Sept. 40, Oct. 50, Nov. 48,	Don	60
naliyer 27 Fivi, FE Sept. 40, Oct. 50, Nov. 48,	Dec.	00
Remote Keyboard by J. Lewis	Sept.	24
Rhythm, PE Master	Jan.	22
Robots, PE Nov. 24,	Dec	42
Semiconductor Curve Tracer by D. Mandelzweig	Lung	EO
Semiconductor curve tracer by D. Wandelzweig	June	20
Simulator, Echo	Aug.	38

Space Evaders by Tom Gaskell B.A	Dec. 22
D. Whitfield B.A., M.Sc.	April 38
Starspinner, PEJan.	30, Feb. 42
Synthesiser, Drum	Sept. 50
System, Inductive Discharge Ignition	Oct. 40
Tester, Continuity	luna 64
Tester Desired Desired	June 04
Tester, Period Power	March 38

Thermometer, Digital May 36 Timer, Digital Counter Feb. 32, March 66 Train Controller by Andy Flind Aug. 32 Tuner, PE Quasar July 26
UK101 Monitor Change by Ian Hickman
27/28MHz Converter by M. Tagley R.A. March 30

GENERAL FEATURES

Audio Lines by Ben Duncan	2
Cegmon Reviewed by Mike Abbott	3
Digital Design Techniques by Tom Gaskell B.A Aug. 42 Sept. 60, Oct. 58, Nov. 40, Dec. 38	3
INGENUITY UNLIMITED Jan. 70, Feb. 64, March 62 April 66, May 66, Aug. 66 Oct. 58, Nov. 62	ì,
Accented Beat Metronome by P. C. French Nov. 62	2
Bargraph Thermometer by R. Eley	5
Capacitive Touch Switch by F. T. Dart	
Day to Remember Clock by C. T. Chantler April 66 Delayed Output Protected Mono by C. Embleton May 68 Disco Light Sequencer by J. Rennie	8
Envelope Differentiator by M. Russ April 68	8
Fuse Failure Indicator by K. A. RochfortJan. 72	2
Graph Display Unit by B. Darnton	3
L.F. Analogue Monitor by C. J. Collins	7
Model Train Controller by R. HayesJan. 70 Motor Controller by O. AlbrechtAug. 66	0
Pip Generator by J. D. Richie	2
Storm Distance Estimator by R. Jonasar March 64 Synth Processor by M. Russ	3
Telephone Timer by C. Dakin Aug. 6: Thump Eliminator by A. A. Jeffree May 6: Timer for Cine Camera by Odd Bjorkli Jan. 7: Torpedo Game by M. Crisp Jan. 7: Traffic Lights by R. J. Jones March 6: TTL Protection by J. Piper Jan. 7: TVC for Synths by M. Russ April 6:	70142
0.99S Photographic Timer by G. Crawford Nov. 6; 3 Channel Sound-to-Light by K. Badcock Nov. 6; 6V Regulator Supply by A. J. Flind	3

Introduction to Robotics by Professor W. Heginbotham O.B.E., D.Sc
MICROBUS by D. J. DFeb. 48, April 50, June 76,
Aug. 56, Nov. 66
Micro Prompt Jan. 68, March 69, May 57, July 54,
Sept. 74, Dec. 66
Oscilloscopes—How They Work by Ian Hickman April 26, May 39, June 45
Positive Impedance Converters
Positive Impedance Converters by A. B. BradshawJune 58
SEMICONDUCTOR UPDATE by R. W. ColesJan. 49,
Feb. 41, March 36, April 49, May 26, June 31,
July 42, Aug. 37, Sept. 59, Oct. 35, Nov. 23, Dec. 48
Motorola TMOS, Spague UTN 2886B,
Motorola TMOS, Spague UTN 2886B, Texas TMS 5100, TM56100
AMD 2960, Hewlett Packard HLMP 31XX,
HMP 36XX Feb. 41 Silicon General SG1547, TRW LSI Products
TDC 1023J, National Semiconductor IN58073 Mar. 36
Intersil ICM 7242, SGS-Ates L 209, 1 & 2.
Intersil ICM 7242, SGS-Ates L 209, 1 & 2, Mostek 4118, 4801 & 4802
Texas TMS 9909, Siemens SAB 0600, Analog
Devices AD636
Cybernetic Micro Systems CY500, National Semiconductor LM330 Jun. 31
Intel: APX 432, AEG Telefunken U410B Jul. 42
National Semiconductor MM 5439, Ferranti
ZN470. Mild MD4101
Motorola MC13002, Sharp G1-105XX, IR-2E01 Sep. 59
TRW Optron OPL100, Western Digital WD55,
National Semiconductor
Zilog Z6132
National Semiconductor LM1894, Intersil
ICL 7663, Zilog Z800S Dec. 48
Solar Power Satellites by Dr. J. P. StarkJan. 38
Speech Recognition System by Anthony Young Sept. 36
Strictly Instrumental by K. Lenton Smith March 44, June 38
Suite 38
Tandy TRS80 by Dr. A. A. Berk Feb. 28
The Electronic Car by Walter Musty Sept. 36
UK101 Colour Board Review by Mike Abbott Aug. 30
UK101 Graph Plot by Toby WalshJan. 66
Wemon Review by Dr. A. A. BerkJune 26
Wire Wrapping by John LewisJune 24
600 Baud Cassette Interface by P. Martin May 56

NEWS AND COMMENT

A New Generation of Softys	. Sept. 73
BOOK REVIEWS	Jan. 28
CB Proposals	, May 31,
EDITORIALJan. 17, Feb. 15, March 17 May 17, June 17, July 17, Aug. 17, Oct. 17, Nov. 1	Sept. 17,
INDUSTRY NOTEBOOK by Nexus Jan. 2 March 21, April 21, May 21, June 2 Aug. 23, Sept. 23, Oct. 21, Nov. 2	3, July 23,

MARKET PLACE May 18	3, June 18, July 18	March 18, April 18, 8, Aug. 18, Sept. 18, 8, Nov. 18, Dec. 18
NEWS BRIEFS	Ma rch	24, 45, 61, May 25
PATENTS REVIEW April 6	5, May 53, June 7	Feb. 66, March 60, 8, July 58, Aug. 69,
PE Inventors Compe POINTS ARISING .	tition Results	85, Oct. 21, Nov. 56 March 43
TOINTS ANISING .		0, July 20, Sept. 21
READOUT		March 42, April 63, July 59, Nov. 54, 61
	1, April 35, May 3!	Jan. 59, Feb. 63, 5, June 32, July 61, 8, Nov. 31, Dec. 51

SPECIAL SUPPLEMENTS AND OFFERS

Circuits Supplements July (between pages 42 & 43)	
Extra Case Coupon June 28, July 57, Aug. 35, Sept. 27	
Hi-Fi Offer Dec. 29	
Multimeter Offer Dec. 59	
Special Offer—Car Audio Booster June 39, Aug. 62	

Special Offer—Digital Panel MeterMay 51,
June 57, July 45
Special Offer—Graphic Equaliser Sept. 49
Special Offer—GSC Experimentor Kit Oct. 73, Nov. 46
Special Offer—In Car Entertainment Jan. 34, Oct. 49
Special Offer-Telequipment S61 Oscilloscope Feb. 25,
March 37
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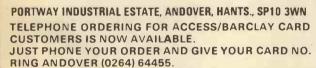
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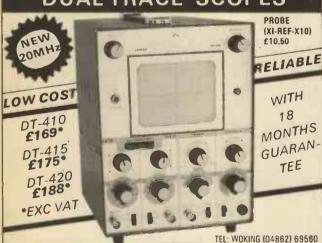
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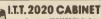
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High quality plated thru hole printed circuit board, solder resist and silk screened component identification TC sockets for maximum expansion 64 Way D.I N. edge connector TK RAM, cassette interface, 16 parallel I/O lines, a TTL serial I/O port, two 16 bit counter timers, data bus Environments for buffering, memory mapping, logic and discrete components maximum expansion TANEX users manual

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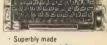
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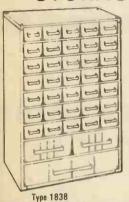
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HEAVY DUTY without heatsinks

HD 120P	60w/4-8Ω	0.01%	<0 006%	±35±40	120×26×50	265	£22.82	£19 84
HD 200P	120w/4-8Ω	0 01%	<0.006%	±45±50	120×26×50	265	€27.17	£23.63
HD 400P	240w/4Ω	0.01%	<0 006%	±45±50	120×26×70	375	€39.42	£34.28



Protection: Load line, PERMANENT SHORT CIRCUIT (ideal for disco/group use should evidence of short circuit not be immediately apparent). The Heavy Duty range can claim additional output power devices and complementary protection circuitry with performance specs as for standard types.

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You can also use it as an EPROM programmer and to copy EPROMs from a master or to/from

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60c

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with 16 on/off outputs or three analogue outputs for controlling TV or hi-fi systems, the kits are easy to build and simple to set up – and they are extremely versatile so you can use them for controlling anything from garage doors to room lighting just by adding the required output circuits, i.e. relays, triacs, etc.

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This unit has, of course, considerable practical uses, especially for the old, infirm and disabled. It works like a conventional dimmer, enabling you to switch the lights on or off, or to dim-them to whatever brightness you require, by touch or remotely using the hand-held infor and transmitter. When assembled, it fits into a plaster depth box to replace your conventional switch or dimmer with no rewlring.

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prices).
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IS IN OUR LOCK



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ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013	2.10 28.09 22.59 56.63 OUS 3.90 3.45	DISCOU	NT O	NANYO	THIS	74LS243 74LS244 74LS245 74LS247 74LS248	0.79 0.79 0.89 1.34 1.00
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ZN429E-8 ZN432CJ-10 ZN4433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1489	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64	DISCOU IF ACCO AD' 4019 4020	OMPA VERTI	NANY ONIED BY SEMENT	0.13 0.13	74LS243 74LS245 74LS245 74LS247 74LS248 74LS249 74LS251 74LS253 74LS253 74LS258 74LS258	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.44 0.38
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1489 MC14411	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 6.94	DISCOU IF ACCO AD' 4019 4020 4021	0.29 0.58 0.60	N ANY ONIED BY SEMEN. 74LS05 74LS08 74LS09	0.13 0.13 0.13	74LS243 74LS244 74LS245 74LS247 74LS248 74LS249 74LS251 74LS253 74LS253 74LS257 74LS258 74LS259 74LS259	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.44 0.38 0.38
ZN429E-8 ZN432CJ-10 ZN4433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1489	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64	DISCOU IF ACCO AD' 4019 4020	OMPA VERTI	NANY ONIED BY SEMENT	0.13 0.13	74LS243 74LS245 74LS245 74LS247 74LS248 74LS249 74LS251 74LS253 74LS253 74LS258 74LS258	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.44 0.38
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 RO-3-2513L RO-3-2513L	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.70	DISCOU IF ACCO ADV 4019 4020 4021 4022 4023 4024	0.29 0.58 0.60 0.62 0.17 0.38	74LS05 74LS08 74LS09 74LS10 74LS11 74LS11 74LS12	0.13 0.13 0.13 0.13 0.14 0.15	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS253 74LS257 74LS258 74LS258 74LS259 74LS261 74LS273	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.38 1.90 0.23 0.90
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 RO-3-2513U ZN450E	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.70 7.61	DISCOU IF ACCC AD' 4019 4020 4021 4022 4023 4023 4024 4025	0.29 0.58 0.60 0.62 0.17 0.38 0.16	74LS05 74LS05 74LS08 74LS10 74LS11 74LS11 74LS11 74LS12 74LS13	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22	74LS243 74LS245 74LS245 74LS248 74LS248 74LS251 74LS253 74LS257 74LS266 74LS266 74LS273 74LS273 74LS279	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.38 1.90 0.23 0.90
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L R0-3-2513U ZN450E	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.70 7.70 7.70 7.61 0.50	DISCOUL F ACCC AD' 4019 4020 4021 4022 4023 4024 4025 4026	0.29 0.58 0.60 0.62 0.17 0.38 0.16 0.99	N ANY O NIED BY SEMEN' 74LS05 74LS08 74LS09 74LS10 74LS11 74LS12 74LS12 74LS13 74LS14	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS251 74LS257 74LS257 74LS257 74LS258 74LS258 74LS258 74LS259 74LS259 74LS261 74LS279 74LS279 74LS279	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 1.90 0.23 0.90 0.34 0.44
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 RO-3-2513U ZN450E	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.70 7.61	DISCOUL IF ACCC AD' 4019 4020 4021 4022 4023 4023 4024 4025 4026 4027	0.29 0.58 0.60 0.62 0.17 0.38 0.16	74LS05 74LS05 74LS08 74LS10 74LS11 74LS11 74LS11 74LS12 74LS13	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22	74LS243 74LS244 74LS245 74LS248 74LS248 74LS253 74LS253 74LS255 74LS256 74LS256 74LS257 74LS273 74LS273 74LS273 74LS273 74LS273 74LS290 74LS293	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.38 1.90 0.23 0.90
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-2376 MC1488 MC1489 MC14411 MC14412 RO-3-2513U ZN450E 7805	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 6.94 7.90 7.70 7.70 7.61 0.50	DISCOUL F ACCC AD' 4019 4020 4021 4022 4023 4023 4024 4025 4026 4027 4028	0.29 0.58 0.60 0.62 0.17 0.38 0.16 0.99	N ANY O NIED BY SEMEN' 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS21	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13	74LS243 74LS244 74LS245 74LS248 74LS251 74LS251 74LS253 74LS255 74LS256 74LS256 74LS261 74LS261 74LS273 74LS273 74LS273 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.90 0.23 0.90 0.44 0.56 0.45
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L R0-3-2513U ZN450E 7805 7812	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 7.99 7.70 7.61 0.50 0.55 0.55	DISCOUL IF ACCO ADV 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033	0.29 0.58 0.62 0.17 0.38 0.16 0.19 0.30 0.55 1.65	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS10 74LS11 74LS12 74LS13 74LS14 74LS13 74LS22 74LS21 74LS21 74LS21	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS258 74LS258 74LS258 74LS261 74LS262 74LS262 74LS265 74LS263 74LS279 74LS279 74LS279 74LS279 74LS283 74LS299 74LS299 74LS299 74LS299 74LS299 74LS266	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.48 0.38 1.90 0.23 0.23 0.34 0.45 0.45
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L ZN450E 7805 7812 7905 7912	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 6.94 7.90 7.70 7.61 0.50 0.55 0.55	4019 4020 4021 4021 4022 4024 4025 4026 4027 4026 4027 4028 4031 4033 4034	0.29 0.58 0.60 0.62 0.17 0.38 0.16 0.90 0.55 1.65 1.60	N ANY O NIED BY SEMEN' 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS22 74LS22 74LS22 74LS22 74LS22 74LS22	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS253 74LS255 74LS255 74LS256 74LS257 74LS258 74LS273 74LS273 74LS273 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS366 74LS293 74LS293 74LS293 74LS293	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 1.90 0.23 0.90 0.24 0.44 0.55 0.34 0.45
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L R0-3-2513U ZN450E 7805 7812	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 0.64 7.99 7.70 7.61 0.50 0.55 0.55	DISCOUL IF ACCO ADV 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4035	0.29 0.58 0.60 0.17 0.38 0.16 0.16 1.65 1.60 1.55	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS22 74LS22 74LS26 74LS27	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS258 74LS266 74LS266 74LS273 74LS273 74LS273 74LS273 74LS279 74LS283 74LS283 74LS283 74LS293 74LS365 74LS366 74LS367 74LS367	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.48 0.38 1.90 0.34 0.44 0.45 0.45 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1488 MC1488 MC14411 MC14411 R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG ZBO FJ ZBO CPU ZBO A CPU ZBO A CPU ZBO CTC	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 6.94 7.99 7.70 7.61 0.50 0.55 0.55	DISCOUL IF ACCC AD' 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4031 4031 4031 4035 4040 4041	0.29 0.58 0.62 0.17 0.38 0.16 0.16 0.19 0.30 0.55 1.65 1.65 1.65 1.65	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS09 74LS11 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS27 74LS27 74LS27 74LS28 74LS28 74LS28	0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.14 0.14 0.14 0.14 0.14 0.14	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS253 74LS255 74LS258 74LS256 74LS266 74LS273 74LS273 74LS273 74LS283 74LS293 74LS293 74LS293 74LS365 74LS365 74LS367 74LS373 74LS373	0.79 0.79 1.34 1.00 0.88 0.39 0.44 0.38 0.90 0.23 0.90 0.34 0.56 0.45 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L R0-3-2513L R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG ZBO FJ ZBO CPU ZBO CPU ZBO CTC ZBO CTC ZBO CTC	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.70 7.70 7.61 0.50 0.55 0.55	DISCOUL 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4040 4041 4042	0.29 0.50 0.60 0.62 0.17 0.38 0.16 0.90 0.30 0.55 1.65 1.60 1.55 1.60 1.57 0.57	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS26 74LS27 74LS27 74LS28 74LS28 74LS28 74LS28 74LS28 74LS28 74LS28 74LS30 74LS30 74LS31	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.19 0.19	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS257 74LS257 74LS257 74LS258 74LS258 74LS261 74LS261 74LS261 74LS273 74LS273 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS283 74LS366 74LS366 74LS367 74LS367 74LS377	0.79 0.79 1.34 1.00 0.88 0.39 0.39 0.38 0.38 0.38 0.38 0.44 0.34 0.45 0.45 0.45 0.34 0.34 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513U ZN450E 7805 7812 7905 7812 7905 ZILOG ZBO FJ ZBO CPU ZBO CPU ZBO A CTC ZBO A CTC ZBO ART	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 6.94 7.79 7.70 7.70 7.70 0.50 0.55 0.55	DISCOUL 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4031 4033 4034 4040 4040 4040 4040 4040 4040 4050 4060 407	0.29 0.58 0.62 0.17 0.30 0.55 1.60 1.55 0.72 0.72 0.69 0.59	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS14 74LS15 74LS20 74LS20 74LS21 74LS21 74LS22 74LS22 74LS27 74LS27 74LS28 74LS28 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS33	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14	74LS243 74LS244 74LS244 74LS248 74LS248 74LS251 74LS253 74LS255 74LS258 74LS258 74LS261 74LS261 74LS263 74LS279 74LS283 74LS283 74LS283 74LS283 74LS366 74LS366 74LS367 74LS377 74LS377 74LS377	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 1.90 0.24 0.34 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 R0-3-2513L R0-3-2513L ZN450E 7805 7812 7905 7912 ZILOG ZBO FI ZBO CPU ZBO A DART	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.70 7.50 0.55 0.55	DISCOUL IF ACCO ADV 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4040 4041 4042 4043 4044 4043	0.29 0.52 0.60 0.62 0.16 0.30 0.55 1.65 0.72 0.54 0.54	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS11 74LS11 74LS12 74LS13 74LS14 74LS15 74LS22 74LS22 74LS26 74LS27 74LS28 74LS27 74LS28 74LS28 74LS28 74LS28 74LS28 74LS28 74LS33 74LS33 74LS33 74LS33 74LS33	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.14	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS256 74LS258 74LS261 74LS261 74LS279 74LS273 74LS376 74LS366 74LS367 74LS367 74LS377 74LS377	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.38 1.90 0.23 0.23 0.44 0.45 0.45 0.45 0.45 0.45 0.45 0.34 0.34 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513U ZN450E 7805 7812 7905 7812 7905 ZILOG ZBO FJ ZBO CPU ZBO CPU ZBO A CTC ZBO A CTC ZBO ART	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 6.94 7.79 7.70 7.70 7.70 0.50 0.55 0.55	DISCOUL 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4031 4033 4034 4040 4040 4040 4040 4040 4040 4050 4060 407	0.29 0.58 0.60 0.62 0.17 0.38 0.16 0.16 0.55 1.65 1.65 0.77 0.57 0.57 0.59 0.69	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS14 74LS15 74LS20 74LS20 74LS21 74LS21 74LS22 74LS22 74LS27 74LS27 74LS28 74LS28 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS32 74LS33	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS253 74LS255 74LS255 74LS256 74LS273 74LS273 74LS283 74LS273 74LS283 74LS293 74LS293 74LS293 74LS293 74LS293 74LS366 74LS377 74LS377 74LS377 74LS377 74LS378 74LS378	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 1.90 0.23 0.90 0.34 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-25131 ZN450E 7805 7812 7905 7912 ZILOG ZBO FJ ZBO CPU ZBO CTC ZBO DART ZBO D	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 0.50 0.55 0.55 0.55 AMILY 4.00 4.00 7.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4031 4034 4034 4034 4040 4041 4042 4043 4044 4045 4047	0.29 0.58 0.60 0.62 0.17 0.38 0.16 0.99 0.30 0.55 1.65 1.65 0.57 0.57 0.54 0.54 0.54 0.54 0.64	NANY ONIED BY SEMEN' 74LS08 74LS08 74LS08 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS20 74LS21 74LS22 74LS26 74LS27 74LS28 74LS30 74LS33 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38 74LS38	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.12 0.44 0.13 0.12 0.14 0.19 0.14 0.19 0.19 0.19 0.19 0.19 0.19 0.19	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS256 74LS258 74LS261 74LS261 74LS279 74LS273 74LS376 74LS366 74LS367 74LS367 74LS377 74LS377	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.44 0.38 0.38 1.90 0.23 0.23 0.44 0.45 0.45 0.45 0.45 0.45 0.45 0.34 0.34 0.34 0.34 0.34
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L ZN450E 7805 Z812 7905 Z812 Z80A CPU Z80A CPU Z80A CPU Z80A CTC Z80 DART Z80A DART Z80A DMA	2.10 28.09 22.59 56.63 OUS 3.90 3.45 6.95 0.64 6.94 0.64 6.94 7.70 7.70 7.70 7.70 0.50 0.55 0.55 0.55	DISCOUL IF ACCO ADV 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042 4043 4044 4045 4046 4047 4048	0.29 0.58 0.60 0.62 0.17 0.38 0.60 0.55 1.65 0.72 0.57 0.54 0.69 0.54 0.68 0.68 0.68 0.54	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS09 74LS11 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS26 74LS27 74LS28 74LS32 74LS32 74LS32 74LS33 74LS33 74LS33 74LS33 74LS33 74LS34 74LS34 74LS34 74LS34 74LS34 74LS37 74LS38 74LS37 74LS38 74LS37 74LS38 74LS44 74LS44 74LS44 74LS47	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.12 0.14 0.14 0.19 0.12 0.14 0.19 0.19 0.19	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS258 74LS266 74LS266 74LS266 74LS266 74LS273 74LS279 74LS283 74LS293 74LS293 74LS293 74LS366 74LS367 74LS367 74LS368 74LS368 74LS368 74LS377 74LS377 74LS378 74LS378 74LS378	0.79 0.79 0.79 0.89 1.34 1.40 0.38 0.38 0.38 0.38 0.38 0.39 0.44 0.45 0.34 0.44 0.47 0.80 0.64 0.28 0.64 0.28 0.64 0.74 0.74 0.80 0.64 0.28 0.50 0.50
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-2513L R0-3-2513L R0-3-2513U ZN450E 7805 7812 7905 ZILOG ZBO FI ZBO CPU ZBO CPU ZBO CTC ZBO DART ZBO DAR	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.70 7.70 7.61 0.50 0.55 0.55 0.55	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4035 4040 4041 4042 4043 4044 4045 4046 4047 4046 4047 4048 4049	0.29 0.58 0.60 0.60 0.62 0.17 0.38 0.17 0.38 0.17 0.38 0.17 0.38 0.17 0.55 1.65 1.65 0.55 0.57 0.64 0.64 0.64 0.64 0.68 0.68 0.68	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS11 74LS12 74LS13 74LS14 74LS13 74LS14 74LS22 74LS20 74LS27 74LS28 74LS30 74LS33 74LS30 74LS30 74LS30 74LS34 74LS34 74LS38 74LS38 74LS38 74LS38 74LS37 74LS38 74LS38 74LS37 74LS38 74LS40 74LS47 74LS48	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.12 0.14 0.14 0.19 0.14 0.19 0.14 0.16 0.16 0.16	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS257 74LS257 74LS256 74LS256 74LS261 74LS266 74LS266 74LS266 74LS273 74LS293 74LS293 74LS293 74LS293 74LS293 74LS293 74LS366 74LS366 74LS367 74LS377 74LS377 74LS377 74LS378 74LS379 74LS379 74LS386 74LS379 74LS379 74LS379	0.79 0.79 0.79 0.89 1.34 1.40 0.38 0.38 0.38 0.38 0.38 0.39 0.44 0.45 0.34 0.44 0.47 0.80 0.64 0.28 0.64 0.28 0.64 0.74 0.74 0.80 0.64 0.28 0.50 0.50
ZN429E-8 ZN432CJ-10 ZN430CJ-10 ZN440 MISCELLANE MISCELLANE M-3-1015 AY-5-1015 AY-5-1013 AY-5-2376 MC1488 MC14411 MC14412 R0-3-25131 ZN450E 7805 7812 7905 7812 7905 7812 7905 ZELOG ZBO FJI ZBO CPU ZBO CTC ZBO ACTC ZBO A	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 6.94 7.70 7.70 7.61 0.50 0.55 0.55 0.55 0.40 4.00 4.00 4.00	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4031 4033 4034 4041 4042 4040 4041 4042 4044 4045 4046 4047 4048 4049 4050	0.29 0.58 0.60 0.17 0.30 0.55 1.60 1.55 1.60 1.55 0.72 0.54 0.54 0.54 0.68 0.68 0.68	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS09 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS21 74LS22 74LS22 74LS28 74LS28 74LS28 74LS28 74LS32 74LS32 74LS33 74LS33 74LS33 74LS33 74LS34 74LS44 74LS45 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.15 0.12 0.14 0.15 0.12	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS253 74LS255 74LS256 74LS256 74LS261 74LS261 74LS263 74LS273 74LS283 74LS283 74LS366 74LS366 74LS367 74LS377 74LS377 74LS377 74LS378 74LS377 74LS378 74LS379 74LS379 74LS379	0.79 0.79 0.79 0.89 1.34 1.40 0.38 0.38 0.38 0.38 0.38 0.39 0.44 0.45 0.34 0.44 0.47 0.80 0.64 0.28 0.64 0.28 0.64 0.74 0.74 0.80 0.64 0.28 0.50 0.50
ZN429F-8 ZN432CJ-10 ZN43CJ-10 ZN440 MISCELLANE MY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14488 MC14411 MC14412 R0-3-2513L R0-3-2513L R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG ZBO FJ ZSOA CPU ZSOA CPU ZSOA CPU ZSOA CPU ZSOA CPU ZSOA CPU ZSOA DMA ZSOA DMA ZSOA DMA ZSOA DMA ZSOA PIO ZSOA PIO ZSOA PIO ZSOS DIO-0	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.70 7.70 7.61 0.50 0.55 0.55 0.55	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4035 4040 4041 4042 4043 4044 4045 4046 4047 4046 4047 4048 4049	0.29 0.58 0.60 0.60 0.62 0.17 0.38 0.17 0.38 0.17 0.38 0.17 0.38 0.17 0.55 1.65 1.65 0.55 0.57 0.54 0.54 0.64 0.64 0.64 0.68 0.68	NANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS11 74LS12 74LS13 74LS14 74LS13 74LS14 74LS22 74LS20 74LS27 74LS28 74LS30 74LS33 74LS30 74LS30 74LS30 74LS34 74LS34 74LS38 74LS38 74LS38 74LS38 74LS37 74LS38 74LS38 74LS37 74LS38 74LS40 74LS47 74LS48	0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.14 0.14 0.14 0.14 0.15 0.14 0.16 0.16 0.16 0.16 0.16 0.16 0.16 0.16	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS257 74LS257 74LS256 74LS256 74LS261 74LS266 74LS266 74LS266 74LS273 74LS293 74LS293 74LS293 74LS293 74LS293 74LS293 74LS366 74LS366 74LS367 74LS377 74LS377 74LS377 74LS378 74LS379 74LS379 74LS386 74LS379 74LS379 74LS379	0.79 0.79 0.79 0.89 1.34 1.40 0.38 0.38 0.38 0.38 0.38 0.39 0.44 0.45 0.34 0.44 0.47 0.80 0.64 0.28 0.64 0.28 0.64 0.74 0.74 0.80 0.64 0.28 0.50 0.50
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE MY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 R0-3-2513L R0-3-2513L ZN450E Z80 F0 Z80 F0 Z80 CPU Z80 C	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 7.99 0.55 0.55 0.55 0.55 0.55 0.70 4.00 4.00 4.00 4.00 4.00 7.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042 4043 4044 4045 4046 4047 4048 4049 4050 4051 4050 4051 4051 4051 4051 4051 4051 4051 4051 4051 4051 4051 4051 4051 4053	0.29 0.54 0.59 0.64 0.59 0.68 0.69 0.59 0.64 0.59 0.69 0.59	N ANY O NIED BY SEMEN' 74LS05 74LS08 74LS08 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS26 74LS27 74LS28 74LS32 74LS32 74LS33 74LS33 74LS33 74LS33 74LS34 74LS44 74LS54 74LS48 74LS48 74LS48 74LS48 74LS48 74LS48 74LS54 74LS54 74LS54 74LS54 74LS58 74LS54 74LS54 74LS54 74LS54 74LS54 74LS54 74LS54 74LS54 74LS55	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.14 0.14 0.16 0.16 0.16 0.13 0.13 0.13	74LS243 74LS244 74LS245 74LS247 74LS248 74LS254 74LS251 74LS255 74LS255 74LS256 74LS256 74LS256 74LS261 74LS267 74LS273 74LS273 74LS273 74LS273 74LS293 74LS293 74LS293 74LS293 74LS366 74LS366 74LS367 74LS367 74LS377 74LS377 74LS378 74LS37	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.44 0.44 0.56 0.45 0.44 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.34 0.35 0.35 0.35 0.35 0.35 0.35 0.35 0.35
ZN429E-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE MISCELLANE M-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14488 MC14411 MC14412 R0-3-25131 ZN450E 7805 7812 7905 7812 7905 7812 7905 ZELOG ZBO FJ ZBO CPU ZBO AC CPU ZBO	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 6.94 7.99 7.70 0.50 0.55 0.55 AMILY 4.00 4.00 4.00 4.00 4.00 4.00 4.00 4.0	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4035 4040 4041 4042 4043 4044 4045 4046 4047 4048 4049 4050 4051 4052 4053 4054 4054 4054 4054 4054 4054 4054 4055 4056 4057 4058 4068 407	0.29 0.58 0.60 0.60 0.60 0.60 0.60 0.55 1.65 1.65 0.55 0.57 0.64 0.59 0.54 0.64 0.64 0.64 0.64 0.68 0.69 0.69 0.69 0.69 0.69 0.69 0.69 0.69	N ANY O NIED BY SEMEN' 74LS08 74LS08 74LS09 74LS11 74LS12 74LS13 74LS14 74LS13 74LS14 74LS22 74LS20 74LS27 74LS28 74LS30 74LS33 74LS30 74LS38 74LS38 74LS38 74LS38 74LS38 74LS37 74LS38	0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.13 0.12 0.14 0.19 0.19 0.19 0.19 0.19 0.19 0.19 0.19	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS257 74LS257 74LS257 74LS258 74LS279 74LS283 74LS279 74LS283 74LS283 74LS293 74LS366 74LS366 74LS367 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS379 74LS389 74LS399 74LS399 74LS399 74LS399 74LS398 74LS399	0.79 0.79 0.89 1.34 1.00 0.89 0.39 0.39 0.44 0.38 0.38 0.38 0.38 0.38 0.44 0.45 0.45 0.45 0.45 0.45 0.45 0.45
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE MY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1488 MC1489 MC14411 MC14412 R0-3-2513L R0-3-2513L ZN450E 7805 Z8102 ZN450E Z80 F/U Z80A CPU Z80A DART Z80A SIO-0 Z80A SIO-0 Z80A SIO-0 Z80A SIO-1	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 7.99 7.70 7.70 7.61 0.50 0.55 0.55 AMILY 4.00 4.00 4.00 7.18 11.52 9.99 3.78 3.78 3.78 3.78 3.78 3.78 3.78 3.78	DISCOUL 4019 4020 4021 4022 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042 4044 4045 4046 4047 4048 4049 4051 4052 4053 4054 4055 405	0.29 0.58 0.62 0.17 0.38 0.60 0.16 0.90 0.30 0.55 1.65 1.60 1.55 0.72 0.54 0.59 0.64 0.68 0.68 0.68 0.68 0.69 0.69 0.69 0.69 0.69 0.69 0.69 0.69	N ANY O NIED BY SEMEN' 74LS05 74LS08 74LS08 74LS09 74LS11 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS27 74LS28 74LS27 74LS28 74LS30 74LS30 74LS30 74LS31 74LS31 74LS31 74LS31 74LS31 74LS32 74LS33 74LS37 74LS38 74LS37 74LS37 74LS37 74LS37 74LS37 74LS37	0.13 0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.13 0.39 0.65 0.59 0.14 0.15 0.20 0.17	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS256 74LS256 74LS266 74LS266 74LS266 74LS273 74LS273 74LS273 74LS273 74LS273 74LS283 74LS293 74LS293 74LS366 74LS366 74LS367 74LS368 74LS368 74LS368 74LS377 74LS377 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS379 74LS399 74LS399 74LS399 74LS399 74LS399	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.39 0.44 0.38 0.38 0.38 0.38 0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.35 0.39 0.39 0.39 0.39 0.39 0.39 0.39 0.39
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE AY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14488 MC14411 MC14412 R0-3-2513L R0-3-2513L R0-3-2513U ZN450E 7805 ZN450E ZN60 CPU ZN0	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.70 7.70 7.50 0.55 0.55 0.55 0.55 0.55	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4033 4034 4033 4034 4033 4034 4040 4041 4042 4044 4045 4040 4040 4044 4045 4046 4047 4048 4051 4051 4052 4053 4054 4051 4052 4054 4055 4066 4077 4088 4089 4099 4095 4096 4097 4098 4098 4098 4098 4098 4099 4098 409	0.29 0.58 0.60 0.62 0.17 0.38 0.60 0.55 1.65 0.57 0.57 0.64 0.68 0.68 0.68 0.59 0.68 0.50 0.50 0.50 0.50 0.50 0.50 0.50 0.5	NANY O NIED BY SEMEN' 74LS05 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS14 74LS15 74LS22 74LS26 74LS27 74LS28 74LS33 74LS33 74LS33 74LS34 74LS34 74LS35 74LS37 74LS38 74LS40 74LS42 74LS54 74LS51 74LS55 74LS51 74LS55 74LS73 74LS58 74LS73 74LS58 74LS74 74LS55 74LS73 74LS55 74LS73 74LS74 74LS74 74LS74 74LS74 74LS74 74LS74 74LS75	0.13 0.13 0.13 0.13 0.14 0.15 0.15 0.16 0.16 0.16 0.16 0.15 0.15 0.15 0.15 0.17 0.28 0.28 0.28 0.28 0.28 0.28 0.28 0.28	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS257 74LS256 74LS256 74LS266 74LS266 74LS267 74LS273 74LS273 74LS283 74LS293 74LS293 74LS283 74LS366 74LS377 74LS377 74LS377 74LS378 74LS378 74LS378 74LS379 74LS386 74LS379 74LS386 74LS379 74LS3878 74LS388 74LS379 74LS388 74LS3893 LDW PROFILE 1 SOCKETS Number of Pins 8 14 16 18 20	0.79 0.79 0.79 0.89 1.34 0.30 0.39 0.44 0.38 0.38 0.38 0.34 0.44 0.45 0.50 0.50 0.50 0.50 0.50 0.5
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE MY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC1488 MC1489 MC14411 MC14412 R0-3-2513L R0-3-2513L ZN450E 7805 Z8102 ZN450E Z80 F/U Z80A CPU Z80A DART Z80A SIO-0 Z80A SIO-0 Z80A SIO-0 Z80A SIO-1	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 7.99 7.70 7.70 7.61 0.50 0.55 0.55 AMILY 4.00 4.00 4.00 7.18 11.52 9.99 3.78 3.78 3.78 3.78 3.78 3.78 3.78 3.78	DISCOUL 4019 4020 4021 4022 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4035 4040 4041 4042 4044 4045 4046 4047 4048 4049 4051 4052 4053 4054 4055 405	0.29 0.58 0.62 0.17 0.38 0.60 0.16 0.90 0.30 0.55 1.65 1.60 1.55 0.72 0.54 0.59 0.64 0.68 0.68 0.68 0.68 0.69 0.69 0.69 0.69 0.69 0.69 0.69 0.69	N ANY O NIED BY SEMEN' 74LS05 74LS08 74LS08 74LS09 74LS11 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS20 74LS20 74LS27 74LS28 74LS27 74LS28 74LS32 74LS33 74LS33 74LS33 74LS33 74LS34 74LS49 74LS49 74LS54 74LS54 74LS554 74LS55 74LS57 74LS58	0.13 0.13 0.13 0.13 0.13 0.13 0.14 0.15 0.22 0.44 0.14 0.14 0.14 0.14 0.19 0.12 0.14 0.16 0.16 0.16 0.13 0.39 0.65 0.59 0.14 0.15 0.20 0.17	74LS243 74LS244 74LS245 74LS247 74LS248 74LS251 74LS251 74LS255 74LS258 74LS256 74LS256 74LS266 74LS266 74LS266 74LS273 74LS273 74LS273 74LS273 74LS273 74LS283 74LS293 74LS293 74LS366 74LS366 74LS367 74LS368 74LS368 74LS368 74LS377 74LS377 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS378 74LS379 74LS399 74LS399 74LS399 74LS399 74LS399	0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.39 0.44 0.38 0.38 0.38 0.38 0.34 0.44 0.56 0.45 0.34 0.34 0.34 0.34 0.34 0.34 0.35 0.39 0.39 0.39 0.39 0.39 0.39 0.39 0.39
ZN429E-8 ZN432CJ-10 ZN43CJ-10 ZN440 MISCELLANE MISCELLANE M-3-1015 AY-5-1013 AY-5-1013 AY-5-2376 MC1488 MC14488 MC14411 MC14412 R0-3-2513U ZN450E 7805 7812 7905 7912 ZILOG ZBO FJ ZBO CPU ZBO AC CPU ZBO AC CTC ZBO DART ZBO DART ZBO DART ZBO ADART ZBO ADART ZBO ADART ZBO ADART ZBO ADART ZBO SIO-0 ZBO SIO-0 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-2 ZBO SIO-0 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1 ZBO SIO-2 ZBO SIO-1	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 6.94 7.99 7.70 7.61 0.50 0.55 0.55 0.55 0.55 0.7.70 1.18 7.18 7.18 7.18 7.18 7.18 7.18 7.18	DISCOUL 4019 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4033 4034 4031 4035 4041 4042 4043 4044 4044 4045 4046 4047 4048 4049 4050 4051 4052 4053 4054 4056 4066 4077 4088 4089 409	0.29 0.58 0.60 0.17 0.30 0.55 1.60 1.55 1.60 1.55 0.72 0.57 0.69 0.54 0.69 0.54 0.68 0.68 0.68 0.68 0.69 0.72 0.74 0.75	NANY O NIED BY SEMEN: 74LS08 74LS08 74LS08 74LS09 74LS10 74LS11 74LS12 74LS13 74LS15 74LS20 74LS20 74LS21 74LS21 74LS22 74LS28 74LS28 74LS32 74LS33 74LS33 74LS34 74LS34 74LS55 74LS47 74LS58 74LS47 74LS58 74LS47 74LS58 74LS78 74LS78 74LS78 74LS78 74LS78 74LS78 74LS78 74LS78 74LS78	0.13 0.13 0.13 0.13 0.13 0.13 0.13 0.13	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS253 74LS255 74LS256 74LS256 74LS279 74LS261 74LS279 74LS283 74LS293 74LS293 74LS293 74LS293 74LS293 74LS293 74LS366 74LS377 74LS378 74LS377 74LS378 74LS37	0.79 0.79 0.79 0.89 1.34 0.39 0.39 0.44 0.38 0.38 0.38 0.34 0.44 0.44 0.74 0.47 0.47 0.49 0.89 0.89 0.89 0.90 0.90 0.90 0.90 0.9
ZN429F-8 ZN432CJ-10 ZN433CJ-10 ZN440 MISCELLANE MY-3-1015 AY-5-1013 AY-5-2376 MC1488 MC14489 MC14411 MC14412 R0-3-2513L R0-3-2513L ZN450E Z806 Z806 Z806 Z806 Z806 Z806 Z806 Z806	2.10 28.09 22.59 56.63 0US 3.90 3.45 6.95 0.64 0.64 7.99 0.55 0.55 0.55 0.55 0.55 0.55 0.55 0	DISCOUL F ACCO ADV 4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4031 4034 4035 4040 4041 4045 4046 4047 4048 4049 4051 4055 4060 4051 4055 4060 4056 4068 4069	0.29 0.54 0.54 0.54 0.54 0.59 0.64 1.66 0.68 0.54 0.59 0.64 0.68 0.69 0.69 0.69 0.69 0.69 0.69 0.69 0.69	N ANY O NIED BY SEMEN' 74LS05 74LS08 74LS08 74LS09 74LS11 74LS12 74LS13 74LS14 74LS15 74LS20 74LS26 74LS27 74LS28 74LS32 74LS32 74LS33 74LS33 74LS33 74LS34 74LS44 74LS15 74LS54 74LS55 74LS57 74LS58	0.13 0.13 0.13 0.13 0.13 0.13 0.13 0.13	74LS243 74LS244 74LS245 74LS248 74LS248 74LS251 74LS251 74LS251 74LS255 74LS256 74LS256 74LS256 74LS266 74LS273 74LS293 74LS293 74LS293 74LS293 74LS293 74LS366 74LS366 74LS367 74LS367 74LS366 74LS377 74LS378 74LS378 74LS378 74LS379 74LS378 74LS379 74LS378 74LS379 74LS378 74LS379 74LS378 74LS379 74LS378 74LS37	0.79 0.79 0.79 0.89 1.34 1.00 0.88 0.39 0.39 0.39 0.44 0.38 0.38 0.38 0.34 0.44 0.44 0.66 0.39 0.79 0.70 0.70 0.70 0.70 0.70 0.70 0.7
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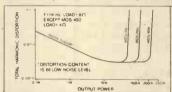
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MO\$ 200	120w/4-812	<0 005% <0 006%	+55+60	120 × 78 × 80	850	£38 48	£33 46
MOS 400	240w/4Ω	<0 005% <0 006%	±55±60	120 × 78 × 100	1025	£52 20	£45 39

MOSFET Ultra-Fi without heatsinks

ľ	MOS 120P	60w/4-8s2	< 0 005% < 0 006%	+45±50	120 × 26 × 40	215	£58 85	£23 32
ı	MOS 200P	120w/4-852	< 0 005% < 0 006%	+55±60	120×26×80	420	£32 81	£28 53
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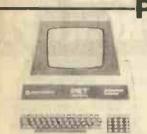
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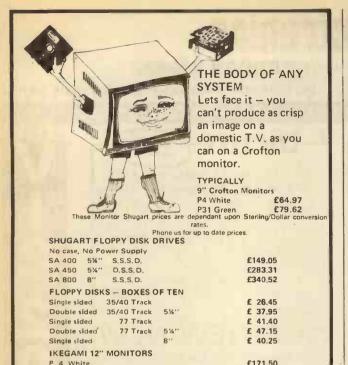
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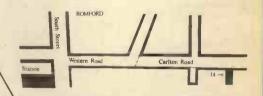
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dio & Ele								10
dio & Ele								
dio & Ele								1.0
dio & Ele								
dio & Ele		s School						11
ronics	ctronic	s School						75
ronics								13
ronics								72
ronics								74
								84
								82
								83
								84
								7
								Ar
for								49
								83
achine 8	Tool)							41
								7:
								12
								7:
				2				8
								80
								75 7
								75, 7
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						75, 77	, 79, 81,	83, 8
								7:
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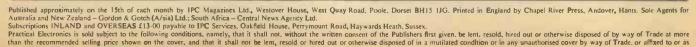
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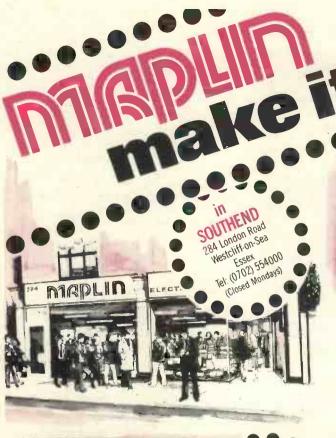
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