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M60

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## The Sinclair ZX80.

Until now, building your own computer could easily cost around $£ 300$ - and still leave you with only a bare board for your trouble. The Sinclair ZX80 changes all that. For just $£ 79.95$ you get everything you need to build a personal computer at home... PCB, with IC sockets for all ICs; case; leads for direct connection to your own cassette recorder and television; everything!
And yet the ZX80 really is a complete, powerful, full-facilizy computer, matching or surpassing other personal computers on the market at several times the price. The ZX80 is programmed in BASIC, and you could use it to do quite literally anything from playing chess to running a power station.
The ZX80 is pleasantly straightforward to assemble, using a fine-tipped soldering iron. Once assembled, it immediately proves what a good job you've done. Connect it to your TV set... link it to an appropriate power source *. and you're ready to go.

## Your 2X80 kit contains...

- Printed circuit board, with IC sockets for all ICs.
- Complete components set, including all ICs - all manufactured by selected worldleading suppliers.
- New rugged Sinclair keyboard, touchsensitive, wipe-clean.
- Ready-moulded case.
- Leads and plugs for connection to any portable cassette recorder (to store programs) and domestic TV (to act as VDU).
- FREE course in BASIC programming and user manual.
Optional extras
- Mains adaptor of 600 mA at 9 V IC nominal unregulated (available separately - see coupon).
- Additional memory expansion board plugs in to take up to 3 K bytes extra RAM chips. (Chips also available see coupon.)
*Use a 600 mA at 9 V DC nominal unregulated mains adaptor. Available from Sinclair if desired (sec coupon).

Two unique and valuable components of the Sinclair ZX80.
The Sinclair $Z \mathrm{X} 80$ is not just another personal computer. Quite apart from its exceptionally low price, the ZX 80 has two uniquely advanced components: the Sinclair BASIC interpreter; and the Sinclair teach-yourself BASIC manual.
The unique Sinclair BASIC interpreter... offers these remarkable programming advantages

- Unique 'one-touch' key word entry: the ZX80 eliminates a great deal of tiresome typing. Key words (RUN, PRINT, LIST, etc.) have their own single-key entry.
- Unique syntax check. Only lines with correct syntax are accepted into programs. A cursor identifies errors immediately. This prevents entry of long and complicated programs with faults only discovered when you run them.
- Excellent string-handling capability - takes up to 26 string variables of any length. All strings can undergo all relational tests (e.g. comparison). The ZX 80 also has string inputto request a line of text when necessary. Strings do not need to be dimensionied.
- Up to 26 single dimension arrays.
- FOR/NEXT loops nested up 26.
- Integer names of any length.
- BASIC language also handles full Boolean arithmetic, conditional expressions, etc.
- Exceptionally powerful edit facilities, allows modification of existing program lines.
- Randomise function, useful for games and secret codes, as well as more serious applications.
- Timer under program control.
- PEEK and POKE enable entry of machine code instructions, USR causes jump to a user's machine language sub-routine.
- High-resolution graphics with 22 standard graphic symbols.
- All characters printable in reverse under program control.
... and the Sinclair teach-yourself BASIC manual.
If the features of the Sinclair interpreter listed alongside mean little to you-don't worry. They're all explained in the specially-written 96 -page book free with every kit! The book makes learning easy, exciting and enioyable, and represents a complete course in BASIC pro-gramming-from first principles to complex programs. (Available separately-purchase price refunded if you buy a ZX 80 later.)



## Fewer chips, compact design, volume production more power per pound!

The ZX 80 owes its remarkable low price to its remarkable design: the whole system is packed onto fewer, newer, more powerful and advanced LSI chips. A single SUPER ROM, for instance, contains the BASIC interpreter, the character set, operating system, and monitor. And the ZX80's 1 K byte RAM is roughly equivalent to 4 K bytes in a conventional computer, because the ZX80's brilliant design packs the RAM so much more tightly. (Key words, for instance, occupy just a single byte.)

To all that, add volume production -and you've that rare thing: a price breakthrough that really is a breakthrough.

The Sinclair ZX80. Kit: £79.95. Assembled: £99.95. Complete!

The ZX80 kit costs a mere $£ 79.95$. Can't wait to have a ZX80 up and running? No problem! It's also available, ready assembled, for only ${ }^{6} 99.95$.

Whether you choose the kit or the readymade, you can be sure of world-famous Sinclair technology - and years of satisfying use. (Science of Cambridge Ltd is one of the Sinclair companies owned and run by Clive Sinclair.)

To order, complete the coupon, and post to Science of Cambridge for delivery within 28 days. Return as received within 14 days for full money refund if not completely satisfied.


Science of Cambridge Ltd
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## Order Form

To: Science of Cambridge Ltd, 6 Kings Parade, Cambridge, Cambs., CB2 1SN. Remember: all prices shown include VAT, postage and packing. No hidden extras.

Please send me:

| Quantity | Item | Item price | Total |
| :---: | :---: | :---: | :---: |
|  | Sinclair ZX80 Personal Computer kit(s). Price includes ZX80 BASIC manual, excludes mains adaptor. | 79.95 |  |
|  | Ready-assembled Sinclair ZX80 Personal Computer(s). Price includes ZX80 BASIC manual, excludes mains adaptor. | 99.95 |  |
|  | Mains Adaptor(s) ( 600 mA at 9 V DC nominal unregulated). | 8.95 |  |
|  | Memory Expansion Board(s) (takes up to 3K bytes). | 12.00 |  |
|  | RAM Memory chips - standard IK bytes capacity. | 16.00 |  |
|  | Sinclair ZX80 Manual(s) (manual free with every 7.X80 kit or ready-made computer). | 5.00 |  |
| NB. Your Sinclair ZX80 may qualify as a business expensc. |  | TOTAL |  |
| I enclose a cheque/postal order payable to Science of Cambridge Ltd, for $\mathcal{L}$ Please print <br> Name: $\mathrm{Mr} / \mathrm{Mrs} / \mathrm{Miss}$ |  |  |  |
| Address |  |  |  |

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MODEL LC18 18 watts


Lightweight, high-performance iron for all soldering from calculators to T.V. sets. Fitted with 3.2 mm bit and complete with spare bits $1.6 \mathrm{~mm}, 2.4 \mathrm{~mm}$ and $4.7 \mathrm{~mm} . £ 7.89$ including $P \& P$ and V.A.T. 240 volts standard but also available 12 and 24 volts.

MODEL LA12 12 watts


Similar to LC18 but with extra slim shaft and bits for fine work. Fitted with 2.4 mm bit and complete with spare bits 1.2 mm and 3.2 mm £6. 69 including $P$ \& $P$ and V.A.T. 240 volts standard, also available 6, 12 and 24 volts.

No. 3 SAFETY SPRING STAND for LC18 \& LA12

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LIGHT SOLDERING DEVELOPMENTS LIMITED

## YOURS DISGUSTEDLY

DEAR Sir, I am disgusted that in your recent car washing machine project you failed to give the winding details of the special toroidal transformer used in the automatic brush plunger. It would also have been very useful to have exact dimensions of the stainless steel slop tank to enable me to make one up.
I was annoyed to find this article was little more than a kit review, because some parts are only available from one supplier and no manufacturer's name has been given."
"Dear Sir, I am disgusted that in your recent automatic flasher project no kit of parts seems to be available. I have had to buy the p.c.b. from one supplier, the case from another and other components from a third source. Would it not be possible to arrange a kit of parts from one supplier for all your projects. This would greatly assist readers.'
These are of course fictitious letters but are typical of many we receive.
Assuming we have identified a requirement amongst readers for a car wash and have achieved a useful, inexpensive, working design-maybe after years of trying-we find that some of the components required are not available to the hobbyist and others
have to be specially made. The toroidal transformer, for instance, employs a new core material, only available from one industrial supplier who operates a minimum order charge of $£ 20$ (not at all unusual). The core then requires a primary winding of 1,000 turns and a secondary of 400 turns.

The questions we must ask ourselves are:
(1) Are many readers going to want to wind their own toroids and, if so, is it a practical proposition.
(2) Can we arrange manufacture of the complete transformer at a realistic price.
In view of the complexity of this particular component and the fact that by manufacturing in quantity our supplier can obtain transformers at a very good price, there is only one practical answer.

Moving on to the slop tank; the supplier imports a special pressed stainless steel tank-essential for normal operation of the design-from the States. Is it really worth giving full details to enable constructors to buy the stainless and make up a tank. Have you tried working stainless steel? Once again we must decide if this is a practical proposition for most readers. Obviously it is not, so we are back with our one kit supplier.

The truth in many situations is that a number of parts are so specialised it is only practicable to source a complete kit (or the special bits individually).

Going to the other argument, the second letter is one that we see more often.

It would be most unfair if we went to one supplier and asked him to supply kits; that excludes all the others. If we make no recommendations all retailers have the chance to supply. However, it takes time to assess demand and decide if it is worth buying-in any parts they do not normally carry. They must also decide if their price would be competitive with other companies who may also sell the kit.

It is our policy only to mention specific companies if: they have an involvement in the design; or they can supply parts not readily available to the hobbyist; or they own copyright. We believe this is in everyone's interest, it allows competition on most projects and ensures parts are available to readers. It also allows us to bring you some exceptional designs which might otherwise be lost.

## EDITOR

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## Technical Queries

We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in Practical Electronics.

All letters requiring a reply should be accompanied by a stamped, self addressed envelope and each letter should relate to one published project only.

Components are usually available from advertisers: where we anticipate supply difficulties a source will be suggested.

## Back Numbers

Copies of most of our recent issues are available from: Post Sales Department (Practical Electronics), IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SE1 OPF, at 75p each including Inland/Overseas $\mathrm{p} \& \mathrm{p}$.

## Binders

Binders for PE are available from the same address as back numbers at $£ 4.10$ each to UK or overseas addresses, including
postage and packing, and VAT where appropriate. Orders should state the year and volume required.

## Subscriptions

Copies of PE are available by post, inland or overseas, for $£ 10.60$ per 12 issues, from: Practical Electronics, Subscription Department, Oakfield House, Perrymount Road, Haywards Heath, West Sussex RH 16 3DH. Cheques and postal orders should be made payable to IPC Magazines Limited.


## CLEANING KIT

The CK-90 multi-purpose cleaning kit from 3 M has been designed 10 clean recording heads, guides, capstans and tape paths. The cleaning solution which is also suitable for computer systems and typewriters is quick drying, leaves no residue, is non-flammable

and will not harm metals, plastics or painted surfaces.

Each kit contains two 4 oz bottles of solution. ten double-ended cleaning wands and fifty lint free wipes.

The kit is priced at $£ 11.50$ and is available from accessory shops.

## MICROPROCESSORS FOR HOBBYISTS

A new book which has just been published called Microprocessors for Hobbyists is based on two popular series from PE (Microprocessors Explained and Home Com puters) both of which were written by our regular contributor R . W. Coles.

The book is a general introduction to microprocessors. typical architecture, instruction sets. machine code programming and peripheral chips.

The home computer section covers a
typical system, the S 100 bus structure, various peripherals available and a guide to choosing a suitable system. The final chapter deals with software and the high level language BASIC. A glossary of terms is also included to explain many of the "buzz words" used with microprocessors.

Copies of the book (1SBN 0-408-00414-2) which is priced at $£ 2.95$ including p\&p are available from. Newnes-Butterworths Borough Green, Sevenoaks, Kent.

## ELECTROVALUE

The latest Electrovalue catalogue which is now available covers a wide range of items including chokes, coils, i.c.s. books and a very comprehensive range of ferrite components. There is also a complete range of Nascom microcomputers and peripheral devices.

The cataloguc is available free of charge together with a separate price list which is valid until the end of July. Updated price lists can be obtained by sending a stamped addressed envelope to Electrovalue, 28 St. Judes Road, Englefield Green, Surrey TW20 0HB.

## NEWTRONICS

Newtronics have moved to larger and more convenient premises close to Highgate tube station.

At their new showroom they will be demonstrating the popular ELF II and the new Explorer $/ 85$ computer kits and peripherals. The company are now supplying products direct to the consumer with the result that all their prices have been drastically reduced. The EIf II is reduced from $£ 79.95$ to $£ 59.95$ with all peripherals being reduced prorata.

Newtronics, 255 Archway Road, London N6 5BS.

## NEW CASE

A new size of vacuum-formed case has been introduced by Vero to compliment their existing range. Although specially designed for housing a keyboard and display panels, this enclosure has wide ranging applications in the instrument field.

Vacuum-formed from black textured ABS, the case is supplied in two sections which screw together. A flat area is provided at the rear of the case for cable entry.


This intermediate size, with outside dimensions of $340 \times 360 \times 130 \mathrm{~mm}$ is supplied with an anodised aluminium front panel $254 \times 170$ $\times 2 \mathrm{~mm}$ thick, four self-adhesive rubber feet and case assembly screws.

The price of the case $(75-2439 \mathrm{~K})$ is $£ 16.71$ excluding VAT and $p \& p$.
Vero Electronics Limited, Industrial Estate, Chandler's Ford, Eastleigh, Hants.

## L.C.D. CONNECTORS

A range of snap-on connector strips which provide an alternative to dual in-line mounting for liquid-crystal displays is now available from Hamlin Electronics. The strips, which

are available with pin lengths of 0.1 or 0.2 in . are supplied in 2 in . lengths for a range of standard displays, and have pin compatibility with the standard dual-in-line pins.

Hamlin Electronics Ltd., Diss, Norfolk, IP22 3AY.

## DEMA SYSTEMS

The Dema electronic ignition unit which is claimed to provide both petrol economy and improved performance is being marketed by Maywood Technical Developments Ltd.

The system takes a 5 to 15 V supply directly from the coil and stores 400 V in the units capacitor. The points which normally determine the build up time are used simply to trigger off a thyristor. A variable pulse width circuit determines when the voltage should be fed to the HT coil and the spark plug. The unit monitors the revs and varies the length of time the spark is at the plug in order to achieve complete combustion.

The system is priced at $£ 49.50$ including VAT and p\&p. For further information contact M.T.D. Ltd., Peake House, 232 High Street, Harlington, Hayes, Middlesex.

## FLUKE DMM

The latest DMM from Fluke is a $3 \frac{1}{2}$ digit handheld DMM ideally suited for test and service applications. Fluke claim it is the first handheld DMM to offer logic level detection, direct temperature readout. a peak-hold facility and intermittent short circuit detection in addition to a full DMM capability.

Among the many features on the 8024 A are direct temperature measuring capability from $-100^{\circ}$ to $1625^{\circ}$ with any K type thermocouple. a peak-hold facility to store and display any a.c, or d.c. voltage or current peak, fast audible continuity checking and TTL logic state indication by visual or audible signal.

The peak-hold facility opens up many interesting applications such as transient detection for example in motor or lamp starting. Additionally, with hazardous circuits the operator can safely remove the leads before reading the display.

In logic cireuits, the 8024 A gives an instant visual or audible indication of TTL logic high or low. Fast response means it can also detect pulses or pulse trains up to 100 kHz . On low frequencies. the tone warbles to give an indication of frequency level.

A fast $50 \mu$ s settling time means that it is practically impossible to heat its high speed response even by rumning the leads very quickly down, say, an edge connector. Continuity is positively indicated by an arrow

pointing up or down or by a 100 ms 2 kHz bleep.

The 8024 A has $3 \frac{1}{2}$ digit readout and a basic d.c. accuracy of 0.1 per cent. Temperature accuracy is 3 degrees $\pm 1$ digit from -20 to $+300^{\circ} \mathrm{C}$ and the instrument is specified for a full one year. The price of the 8024 A is $£ 135$ ex VAT and $p \& p$.

For details contact Fluke International Corporation, Colonial Way, Watford, Herts. WD2 4TT.

## LOGIC MONITOR

The LM-2 logic monitor from CSC is for testing digital i.c.s. It simultaneously displays the static and dynamic logic states at each pin of a 14 or 16 pin dual in-line circuit. The device comes complete with an isolated power supply and has a selectable threshold control which allows it to be used with a variety of logic families.


There are two basic units which comprise the LM-2: the connector/display unit which clips over the circuit under test and contains the comparator circuitry and 16 l.e.d. indicators: and the power-supply module. which contains a precision reference power supply and a logic-family selection switch covering CMOS. HTL. TTL. DTL and RTL circuitry.

The threshold switch is used to select the appropriate logic family, a clip lead is connected to the negative or ground line of the circuit
under test (except for CMOS. when an additional positive lead is provided), and the clip module is slipped over the circuit under test.

Typical of states that can be seen from the monitor's 16 l.e.d. display are gate inputs rising and falling, pulses passing from circuit to circuit, flip-flops changing state, and decoders and encoders accepting and recording information.

Because of the self-contained power supply, there is no loading of the circuit under test-a problem that can cause logic-level shifts, false triggering and power-supply loading with some types of equipment. In addition. the power supply, in conjunction with the comparators, also provide a constant-current drive to the display indicators, ensuring uniform brightness.

CSC Shire Hill Industrial Estate, Saffron Walden, Essex CBl1 3AQ.

## BAR CODE READER

A handheld light pen which reads standard black and white bar codes and outputs the digitally coded information. is now available from Jermyn-Mogul Distribution.

Manufactured by Hewlett-Packard and called the HEDS-3000 Digital Bar Code Wand, this low cost device is completely selfcontained. comprising optical sensor, amplifier and digitiser.

Features include a single, non-critical supply voltage. a replaceable low friction tip, push to read switch, full TTL and CMOS compatibility and solid state reliability throughout.

Apart from its already obvious use in supermarkets, a major demand for this device will be in the field of portable data entry as bar code scanning is not only faster than keyboard
entry but inherently more accurate.
One particularly interesting application is in service and repair where bar code labelled

printed circuit boards automatically set the parameters of the test equipment.

The price of the HEDS-3000 is $£ 61.79$ ex VAT. Jermyn-Mogul Distribution, Vestry. Estate, Sevenoaks, Kent. 073250155.

## VSWR/POWER METER

A combined VSWR and power meter offering direct reading of both functions without interpolation is now available from Zycomm Electronics.

In operation, the unit is autoranging for power output. covering 20 W to 2 kW in three ranges for $1.8-30 \mathrm{MHz}$ and $50-150 \mathrm{MHz}$, and 2 W to 200 W for the $430-470 \mathrm{MHz}$ range. VSWR from 1:1 to infinity can be measured.

Separate sensing heads are supplied to

cover each frequency range. and these can be connected at any position in the feed lineincluding the mast head for precise radiated power indication. Press switches on the front panel allow the selection of the appropriate head, and the display of forward and reverse power as either peak or r.m.s. readings.

The electronic comparator included in the unit allows constant readout of VSWR irrespective of power variation, i.e. gives true indication during speech on SSB.

The price of the meter is $£ 147.20 \mathrm{inc}$. VAT. Zycomm Electronics Ltd., 47, 49 and 51 Pentrich Road, Ripley, Derbys.

## The Perfect Lead...

 Acorn Microcomputer System 1Price $£ 65$ plus VAT in kit form
This compact stand-alone microcomputer is based on standard Eurocard modules, and employs the highly popular 6502 MPU (as used in APPLE, PET, KIM, etc). Throughout, the design philosophy has been to provide full expandability, versatility and economy.

## Specification

The Acorn consists of two single Eurocards.

1. MPU card

6502 microprocessor
$512 \times 8$ ACORN monitor $1 \mathrm{~K} \times 8$ RAM
16-way I/O with 128 bytes
of RAM
1 MHz crystal
5 V regulator, sockets for 2 K EPROM and second RAM I/O chip.
2. Keyboard card

25 click-keys (16 hex, 9 control)
8 digit, 7 segment display CUTS standard crystal controlled tape interface circuitry.
Keyboard instructions:
Memory Inspect/Change
(remembers last address used)
Stepping up through memory
Stepping down through memory

Set or clear break point
Restore from break
Load from tape
Store on tape
Go (recalls last address
used)
Reset
Monitor features
System program
Set of sub-routines for use in programming
Powerful de-bugging facility displays all internal registers
Tape load and store routines

## Applications

As a self teaching tool for beginners to computing. As a low cost 6502 development system for industry. As a basis for a powerful microcomputer in its expanded form.
As a control system for electronics engineers.
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(qty) Acorn Power Supply (for System 1 only) @ $£ 5.95$ plus £ 0.89 VAT.
(qty) Acorn Microcomputer assembled and tested @ £79 plus $£ 11.85$ VAT.
$\square$ (qty) Acorn VDU assembled and tested @ £98 plus $£ 14.70$ VAT.

[^1]

## Outlook

Never, in recent years, has the crysta! ball been so clouded by external events. both political and economic. The domestic scene is unclear enough with the impact of contraction of heavy industries and industrial unrest, but when international factors are added the only certainty is uncertainty. Nobody knows the eventual outcome of the Iranian revolution, of the Rhodesia elections, of the U.S. elections, the nuclear power debate, arms limitations, the expected deaths or resignations of the elderly leaders in the USSR, the future cost and availability of oil, world interest rates and inflation, squabbles in formerly stable institutions like NATO and the European Common Market.

The list seems endless and probably is, the only good point emerging being a gradual realisation that no nation can plan and conduct its affairs in isolation from events elsewhere, maybe many thousands of miles distant and sometimes, by world standards, of only a minor character.

But through all the turbulence of the late 1970 s and now for at least the early years of the 1980 s if not beyond, the electronics industry has not only survived but has grown. Growth may have been erratic, profits wobbled, ownership of companies changed, as is characteristic of a dynamic industry in response to surges in demand and in rapid technological change, but the trend is still upwards. Apart from oil, electronics is probably the safest business to be in. But oil is said to be finite while electronics will go on for ever, which is a cheerful note for a winter's day.

## Trend

The general trend was revealed in recent financial results of industry leaders. GEC showed a dip in profits, the result of heavy involvement in general engineering which was affected by the long engineering workers' strike. GEC's electronic companies continued to prosper. Racal only just
scraped home with an unblemished record of 25 years of continuous growth and profits. In their case the engineers' strike was probably more marginal in effect, the major obstruction being the greatly increased value of the pound sterling in the international market.

But both the Racal and GEC are still active on the takeover trail. Avery is now in the bag for GEC and was followed by the acquisition of the industrial robot company Hall Automation Ltd. Racal, at the time of writing, had not revealed an expected bid for all or part of Decca but, in the interim, extended overseas activities through buying 65 percent of the New York based Vikonics for $\$ 1$ million. This company will become Racal-Vikonics and as it is in the systems security business will be complementary to and a first-class U.S. outlet for Racal-MESL which came into the Group in January 1979. Racal has the option of buying the remaining shares in due course although some are likely to remain with Vikonics' founders as an incentive.

It is tedious to list orders but two are worth mentioning as significant in trend. First, for GEC whose Marconi Space and Defence Systems has booked its first defence order from China. It is for five FACE (Field Artillery Computing Equipment) systems worth $£ 1$ million with long-term business expected to follow. Second, for Racal-Milgo who in a single month recently won export orders worth $£ 3 \frac{1}{4}$ million. Only six years ago total exports for a whole year were less than this figure. The record month coincided with Racal-Milgo's move to a new headquarters building with 24,000 sq.ft. of floor area at Fleet, Hants.

Plessey, too, looks in much finer shape than for a long time and is firming everything up with yet another reorganisation.

## Distribution

Although there are a handful of large electronics component distributors in the UK and lately a sprinkling of specialist MPU and instrument distributors, most are comparatively small businesses. We all used to believe that all the little firms would eventually be swallowed up by the big fish so that perhaps only six or seven "supermarkets" would blanket the country. We were wrong. True there are some big 'uns and doing very nicely thank you, but the great bulk are over 100 small independent companies carrying on the tradition of the corner shop.

The name of the game is customer service and this is an area where the small company can, and obviously does, score. It also attracts new entrants willing to have a go on their own rather than continue working in a big organisation.

The most recent example is House of Instruments (HI) which opened for business on January 1, 1980, from premises in Saffron Walden, Essex. The key figures are Gordon Pope and Fred Hutchinson both exexecutives from Gould Advance, Pope giving up his job as chairman and Hutchinson as instrument manager. It needs courage these days to start a new business but the
two principles are extremely well-known, have a fine track record in the business and have some good products lined up as well as four salespeople on the road.

## Crime

We have all heard of computer crime but nobody knows its extent. Interpol suggests that industrialised nations are losing as much as 2.5 percent of gross national product through fiddles by white-collar workers with the bulk being due to computer fraud. But this must be guesswork as it is admitted that computer fraud can be conducted successfully for years without detection.

At a recent Interpol conference some 50 basic types of fraud were listed but each has so many variations and subtleties that a full catalogue is a practical impossibility. The solution is that police fraud squads should now receive specialist training in computer technology and programming.

## Fall of France

Chauvinistic France after years of struggle has at last surrendered to Sony advances. The news is that the Japanese company will open its first factory in France in 1981. It will be sited at Bayonne, close to the Spanish border, turning out tape recorder cassettes with a French workforce of 300 people. Sony has had a sales subsidiary in France since 1964 and has been hoping to expand ever since. The French government have now reversed their policy of exclusion in the interests of hoped-for exports and almost certain import savings to satisfy France's domestic market currently estimated at 25 million cassettes per year. Sony video cassettes will also be produced at Bayonne.

## Spin-off

I recently spent an interesting day at the Royal Signals and Radar Establishment, Malvern. This was the home of the former TRE which generated so many war-winning inventions 40 years ago. I am pleased to report that a later generation of boffins are still at it as hard as ever although the urgency is less great than in the hectic years of World War 2.

Among the projects unveiled were a new battlefield radar for ground troops, highly portable, and a novel helicopter-borne radar which uses one of the rotor blades as the scanning antenna.

Those who are worried about the level of defence spending may be re-assured that all the money spent and the technology won does not go down the military drain. Much of it goes virtually as a free gift to industry and some is charged for. RSRE has two Queen's Awards for Technology under its belt, both won in 1979, a unique event for a single organisation.

Some of the fundamental research looks as far ahead as 1995 which even the most forward-looking commercial companies would have difficulty in financing. At the same time RSRE is still supporting older projects such as the Rapier missile system which has seen continuous improvement and is a world best-seller for Britain.

## Audio

## G.Davies

THE audio opto-isolator is powered from a nine volt PP3 battery and completely isolates input from the output via an infrared light beam. Applications include safer connections from guitar to amplifier, microphone to PA, and is ideal for out of doors where added safety is required. It is also ideal when connection from one amplifier to another without the problem of earth loops is required. The unit switches on when the input jack is inserted. See Fig. 1

## SPECIFICATION

Maximum input 100 mV R.M.S. (impedance up to 500 k ) Output up to 100 mV R.M.S. (impedance greater than 50k)

## COMPONENTS

## Resistors

R1, R2
R3
1M(2 off)
10k
R4 100k
k
All resistors $\frac{1}{4}$ W 5\%
Potentiometers
VR1 100k hor. min. preset
Capacitors

| C1 | $10 n / 50 \mathrm{~V}$ cer. |
| :--- | :--- |
| C2. C4 | $4 \mu 7 / 16 \mathrm{~V} \operatorname{tant} .(2 \mathrm{off})$ |
| C3 | $100 \mathrm{n} / 35 \mathrm{~V} \operatorname{tant}$ |

Integrated circuits
IC1 741
IC2 CNY171

## Miscellaneous

ABS box $100 \times 75 \times 40 \mathrm{~mm}$
Printed circuit board
Stereo jack sockets (2 off)
PP3 battery and clip

## CIRCUIT DESCRIPTION

Resistors R1 and R2 form the bias for the 741 op. amp and C 1 decouples the input. The input signal modulates the input bias for IC 1 applied to the non-inverting input. The output of IC1 is fed through the I.e.d. of the opto-coupler, IC2, the current being limited by R5. Negative feedback is applied from the potential developed across R5 to ensure low distortion driving the l.e.d. in a true current mode. (The voltage developed across R5 is proportional to the current passing through it, and the l.e.d.)

R4, R3 and C2 give an a.c. voltage gain of ten to provide adequate drive to the l.e.d

The phototransistor in the opto-coupler in the configura tion shown, acts as a current source which is converted into a voltage across VR1. The output voltage is limited to 0.6 volts peak to peak because of the forward voltage drop of the transistor junction. To ensure maximum output swing, the output transistor is biased at approximately 300 mV by adjusting VR1 and measuring the d.c. voltage between pins 5 and 6 of IC2 with a high impedance meter. The pot VR1 can be adjusted by applying an input signal and adjusting for minimum distortion.

## CONSTRUCTION

All components are p.c.b. mounted (see Figs. 2 \& 3) and the whole p.c.b. assembly fits into two holes 12.5 mm diameter, 38 mm between centres in the side of an ABS box. To mount the jack sockets onto the p.c.b., junior hacksaw saw cuts in between the pads form an ideal solution for easy assembly



Fig. 1. Full circuit diagram. The input stereo jack socket is wired so that insertion of a mono jack plug will connect the battery to the circuit, thus eliminating the need for an ON/OFF switch

Fig. 2. Printed circuit layout (full size)


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#  ffaturing 

## SLAVE CHIP

When any digital processor (such as a microprocessor chip) needs to converse with the outside world it usually has to rely heavily on external peripheral control logic circuits. This logic external to the processor itself can be minimised by trading off hardware for software; so that the processor becomes intimately involved in the transfer of data to or from the peripheral, perhaps a tape cassette or a printer.

The software solution certainly looks good from the circuit complexity point of view. A complete asynchronous serial $1 / O$ scheme can be implemented in software with the use of only two microprocessor pins and a couple of external buffers to replace the few dozen TL packages which could be needed otherwise. The trouble is that software (or rather firmware in ROM) is quite expensive to write, and this method does not involve the CPU in a great deal of time wasting activity when in many situations its talents are urgently needed elsewhere. The software solution is therefore suitable only for very simple applications where the CPU doesn't have a lot of other things to do anyway.

For those more important CPUs, who find dealing with troublesome peripherals a tiresome job, the semiconductor manufacturers have produced dedicated $1 / O$ controllers which do some of the often needed jobs in hardware but with all the necessary logic squeezed on to a single LSI chip. The best known of these devices is probably the UART/USART/ACIA chip which can be used to relieve a processor of the need to control, slow, asynchronous serial I/O transfers. Data transfer of this sort, to and from teletypes, VDUs and other serial devices, can be a painfully slow process with the wide variety of transmission rates and data formats making the task quite complex. The necessary serial to parallel and parallel to serial registers, parity checking logic and start and stop bit insertion can all be done by the UART controller which the processor can treat just like a section of its own memory.

## SOFT OR HARD?

Taking stock then, you can do it in software, or you can do it in hardware, but unfortunately, you can only do it with LSI hardware if it is an I/O function which is so common that the semiconductor manufacturers find it economic to make a special device to do the job. If you happen to be an industrial microprocessor user, however,
there will be many occasions wheré your particular I/O control function is so special that you either have to go back to software or put up with a board full of TTL. Or, rather, you did have to until Intel introduced their 8041 A Universal Peripheral Interface chip.

The 8041 A is actually a complete microprocessor system in a 40 pin package, like the 8748 we considered last month, but unlike the 8748 the 8041 is optimised for use as the "slave" of a main processor such as the 8080,8085 or 8086 . The main processor converses with the 8041 over its normal eight bit data bus while the 8041 itself takes over all the time consuming data and formating and timing operations under the control of its own built in ROM based software. This solution provides the system designer with the best of both worlds: The flexibility of software driven I/O with all the convenience of a single LSI chip to do the work.

This is great for the industrial user who needs a thousand of these chips all with the same program, but what about those oneoff jobs where ROM mask costs cannot be absorbed? Is it back to TTL? Well no, because good old Aunty Intel has considered the plight of small users like us and has programmed up an 8041 with a set of nine general purpose 1/O routines which can be individually selected via the system bus.

The routines are aimed mainly at industrial applications such as switch sensing, motor speed control, stepper motor drive and simple serial I/O communications, but many other uses suggest themselves. This "custom" chip is coded ISBC 941 and it has all the usual facilities of the 8041 including 16 programmable $1 / 0$ lines which can be used individually to implement functions such as pulse counting, pulse generation, period and frequency measurement and sensor monitoring.

The ISBC 941 comes in a 40 pin package, runs from a 5 V supply, and can use either its own internal clock oscillator or one derived from the main processor clock.

## E-LINE MUSCLE (SUPERELINE)

I like to buy British, but it is very difficult sometimes, especially in the electronics field. I know that Texas Instruments, National Semiconductor, General Instrument, Motorola, and several other American semiconductor firms do manufacture devices here, but when you buy from these firms, as of course you must
in many instances, there is usually no guarantee that the devices you get will really be British or that your purchase will benefit the British economy in any way.

I for one have my fingers crossed that the ambitious plans of the new British Inmos memory and microprocessor organisation will bear fruit in due course, but until that great day arrives you can still do your bit for Britain by using the home-grown discrete transistors like those from Ferranti. Now we all have to use discrete transistors from time to time, don't we, and (own up now) I bet you use devices from Texas or Motorola without even thinking about it. Well don't, because if you need a good range of plastic silicon transistors you can't do much better than to buy them from our very own Ferranti Semiconductors.

Their main range of devices, which I would like to commend to you, is the family known as "E-line". This range comes in a plastic package of a very compact and neat design, and family members can be recognised by the fact that their code number begins "ZTX". These devices are by no means new, but you may not be too familiar with it because Ferranti don't have the same kind of advertising budget as some of their competitors. If my own experience with E-line devices is anything to go by however, they certainly make up for their lack of advertising in the quality of their transistors and you can pick just about any combination of polarity (n.p.n./p.n.p.), current gain, and voltage rating you are ever likely to need from this versatile family.

Well so much for the unashamed plug of a British manufacturer-now for the hot news. To augment their existing range of $E$ line devices Ferranti have now introduced a brand new range of plastic transistors called "Super E-line". "Super" is the right word too, because 1 don't know of any other manufacturer anywhere who can pack so much power into such a tiny plastic T092 type package. Super E-line devices will dissipate 1.5 W at a case temperature of 25 degrees $C$, and they'll handle voltages of up to 100 V . Under surge conditions these very muscular transistors can sink 6 amps, and they have a minimum gain of 25 at 2 A or 55 at 1 A .

These sort of specs make Super E-line ideal for use in audio amplifiers, relay and lamp drivers, and anywhere else you need a very small device with a very hairy chest. In many circuits you will be able to use Super E-line in place of much bulkier power devices.

So do yourself a favour and buy Britishit really is best sometimes!

# BREADBDARD Risul|elu 

LAST year over the period December 4-8. the second Breadboard was held at the Royal Horticultural Halls. Westminster. At this annual event for the amateur of the technology, electronics was unchained from its usual husiness-like decorum, and the sixty or so exhibitors combined to produce a preChristmas electronic menagerie of synthesisers. effects units and microprocessor music; and a fulgurous psychodelia of lighting novelties. including a laser at the Watford Electronics stand.

The exhibition provided a panoptic view of the state of the art. with no unfair bias towards computers. musical instruments. hi-fi or anything else. although a radio enthusiast need not have spent long at the show. Robots demonstrated their agility, and cybernetic bits and pieces were seen "lopping" around under battery power. How long before some of the visitors fit this description? Demonstrations of various keyboard instruments by the maestros took place in listening areas. We even found Alan Boothman playing the PE String Ensemble

## SHARP MZ80K

The Newbear display included the Sharp MZ80K personal computer. This system is based on the Z80 microprocessor with a 14 K extended BASIC. 10 in VDU (40 characters $x$ 25 lines). 78 key ASC 11 keyboard. 50 pin connector for system expansion and a music synthesizer with 3 octaves. The machine is available in a range of memory sizes ( 6 K . $10 \mathrm{~K}, ~ 18 \mathrm{~K}, ~ 22 \mathrm{~K}, ~ 34 \mathrm{~K}$ plus 14 K for the BASIC) and a PASCAL compiler will be available in the near future.


The cassette speed at 1200 b.p.s. is quite fast and the machine includes a tape counter.

The music synthesizer can be programmed ether in BASIC or machine code and the volume is adjustable from inside the case. The two instructions for the synthesizer are MUSIC and TEMPO with the TEMPO instruction either increasing or decreasing the length of the note

The Basic has to be loaded from cassette which takes about 2 mins but this system does enable other languages to be used.

The price of the machines range from $£ 520$. Newbear Computing Store Ltd., 40 Bartholomew Street, Newbury, Berks. (0635 30505)

## WEST HYDE DEVELOPMENTS

TheWest Hyde stand had a wide range of cases and components on display including their latest keyboard enclosure, the Bocon Commander. This moulded ABS case has anodised aluminium front and rear panels with the rear aperture accepting a 19 in rack frame 100 mm high. The housing which has been designed to accept most proprietary keyboards is priced at $£ 77.50$ ex VAT and p\&p.


A catalogue covering the complete range of cases. components, test equipment and tools is available free of charge from West Hyde Developments, Unit 9, Park Street Industrial Estate, Aylesbury, Bucks HP20 1ET.

## THE DIGITAL WAY

A device of considerable potential seen at the show is called a "Graph Transducer". A versatile interface between the analogue and digital worlds. this invention can form part of a range of instruments which synthesise virtually any waveform. graphically equalise. and allow serial analogue control.

Produced by Turner Electronics under licence from Aragorn Dynamics, the S201S is a completely digital graphic equaliser of electrically similar characteristics to conventional units. but which allows narrow band frequency control superior to tone controls, and presents no interface problems. Cascading permits resolution to the desired degree. Specification: $2 \times 10$ bands at 1 octave spacing. $\mathrm{S} / \mathrm{N}$ ratio $>80 \mathrm{~dB}$ below IV. Distortion $<0 \cdot 1 \%(20 \mathrm{~Hz}-20 \mathrm{KHz})$. The AD2000 series console comprises four stereo equalisers.

Operated in the Voltage versus Time Mode the Graph Transducer forms the basis of a range of units called "Arbitrary Waveform and Control Sequence Generators" which are capable of envelope shaping. wave form generation (timbre or tone), and. for example. sequential lighting control. The cycle pattern is set up using precision conductive plastic slider potentiometers and the time-base can be varied from microseconds to hours. An exceptionally stable logarithmic VCO is incorporated.

With the S 103 unit, a counter indicates incremental status, measures frequency and

## CLEF PRODUCTS

The very busy Clef Products st and featured the PE String Ensemble (March-July 78) which is still a very popular design with constructors. Also on display were Clef"s latest piano kits which have been based on the successful PE Joanna design with considerable refinements. The two designs are a $7 \frac{1}{1}$ octave ( 88 note) and a 6 octave ( 72 note). A stage version of the 6 octave piano is also available which requires an external amplifier and speaker whilst the domestic versions contain

their own power amplifier and will operate with either a speaker or an external amplifier/speaker system.

Clef can supply ready built and tested instruments with full service instructions.

The price of the String Ensemble is £164.00, the 6 octave piano is $£ 184.00$ and the $7 \frac{1}{\mathrm{~J}}$ octave $£ 209.00$ part kits are also available.

For a complete price list covering all Clef"s kits. contact Clef Products, (Electronics) Ltd., 16 Mayfield Road, Bramhall, Cheshire.

voltage, and an auto-ranging integrator smoothes the clastic signal. By superimposing the synthesiser"s output on an existing waveform using a dual beam scope. the output can be adjusted to follow the original waveshape. In fact, using the memory mapped Computer Interface Board also available, a computer is conceivable which could learn to imitate any sound just by listening to it.

Two units from the range ( S 101 and S 102 ) can be used to form a very superior conventional music synthesiser. Program card templates can remember waveforms.

Some sample prices are: S20IS Stereo Equaliser-£ 142 . S101 Control/Waveform Source-£184. AD2000 Equaliser Bank£694. Details are available from Precision Instrument Laboratories, Instrument House, 727 Old Kent Road, London SEI5.

## TRANSPORTABLE ORGAN

If your last project was encased in a tobacco tin then it may not be a good idea to attempt to build one of the organs seen on the Aura Sounds stand, although Wersi do say that the Saturn (pictured below) is based on their "novel" d.i.y, method which makes construction easier. The console comes assembled, and prefabricated laced wiring harnesses eliminate one of the main causes of error.


The Saturn is described as a transportable organ, and has a list of attributes too long to quote here. Basically it has five-octave polyphonic keyboards. with an overall eight octaves available from a master generator providing a range of simultaneously available waveforms. The fixed stops give: Principal. Cello, Horn. Accordion. Trombone and Sax-
aphone all at 6: English Horn. Principal. Viola. Clarinet. Oboc. Schalmei and Trumpet all at $8^{\circ}$. plus others.

The piano section gives: Celeste. Kinura. Honky Tonk. Harpsichord and Banjo, with tremolo. echo and damper functions

Wersivoice rotating battle effect is included. along with auto accompaniment, and somewhere inside the cabinet is a string orchestra! Just to utilise any remaining space. a sound computer is also incorporated to give 32 user adjustable preset buttons. So if you have $£ 5197$ in your pocket. plus some petty cash for loudspeahers to go with it, the Saturn could be yours.

Anra Sounds, 14/15 Royal Oak Centre, Brighton Road, Purley, Surrey.

## COMPSHOP

The main feature on the Compshop stand was our Compukit UK 101 which has rapidly established itself as the country's fastest selling single board computer.


Also on display was the ITT 2020 which is the English version of the Apple II microcomputer. Compshop, 14 Station Road, New Barnet, Hertfordshire.

## CHROMATRONICS

The Chromascope from C hromatronics is a video synthesiser which can create a whole range of abstract colour patterns on a TV set. The display which responds to a musical input is available in kit form for $£ 169.95$ inc. VAT,

The kit includes a cabinet. components ready built encoder, modulator power supply and manual. Chromatronics Coachworks House, River Way, Harlow, Essex CM20 2DP.

## COMPETITION WINNER

The winner of our Lektrokit competition (Sept 79) Mr. D. J. Speakman was at the exhibition to receive his prize of a Powerace 102 with a jumper wire kit and 16 pin test clip.


Our Advertisement Manager Mr. D. Tilleard (left) presented the prizes together with Mr. G. Wilson of Lektrokit. lowing projects in PRACTICAL ELECTRONICS Use this order form for a year's supply to be posted to you.
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If the amplitude of applied signals is small it is more convenient to replace the transistors by an equivalent circuit for determining externally, circuit parameters. Here common emitter hparameters are discussed.
|OOKING through transistor data sheets can be very frustrating if you don't know what the mass of data means or how it can be applied in designing a circuit. This article explains what the most commonly quoted parameters mean, and their relevance to a transistor circuit.

## COMMON EMITTER MODE

Fig. 1 shows an n.p.n. transistor connected in the common-emitter mode, with normal voltage supplies and current flows labelled. Fig. 2 illustrates the typical output characteristics for such a transistor, and similar graphs can be found on most data sheets. The transistor is a current controlled device, and in the common-emitter mode a large current gain is possible, as shown by Fig. 2 where $I_{c}$ is the collector current in mA , and $\mathrm{I}_{\mathrm{b}}$ is the base current in $\mu \mathrm{A}$.
Note that the collector current depends only on the value of the base current, and not on the collector voltage (which means that $\mathrm{V}_{\mathrm{ce}}$ can be a constant voltage power supply). For a given value of $I_{b}, I_{c}$ is nearly constant for all values of $V_{c e}$. except near 0 volts and at very high levels (not illustrated, but typically about 50 volts). To appreciate what happens at these two extremes, look at the diode equivalent in Fig. 3 of a transistor. The collector-base diode is reverse biased, and the emitter-base diode is forward biased for normal operation. Fig. 4 shows the current flow in a semiconductor junction diode, and it will be noticed that in the reverse biased condition there is a sudden increase in current flow when the voltage at point $A$ is reached. This is a result of avalanche breakdown, which will cause the destruction of the junction, and must not be allowed to occur. Hence for "safe" operation of the device the voltage at point $B$ must not be exceeded. As the other diode is forward biased there is little voltage drop across it, and consequently approximately all $V_{c e}$ is dropped across the reverse biased collector-base diode, and so a maximum value of $V_{c e}$ must be stated to prevent destruction of the transistor from too high a power supply voltage. This parameter is quoted as $\mathrm{V}_{\text {ceo }}$ (max), the O suffix indicating that this voltage is measured with the base open circuit.

It will be noted from Fig. 4 that between zero volts and point C no current flows in the forward biased diode. This means that before current can flow into the emitter there must be at least this small voltage present across the baseemitter. It is called the "knee" voltage, and for silicon devices it is 0.7 volts. This explains why $V_{\text {ce, }}$, as illustrated in Fig. 2, must have a minimum value (point A) before collector current will flow.

When the base current is zero, it is also evident from Fig. 2 that a small collector current, shown by B, flows. This is the leakage current which arises from the reverse biased diode across the collector-base.

## R. A. HATTON



Figs. 1 and 2. Transistor in common emitter mode with output characteristics


Figs. 3 and 4. Diode equivalent of transistor with diode voltage/current characteristic


Fig. 5. Hybrid parameter circuit
Where i indicates input, o output, $f$ forward, $r$ reverse, e common emitter. Solving the equations for various circuit conditions:

$$
h_{i e}=\left.\frac{v_{b e}}{i_{b}}\right|_{v_{c e}=0} \quad \begin{array}{ll} 
& \begin{array}{l}
\text { input resistance with output } \\
\text { short-circuit (ohms) }
\end{array}
\end{array}
$$

$h_{r e}=\left.\frac{v_{b e}}{v_{c e}}\right|_{i_{b}=0}$
$h_{f e}=\left.\frac{i_{c}}{i_{b}}\right|_{V_{c e}=0}$
$h_{o e}=\left.\frac{i_{c}}{v_{c e}}\right|_{i_{b}=0}$
output admittance with input open-circuit (mhos)


Fig. 6. Simplified hybrid parameter circuit

This current, which should be as small as possible in a good transistor, is termed $I_{\text {ceo }}$, the 0 suffix indicating that it is measured with the base open circuit. This leakage current flows in addition to the required collector current, and so the actual collector current $I_{c}=\beta I_{b}+I_{\text {ceo }}$, where $\beta$ is the current ratio $I_{c} / I_{b}$, which is the ratio of output current to input current or current gain. This is an important parameter in circuit design, and $\beta$, which is called the common-emitter current gain, is usually found on data sheets as $h_{f e}$.

## HYBRID PARAMETERS

The $h$ parameters are derived from a model of the transistor. Fig. 4 shows this for small signal changes about an operating point, and is known as the hybrid-parameter equivalent circuit for a common emitter bipolar transistor.
The input side consists of the base circuit resistance $h_{i e}$ and a voltage generator to take account of the junction potential, and it produces a voltage given as $h_{r e} . V_{c e}$, where $h_{r e}$ is the reverse voltage transfer ratio. The output side consists of a current generator which produces a current given by $h_{f e} i_{b}$ (small letters indicate small signals) or $\mathrm{i}_{\mathrm{c}^{\prime}}$ and a paralle resistance which covers the output admittance (inverse of resistance) $h_{o e}$. The parameters are defined by the following two equations:
input voltage $v_{\mathrm{be}}=h_{\mathrm{ie}}, i_{\mathrm{b}}+\mathrm{h}_{\mathrm{re}} \cdot \mathrm{v}_{\mathrm{ce}}$
output current $i_{c}=h_{f e} \cdot i_{b}+h_{\text {oe }} \cdot v_{c e}$
Of these parameters $h_{r e}$ and $h_{o e}$ are usually very small, and consequently they are often neglected in circuit calculations in order to simplify things. Fig. 6 shows the simplified model, using just $h_{\text {fe }}$ and $h_{\mathrm{ie}^{\prime}}$ and Fig. 7 shows a common-emitter amplifier with its (simplified) model equivalent circuit.

As may be expected, the larger the current flowing through a semiconductor device, the larger the quantity of heat which is dissipated through the bulk of the material. There comes a point when the material has too high a current flowing through it to allow the necessary rate of dis-


Fig. 7. Simple common emitter amplifier and its hybrid equivalent circuit
sipation for safe operation. This results in the destruction of the device, and you will find stated in data sheets the maximum collector current above the device will break down- $i_{c}$ max.

## POWER DISSIPATION

The manufacturer also quotes the maximum power dissipated by the transistor, and when this power has been reached it must equal the electrical power input, given approximately by $V_{\text {ce }} \cdot I_{c}$, and, of course, must not be exceeded. It may be necessary to calculate the power dissipation of a transistor when mounted on a heatsink, in which case the following equation would be used:

$$
P_{\max }=\frac{\left(T_{i}-T_{a}\right)}{\left(\theta_{1}+\theta_{2}+\ldots\right)}
$$

Here $T_{i}$ is the maximum junction temperature for safe operation, and $T_{a}$ is the ambient air temperature around the device (which will probably be above room temperature, because of the surroundings). $\theta_{1}$ is the thermal resistance between the junction and the transistor mounting base, and can be found on the data sheet as $R_{\text {th(j-case) }}$ and it is expressed in $\mathrm{C}^{\circ} / \mathrm{mW} . \theta_{2}, \theta_{3}$ etc., are the thermal resistances of all other components in the heat flow path to, and including, the heat sink.

One final point, while considering temperature, $I_{\text {ceo }}$ is very much temperature dependant so any quoted value must be at a stated temperature (usually room temperature) and steps must be taken in a design to exclude the effect of temperature.

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Fig. 1.
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## ANALYSER

The analyser section has a bridge network consisting of VR1 (known as the time dial) forming two arms, the third is R1, which is the reference from which the time dial is calibrated, against the resistance of the cadmium sulphide cell that makes up the fourth arm. VR2 is used to calibrate the unit for various speeds and makes of paper. Across the bridge is the 9 volt supply, which provides the potential and polarity at the output depending upon the resistance of the cell and where the wiper arm of VR1 is set (disregarding VR2 for the moment).

When the wiper of VR1 is at the zero side of the 9 volts the voltage appearing at the wipers (being the output of the bridge) will be out of balance. The negative output goes to the input of the op amps which will have a positive output if the inverting input is more negative than the non inverting input.

It should be noted that the inputs of the two op amps are reverse connected so that when there is a voltage at their inputs their outputs are out of phase i.e. one will have a positive output and the other will have a negative output. Since VR1 wiper of the bridge goes to ( - ) of IC 19 b and ( + ) of IC19a only the former will have a positive on its output causing D9 to be forward biased and illuminating it. This l.e.d. is termed low ( 10 '), meaning that VR1 needs to be rotated to a higher dial reading. Moving VR1 wiper towards positive will eventually balance the bridge so that there is no voltage across the inputs of the op amps.

As the wiper arm of VR1 passes through balance the output of the bridge will now be positive with respect to VR2 wiper causing IC19a to have a positive output bringing on D8 ('hi').

## TIMER

The timer section can be divided into three areas, clock, control and counter with display. Decade counters make up the clock circuit deriving its timing from the mains frequency. The input is squared by D4 and C7 then passed to IC14 which divides by 5 to give a 0.1 second pulse that goes to S7 and to IC15 which further divides by 10. A 1 second pulse will result that is used to indicate the decimal point on the three digit display, as well as going into IC16 to divide it by 6 to give a 0.1 minute clock pulse. This pulse also goes to S7 which will select whether the display, that can read up to 99.9 , will be in minutes or seconds.

Pins 2 and 3 of the 7490 chips are the reset pins and if both of these go high the b.c.d. output will be zero, and will stay that way until one or both inputs go to a low. When these chips are used as straight decade dividers resetting them is easy, however, as pins 2 and 3 of IC16 are used to reset automatically when the count reaches 6 it becomes slightly more complex to reset it to zero on demand. IC17 wired as two OR gates solves the problem.

Under operating conditions pins 5 and 9 of IC17. have a low on them that has no effect, allowing the outputs 8 and 9 of IC 16 to pass through the NOR gates to 2 and 3 of IC 16 . When the reset pulse comes along the high that is now on pins 5 and 9 of IC 17 will pass onto 2 and 3 of IC16 regardless of what is on 6 and 8 of IC17. If this resetting wasn't done, clock errors could occur in the minute timing because at the first clock pulse there could be a number stored in IC16 which may only need another pulse on its input to give an output pulse. In other words 0.1 minute will be on the display yet only 1 second has passed.

The control circuitry consists of IC1 and IC2 and three thumbwheel switches, S8, 9 and 10 . When S4 is pushed the debounce circuit IC1a and IC1b does two things, the low going pulse at pin 6 sets a second bistable IC2c and IC2b to
put a high on pin 9 of IC1c to enable the 50 Hz on pin 10 to get through, as well as turning TR 1 on that energises the relay to bring on the enlarger. A second function of the high going pulse at IC 1 a , pin 3 is to reset all the 7490s, and since R14 ties the reset line to a low, and C6 stops the high on pin 3 of IC1a from having any further effect on them, they will commence to count at the same time the enlarger comes on. After 2 seconds a low appears on pin 5 of IC 1 b that comes from pin 10 of IC17 and is used to reset the debounce circuit for further timing.

A problem occurs with this arrangement in that timing cannot be achieved below 2 seconds, because until this circuit is reset a low appears on pin 3 of IG2b. This stops IC2b/2c from being reset by the low going pulse that will come from pin 12 of IC2a when the time set by the thumbwheel switches is achieved, however it is extremely unlikely that the enlarger will be on for less than 2 seconds. One advantage of using this pulse to reset the debounce circuit is that a SPDT push button is not necessary.

Two other push buttons are used in the control circuitry and they are S 5 and S 6 . 55 will stop the count by resetting IC2b/2c putting a low on IC1c inhibiting the 50 Hz and turning the enlarger off with the display not affected by this action. To continue the timing, S 6 is pushed setting $/ \mathrm{C} 2 \mathrm{~b} / 2 \mathrm{c}$, allowing the 50 Hz to pass once more. These switches allow the exposure to be stopped and then commenced again without losing track of the time.

## COUNTER AND DISPLAY

The counter and display section is made up of a very common circuit in three decade counters ICs 4,5 and 6 with seven segment displays driven by ICs 7,8 and 9. Connected to the b.c.d. outputs of IC4, 5 and 6 are hex inverters, connected to form buffers between the decade counters and thumbwheel switches S8, 9 and 10 . The buffers are necessary because whatever number is selected it is connected to the common, e.g. if 5 is selected 1 and 4 are commoned, consequently without the buffers pins 8 and 12 of the 7490 would also be connected giving a false display.

When the time that is selected by the thumbwheel switches is reached the b.c.d. output from the 7490 s will match the b.c.d. of the switches which now will have a high on all commons. These highs go to IC2a putting a low on its output pin 12 which resets $1 \mathrm{C} 2 \mathrm{~b} / 2 \mathrm{c}$ stopping the count and turning the enlarger off. The elapsed time will be on the display and will stay there until the start button is pushed.

## ALARM

The remaining circuitry consisting of a 555 chip IC18 and a bistable IC1d and IC3c is used to turn on an alarm via S 1 . As mentioned when the timer reached its count a low appeared on pin 12 of IC2a. Besides being responsible for stopping the count it is also used to reset IC1d and IC3c. When this happens the low going pulse at pin 11 of IC1d is differentiated by C5/R11 which triggers IC18, that is wired up as a monostable, to give a high out of pin 3 to energise a small solid state buzzer.

The time the buzzer will be on depends upon the RC network R9/C4 which will be about 1 second with the components in circuit.

The purpose of the bistable is to stop the 555 from triggering every time the thumbwheel switches are moved. By its action no more trigger pulses can appear until it is set again by pushing the start button S 4 .

Leading zero blanking can be wired into the circuit if desired as well as lamp test.

The switch S2 associated with the safelight is wired so that in the auto position RLA 1 will control when the safelight


Fig. 2. Underside of printed circuit board
is on, i.e. with the enlarger on the safelight is off and vice versa. In the manual position the safelight is on ali the time. This allows working with black and white papers (cutting, developing) while the timer is being used for the developing of films. It saves unplugging the safelight and putting it in a wall plug

The timer is very useful in controlling motorised agitators when developing films or colour paper and this is what was in mind when the buzzer circuit was put in as it allows other work to be carried out, without having to constantly worry about the time

S1 keeps the alarm from sounding which can be annoying when exposing paper. For colour work the safelight switch is put in the off position

## CONSTRUCTION

It is recommended that the double sided p.c. board shown be used due to the number of i.c.s in the circuit. The cost of the board is a small price to pay for the ease in constructing the circuit. If using the board begin at the bottom section of the board by inserting and soldering pieces of wire at the pads that have a dot next to them as these are used to connect both sides of the board together. Under two of the

7447 chips there are pads that have two dots and these will also have to have the wire "pins" if leading zero blanking is required. There should be 14 joins plus the two for blanking.

The i.c. sockets can now be inserted if they are to be used otherwise solder the chips direct to the board making sure they are correctly orientated by taking note that on the top of the board pin 1 is marked for all chips as is the last two digits for all the TTL i.c.s. After making sure all the pins on the chips are soldered insert the diodes and resistors and solder them on the bottom side of the board. Turn the board over and you will notice that some of the components go through pads which have to be soldered to the component leads as they are also used to complete the circuit from top to bottom, as do some of the capacitors that can now be inserted.

Finally the regulator chip can be mounted by carefully bending the leads so that the hole through the metal tag will line up with the one in the board having the copper area that is the heat sink. This now completes the main board.
The display board is fairly straightforward as it only has the three displays and R40 which is used to limit the current to D10 which is the indicator for the calibration dial. A 40 pin 0.6 in socket can be used if desired.


Fig. 3. Topside of printed circuit board

Mounting and wiring the mains voltage components should now commence paying attention to the safelight switch and making sure the lamp sockets bypass the fuse. A power switch can be mounted on the back of the panel if desired. Once all the chassis mounting components are in place (all but the main board) the wiring of the board to the chassis can go ahead. The board should ideally be mounted in edge connectors that are mounted on the front panel.

Ribbon cable is a definite advantage especially for the displays and b.c.d. switches, heavier gauge wire will be necessary for the 6 volts from the transformers. After checking the wiring you are in a position to turn the unit on.

## TESTING

Place the alarm switch on, sec/min switch to sec., focus to off, safelight to auto and the three thumbwheel switches on 111. When power is applied the buzzer will sound and a random number will appear on the display that will read 0 when the start button is pushed. The display should now be counțing at a 0.1 second rate and will continue so until either the stop button is pushed or the count reaches 111 . If the stop button was pushed, the continuous button once pushed will allow the count to continue until 111 is reached. Failure for this to happen or if the count stops at some other
number then the odds are the thumbwheel switches are wired up incorrectly.

While the unit is counting the decimal point in the units display will be flashing at a second rate and anything plugged into the enlarger socket will be on. This will go off at the end of the count and whatever is plugged into the safelight socket will come on. Flicking the focus switch to on will switch the enlarger on and turn off the safelight that can be turned back on by placing the safelight switch to manual. The only time the buzzer should sound (other than when the unit is first turned on) is when the display reaches whatever is set by the thumbwheel switches.

## DIALS

The two dials are made of 3 in diameter perspex and are shown full size in Fig. 5. Calibration batons and numerals can be used from Letraset. Accuracy of the time dial depends upon the quality (tolerance) of the 5 k lin potentiometer and if in doubt it will be necessary to calibrate your own dial using 1 or 2 per cent resistors.

Four 100k and four 10k in parallel and series combinations will give all the times necessary.

For the CL705HL cell the following values with times, will


Fig. 4. Component overlay for the printed circuit board
be needed, 10 k for $1 \mathrm{sec}, 20 \mathrm{k}$ for 2 secs, 100 k for 10 secs, 150 k for 15 secs etc. These resistors take the place of the cell during calibration with the calibration dial at 0 .

To zero calibrate short out VR2 with a piece of wire. The hole in the centre of the dials need to be enlarged to just over $\frac{3}{8}$ in so that it clears the potentiometer mounting screw. Ideally, D8 and D9 should be a dual colour type l.e.d. that is situated under the time dial both to partially illuminate the dial and act as a marker. D10 is a single red type used to illuminate the cap dial.

The advantage of the dual type is that it will always be on so at, or near, balance the dial reading can easily be seen.

## COMPLETION

In the prototype the fascia panel was made up of thin gauge aluminium as it is not loaded. Two bends are introduced to produce a sloping front and the whole drilled to suit components purchased

## USING THE ANALYSER

There are two basic methods of taking readings, one is the integrated (average) that uses a diffusion screen under the lens to give an average light to the cell and is the method that I use on most occasions. If the negatives are not average then the spot method may have to be used. Separate calibrations are necessary for both methods.

## INTEGRATED METHOD

Make a test print using an "average" negative. Select what you consider to be the best exposure as the analyser accuracy for all future exposures depends upon this step. Say 10 seconds gave the best print. Leaving the enlarger exactly as it was for the test print put the cell on the masking frame.

The diffusion screen is placed under the lens making a note where you put it as it needs to be in the same position for future prints. A good idea is to make a holder similar to the one used to hold the red filter that is on most enlargers.

## COMPONENTS

| Resistors |  |
| :--- | :--- |
| R1 | $100 \mathrm{k} 1 \%$ |
| R2 | 100 k |
| R3, R4 | $330(2 \mathrm{off})$ |
| R5 | 470 |
| R6 | 1502 W |
| R7 | 751 W |
| R8 | 3 k 3 |
| R9 | 4 M 7 |
| R10 | 3 k 3 |
| R11 | 15 k |
| R12, R13 | 3 k 3 |
| R14 | 56 |
| R15-R17 | 3 k 313 off) |
| R18-R40 330 (23 off) |  |
| All $5 \% \frac{1}{4}$ W carbon film |  |

## Capacitors

| C1 | $470 \mu 16 \mathrm{~V}$ elect |
| :--- | :--- |
| C2 | $1000 \mu 16 \mathrm{~V} \mathrm{elect}$ |
| C3 | $2000 \mu 16 \mathrm{~V}$ elect |

C4-C12

Diodes

| D1-D3 | 1N4001 (3 off) |
| :--- | :--- |
| D4 | BZY88C-4. |
| D5-D6 | BZY88-6.2(2 off) |
| D7 | 1N4001 |

D7 1N4001 D8-D10 TIL209 (3 off) (D8-D9) preferably MV5491. XC5491 two colour I.e.d.)

## Integrated Circuits

IC1 74LSOO

IC2 74LS10
IC3 74LSOO IC4-IC6 74LS90 IC7-1C9 74LS47 IC10-IC13 74LSO5 IC14-IC16 74LS90 IC17 74LSO2
IC18 555 IC19 MC1458
IC20 7805

| Transistor <br> TR1 | Photocell <br> R41-CL705HL (Clairex) <br> Ace Mailtronix, Tootal St. <br> Wakefield, West Yorkshire |
| :---: | :--- |
| Switches |  |
| S1 | Single pole on/off |
| S2 | Single pole three way |
| S3 | Single pole on/off |
| S4 | Press to break |
| S5 | Press to break |
| S6 | Press to break |
| S7 | Single pole change over |
| S8-S10 | BCD thumbwheel (3 off) |
| Displays |  |
| X1-X3 | FND507 (3 off) |

## Miscellaneous

Miniature buzzer $6-9 \mathrm{~V}, 15 \mathrm{~mA}, \mathrm{T1}$-Mains transformer with independent $6 \mathrm{~V}, 0.5 \mathrm{~A}$ independent secondaries (R.S. 207-194)

Control fascia of timer. The enlarger, safelight and probe sockets are arranged at the back. ' Hi ' and 'Lo' I.e.d.s appear at either side of the display

Now with the time dial on 10 seconds and with the safelight switch in auto (as the cell is sensitive to all colours) adjust the calibration dial until the l.e.d.s are both on and make a note of the reading on the calibration dial. Whenever that brand and grade of paper is used, just set the calibration dial to it. If you use several types of paper make a calibration for all of them

## SPOT METHOD

The only difference between this method and the integrated one is the diffusion screen isn't used and the cell looks at a shadow (brightest portion)

A diffusion screen can be made from a piece of perspex that has been rubbed with a piece of fine emery paper. Another suitable material is draughtmen's tracing film or they are available from photographic shops.


Fig. 5. Full size details of dials


THE PE Traveller car radio has been designed around a pre-aligned tuner unit, ceramic filter and 6 watt audio amplifier i.c. The result of this design is a receiver which is straightforward in both construction and alignment. The Traveller, which costs approximately half the price of an equivalent commercial car radio and is available in complete kit form from RTVC, achieves an excellent performance, with one long wave and four medium wave push buttons and includes tone control.

## CIRCUIT DESCRIPTION

The complete circuit diagram of the Traveller is shown in Fig. 1. The aerial signal which is fed to the aerial tuning circuit via the r.f. choke L1 is impedance matched to the wide band amplifier designed around TR1. The input to this amplifier is protected against static discharge by the voltage dependent resistor (R1)

The output of the wide band amplifier (TR1 collector) is fed to the input of mixer/oscillator circuit. The resistor R8 sets the internal a.g.c. range of the prealigned i.f. module and a second a.g.c. line is fed to the input of the wide band amplifier via resistor R7. The value of this resistor can be altered to adjust the sensitivity of the receiver or a 100 k preset resistor can be used. Any adjustment of the sensitivity will of course be a compromise between sensitivity, signal handling, interference etc. The maximum signal capacity of the set can be achieved by ensuring TR 1 is ultimately reverse biased by the a.g.c. circuit. However, one problem encountered when using an amplifier in front of a self oscillating mixer is r.f. blocking.


the excess leads from the components including the pins above the wave change switch S1. Carefully check the orientation of the electrolytic capacitors and the transistor TR1. Also check that there are no solder splashes shorting out the p.c.b. tracks

A small modification should be carried out to the tuner unit before the p.c.b. is fitted. The switch bar at the back of the tuner should be removed and the modified switch bar and p.c.b. mounting bracket fitted in its place. Take care not to disturb the slide biasing spring fitted underneath the switch bar. A self tapping screw should be used to "tap" the two holes on the mounting bracket.

The tone, volume and on/off switch S2 should be fitted to the tuner unit and the capacitor C14, resistor R 10 and wire links soldered as shown in Fig. 4. The p.c.b. can now be mounted onto the tuner unit using two self tapping screws



Fig. 2. Printed circuit board design


Fig. 3. Component layout



The switch bar (shown arrowed) should be removed and the modified unit screwed on to the tuner in its place. Check the operation of the pushbuttons before the printed circuit board is fitted into position.


Fig. 4. Complete wiring diagram for the Traveller

## COMPONENTS ...

## Resistors

| R1 | VDR 6 V |
| :--- | :--- |
| R2 | 220 k |
| R3, R9 | $10 \mathrm{k}(2$ off) |
| R4, R5 | $680(2$ off) |
| R6, R11 | $1 \mathrm{k8}$ (2 off) |
| R7,R8, R10 | 47 k (3 off) |
| R12 | 22 |
| R13, R15 | $100(2$ off) |
| R14 | 1 |

R14
1
All resistors $\frac{1}{2}$ W 10\% carbon

## Potentiometers

VR 1

## Capacitors

C1, C2, C3, C18
C4
C5, C12, С16, C19
C6, C7
C8, C13
C9
C10
C11, C14, C17
C15
C20, C22
C21, C23
C24, C25 (feed through
capacitors)
VC1

VC2
Semiconductors

## TR1

IC1

Duai concentric log with on/off switch (approx. 20k)

2n2 (4 off)
56n
47 (4 off)
680p (2 off)
$10 \mu$ (2 off)
1n
4n7
10n (3 off)
$220 \mu$
100n (2 off)
$1000 \mu$ (2 off)
1 n (2 off)
Attached to tuner
approx. 80p max
140p max

BF394 or BF195
TBA 810 S

Miscellaneous
LP1181 r.f. - i.f. module. Tuner unit. P.c.b. Control knobs. Aerial socket

L1 r.f. choke, L2, L5 I.w. coils L3, L4 attached to tuner, L6 supply choke.

## Constructor's Note

A complete kit of parts for the Traveller is available from Radio \& TV Components (Acton) Ltd., 21 High Street, Acton, London W3 6NG. The price is $£ 10.50$ plus £. 1.75 p. \& p. (pack 7).
with insulated washers. Before tightening the screws ensure the switch bar lug is located into the arm of S 1 . The operation of the wave change switch (S1) should then be checked by pressing the two push buttons nearest the tuning control. The movement should be the same in both directions. If necessary release the screws and adjust the position of the p.c.b. until the switch movement is correct

The indicator lamp LP1 should be fitted next and the p.c.b. should be wired to the tuner unit (Fig. 4). The back panel of the radio should be drilled to accept the battery and speaker feed-through capacitors (C24, C25) (Fig. 4). Before soldering the battery and speaker capacitors clean the panel and terminals with emery cloth. The connections to the back panel should be made before the panel is fitted into position. The earth braid should be fitted to the case using a 6BA screw and nut.

## TESTING AND ALIGNMENT

Oscillation may be prevented on longwave if the oscillator coil's inductance (L5) is too far out from its correctly aligned position. To overcome this problem unscrew the core of the coil (anticlockwise) so that the plastic top of the core is approximately 2 mm above the can. If the problem still occurs the value of $R 3$ should be reduced.

For simplicity, "bench alignment" is recommended. The speaker, 12 V supply and aerial (if an r.f. signal generator is not available) should be connected, then the set switched on and tuned to the medium wave. The scale (attached to the escutcheon) should be held in front of the radio and the set manually tuned to 250 metres. Adjust the trimmer (VC2) on the p.c.b. to receive radio 3 (247 metres). Switch to the longwave, tune to 1500 metres and adjust the I.w. oscillator coil (L5), with a non-metallíc tuning tool, to receive Radio 4. Adjust the I.w. aerial coil (L2) for maximum output.

If an r.f. generator is available tune the set to the extreme h.f. end on m.w. and adjust the trimmer (VC2) on the p.c.b. to receive 1620 kHz modulated signal. On I.w. tune to the extreme l.f. end and adjust the I.w. oscillator coil (L5) to receive a 150 kHz modulated signal. Then set the generator to 200 kHz and tune the set to receive this signal $(1500$ metres) and adjust the I.w. aerial coil (L2) for maximum output.

After the set has been correctly aligned fit the back and top panels into position using self tapping screws.

NEXT MONTH: Installation and suppression.

## SPECIAL SUPPLEMENT



Fig. 1. Showing the various parts and operation of the horn
accept poor sound quality" is a common view, but this statement needs careful qualification. Running a sound system into severe overload on peaks is often acceptable, but indistinct vocals are not. The concept of live sound quality embraces far more than the main criterion of domestic sound quality-harmonic distortion. The vagaries of hall acoustics, phase interactions in multiple speaker arrays and the frequent need to push sound systems to their limits are other criteria which are unique and crucial to the sound quality of a live performance.

## SOUND LEVELS

Discotheque levels lie between 105 dB and 115 dB , yet a Ione soprano can exceed 104 dBA , a symphony orchestra can notch up 115 dBA , jazz bands have been measured at 125 dBA and a lone rock drum kit at one metre can hit 130 dB . Audience noise in excess of 120 dBA has also been measured. These figures put typical rock concert levels of between 110 and 125 dB into sharp perspective, especially when it is borne in mind that many musical instruments are capable of exceeding the threshold of pain (125dB) on their own.

## HORN LOADED LOUDSPEAKERS

Most PA systems spend their life on the road, and apart from the need for exceptional physical robustness, size and weight must be sensibly limited if life on the road is to be tolerable. The heaviest and bulkiest items in a PA rig are the loudspeakers; clearly, the fewer needed the better. Thus very efficient loudspeakers are sought. It is pertinent to bear in mind that direct radiator (infinite baffle) loudspeakers are, at best, 2 per cent efficient.

If high sound quality is required, then this can only be achieved in exchange for even lower efficiency, as exemplified by domestic high fidelity loudspeakers, which are frequently less than 0.5 per cent efficient. Vented (bass reflex) enclosures offer somewhat higher efficiency, typically around 2-8 per cent, but only at low frequencies.

Prior to the birth of heavy metal rock and giant outdoor festivals, it was rarely considered necessary to amplify a whole band, and column loudspeakers were adequate for vocal amplification. Then, about a decade ago, the quintessential rock band Iron Butterfly used a 30 year old RCA loudspeaker design on stage at the Albert Hall, and a power revolution had begun.

Iron Butterfly had discovered the RCA "W-Bin", a hornloaded loudspeaker designed for cinemas. Horn loading provides the most efficient loudspeaker action; horn loudspeakers are typically $25-50$ per cent efficient. Moreover, the best horns provide arguably the most realistic
sound reproduction available. There is no doubt that for high power sound reproduction, horn loudspeakers are superior to all others on the basis of sound pressure level (SPL) per f , size, weight, sound quality and control of dispersion.

Inevitably, horn loudspeakers are the mainstay of live sound systems. Thus a knowledge of horn characteristics is essential if high power sound systems are to be competently engineered.

## THE HORN

The horn is an acoustic transformer, matching the elasticity of air (a low impedance) with the stiffness (high impedance) of a loudspeaker diaphragm, by a graduated change in air pressure (Fig. 1). This transformer action is the secret of the horn's high efficiency. If a horn is asked to reproduce sufficiently long wavelengths, then adequate air pressure cannot be achieved at the throat. The horn then reverts to direct radiation, and its output falls sharply. The horn is thus a high pass filter, and cannot be used below this critical point, known as the cutoff frequency.
The area of the throat and the mouth, the flare curve, the cutoff frequency, and the length of a horn are closely related by physical equations. This relationship of five variables leads horns to have very definite and critical dimensions, unlike other loudspeakers.

Whenever a diaphragm moves, it causes distortion, particularly intermodulation distortion. This is perceived as "muddiness" and is very objectionable at high levels. A direct radiator diaphragm must move large distances to produce high SPL's, especially at low frequencies. Large diaphragm movements produce correspondingly large amounts of intermodulation distortion. The movement of a horn loaded diaphragm is typically 10 to 500 times less for the same sound output, thus horns can be driven harder without incurring excessive levels of intermodulation products.
The author's horn stack. The mouth of the horn is $\mathbf{6 f t}$ high and the wide dispersion piezo-electric treble horns (3) are $10 f t$ from ground level. The giant bass horn (1) has a frequency range of $20-280 \mathrm{~Hz}$ and the diffraction midhorn (2) a range of $\mathbf{2 5 0 H z} \mathbf{- 3 k H z}$

In exchange for greatly reduced levels of intermodulation distortion, horns produce low order harmonic distortion. This is a consequence of the high air pressure at the throat, which causes air overload distortion. The magnitude of this distortion is governed by the flare curve, the power input and the horn's operating bandwidth. The flare curve is usually exponential or tractrix (involute cantenary) for high power sound systems, these curves being a compromise between efficiency and air overload distortion.

Limiting the operating bandwidth of any loudspeaker reduces intermodulation distortion, but bandwidth limitation in horn loudspeaker systems is especially useful, since it also minimises air overload distortion. Crossover networks, to achieve these limitations, are considered later.

Air overload distortion is predominantly 2 nd harmonic, and is thus palatable to the ear. Thus horn loading exchanges low efficiency and high levels of dissonant intermodulation distortion (IMD) for high efficiency, critical dimensions and low order, and hence innocuous, harmonic distortion.

## HORNS IN PRACTICE

Horn dimensions are closely related to the wavelengths of the sound they handle, thus bass horns (affectionately known as bass bins) are inherently large. Ideally, for smooth frequency response, the perimeter of a horn's mouth should be four times greater than the lowest wavelength to be reproduced. Thus for a cutoff frequency of 20 Hz , a mouth of $40,000 \mathrm{ft}^{2}$ is indicated! Clearly some compromises must be accepted in practice. This figure assumes loading into free space.

Each time the solid angle of radiation is halved, the mouth area can be halved. Likewise, if a higher cutoff frequency and a less than perfectly regular response is accepted, the mouth area can be greatly reduced. For example, a typical PA horn which is wall loaded lagainst a wall and on the floor, and thus radiating into a solid angle of $\pi$ radians) and exhibits a 60 Hz cutoff frequency, will only require mouth dimensions of 3 ft by $2 \frac{1}{2} \mathrm{ft}$.

Because horn length and mouth size are closely related, a horn with a nominal cutoff frequency of 60 Hz with smaller mouth dimensions is possible, or instead, a lower cutoff frequency for any given mouth size. This is achieved by foreshortening the horn, that is, cutting it short before its mouth area expands to excessive dimensions.

Foreshortened bass bins exhibit a highly irregular response over the first two octaves, which result in coloured and distorted low bass. Thus it is far better to sacrifice the low frequencies and attain a smooth response than to drive a horn below its legitimate cutoff frequency. Table 1 shows the minimum mouth dimensions for wall loaded bass bins.

Table 1. The minimum mouth dimensions of bass bins for audibly smooth frequency response.

| Mouth Area $\left(\mathbf{f t}^{\mathbf{2}}\right)$ | Min. driving frequency in Hertz <br> Wall loaded | Corner loaded |
| :---: | :---: | :---: |
| 28.0 | 30 | 20 |
| 15.7 | 40 | 29 |
| 10.1 | 50 | 35 |
| 7.0 | 60 | 42 |
| 5.1 | 70 | 50 |
| 3.9 | 80 | 62 |
| 3.1 | 90 | 65 |
| 2.5 | 100 | 71 |
| $\mathrm{ft}^{2}$ | Hz | Hz |

This table can be used to ascertain the minimum frequency at which a bass bin may be driven for an audibly smooth frequency response, regardless of manufacturers' specifications, which are rarely euphemistic.

In practice, few bass bins are sufficiently big to have a regular frequency response below 60 Hz for reasons of mobility. Corner loading extends the response, as shown in the table, but mounting bins in a corner is not often possible.

It is common to use vented enclosures to cover the first two octaves; many bass bins have reflex ports which are driven by the rear radiation from the diaphragm. This seems an elegant solution, but it is far better to use a separate vented enclosure and to enclose the rear of the horn driver. The compression chamber so formed linearises the response of the horn.

For all their advantages, bass horns are a perpetual problem in live sound systems. The three best solutions if smooth frequency response is desired are:
(1) Use a giant bass horn to provide smooth response down to 20 Hz or lower.
(2) Use a readily portable bass horn, typically responding down to 60 or 50 Hz , together with several (less efficient) vented cabinets to cover $20-60 \mathrm{~Hz}$.
(3) Corner load the above horn to provide a smooth response down to $35-42 \mathrm{~Hz}$, and accept the absence of the lowest audio frequencies. (In practice, a frequency response which rolls off sharply around 40 Hz is quite adequate in live sound systems.)
Midrange and treble horns are small and rarely need to be compromised in the manner that bass horns are. However, they may also suffer from an uneven response over the first octave above their cutoff frequency, which is heard as a "honk". This characteristic has given horns a bad name, but it is simply a case of inexpert application. The simple solution is to drive a honking horn at a higher frequency, that is, crossover at a higher frequency.

## CROSSOVERS AND BANDWIDTH LIMITATIONS

A small direct radiator will handle the entire audio bandwidth, but limiting the bandwidth over which a loudspeaker operates greatly lowers IMD, particularly at high powers. Moreover, air overload distortion in horns is proportional to the operating bandwidth. For this reason, horns are rarely driven over more than three octaves.

When a horn is driven below its cutoff frequency, the diaphragm is no longer pressure loaded and it reverts to direct radiation. This implies large amplitude diaphragm excursions, which quite apart from producing highly distorted sound, may endanger the diaphragm. This effect is particularly fatal to high frequency horns, since they commonly use compression drivers which are designed solely for horn loading and cannot withstand the large diaphragm movements that are inherent to direct radiation. Clearly a good, steep crossover network is essential in horn loudspeaker systems if driver damage is not to occur.

The simplest crossover networks are passive (Fig. 2). In order to handle high powers-even over 50 watts-without great losses, these are expensive.

The performance of the simple LC filter illustrated is dependent upon the loudspeaker to which it is connected, and the combination presents a capricious load to the power amplifier, which may be upset. For these reasons, more complex LC or RLC networks are used in domestic sound systems. These provide very good performance, but unfortunately at the expense of efficiency. For this reason alone, crossover networks which appear in series with loudspeakers are to be strongly depreciated in high power


Fig. 2. Simple LC filter arrangements


Fig. 4. Typical 3, 4 and 5 way systems
sound systems. Furthermore, any form of network in series with a loudspeaker can give rise to an audible "dullness", especially when the characteristically transparent sound of horn loudspeakers is considered.
The vast majority of professional high power sound systems now employ active crossovers. The term "active" indicates that the crossover filters use active devices; transistors or op-amps. Such filters are usually located immediately prior to the power amplifiers. By imposing bandwidth limitations in the small signal stages, a separate amplifier is required for each frequency band (Fig. 3). Thus a three-way active crossover uses three amplifiers and is said to be "tri-amplified".

The advantages of this method far outweigh the cost of additional amplifiers:
(1) Active filters can be readily produced with steep slopes without the great losses inherent in steep passive filters. Steep slopes allow horns to be driven harder and closer to their cutoff points with less risk of damage. Active filter slopes are commonly two to four times steeper than passive filter slopes.
(2) Switchable slopes and crossover frequencies are a practical proposition. It is possible to compensate for the difference in cutoff frequency of a bass bin when it is wall or corner loaded.
(3) The load resistance of an active filter is well defined. This ensures predictable filter performance.
(4) The power amplifiers are connected directly to their respective loudspeakers. This ensures good damping at low frequencies.
(5) Around 50 per cent of the energy of rock music lies below 350 Hz , thus amplifier clipping occurs initially in
the bass power amplifier(s). The resultant high order harmonics are directed solely to the bass bins, whose drivers are incapable of reproducing these high frequencies efficiently. Thus they are masked by legitimate (urtdistorted) high frequency signals.
(6) Intermodulation distortion is minimised in the power amplifiers as well as in the loudspeakers.
(7) Amplifer-loudspeaker combinations can be optimised, particularly in terms of power and impedance.
As a result of factors $1,5,6$ and 7 , a system using active filters can be driven much harder before the sound becomes "dirty". Thus a 1,000 watt tri-amplified system sounds much louder than a 1,000 watt system using passive filters. The improvement in sound quality is also far from subtle.

For an acceptable level of air overload distortion, the operating bandwidth of a horn is usually limited to three octaves. Thus a minimum of three horns is needed to cover the audio band. Starting at 40 Hz , the typical crossover frequencies will be around 320 Hz and 2.5 kHz . Restricting the bandwidth even more, and using many horns to cover the audio spectrum may appear to be a means to very high quality. Whilst this is broadly true, anomalies around the crossover points, particularly if they infringe upon the critical midrange frequencies, are troublesome. Moreover, the proliferation of amplifiers and horns would be costly and leads to great bulk and weight.

The law of diminishing returns sets in after five way systems, and tri- and quad-amplification are the most common configurations. Fig. 4 illustrates typical 3,4 and 5 way systems. Note that it is difficult to avoid the critical midrange frequencies $(750-3,000 \mathrm{~Hz})$ with the 5 way system if the number of octaves handled by each filter is to be kept
reasonably constant. Note also that the bass filters have a bandpass characteristic, in order to protect the bass bins from high level signals below their cutoff frequency.

## POWER AMPLIFIERS

All loudspeakers are readily damaged by excessive power inputs over long periods. When sound systems are operated by people who are not technically minded, it is always a good rule to use a loudspeaker rated at 10 to 100 per cent over the amplifier power. In high power systems, it is preferable for the loudspeakers to be overloaded before the power amplifiers, because the sound of an overloaded loudspeaker is much more pleasant than that of an amplifier driven into clipping. This assumes that the sound engineer is familiar with the sound of a distressed horn, and does not prolong its agony for any longer than absolutely necessary.

One of the essences of live music, especially rock, is the crescendo. It is necessary to try to achieve real dynamic range, because of this it is often not possible for sound systems to handle rock crescendos at realistic levels. One solution is to reduce the dynamic range requirements by using a limiter, but this greatly detracts from the performance. A compromise solution is to accept that something has to be overloaded on occasions, and this is usually the bass loudspeakers. A good 100 watt, 15 inch driver will, for instance, accept 500 watts of programme for a few seconds without undue distress. This reserve power handling capability should only be needed or used at climatic points, otherwise the loudspeakers will not live long.

In a tri-amplified system, each horn will usually have its own amplifier. High frequency horns generally use compression drivers which are capable of providing SPL's in the region of 140 dB at full power, thus it is unlikely that they will need to be overloaded. Compression drivers are quite easily damaged by excessive inputs, thus it is unwise to use an amplifier rated in excess of 100 W to power a horn rated at "100 watts programme". If the horn is rated at "100 watts r.m.s.", then it is in order to drive it with a slightly higher power amplifier provided (a) the system is never driven with pure sine waves and (b) the excess power capacity is only used sparingly. Bearing in mind that clipping will usually occur in the bass channels first, the bass amplifiers should be rated well above the r.m.s. rating of the loudspeaker amplifier powers, typically being two to five times greater.

The majority of professional power amplifiers on the market are very good, but the distortion figures at the maximum power output, particularly at the extremes of the audio band, are always revealing. Also, a good power amplifier should drive impedances well below its nominal load impedance at full power. In PA applications, the need for absolute reliability cannot be overstressed. Amplifiers with massive heatsinks, "redundant" output stages, thermal cutouts, failure and status indicators, welded steel cases, robust panel components and readily accessible fuses or circuit-breakers are a great help.

## HORN DISPERSION CHARACTERISTICS

PA loudspeaker dispersion characteristics should be neither laser-like nor omnidirectional. When sound emanates from an aperture much smaller than the wavelength of that sound, the aperture is said to be a point source, and the radiation is omnidirectional. The wavelength of a 200 Hz note is about five feet. At this frequency then, a 15 inch direct radiator acts as a point source. A bass bin, however, has dimensions which approach five feet, and thus bass bins are relatively directional at this, and higher frequencies. The larger a horn's mouth, the lower the frequency


Fig. 5. The midrange diffraction horn
at which sound dispersion can be channelled forwards. Therefore, giant horns are to be preferred at outdoor concerts, where sound that does not reach the audience directly is lost sound.

Unlike direct radiators, the dispersion characteristics of high frequency horns are readily tailored. Direct radiators are invariably very directional at high frequencies, but the flare of a horn can be modified to diffract sound waves and provide very wide vertical and horizontal dispersion. The midrange diffraction horn (Fig. 5) has flares which suggest wide vertical dispersion. Indeed, diffraction horns are regularly seen mounted sideways, in the mistaken belief that this gives the best dispersion! In fact, the dispersion is typically 150 degrees in the horizontal plane when the horn is used as illustrated. This dispersion is a result of diffraction about the sharp vertical (unflared) edges of the horn.

If we couple lots of small horns, which approach point sources, to a common driver, we can achieve wide dispersion, and because the total mouth area of the horn (equal to the sum of the individual horns or segments) is large, the cutoff frequency can be low. Also, each segment points in á slightly different direction, which further promotes wide angle dispersion. The dispersion characteristics are thus partially controlled by the segments, which gives such a horn very flexible dispersion properties. This is the multicellular horn (Fig. 6), with a dispersion angle of 150 degrees by 60 degrees vertical. Many horns which look like multicells are merely bifurcated.

Fig. 6. A $5 \times 2$ multicellular horn (Vitavox)



Fig. 7 (left). The acoustic lens

Fig. 8 (right). A radial horn


Fig. 9 (left). A "JBL" style long throw ''mid-bin'"

The acoustic lens (Fig. 7) defies visual analysis. It diffracts sound, but not only in the direction implied by the slanted plates. In fact, the dispersion is predominantly horizontal, being typically 140 degrees by 40 degrees. The lens is currently popular in live sound systems, but its cutoff frequency is usually quite high, and it usually has to be used with direct radiators. It is inferior to the multicellular horn in terms of efficiency, cost and dispersion flexibility. Moreover, the multicell has a much lower cutoff frequency, although the levity of a lens makes it amenable to roadies!

The wide dispersion horns described so far are used to cover the front rows at a large venue, or for comprehensive coverage in clubs and small halls. These are short throw horns. In larger halls, narrow dispersion horns are required to supply concentrated sound to the rear seats; these are long throw horns, and are usually of the radial variety (Fig. 8). However, in four way systems, high bass frequencies (200 to 600 Hz ) are often handled by the "JBL" style long throw "mid-bin", as depicted in Fig. 9. Both types of long throw horn have typical dispersions of 60 degrees by 30 degrees vertical.

## THE MINIMUM SOURCEIDEAL

When several loudspeakers operating over the same bandwidth are close together, interaction occurs and spurious phase cancellations result. This "phase distortion" causes colouration (which is displeasing to the ear) and upsets the dispersion properties of loudspeakers. It can also exacerbate acoustic feedback problems. To minimise phase distortion, the minimum number of sound sources lover each band) should be used. This is an especially good reason for never using direct radiators in high power sound systems. A horn will replace 10 to 50 direct radiators, thus it is possible to get much nearer to the minimum source ideal using horns; indeed, in small PA systems it is often possible to achieve the ideal of only one sound source over each band of frequencies.

## THE STACK

PA horns should be stacked up-hence "the stack". For small halls, where wide dispersion is all that is required, assuming a tri-amplified system is used, a three horn stack is ideal. This consists of bass, midrange and treble horns in ascending order. In larger halls, long throw mid and treble horns may be necessary, hence a minimum of five horns.

Interactions between long and short throw horns can be minimised by thoughtful angling and stacking. If higher SPL's are required, additional three or five horn stacks can be used. The stack is thus a module. However, the concept of a stack as a certain physical configuration must be dispensed with when several stacks are used in tandem. If they are merely used like building bricks, serious phase irregularities will occur; a rearrangement of the components of the composite stack is usually necessary. As SPL requirements increase, given that efficiency cannot be augmented, a proliferation of horns is inevitable, and skill is required in order to produce good results.

## STACKS FOR SMALL HALLS

The author's horn stack shown on the second page of this supplement uses a giant bass horn with a cutoff frequency of 2 Hz when corner loaded. A midrange diffraction horn provides wide angle coverage, and for low cost, piezoelectric diffraction horns are used above 3 kHz . Several are required to counter the "deadness" common to small halls which have been acoustically treated. Note the strategic angling for minimal interaction. The stack is often at audience level, thus the midrange and treble horns are mounted well above head level to prevent excessive sound absorption. This stack weighs 80 kg and is equivalent to 2 tonnes of direct radiator loudspeaker cabinets!

Table 1 shows that corner-loading a bass bin provides the lowest cutoff frequency, but wall loading is often the best that can be achieved. When using a single bass bin, however, wall loading is negligible. In this case, providing a solitary bin with baffles (Fig. 10) will greatly enhance the low frequency response. If a separate vented enclosure is used, this should be stacked immediately above the bass bin(s). Using the high frequency horns described earlier, Figs. 11a and 11 b show alternative and broadly equivalent approaches to horn stacks for small venues, where wide dispersion is all that is required.

## STACKS FOR LARGE HALLS

In large halls, long throw horns are required to reach the furthermost seats, though these should not be used unless

Fig. 10. A bass bin fitted with baffles



Fig. 11a (above) and 11b (right). Alternative approaches to horn stacks for small venues
absolutely necessary. Bass reflex enclosures do not have long throw properties, thus large numbers of bass bins will be required to project the low bass; when these are tightly stacked together, they provide highly effective mutual wall loading which greatly augments their bottom end response. Fortunately, the inevitable phase irregularities resulting from this arrangement will usually cause cancellation well above the bass crossover point.

The long throw horns should preferably be mounted well above the main stack, typically on the proscenium arch or on scaffolding, as shown in Fig. 12. In the side view of this stack note that the long throw horns are angled downwards, into the audience. Otherwise the sound is likely to hit the rear wall-and be reflected back! This slap-back echo is a perpetual problem in clubs with low ceilings and the need to build stacks high whenever possible cannot be overemphasised. As stack height increases, it becomes progressively easier to set the long throw horns at an angle that discriminates between the rear wall and the rear seats. If scaffolding is not available, it will be necessary to mount the long throw horns on top of the stack. A wedge provides the necessary 10 degrees to 15 degrees of downwards tilt.

Fig. 12. A horn stack with long throw horns mounted above the main stack and angled downwards



A " 5 kW "' horn loaded stack made up of: two Cerwin-Vega bass bins each with two 750W 18in drivers (frequency range $\mathbf{4 0 - 2 5 0 H z}$ ); five "Philishave" bins-shown in the centrethese are Martin 212 midrange horns, 250W each (frequency range $250-1500 \mathrm{~Hz}$ ); 8 JBL 2350 radial horns- 80 degrees by 60 degrees dispersion-100W each (frequency range $1500-4800 \mathrm{~Hz}$ ); four JBL 2345 radial horns- 90 degrees by 40 degrees dispersion-100W each (frequency range $\mathbf{4 8 0 0 H z}-16,000 \mathrm{~Hz}$ ) ; these incorporate four JBL Bullet treble horns which cover the same frequency range. (Courtesy Muscle Music)

Fig. 13. An impressive PA stack


An alternative, and very impressive looking stack is depicted in Fig. 13. It is currently fashionable to use acoustic lenses and direct radiators in place of the multicellular horn for short-throw midrange and treble. However, the low efficiency of these units takes us away from the minimum number of sources once again. When a stack of this kind is used, or several of the stacks illustrated in Fig. 12 are partnered, it is expedient to angle the high frequency speakers away from each other as much as possible to minimise interaction (Fig. 14). Excessive angling, however, will cause a lot of sound to hit side walls which is not helpful! Likewise, boxes containing arrays of piezo-electric horn tweeters usually have bevelled fronts.

## STEREO

So far it has been assumed that the sound system has a stereo format, with stacks either side of the stage. This layout is far from ideal. A minority of the audience will be suitably seated to hear an acceptable stereo effect. Transient sounds from percussive instruments will sometimes be heard as two discrete signals out of time with the music. Many sound engineers limit stereo to drums and special effects, hence the stacks will be working largely in mono. If we dispense with stereo altogether, then phase anomalies between the two stacks can be eliminated. A central horn cluster on the proscenium arch is sometimes a viable solution, and works very well.

The heavy bass bins may have to remain on stage, but by a suitable choice of crossover frequency, their phase anomalies can be minimal. In theory, the vertical displacement of the $\mathrm{mid} /$ treble horn cluster is not readily sensed by the ears. However, people differ and the cluster can be distracting, particularly if it is more visible than the musicians!

Another idea, akin to tri-amplification, is to split up a sound system such that separate amplifiers and loudspeakers are used for drums, keyboards, vocals, etc. The advantages are similar: the sound is subjectively louder, it is cleaner and the imagery is also greatly improved, giving a better impression of live performance rather than a glorified discotheque plus stage act. This is, of course, the situation in a small band without a "PA"!

## MIXING

In a four piece jazz-rock band, for instance, it is possible to increase the power of the instrument amplifiers to around 300 watts before the drum kit requires amplification. At this point it would be usual to consider mixing the whole band and using a common sound system. If the advantages of mixing could be sacrificed for sound quality and imaging, an alternative step would to be use a "drums amplifier". Each instrument would retain its own separate "sound system", little intermodulation can occur and the music remains totally realistic in terms of imagery.

Fig. 14. Angling of $h . f$. horns for minimum interaction



A rear view of the 5 kW stack, shown on the previous page, together with a $\mathbf{2 k W}$ sidefill stack (shown on the extreme right) for stage monitoring. Also shown are two 5ikW amplifier and crossover racks. (Courtesy Muscle Music)


The mixing desk used with the equipment shown above and on the previous page. The group can be seen in the background together with the sidefill stack and 5 kW stack which are just visible above the equipment on the right

With a little ingenuity and a few wires, sound balance could still be controlled by someone off stage. For small bands, the important point is to avoid using a common sound system until it is both absolutely necessary and sufficient funds are available to purchase good equipment of high power handling capacity. It is often far better to hire a good PA system when the need arises than to own cheap but inadequate equipment.

Always place your amplifiers as close as possible to the loudspeakers so that the shortest possible speaker cables can be used. Although it is feasible to have long speaker leads with little power loss by using $6 \mathrm{~mm}^{2}$ and similarly massive cables, the large capacitance and inductance of such cables may have insiduous effects on sound quality, quite apart from the great cost. For lengths under 5 metres, $1 \mathrm{~mm}^{2}$ two core sheathed p.v.c. cable is ideal, though butyl rubber cables are somewhat tougher.

Budding sound engineers are reminded that in large PA rigs, unseen, and sometimes rather humorous, dangers can lurk. In 1972, a well-known British manufacturer equipped the Lincoln rock festival with a 10 kW system. This promptly blew someone off the stage when an organist hammered on a chord miked through the system!

FURTHER READING
Paul Klipsch—Loudspeaker performance (Wireless World, February 1970)
Jack Dinsdale-Horn loudspeaker design (Wireless World, March-June 1974)
Adrian Hope--Hearing Damage (Studio Sound, August \& December 1975)
Dave Martin-Speaker technology for sound reínforcement (Studio Sound, March 1976)
Stephen Court-Quality performance IStudio Sound, November 1976)
Richard Galbraith—Rock music and hearing loss (J.AES, March 1977)
Ken Dibble—Design considerations for a PA speaker system (Studio Sound, May 1977)
Terry Nelson-Sound on stage (Studio Sound, May 1978)

| CMOS |  | 4020 | 50p | 4050 | 25p |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 4022 | 50p | 4060 | 80p |
|  |  | 4023 | 13p | 4066 | 30p |
|  |  | 4024 | 40p | 4068 | 13p |
| 4001 | 13p | 4025 | 13p | 4069 | 3 p |
| 4002 | 13p | 4026 | 90p | 4070 | 13 p |
| 4007 | 13p | 4027 | 28p | 4071 | 13 p |
| 4009 | 30p | 4028 | 45p | 4072 | 13 p |
| 4011 | 13p | 4029 | 50p | 4081 | 13p |
| 4012 | 13p | 4040 | 55p | 4093 | $36 p$ |
| 4013 | 28p | 4041 | 55p | 4510 | 60 p |
| 4015 | 50p | 4042 | 55p | 4511 | 60 p |
| 4016 | 28p | 4043 | 50p | 4518 | 65p |
| 4017 | 47p | 4046 | 90p | 4520 | 60 p |
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| 7400 | $10 p$ | 7474 | $22 p$ | 74145 | $55 p$ |
| 7401 | $10 p$ | 7476 | $25 p$ | 74148 | $90 p$ |
| 7400 | 74150 | $55 p$ |  |  |  |
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| 7444 | $39 p$ | 7494 | $45 p$ | 74174 | $55 p$ |
| 7420 | $12 p$ | 7495 | $35 p$ | 74177 | $50 p$ |
| 7427 | $20 p$ | 7496 | $45 p$ | 74190 | $50 p$ |
| 7430 | $12 p$ | 74121 | $25 p$ | 74191 | $50 p$ |
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DL707 0.3 in CA

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| :--- | ---: | ---: | :--- |
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| $2.5 \times 3.75$ | $45 p$ | $45 p$ |  |
| $2.5 \times 5$ | $54 p$ | $54 p$ | Pin insertion |
| $3.75 \times 5$ | $64 p$ | $64 p$ | toot $108 p$ |
| $3.75 \times 17$ | $205 p$ | $185 p$ |  |
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## CONSTRUCTION

THE SYSTEM is constructed on four boards, two of which are non clad Veroboards, and the remaining two are i.c. stripboards which have edge connector pads that are notused in the Modem. See Figs. 2.2, 2.3, 2.4 and 2.5.

$$
v^{\prime \prime \prime} .
$$

## APPLICATIONS INFORMATION

The following section deals with uses to which the acoustic modem may be put. It deals with various applications; many enabling things to be done over a telephone that could not normally have been achieved without the use of this device.

Data transfer from a data bus on any minicomputer, or for that matter any device that presents its output in parallel form is possible. Obtain and study the AY-5-1013A data sheet.

This Integrated circuit is probably the most useful device ever produced for data transmission and interfacing with microprocessors etc. Basically its function is to accept a parallel 8 bit word, and when it is told, clock that data out in serial format, inserting its own start, stop, and parity checking bits automatically. The device is split into two parts, transmitter and receiver. It is designed to interface directly a parallel output device to a modem.

Having incorporated this device in the acoustic modem system the possibilities are endless in terms of data that may be sent over long distances. A block diagram of the UART is "shown in Fig. 2.1, as a transmitter with associated waveforms.

It so happens that General Instruments produce another Integrated circuit that is able to interface directly with the 'UART'. This i.c. is General Instruments 2376 Keyboard Encoder. This device is a Read-Only Memory (ROM) that will


Fig. 2.1(a). UART Transmitter block diagram

Fig. 2.2. Modem Transmitter board


Fig. 2.3. Modem
Receiver board

Fig. 2.4. LED Driver board


Fig. 2.5. Power Supply board


OVER RUN
Fig. 2.7. Circuit for automatic

Send/Receive
encode all the characters on a standard "OWERTY" (typewriter) keyboard and produce an output in parallel ASCII along with a strobe pulse. Keyboards have become inexpensive now and some of them are already encoded using the 2376 ROM. The Parallel output and strobe from such a keyboard may be used to directly load the AY-5-1013A UART with ASCII code and then send by FSK via the modem to the distant terminal.

## SIGNALS IN DC LEVELS

It is even possible to send and receive low frequency signals, and d.c. may also be transmitted using Binary coded data, since our UART may be loaded with any 8 bit parallel data. From 8 bits of data we could resolve a quantity into $2^{0}+2^{1}+2^{2}+2^{3}+2^{4}+2^{5}+2^{7}+2^{8}$ parts which decimally speaking $=1+2+4+8+16+32+64+128$ which is equal to 255 . The method used to convert a d.c. voltage or a slowly changing signal to 8 bit binary is known as Analogue-to-Digital conversion. A very useful integrated circuit can be used to do just this. It is the Ferranti ZN425E. This chip will accept an analogue signal and decode it into 8 bit parallel data. It will also work in the reverse mode converting the digital data to an analogue signal.


There is a worthwhile modification to the acoustic modem to further improve its versatility. This is to do away with the Send/Receive switch and arrange for remote Send/Receive under the control of an external device. This modification is outlined in Fig. 2.7.

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## WAH-WAH PEDAL

MANY guitarists will be familiar with the Wah-W ah pedal, but many of the available makes or published designs are often unsatisfactory-especially for professional use. As a guitarist, I have found that some units have a very small or uneven range, whereas others are too harsh-often with annoying 'clicks', which give a scratchy sound, which totally spoils higher notes. Even commercially available units have disadvantages-notably in background noise, changing in volume as the pedal is moved, and. of course. price.

So, with these problems in mind, it was decided to sit down to design a really highquality unit, delicately balanced between the harsh and smooth sounds. This circuit produces a subtle but effective sound, perfect for guitars or other electric instruments, and may be used as a treble booster.


This unit is best built as a pedal. One of the jack sockets (usually the input) should be replaced with a switched socket to connect the negative supply when a plug is inserted. A foot-switch should be fitted to by-pass the circuit. The value of the potentiometer depends on the swing of the pedal, but it should be made to give a swing from 0 to 50 k .

The unit is simple to construct and gives a really superb sound-especially if used with the recently published 'phaser'. A really worthwhile project.

Richard Fuller, Much Hadham, Herts.

## 200 WATT TEMPERATURE CONTROLLER

THE temperature controller described was designed for propagator heaters rated up to 200 watts, but can be used in many applications where good control is required. The stability is typically within $0.5^{\circ} \mathrm{C}$ of the set point and the temperature range is approximately 10 to $40^{\circ} \mathrm{C}$.

For safety considerations, the temperature sensor is electrically isolated from the mains side of the controller and con nected to earth. This means that if the device itself becomes damaged in any way. there would be no danger.

A National Semiconductor LX5700H integrated circuit temperature transducer is used as the sensor. This has an operational amplifier fabricated on the same chip and gives an output directly proportional to degrees Kelvin at $10 \mathrm{mV} /{ }^{\circ} \mathrm{K}$. (Note: ${ }^{\circ} \mathrm{K}={ }^{\circ} \mathrm{C}+273.2$.)

The output is compared with a stable reference voltage by means of the differen tial amplifier 1 CL . Potentiometer VRI determines this reference voltage which thus sets the operating temperature of the controller.

If necessary, the lower and upper limits can be extended by altering RI and R2 respectively. However, one should have in mind that the maximum permissible temperature is restricted by the operating temperature range of the sensor which is -85 to $+125^{\circ} \mathrm{C}$ for the one used. The output from ICI is applied via TRI to the unijunction transistor (TR2) firing and triac switching circuit to provide full-wave, phase-controlled a.c. power to the load.

The 2.7V Zener diode D2 in series with the lok base resistance is included to ensure TRI can switch hard off even if the output voltage swing from 1 Cl does not fully approach the supply voltage.

The pulse transformer Tl provides isolation for the triggering pulses and the supply to the unijunction trigger circuit is isolated from the mains with transformer T2. This supply is then rectified and clamped to 20 volts by the Zener diode D4. The Zener diode DI and associated capacitor C1 provides a 6.2 V smoothed d.c. supply for the preceding circuitry. As the reference voltage is taken off this supply

D1 should preferably be a temperature compensated Zener such as a Muliard 1N82I or RS 283-097.

The mains filter is provided to suppress radio frequency interference caused by triac switching. It should be mentioned that as the triac is rated at 8 A it is only necessary to increase the rating of the filter to improve the maximum power capability of the controller (e.g. RS 238-435 for 2A or $238-\mathbf{3 9 0}$ for 5 A load). However, if used at maximum power, the triac must be mounted on a heat sink having a thermal dissipation of $4^{\circ} \mathrm{C} / \mathrm{W}$ (e.g. RS 401/497). Alternatively, as the tab is electrically isolated, it may be fixed directly to the chassis for heat dissipation.

The sensor itself can be made into the form of a probe by insulating with sleeving and encapsulating with silicon rubber compound or expoxy resin.
D. Wedlake,

University College.
Newport Rd.,
Cardiff,
CF2 ITA.


## SIMPLE GUITARTREMOLO

FROM switch-on, C1 starts to charge via R1, R2, and VR1 (VR1 determines the rate of charge/discharge and therefore the frequency of operation). When Cl reaches $\frac{2}{3}$ of the supply voltage, pin 7 switches to the low state, C1 then dis charges through pin 7, via R2, VR 1. Discharge stops when the voltage across C decreases to $\frac{1}{4}$ of the supply, this activates the trigger (pin 2) and the cycle starts again.

The output from pin 3 is used to drive the l.e.d. via a 470 ohm resistor. The light from this varies the resistance bf R4 which via C3 and VR2, short-circuits the line and consequently attentuates the signal, VR2 controls the depth of attentuation

Although the output from the 555 is a square-wave this is not really noticeable except at low frequencies, which are not normally used for this application anyway.
is important to house the unit in a light-proof box.
A.R. Curtis,

Bedhampton, Hants.


## HEXADECIMAL DISPLAY



T
THIS four-digit display is capable of dis playing the letters $\mathrm{A}-\mathrm{F}$ as well as the digits $0-9$, making it ideal for use with microprocessors, in the monitoring of address buses for example. However, it may be used in frequency meters, clocks, etc., although using 7447 s would be easier in these latter cases. It is designed to act as a direct drive display, but may be used as a multiplexed display by applying the inputs directly to IC8 and the display drivers, leaving out ICs $1-7$.

ICIb, VRI, and Cl form an oscillator which clocks the two-stage counter IC4.

The outputs are decoded by IC 5 to mul tiplex the display. ICla, IC2, and IC3a-c reset the two flip-flops; SI controls at which point in the count they reset. It is therefore possible to alter the number of operating digits in the display.

ICs 6 and 7 are multiplexers, controlled by IC 4 , which sequentially feed each of the four four-bit binary data inputs through to the decoder IC8. The sixteen outputs are fed to a diode matrix.

The seven diode matrix outputs are fed through invertors and current-limiting resistors to a common anode display ( $4 \times$

FND507, $2 \times$ DL727, etc., etc.). Nonl.e.d. displays may be used, provided that suitable display drivers are used, and that no more than 16 mA are required per segment. The gates ICSa-d will source $800 \mu \mathrm{~A}$ to operate the display drivers. The decimal points may be separately driven, if they are required. L.c.d. displays should not be used.

## R. G. Stubbs, <br> Dartford, <br> Kent.



## 

A hi-fi stereo amplifier producing 30 W r.m.s. per channel and employing a new high quality hybrid power amplifier IC for ease of construction.

This design has switchable active filters for scratch and rumble giving 12 dB per octave roll off. The filters are switched completely out of circuit when not in use to obtain excellent noise figures. The amplifier also has a tone defeat facility which switches the tone control network out of circuit. Other facilities are; inputs for phono, aux, tuner, tape and a mono/stereo switch.

A complete kit of parts will be available.
To enable a hi-fi system to be put together at an exceptional price we have also been able to arrange a special offer on a pair of Videotone GB3 bookshelf speakers.

This special offer is available on an individual basis to all readers.
The GB3 speakers are an improved version of the Videotone Minimax 2 which has been highly acclaimed by the hi-fi press over the past five years and has been favourably compared with monitor speakers.


This unusual design enables a number of model locomotives to be individually controlled on one interconnected layout. Construction for a four channel controller will be described in detail but it is possible to extend the unit up to ten channels.


# 8 Pare Suptlomerdt... UIDED for EVERYONE 

## PRACTICAL



DEAFNESS is one of the most disabling afflictions amongst the elderly. While not life endangering this condition causes considerable hardship and places many restrictions upon the sufferer. These mainly elderly people tend to miss vital parts of a conversation, misunderstanding the simplest of messages resulting in them being regarded as slow witted and dull. The effort of repeating a casual remark three or four times is enough to deter most of us from even attempting to hold a conversation with anyone who is 'hard of hearing'. The result is that the deaf often become isolated and ignored. Whereas the blind receive sympathy all too often the deaf receive nothing but contempt and irritation.

## HEARING AIDS

The electronic design and construction of hearing aids is no problem in the age of microelectronics. However, conventional hearing aids while having the advantage of cosmetic concealment and portability are often inadequate when conversation rather than the stereotyped responses of everyday life is required. It is the striving for cosmetic acceptability (very important if they are to be worn and not rejected in embarrassment) which renders these hearing aids acoustically rather than electronically poor.

Moulded earpieces may be worn comfortably and inconspicuously, but are poor at reproducing the lower frequencies because of their size. The sound from such earpieces is of necessity distorted and may cause problems even for those of us with normal hearing.

The other acoustic/electronic interface is the microphone. Here again size may be the problem in the behind the ear type of hearing aid where the size of the sound receiving surface is limited. This restriction causes low sensitivity and distortion. The pocket type of hearing aid is less attractive to the wearer because of the unsightly cord leading to the ear, but it can incorporate a reasonably sized microphone with minimal distortion. The disadvantage of this type of aid is that it is usually hidden in the clothing and is liable to pick up the rustle of the fabric as the wearer moves.

In order to hold a conversation with a relatively deaf person, a device with better acoustic rather than electronic properties than the conventional hearing aid would be of use. A circuit was therefore built which would amplify a
signal from a microphone and supply a pair of earphones. In fact the results obtained using a cheap crystal microphone insert, and a pair of inexpensive headphones was quite encouraging, and the prototype unit is now in daily service being preferred by the patient to her NHS hearing aid for one-to-one conversation, and watching television.

## THE CIRCUIT

The input from a high impedance microphone is fed to IC1 through C1. The 741 i.c. functions as a high gain preamplifier driving the push pull output stage. The gain obtained by the 741 may be varied by changing the value of R5, the gain increasing as the resistance is increased. Base bias for the output transistors is provided by the R3, R4, D1, network, the forward voltage across D1 separating the bases sufficiently to reduce crossover distortion.

The circuit diagram in Fig. 1 shows this simple unsophisticated device which is easily constructed by most people in a very short time, and costs little more than $£ 1$ (the microphone and earphones, may be bought for about $£ 5$ ).

## COMPONENTS .

## Resistors

| R1 | $33 k$ |
| :--- | :--- |
| R2 | $33 k$ |
| R3 | $10 k$ |
| R4 | $10 k$ |
| R5 | $180 k$ |

All $\frac{1}{2} \mathrm{~W}$ carbon $10 \%$
Capacitors

| C 1 | $0.33 \mu$ |  |
| :--- | :--- | :--- |
| C 2 | $100 \mu$ | 16 V electrolytic |

Semiconductors

| D1 | IN 4002 |
| :--- | :--- |
| TR1 | BC 142 |
| TR2 | BC 143 |
| IC1 | 741 |

Miscellaneous
0.1 in Veroboard. Stereo jackplug socket. PP7 battery. $3 \frac{1}{2} \mathrm{~mm}$ jack plug and socket. Microphone (crystal insert is sufficient). Light coax, cable. Headphones


Fig. 1. Circuit of Conversation Aid
A Veroboard layout is presented in Fig. 2, the output of the mono amplifier being supplied to the headphones.

The whole unit including a PP7 battery was housed in a plastic box 5 in $\times 2.5$ in $\times 2.5$ in and the microphone was attached using light weight coaxial cable, and a $3 \frac{1}{2} \mathrm{~mm}$ jack plug and socket.

## APPLICATION

Cheap and simple electronics can play a useful role in alleviating the problems of deafness once the idea of miniturisation is abandoned. Concealed hearing aids are a great benefit to the deaf, but they have their limitations. The


Fig. 2. Veroboard Layout
device described will not help the truly 'stone deaf', but may do much to retain or restore the domestic harmony and social life of the 'hard of hearing'.

## Honnidnun

Waveform And Function Generators (mini) Feb. 19-21. National Microprocessor and Electronics Centre. London (close to Tower of London). All mini-exhibitions held at this centre run concurrently with a permanent exhibition of electronics. LI
BEX Feb. 20-2 1. Pavilion Bournemouth. K
IEA/Electrex Feb. 25-29. National Exhibition Centre, Birmingham. I Wire Preparation (mini) Mar. 4-6. National Microprocessor and Electronics Centre. London. L 1
Keyboards And Switches (mini) Mar. 18-20. National Microprocessor and Electronics Centre, London. Ll
Viewdata March 26-28. Wembley Conference Centre, London. O Computer-Aided Design (conference \& exhibition) March 31-April 2. Metropole, Brighton. Details: CAD 80/0483-31261
Small ATE April 1-3. National Microprocessor and Electronics Centre, London. LI
Applying Microprocessors April 8-10. National Microprocessors and Electronics Centre, London. L1
Seminex April 14-18. Dept. Physics, Imperial College, London, H1
Communications 80 April 14-18. National Exhibition Centre. I
Calibration April 15-17. National Microprocessor and Electronics Centre, London. El
Welsh Amateur Mobile Rally April 20. Memorial Hall. C
Electronic Test \& Measuring Information April 22-24. Wythenshaw Forum, Manchester. T
International Conference On The Electronic Office April 22-25. London Penta Hotel. Organised principally by the Institute of Electronics \& Radio Engineers. 99 Gower St., London WC1E 6AZ
North Midlands Mobile Rally April 27. Drayton Manor Park, Tamworth, Staffs. Details: Norman Gutteridge, 68 Max Rd., Quinton, Birmingham.
All-Electronics Show April 29-May 1. Grosvenor House, London. E
The Mersey Micro Show April 30-May 2. Adelphi Hotel, Liverpool. O Compec Europe May 6-8. Centre International Rogier, Brussels. L

Great British Electronics Bazaar June 20-22. Alexandra Palace, E
Intel Fair June 24. Wembley Conference Centre, London. U.
Tempcon July 1-3. Wembley Conference Centre. Exhibition devoted to temperature control \& measurement. T
Transducer July 1-3. Wembley Conference Centre. T
Microsoftware (symposium) July 7-10. University of Sussex. S 1
The 1980 Microcomputer Show July 10-12. Royal Lancaster Hotel, London. 0
Avionics (symposium) Sept. University of Surrey. S 1
Harrogate International Festival of Sound Aug. 16-19 (18 \& 19 trade). The Exhibition Centre + hotels. X

E Evan Steadman, 34-36 High st., Saffron Walden, Essex. 60799 22612
C Barry College of F.E. Radio Society, College of Further Education, Colcot Rd, Barry, S. Glam. CF6 8YJ.
H1 Seminex Ltd., 79 High st., Tunbridge Weils, Kent. TN1 1 XZ. 6 0892 39664/5
I Industrial Trade Fairs, Radcliffe Ho., Blenheim Court, Solihull, W. Midlands B91 2BG. 021-705 6707
K Douglas Temple Studios, 1046 Old Christchurch Rd., Bournemouth, Dorset BH1 LLR. 02020533
L Iliffe Promotion, Dorset Ho., Stamford St., London SE1 9LU. 6 01-2618437/8
O Online Conferences, Cleveland Rd., Uxbridge, Middx. UB8 2DD. § 089539262
T Trident International Exhibition, Abbey Mead Ho., 23a Plymouth Rd., Tavistock. Devon PL19 8AU. 08224671
U Brian Crank Associates, 58 London Rd., Southborough, Kent. 6 0892-3181238414
X Exhibition \& Conference Services, Claremont Ho., Victoria Ave., Harrogate, Yorks. © 0423-62677
L1. P. Smith, London World Trade Centre, Europe House, London E1 9AA. 6 01-488 2400
SI Society of Electronic \& Radio Technicians, 57-61 Newington Causeway, London SE1 6BL. / 01-403 2351


Copies of Patents can be obtained from: the Patent Office Sales, St. Mary Cray, Orpington, Kent Price 95p each

## NOISE ELIMINATOR

Pioneer of Japan has filed a British patent application (No. 2020 131, dating back to March 1978 under the new laws) which describes an interesting idea for rejecting unwanted noise from audio circuits. The invention is directed primarily at car stereo systems but could have wider applications.

In a car system the cassette deck or radio receiver sections are coupled to the amplifier section by screened leads, but noise from the ignition, windscreen wipers and switches can still breakthrough and pollute the reproduced programme signal. Moreover if power supply current for the amplifier also flows in the screens, any ripple in the power supply will superimpose on the audio signal. These problems are especially noticeable with modern car systems, which are of very high amplification power and aim at true high fidelity. Interference can be rejected by the use of a transformer or photo coupler ahead of the amplifier, but the additional components are expensive if distortion or band limiting is to be avoided. Pioneer now claim that a differential amplifier system is the solution.

As shown in figures 1 and 2 , audio source A (tape deck or radio receiver) feeds amplifier B via a pair of differential am-

FIG. 1


FIG. 2

plifiers. The first differential amp (for the left channel) is based on transistors Q1, 02 with their emitters connected commonly through load resistors R1, R2. A constant current source transistor Q 5 is collector coupled to the emitters of Q1 and Q2. The second diff amp (for the right channel) is similar i.e. transistors Q3, Q4, load resistors R3, R4 and transistor 06 (Fig. 2).

If a noise voltage $e_{n}$ is induced between the terminals $\mathrm{E}_{\mathrm{in}}$, E 2 this voltage is directly applied to common input $\mathrm{E}_{\text {in }}$ and common terminal E2. So noise $e_{n}$ is applied to the bases of transistors Q1, Q3. Audio signal $\mathrm{e}_{\mathrm{i}}$, on which the noise voltage $e_{n}$ is superimposed is applied to the input terminals $L_{\text {in }}$ and $\mathrm{R}_{\text {in }}$, so a corresponding signal is applied to the bases of transistors Q1, Q4. A differential signal appears at the outputs $\mathrm{L}_{\text {out }}$ and $\mathrm{R}_{\text {out }}$ of the diff amps. This signal exactly corresponds to the audio signal output from the source $A$. So there is no trace of noise $e_{n}$ at the output terminals $L_{\text {out }}$ E2, and $R_{\text {out }}$. Hence the amplifier B produces a noise-free signal.

Pioneer point out that because the circuit does not rely on a bulky transformer or photocoupler it can be reduced to i.c. chip form and mass produced at low cost and of very compact size.

## ANTI-SOUND

The concept of anti-sound is not new. For years engineers have been working towards a system which mimics ambient noise, but in anti-phase, so that the net result is silence. The Munich company Messerschmitt-Bolkow-Blohm GmbH already has several patents on inventions in this field and is now applying for protection on an interesting idea intended to overcome one of the major problems to date. This is non-linearity between the noise sensor and the anti-noise generator. UK patent application no. 2019695 (filed under the new laws and dating back, hopefully not significantly, to 1 April 1978), offers lengthy mathematical back up for Messerschmitt's claim to success with an apparently novel approach. This involves the integration of both the sound sensor and sound generator into a single unit.

Figure 1 shows a combined sensor/generator with a thin walled diaphragm

1 which seals air space 3 in a housing 2. A capacitive distance measuring device 4 determines the time characteristics of the deflection of the diaphragm caused by arriving sound. A signal derived according to the maths in the patent is transmitted to a control 5 which applies a voltage proportional to the deflection signal to a grid electrode 6 beneath the diaphragm. The diaphragm generates appropriate sound waves to interfere with those which it
senses, thereby reducing the ambient sound level in the immediate vicinity.

Figure 2 shows a slightly different combined unit, with a rigid plate diaphragm 11 buckled into housing 12 to seal a vacuum 13. A control force is derived from an accelerometer 14 and applied to the diaphragm 11 via control circuit 15 for a magnet and moving coil arrangement 16. The diaphragm is thus again driven to transmit sound waves which interfere with those which it senses.

FIG. 1

FIG. 2



For some time now we have been on the lookout for a well presented ladies digital watch that would make a good offer. The problem with
most inexpensive ladies watches is that the case that would make a good offer. The problem with
most inexpensive ladies watches is that the case design is often ugly, bulky, flashy or a combination of all three. However, when this particular ladies of all three. However, when this particular ladies
watch appeared in the office and girls from other magazines in the building started asking for them we knew we had a winner.

The watch is a straightforward design with a built-in light. It displays time, date and seconds with a four-year calendar, is I.c.d. and has no extra gimmicks. It comes in a stainless or gilt case with matching fully adjustable bracelet and has a black face surrounding the display. Above all, it is neat and good looking for everyday use.

The twenty or so watches now in use by various staff, their wives or daughters, have proved completely reliable over the past three months; each watch comes with a year's guarantee. In short a nice, no nonsense watch at an excellent price.


To: Videotime Products (PE Ladies Watch Offer), 56 Queens Rd., Basingstoke, Hants. RG21 1 RER. Tel. (0256) 56417 \& 26620 (offer limited to UK and Eire


FRANK W. HYDE

## INDIA AND HER SATELLITES

Since the first satellite coverage of India, when a geostationary craft was moved to a position to serve for a trial period, plans were finalised for the future of the continent's own system. The geostationary craft was the ATS6 and was used to test the use of television educational methods. This craft was moved back to its position about a year later and proceeded with its normal tasks.

India has now leased one quarter of an Intelsat 4 A transponder which has been operating with the first of the Insat ground stations. Testing of the first domestic link, between Madras and Delhi began operation in August 1979 and completed its task in November, Another experiment aboard the Ariane as a passenger payload, is an Indian built two transponder experimental satellite which is due for launching in 1980.

In addition to the transponder India has built two scientific satellites. These were launched by Russia.

At the present time the finalising of the design of a remote sensing satellite for dealing with natural resources to be launched in the 1983-1984 period in a polar orbit, is in progress.

## COVERAGE AND CONTROL

The whole continent including remote islands, Andamans and Nicobar, will be covered with levels of ground facility. These are large earth stations with two 14.6 m antennae, medium stations with one $14 \cdot 6 \mathrm{~m}$ antenna and remote area terminals. In addition to these principle stations there will be more than 100 meteorological platforms for data collection and six mobile ground stations. There will also be a number, not defined, of low cost ground stations which will be used for direct television coverage.

The Master Control Facility will be at Hassan in Southern India, a Network Control

Facility will be at New Delhi and the third major facility will be the Meteorological Data Utilisation Centre also at Delhi.

The Master Control Facility will provide orbit raising as well as in-orbit control. It is expected that this will be completed by October 1980. The Metec-ological Facility will be completed before first launch.
Insat 1-A is expected to reach full utilisation by mid 1982. Insat 1-B will be launched in the second half of 1982.

Insat is a joint venture of the nation's Space Department, Posts and Telegraph Department of the Ministry of Communications, Indian Meteorological Department of the Minsitry of Tourism and Civil Aviation, and Doordarsham, the Television section of the Ministry of Information and Broadcasting.

The Space Department is the responsible body for establishment of the space facilities. The Post and Telegraph Department is responsible for the telecommunications ground system and for the utilisation of the ground systems whilst the Meteorological Department is responsible for similar facilities in regard to meteorology.

## LAUNCH OF INSAT 1-A and 1-B

Insat is designed so that it may be launched either by shuttle or by the NASA/McDonnel Douglas Delta 3910 expendable booster. India's agreement with NASA provides for a Delta back-up option should a shuttle slot not be available. In either case a McDonnel Douglas payload assist module will be used to boost the satellite from a low earth orbit to the geostationary orbit. One of the Satellites will be at final orbit longitude $74^{\circ} \mathrm{E}$ and the other at longitude $94^{\circ} \mathrm{E}$.

The satellites will have twelve transponders for telecommunications and two for television and radio direct broadcasting. The telecommunications transponders will receive ground signals at $5935-6425 \mathrm{MHz}$. and transmit on down link frequencies of $3710-4200 \mathrm{MHz}$. The minimum output will be 32 dbw equivalent isotropic radiated power after seven years in orbit.

The television transponders will receive at $5855-5935 \mathrm{MHz}$. and will transmit at 2555 2635 MHz . They are designed to have a final life output of 42 dbw . All the transponders will have a bandwidth of 36 MHz .

The telecommunications transponders will provide 6,000 channels interconnecting a network of 35 fixed and mobile earth stations of various sizes and capacities. Conventional systems will be used in heavily populated areas but in remote or sparsely populated areas small receiver aerials only will be employed. The low cost receiving aerials will be between three and four metres in diameter. Disaster warnings and ordinary radio programmes could be given in this direct broadcast network.

## RADIOMETER

The satellites will carry a high resolution radiometer which will make available at two hourly intervals visible and infra red images of the whole Earth. The visible images $0.55-0.75$ micrometres will have a resolution of 2.5 km and the infra red $10 \cdot 5-12 \cdot 5$ micrometres a resolution of 11 km .
The other facilities are 100 unattended land
and sea based data collection platforms. These will transmit meteorological data, hydrological and oceanographic data to the Delhi centre. Radiometric data will be down linked on a discrete channel at 4034.55 MHz . Collection platform data will have a 200 kHz bandwidth at 4038.1 MHz . The up-link for this data will be 402.75 MHz .

Observations of weather systems will include cyclones, sea surface and cloud top temperatures, water bodies, snow and other terrain changes which will include areas adjacent to India. Thus the close watch of cyclones will enable forcasts up to 12-24 hours in advance of other available methods. This will advance warning times by the direct broadcast system.

The snow coverage facilities are expected to assist the regulation of reservoirs for irrigation, irrigations control and flood control. The sea surface temperature is expected to make it possible to make earlier forecasts of the onset of monsoon periods. In fact there are all the facilities for the Indian continent to control the utilisation of its own natural resources and agriculture.

The meteorological information will be transmitted in real time from the Delhi Earth Station to the New Delhi Data Centre over a microwave link. The New Delhi Centre will analyse process and store data from the platforms and transmit processed images over telecommunications lines to the forecasting offices of the Meteorological Centre.

## THE EARTH STATIONS

Five of the stations will have very high gain facilities of the order of $31 \mathrm{db} /{ }^{\circ} \mathrm{K}$. These will be located at the main switching centres of the National Communications system. They will provide remote area communications and an up-link for feeding the ordinary television networks. They will also provide the telephone trunk service. All but the station at Shillong will have two antennae so that there can be simultaneous links with Insat 1-A and 1-B.

Twelve medium sized stations will be erected at Leh and Jullunder in the north of India, Lucknow, Patna, Bhubaneshwar, Ahmedabad and Jaipur in the centre, Hyderabad and Ernakulam in the south, the Laccadive Islands in the Southwest and the Andaman and Nicobar Islands in the southeast. These will provide trunk telephone service and Television up-link feed. The gain of these stations will be $27.5 \mathrm{db} /{ }^{\circ} \mathrm{K}$.

Twelve remote area terminals with a gain of $19.7 \mathrm{db} /{ }^{\circ} \mathrm{K}$ will be used for the telephone service only. These will be at Srinagar in the north, Arunachal Pradesh, Nagaland, Imphal, Mizoram, Agartala and Gangtok (Sikkim) in the north east, Bujand Johpur in the west, Goa and Pondicherry in the south and Minicoy south of the Laccadives, in the southwest.

Six mobile terminals are also to be included in the system. Four of them will be stations which are transportable having gains of 19.7 $\mathrm{db} /{ }^{\circ} \mathrm{K}$. The remaining two will be emergency stations which can be airlifted or moved on jeeplike vehicles. These will be capable of providing both telephone and televisión uplink feed service. All the high frequency ( $4-6 \mathrm{GHz}$ ) earth stations will be linked via the control centre at New Delhi.

## THE SATELLITES

The satellites will use three axis stabilisation with a precision momentum bias attitude control system. Two off-axis momentum wheels will be used in the primary mode and a single pitch and yaw wheel. Two-axis infrared earth sensors and a digital sun sensor provide attitude reference. Spacecraft thrusters will fire to unload the wheels.
The antennae reflectors will be deployed when the satellite is in orbit. The circular reflector 1.4 metres in diameter will be used for all the $6-\mathrm{GHz}$ up link reception. Down link will use half of the $4-\mathrm{GHz}$ channels. This reflector will use dual band horns for transmit and receive.

A $1.5 \times 1.6$ metre reflector at the opposite end of the satellite will transmit the remaining 4 GHz channels and also the 2.5 GHz down link signals. The antennae produce circular beams but are so designed that the edges limit the flux outside the territory of India. Four printed circuit crossed dipole antennae will be mounted on the earth viewing face of the satellite for the reception of UHF signals from the data collection platforms.

The satellite will weigh $1,279 \mathrm{lbs}$ when in geostationary orbit. The overall dimensions, when all the arrays, antennae. and solar sail are extended, will be $5.8 \times 1.4 \times 17.9$ metres.

The solar sail, to counteract the effect of the
solar wind on the asymmetrical solar array, is a ten foot high conical array.

## POWER ARRAY

The solar power array is made up of five panels arranged with a vacant panel area to allow a clear view for the meteorological sensor. The array is 11.5 sq metres and designed to produce 900 W at the end of the spacecraft's seven year life design.

A boost motor for control at apogee uses liquid propellant. This together with the solar sail and the microprocessor control system are innovations not in operation in any earlier design.

## SECRET POLLING

Here is a suggestion for a "secret" key polling subroutine on the UK 101, sent in by J. M. Leach of Deal, Kent.

## 100 POKE 11, 0 : POKE 12, 253 : X=USR (X) ; A=PEEK (531) : RETURN

## Now try

## 10 GOSUB 100 : PRINT CHR\$

 (A) ; GOTO 10The routine described will return any single character from the keyboard without the need to press the RETURN key. This is useful in computer games. However, we should point out that this routine will wait until a key is pressed before commencing execution.

The method suggested in PE November 1979, on page 30 , whereby the keyboard buffer address ( 57988 ) is POKEd with the appropriate Row number, and the PEEKed for the expected Column number, has one major advantage. With this method, the machine will sweep past the statement and ignore it if no key is pressed. The more dynamic, or realtime games need this feature, so that if there is no operator response, the machine will continue to animate the screen graphics.

When the latter is used, Control C must first be disabled by POKEing 530, 1 . Use the keyboard matrix diagram of page 14 in the Compukit Manual for Row and Column numbers. You will soon discover the relative merits of these two methods.

## 101 USERS' GROUP

Sir-Having read your magazine for some years now I have always been interested in your many and varied projects. In the last few months I have followed with great interest your series on the Compukit UK101. l own one of these and it was verv reassuring to see a magazine of your standing devoting so much space to this item.

Recently / have started a user group for
the UK101 and all the members have at some time read the series. For this reason, when I decided to expand the club, I thought it wise to contact you.

The group serves as a clearing house for programs as well as providing useful hints on construction and most other aspects of the 101. We hope to be able to produce a newsletter in the very near future, and perhaps a cassette or two of the most popular programs.

Adrian Waters,
101 Users Group.
For further information, contact: Mr. Waters, Cadover, 117 Haynes Rd., Hornchurch, Essex.

## DODGEY DIMENSIONS

Sir-I have built the UK101 and consider it to be superb value. However, recently / discovered a bug in my machine. If during a program I DIMension a string array with the first subscript having one of the following values: 1, 3, 4, 6, 7, 9, 10, etc., the machine hangs up when the program has been run, followed by ?FRE(O). When reset, Warm Start ? FRE (O) gives O TERROR.
eg.
10 DIM A\$(3)
RUN
?FRE(0) . . . hangs up
Warm Start
TFRE(0) O T ERROR
10 DIM A\$ $(5,3)$
RUN
PFRE(0) 7302
OK
Not dimensioning means the machine defaults to 10 and is therefore expensive on memory. My solution at the moment is to DIM at the next highest acceptable number. This results in a loss of memory. All other statements perform normally. I would appreciate any advice on this problem.

Our Compukit does the same. Does anyone have a solution to this?

## REACTION TO UFOs

Two CHAMP Programs have been submitted by Peter Davies of Birmingham.

The first program is a "Reaction Timer" which records the time between a signal on the display and the microprocessor being interrupted by one of the keys on the keyboard being pressed. The program first clears RAM register $O$ which is used to store the display data. The index registers are cleared and stored with data for the subsequent delay. A delay of approximately 10 seconds follows, after which, eights are displayed to signal the user to press one of the keys. After a delay of approximately ten milliseconds a three digit counter is incremented. The counter is continually incremented every ten milliseconds until the microprocessor is interrupted.

When an interrupt occurs the contents of the three digit counter are converted to 7-segment code using the subroutine in CHOMP. LOKY. The code is then continually displayed until the reset button is pressed.

Since there is a three digit counter, times between 10 milliseconds and 4.44 seconds are recorded. If a key is pressed before the first delay has run out, the display will show 000 making it very difficult to cheat. Also after 4.44 seconds the display will show 000 to prevent the counter from starting again.
Reartion Tiner

| Adtress | Data | Mnemuir |  |
| :---: | :---: | :---: | :---: |
| 200 | 42 | JU\% ? |  |
| 1 | 05 | 9 |  |
| 2 | CO | Nicp |  |
| 3 | 47 | JEM? | Intarrunt Voctor |
| 4 | ${ }_{4}{ }_{\text {A }}$ | ${ }_{4} \mathrm{~A}$ |  |
| 5 | $2^{9}$ | FIN: |  |
| 6 | 00 | $\infty$ |  |
| 7 | Po | SRC ${ }^{\circ}$ | Clare fam |
| ? | FC | CLE | rekistar 0 |
| 9 | 30 | ${ }_{\text {WF }}{ }^{\text {W }}$ |  |
| A | 72 | 1520 |  |
| B | $\mathrm{C}_{7}$ | 071 |  |
| c | 2 C | Fer 0 |  |
| D | ¢0 | - | Clants repirters |
| E | ? | FIM E | For counter |
| F | 00 | $\infty$ |  |
| 210 | 30 | FIMO |  |
| 1 | $\infty$ | 00 | loade rapistore |
| $?$ | ? | FIM ? | For folizy |
| 3 | ${ }^{\circ}$ | Or |  |
| 4 | 24 | FTM 4 |  |
| 5 | AA | AA |  |
| 6 | $\cdots$ | 1590 |  |
| 7 | 16 | 16 |  |
| \% | $7 \uparrow$ | 1531 | 10 second |
| 9 | 16 | 16 | delay |
| A | - 72 | 15> |  |
| B | 16 | $1 \%$ |  |
| c | 73 | 15? $=$ |  |
| D | 18 | 16 |  |
| E | 74 | 15, |  |
| $F$ | 15 | 18 |  |



The initial 10 second delay can be altered by having different values at address 215.

The second program is a game called "Destroy". The idea is that the user controls a ground base and must destroy U.F.O's flying overhead. This program will be shown in the next Micro Prompt.

## TORPEDO RUN

This program in BASIC, simulates a submarine attack on a ship which moves across the screen from left to right, disappears from the screen, then reappears on the left but slightly lower down. A torpedo is fired by pressing 1 on the keyboard. The number of torpedoes used is displayed on the screen. A maximum of nine torpedoes may be fired, after which the computer comments on your performance.

Hits are achieved by the torpedo striking amidships. The ship then stops in its tracks and an explosion is seen. The number of hits achieved is also displayed on the screen.

4 CLEAR
5 FOR Z $=1$ TO 30 : PRINT : NEXT
$10 \quad \mathrm{~A}=53248$ : $\mathrm{C}=54240$
15 FOR I = 1 TO 1024
$20 \mathrm{~A} 1=\mathbf{A}+1: \mathbf{A} 2=\mathbf{A}+2: \mathbf{A} 3=$ $A+3: A 4=A+4$
$25 \mathrm{~A} 5=\mathrm{A}+5: \mathrm{A} 6=\mathrm{A}+6: \mathrm{A} 7=$ $A+7: A 8=A+8$
$30 \mathrm{~A} 9=\mathrm{A}+9: \mathrm{B} 1=\mathrm{A}+10: \mathrm{B} 2=$ $\mathrm{A}+11: \mathrm{B} 3=\mathrm{A}+12$
32 POKE 54123, 84 : POKE 54134, 72
35 POKE B3, 196 : POKE B2, 158 : POKE B1, 158
40 POKE A9, 159 : POKE A8, 160 : POKE A7, 161
45 POKE A6, 160 : POKE A5, 159 : POKE A4, 158
50 POKE A3, 158 : POKE A2, 198
: POKE A1, 32
52 POKE 530, I
55 POKE 57088, 254 : POKE 57088 , 127
60 IF Q = 1 THEN 500
65 IF PEEK (57088) $=127$ THEN V $=\mathrm{V}+1$ : GOTO 500
$70 \mathrm{~A}=\mathrm{A}+1:$ FOR $\mathrm{G}=1$ TO 50: NEXT G
71 POKE 54125, (48 + V)
72 IF C2 =A9 THEN 600
73 IF C2 = A8 THEN 600
74 IF C2 $=A 7$ THEN 600
75 IF C2 = A6 THEN 600
78 NEXTI
$80 \mathrm{Q}=0$ : GOTO 10
$500 \quad \mathrm{Q}=1$
$510 \mathrm{C} 1=\mathrm{C}: \mathrm{C} 2=\mathrm{C} 1-64: \mathrm{C} 3=\mathrm{C} 1$ $+64$
520 POKE C 1, 149: POKE C2, 193 : POKE C3, 32
$530 \mathrm{C}=\mathrm{C}-64: \mathrm{T}=\mathrm{T}+1:$ IF $\mathrm{T}=17$ THEN 550
540 GOTO 70
$550 \mathrm{C}=54240: \mathrm{T}=0: \mathrm{Q}=0$
560 IF V $=9$ THEN 700
570 GOTO 70
600 POKE (C2 - 64) , 9
610 P = P+1
620 POKE 54136 , (48 + P)

630 FOR D = 1 TO 2000 : NEXT D
640 GOTO 78
700 FOR $\mathrm{Z}=1$ TO 30 : PRINT : NEXT
710 IF P < 3 THEN PRINT" STAY A CIVILIAN!"’: GOTO 770
720 IF $P<5$ THEN PRINT " REPORT FOR AN EYE TEST " : GOTO 770
730 IF P < 7 THEN PRINT " YOU DID WELL" : GOTO 770
740 IF P < 9 THEN PRINT " VERY IMPRESSIVE ": GOTO 770
750 PRINT " EXCELLENT ..... CAPTAIN SIR "
770 PRINT : PRINT : PRINT : PRINT : PRINT
780 INPUT " ANOTHER PATROL ?"; P \$
790 IF P \$ = "Y" THEN 4
800 FOR Z $=1$ TO 30 : PRINT : NEXT
810 PRINT " ENJOY YOUR SHORE LEAVE"
820 PRINT : PRINT : PRINT : PRINT : PRINT
830 END

Lines 5.700 and 800 clear the screen. $X=$ USR (X) could be used instead. to cause a jump to a machine code routine resident in a protected area of RAM. A suitable machine language routine for clearing the screen was published in PE September 1979.
Lines 35 - 50 dictate the shape of the ship
Lines 72-76 detect a hit
Line 520 dictates the shape of the torpedo
Lines 500-570 controls the torpedo travel

> The program is a result of experimenting with the graphics on the Compukit 101 and is certainly not meant to be a lesson in the art of programming. It is a program that runs on less than $2 K$ of memory, and may be of some use to the newcomer to the 101.
> M.D. E. Connor,
> Swansea.

## COMPUKIT UPDATE

NOW that many people are running Compukits, it is possible to sit back and take stock of the situation. As with any new device teething troubles have emerged, but are now mostly resolved by modification to the p.c.b.. The main purpose of this column is to keep Compukit owners, and anyone else interested, abreast of current developments in software and hardware. There are several updates which will be of interest to readers, and these are presented below.

## CASSETTE TAPE SPEED

The cassette interface has provoked a large number of questions and comments on the Compukit, and I shall attempt to answer the most frequent one here.

The speed of transfer of cassette information is dependent upon the clock frequency sent to the ACIA (IC 14). If you double this frequency, each byte sent serially from it, will appear in half the time and hence recording will occur at twice the rate ( 600 BAUD). If the clock speed is doubled again, 1200 BAUD will be achieved. There is one major problem, however. Reading information back from the tape and converting audio frequency signals to digital waveforms, depends upon the tape speed being reasonably constant both in the short term and over a long period of time. The ACIA's normal clock speed, producing 300 BAUD, is quite consistent with all normal variations, and even tolerates most speed variations between different machines. If you refer to the cassette interface diagram in your Compukit

Manual, you will notice that data acquisition depends upon the time-constant of monostable IC69, and a comparator, effectively, IC63.
The timing of this arrangement is independent of tape speed, and as the speed of data retrieved is increased, the tolerances in this system must be more and more exact. The device cannot be expected to function reliably at, say, 1200 BAUD.

Some speed variation does seem possible, however, and doubling the frequency of the Tx clock has produced some reliable results. If you would like to try faster cassette storage, perform the following modification (refer to cassette interface diagram).

At present. pin 9 of IC63 (7474) is connected to Tx clock (pins 4 and 3 of IC I4). Take Tx clock from pin 11 of IC63 instead of pin 9. This bypasses IC63's divide by two function. This modification is worth experimenting with, as several people claim full success. Try modifying and then recording and playing back on the same machine.

The Software of the Compukit takes care of cassette handling automatically for any Tx clock frequency, as it handshakes with the ACIA during the process. Some adjustment to the value of R53 and C11 may prove fruitful if the above modification remains unsuccessful.

## 110 BAUD TELETYPE

The cassette interface is also used to run serially interfaced printers such as the Anadex. Many people have asked if it is possible to run standard 110 BAUD Teletypewriters. The answer now appears to be yes. This is something new, and hence apologies to those who have asked this question before and been given a negative answer.

The problem is that doubling or halving frequencies to change from 300 BAUD to $600,1200,150,75$ etc. is easy. Multiplying by $110 / 300$, however, is not so simple. It requires an interesting calculation around the design characteristics of the 74163 synchronous presettable binary counter. In the present system, a frequency of 125 kHz (output C3 from IC59) is divided by 13 using IC57 and one NAND gate (IC58). IC63 then divides this by 2 to supply a frequency of 4.8077 kHz to the Tx clock of IC 14. 4.8 kHz is the correct frequency for a 300 BAUD rate. This is derived from Freq./BAUD rate $=16$. Here, a 10 -bit frame is used consisting, technically, of one start bit, one byte and one stop bit.

To derive the correct rate for 110 BAUD teletypes, a Tx clock of 1.76 kHz is necessary. IC 57 may be fed with 31.25 kHz from output C5 of IC60 (pin 14) and then made to divide this by


Fig. 1. Before and after TTY modification
nine. IC63 then divides by two and forms a Tx clock frequency of approximately 1.736 kHz which is within about 1.4 per cent of the required frequency and quite accurate enough for this application.

The only problem lies with the format of each frame sent to the teletype. 110 BAUD machines expect two stop bits along with the byte being sent, and the Compukit software sends just one, via IC 14`s internal registers. However, I have an old RO35 working beautifully from the Compukit with the following mod., and I should be most grateful for any feedback on. its success in general. This is at present a send only mod., and no thought has been given to receiving from a teletype as the Compukit has its own full keyboard.

The mod. is shown in Fig. 1, and consists of:
(1) cutting the connection between IC58 pin 4 and IC57 pin 12 ,
(2) joining pins 4 and 5 of IC58 (still joined to pin 11 of IC57),
(3) joining pin 11 of IC63 to pin 12 (instead of pin 11) of IC57,
(4) feeding pin 2 (CLK) of IC57 from output C5 (pin 14 of IC60) instead of C3.
It is worth making this modification switchable if it is to be used to any extent.

Fig. 2. Compukit's screen address map

|  |  | $\begin{array}{\|l\|} \hline 0 \\ 0 \\ \hline \end{array}$ | $\left\|\begin{array}{l} 12-23 \\ \mathrm{C}- \\ \hline \end{array}\right\|$ | $\begin{aligned} & 24 \square \\ & 18-\square \\ & \hline \end{aligned}$ | $\begin{aligned} & 36 \\ & 24 \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| DECIMAL | HEX. |  |  |  |  |
| 53248 | D000 |  |  |  |  |
| 53312 | D040 |  |  |  |  |
| 53376 | D080 |  |  |  |  |
| 53440 | D0C0 |  |  |  |  |
| 53504 | D100 |  |  |  |  |
| 53568 | D140 |  |  |  |  |
| 53632 | D180 |  |  |  |  |
| 53696 | DIC0 |  |  |  |  |
| 53760 | D200 |  |  |  |  |
| 53824 | D240 |  |  |  |  |
| 53888 | D280 |  |  |  |  |
| 53952 | D2C0 |  |  |  |  |
| 54016 | D300 |  |  |  |  |
| 54080 | D340 |  |  |  |  |
| 54144 | D380 |  |  |  |  |
| -54208 | D3C0 |  |  |  | 54254 (DEC.) |


| 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

 K
 1 0
$20 \downarrow \boldsymbol{~} \downarrow$ $30 \quad$ P $\quad \vee \quad$ SP 40 （ ）＊＋，－．ノ 0 1 $\begin{array}{lllllllllll}50 & 2 & 3 & 4 & 5 & 6 & 7 & 8 & 9 & : & 1\end{array}$ 60 〈 $=$ 〉 $\quad$ P $\quad$ A B C DE
 C $\quad \begin{array}{lllllllllll}80 & P & Q & R & S & T & U & U & H & X & Y\end{array}$ H A
R $90 \quad 2 \quad[\quad 1 \quad$ 〕 $\quad$－$a \quad b \quad c$
 $\mathbf{S}$
$\mathbf{E}$
$\mathbf{T}$ $190 \backslash \vee\rangle\langle\wedge\rangle$

 220 （ 1 ，，（ ） $0<1$ 230
 250
Fig．3．UK 101 character set．Note that CHR \＄183－187 are chequered（i．e．halftones）

## GRAPHIC CHARACTER SET AND VDU

To change the subject entirely many people have asked for a copy of the graphic character set with the numbers by which they can be "reached". For instance, if you type:

## PRINT CHRS(53)

the character " 5 " appears on the screen.
The diagram in Fig. 3 shows the complete character set with numbers to be placed inside the CHRS function for their printing. To select the correct value of a given character, add its row and column numbers together. For instance. PRINT CHR\$(179) gives the " $\Rightarrow$ " sign.

The manual supplies a program to help with identifying these characters, but the diagram presents them for "hard copy" reference.

As to the screen address of each character slot. Fig. 2 shows the VDU address map in decimal and hexadecimal for reference. The map has 16 lines and 48 character positions per line. Your TV may show less- experiment with the following to find out:

The first character on each line is given an address. and the top of the map supplies an offset to be added to this for addressing each character on a given line. To simplify the diagram, the 48 characters on each line are split into fields of 12 characters. each with decimal and hexadecimal ranges of offset for each field. For instance, at the centre of the screen there is a square of four character positions. These are addressed as shown in Fig. 4.

Fig. 4. Addresses of centre block of characters

|  |  |  |  |
| :--- | :--- | :--- | :--- |
|  | D1D7 (HEX) | D1D8 |  |
|  | 53719 (DEC) | 53720 |  |
|  | D217 | D218 |  |
| 53783 | 53784 |  |  |
|  |  |  |  |

Try printing the numbers: $1,2,3.4$, as below, in these positions before reading on:

12
34
There are two ways to do this:
(a) Use the machine code monitor and load the numerical code for $1,2.3 .4$ in hexadecimal addresses: D1D7.D1D8.D217. D218.
(b) Use POKE as follows:

## POKE 53719.49 <br> POKE 53720.50 <br> POKE 53783.51 <br> POKE 53784.52

In (a), the codes for 1,2,3,4 are determined by changing the decimal codes in Fig. 2 to hexadecimal; i.e., 49 (decimal) for " 1 " becomes 31 in hexadecimal. 50 (decimal) becomes 32 (hex) and so on. In order to use the machine code monitor in any application. it is essential to be fully conversent with hexadecimal numbering. and its conversion to and from the decimal system. This subject is quite large and complex, and will be dealt with in the next edition of this column, to appear in two months time.

If you examine the end of each line in Fig. 3, you will notice that the address of each line ending (the 48th character on a line) is not contiguous with the address of the start of the next line. For instance, the last character on the first line has address $53248+47$ which equals 53295 (decimal). The second line starts at address 53312 and not 53296. There are 16 character addresses missing, in fact, on the end of each line. These addresses are valid Read/Write memory locations, but do not appear on the screen. They could be used as scratchpad memory by your own machine code routines. Be a little careful how you re-enter BASIC after a low level routine. as BASIC prints "OK" followed by the cursor as well as at least one line of spaces and possibly a screen scroll-up if the cursor is on the lowest line.

A final note concerns a problem mentioned by two people; it appears to be a rare condition. but if the characters on your VDU display scem to flicker after an hour or two of use, several actions are worth trying. First and foremost. clean all the solder flux from your board using methylated spirit or one of the excellent flux solvents on the market. Play special attention to IC28 and any areas where there is an 8 MHz signal line. If this does not work fully, try changing IC 28 and/or adjusting the values of R81 and C60. This capacitor may not be supplied as a small disc ceramic and it may help to change it for one.

It only remains for me to wish all of you who have a Compukit. the very best of luck with your programming, especially if this is your first contact with the art. My feeling is that the ability to program and use a computer, as well as the basic skills of soldering and familiarity with silicon chips, etc., will form a most important area of general knowledge in the future. The Compukit has already played an important part in accelerating this process.

## polits bilisnt

## CORRECTION TO SOFTY REVIEW IN JANUARYISSUE.

A note of correction is in order for the SOFTY Review (Jañuary issue). The origin of the word "Firmware". in that article is stated in a manner which has often been heardperhaps erroneously. The more common meaning, which should have been included; is that it is firm and unchangeable if stored in ROM. Many thanks to those who have been so PROMpt to point this out!

We would also like to correct two other inaccuracies in the review. Firstly Phil Morris is not "of Videotime Products" but provided a limited design service for the interface board-he is chief designer of PCL Lid. Secondly Videotime Products market Softy but do not manufacture it.

## CONSTANT DISPLAY FREQUENCY METER

## (August 1978)

It has recently come to light that 74123 devices from some manufacturers are not compatible with the requirements of the Frequency Meter, and will not oscillate correctly with the circuit as published. In order to achieve correct oscillation it may be necessary to disconnect the end of R2 that is on Pin 13 of IC1 and take it direct to the +5 V line.

## COMPUKIT-4 (November 1979)

Two corrections to PIA, Fig. 4.1. Reset (pin 34) should go to

* $\quad+5 \mathrm{~V}$, pulsed to OV for reset. Pin 25 should go to 02 (pin 31 on J 1 . Also, current amplification is necessary to drive the l.e.d.s.

The address decoding of Fig. 4.3 is incorrect. The two lower NOR gates should be OR gates.

## ACOUSTIC MODEM (February 1980)

*The Test Oscillator "earthy" o/p line, shown in Fig. 4, should be taken from OV and not the bottom end of the $10 \mu \mathrm{~F}$ capacitor. Michael Tooley в.А.


THE digital frequency meter to be described here is a general purpose low-cost unit. It has been designed using some recently available devices to allow the meter to be used for portable applications. The facilities available to the user may easily be extended beyond the basic needs of portable applications, and the performance may be enhanced by the addition of the v.h.f. prescaler which will be described

## DESIGNFEATURES

The specification of the portable DFM is shown opposite. Only two front panel controls are provided; range selection, and a combined power switch and level control. The display readout is limited to 4 digits in order to minimise power dissipation and cost.

A block diagram for the complete instrument is shown in Fig. 1. The input amplifier and all of the logic is included on a single p.c.b. (shown by the dotted line), with only the controls, displays and input/output sockets external to the board. The circuit is simple to set up, requiring only one preset adjustment (adjustment of the time standard oscillator frequency to exactly 1 MHz ).

## CIRCUIT DESCRIPTION

The circuit diagram of the input amplifier, level shifter and waveform shaper is shown in Fig. 2.

The input amplifier consists of a voltage limiter followed by a high impedance amplifier. The peak amplitude of the signal applied to the gate of the junction FET, TR1, is limited to approximately $\pm 600 \mathrm{mV}$ by the action of R1, D1 and D2. The FET itself is used in a self-biasing circuit. The gate is tied to ground by R2, and the stage presents a high impedance to the signal. The d.c. source potential is produced by the current flowing in R3, and this provides the necessary negative bias. The source load is decoupled to a.c. by C2 to give a low frequency roll-off at approximately 10 Hz , while the high frequency performance is enhanced by C1

The excursion limits of the voltage waveform at the emitter of TR2 are varied by the setting of VR1, allowing variation of the level as required for the level shifting function. The actual switching levels of the waveform shaper are preset by the characteristics of the Schmitt-input gate, IC1c.

Fig. 3 shows the overall response characteristics of the input stages to sinewave signals.

## CONTROL LOGIC AND SIGNAL GATE

The circuit diagram of the control logic and signal gate is shown in Fig. 4.

The control logic governs the sampling rate of the instrument (the rate at which the input frequency is re-measured), and performs all of the necessary "housekeeping" functions,

## SPECIFICATION

Frequency Range:

Input Sensitivity:

Display Ranges:

Front Panel:

Rear Panel:

Power Requirements:

10 Hz to 5 MHz (minimum) 8 Hz to 7 MHz (typical performance)
Better than 200 mV r.m.s. 10 Hz to 5 MHz
10 mV r.m.s. at 1 kHz (typical)

1. 1 Hz to 9.999 kHz (kHz units)
2. 10 Hz to 99.99 kHz (kHz units)
3. 100 Hz to 999.9 kHz (kHz units)
4. 1 kHz to 9.999 MHz ( MHz units)

Display range selector switch (S 1)
Input level control (VR1)/power switch (S2)
Signal measurement inlet (SK 1)
TL power indicator (D 18)
kHz display units indicator (D4)
MHz display units indicator (D8)
Display over-range indicator (D3)
4-digit decimal point display
Mains indicator
6 volt d.c. inlet sockets (SK4 and SK5)
D.c. supply fuse (F2)

1 MHz TTL outlet (SK2)
$1 . \mathrm{kHz}$ TTL outlet (SK3)
Mains inlet socket
+5 volts d.c. at 160 mA (standby)
+5 volts d.c. at 250 mA (maximum)
Power from 6 V battery supply or mains regulator

[60288
Fig. 1. Block diagram of the D.F.M.
ensuring, for example, that the display counters are all reset to zero before the input signal is re-sampled. The signal gate acts on commands from the control logic and provides the counting/display circuitry with the number of pulses which is appropriate to the range selected and to the input signal frequency.


Fig. 2. Circuit diagram of the input amplifier, level shifter and waveform shaper
A 555 timer, IC2, is arranged as a control astable which has an output with a HIGH:LOW ratio of approximately $7: 2$. The signal sampling sequence is initiated by HIGH-to-LOW transition of the astable output. This transition causes the next LOW-to-HIGH transition of the selected clock signal to invert the normal output states of IC3a, driving the $Q$ output HIGH, and opening signal gate, IC1a. The next LOW-to-


Fig. 3. Input stage response characteristic
HIGH clock edge restores the state of the outputs on IC3a, thereby closing the signal gate. The Q output on 1 C 3 b is also driven HIGH, and this allows the accumulated count to be transferred to the display latches, as well as inhibiting IC3a until the start of the next sampling period. The LOW-toHIGH transition of the control astable output completes the display latching and, after a short delay introduced by R9 and C6, clears the counters ready for the next sampling period. The circuit is then dormant until the next HIGH-to-


Fig. 4. Circuit diagram of the control logic and signal gate


Fig. 5. Timing diagram

LOW astable transition, when the whole sequence is repeated. A timing diagram for the operational sequence is shown in Fig. 5.

The rate at which the signal is re-sampled is set by the combination of R6, R7 and C4/C5. The values of these components have been chosen, within the other design constraints, to give a sampling rate which, on ranges 2 to 4 , is slow enough to allow the display to be easily read, yet fast enough to allow alterations to be made to the input signal frequency without the display becoming tedious. The resolution of range 1 requires that a much longer re-sampling interval is used, hence the different value of capacitor.

## TIME STANDARD OSCILLATOR AND DIVIDER CHAIN

The portable DFM is designed to provide the user with display resolutions of between 1 Hz and 1 kHz , and employs gate sampling periods of between 1 second and 1 msec , respectively, for this purpose. The signals used to generate these sampling intervals are pulse trains of frequencies 1 Hz , $10 \mathrm{~Hz}, 100 \mathrm{~Hz}$, and 1 kHz , to give display resolutions of 1 Hz ,

## COMPONENTS . . .

| Resistors |  |
| :---: | :---: |
| R1, R11, R12, R13 | 10 k (4 off) |
| R2, R10 | 1 M (2 off) |
| R3, R8 | 1 k (2 off) |
| R4 | 1 k 5 |
| R5 | 330 |
| R6 | 47k |
| R7 | 27k |
| R9 | 100 |
| *R14 | 270 (7 off) |
| *R15 | 150 (7 off) |
| R16, R17.R18 | 470 (3 off) |

All resistors $\frac{1}{4} W 5 \%$ carbon.
*See text.

## Capacitors

C1
C2, C3, C5
470p polystyrene
C4, C10, C13, C14, C16. $100 \mu$ elect. ( 3 off) C22
C6, C9, C11, C12, C15, $\quad 10 n$ ceramic ( 8 off) C17, C18, C21
C7, C8
22p polystyrene (2 off)
C19
C20
VC1
$2200 \mu$ elect.
220 n polyester
2-22p trimmer
Semiconductors
D3, D4, D8, D18
D5, D6, D7, D9, D10.
1N4148 (2 off)
TIL209 (4 off)
D11, D12
D13, D14, D15, D16, D17 IN4001 (5 off)
D19
TR1 2 N3819
TR2 BCY70
IC1 74LS132
IC2 555
IC3 4013
IC4 4049
IC5 4068
TR3-TR6 2N3906 (4 off)
IC6
IC7, IC8, IC9
IC 10
IC11
IC12
IC13
4020 B
40160 (3 off)
74LS74
ZN1040E
74LS32
5 V 1 A i.c. regulator
Miscellaneous
SK1 BNC or similar coaxial socket to mount on front panel
SK2-5 4mm sockets or similar to mount on rear panel
S1 3P 4W rotary switch
S2 2P mains switch (combined with VR1 1 k linear)
F1 100 mA fuse and holder
F2 500 mA fuse and holder
T1 9-0-9V 500 mA transformer
N1 Mains neon
4-digit multiplexed I.e.d. display (see. text)
Display filter
Printed circuit board, Veroboard
Case (Vero G-range 3G)
$\times 11 \mathrm{MHz}$ crystal

## Constructor's Note

Components and p.c.b. are available from Howard Associates, 59 Oatlands Avenue, Weybridge, Surrey KT1 9SU.
$10 \mathrm{~Hz}, 100 \mathrm{~Hz}$, and 1 kHz , respectively. In this case, it is the pulse repetition frequency (p.r.f.) of the generating signals which is important, rather than the individual mark and space intervals.

The time standard oscillator is used to generate a reference signal at a p.r.f. of 1 MHz . A 1 kHz gating signal is derived from this reference, and the remaining signals are then produced from this by a chain of decade dividers. The circuit details for the time standard oscillator and divider chain are shown in Fig. 6.


Fig. 6. Circuit diagram for the standard oscillator and divider chain

The oscillator circuit uses a single inverter, IC4a, in a feedback loop with a 1 MHz crystal, $\times 1$, used to determine the frequency of oscillation. Fine frequency adjustment is provided by $\vee C 1$, and the d.c. path around the loop is completed by R10. A second inverter, IC4b, is used to buffer the oscillator output and to improve the waveform shape. The resulting 1 MHz reference signal is brought out to SK2 on the rear panel for external use

The oscillator output from IC4b is applied to the input of a 14 -stage ripple counter, IC6. This counter, in combination with the decoder IC5, is arranged to produce an output at 1 kHz p.r.f. This is achieved by configuring the decoder to detect a count of 2000. A reset pulse is then generated to the counter, giving the stage an overall division ratio of 1000:1. The output, which is also brought out to SK3, is then at a p.r.f. of 1 kHz , and mark: space of approximately 1:1.

The pulse trains at $100 \mathrm{~Hz}, 10 \mathrm{~Hz}$ and 1 Hz are generated successively by the decade synchronous counters, IC7, IC8 and IC9, respectively. The use of CMOS devices throughout the oscillator and divider stages improves the stability, guarantees oscillator startability, and reduces the power dissipation when compared to equivalent TTL designs.

## COUNTING AND DISPLAY CIRCUITS

The pulse counting and display circuits are shown in Fig. 7. A single VLSI device is used to perform all of the functions of a 4-digit counter, memory latch, 7 -segment decoder, and display driver. The use of a multiplexed display drive also allows the constructor a wide choice of display devices. Four


Fig. 7. Circuit diagram of the pulse counting and display circuit
discrete 7 -segment l.e.d. displays or a 4-digit multiplexed display may be used. Further, the ZN1040E may be programmed to drive either common anode or, with slightly more external components, common cathode types of display.

The pulse train periodically produced by the signal gate (COUNT) is counted by the decade counters within IC11. The resulting count is then saved in the memory latches following the TRANSFER command from the control logic. The decade counters are then cleared down ready for the next measurement cycle. The latched count value is decoded into an equivalent 7 -segment drive format. The internal multiplexing circuits then cause this segment drive information to be presented at the segment drive outputs; each of the 4 digits being strobed in turn. The appropriate "digit select" output is enabled synchronously with the segment outputs, causing only the required segments of the selected digit to become illuminated. In this way, each of the 4 displays are scanned in turn; the scan rate being set by C9. The brightness of the displays is set by R15, with the transistors TR3 to TR6 being used to enable each of the 4 common anode displays in turn. The circuit thus causes the number of pulses presented on the COUNT input line to be displayed to the user. The remaining circuitry is used to handle display over-ranging, decimal point control. and range indication functions.

The maximum number of pulses which may be counted and displayed by the circuit shown is 9999. If an "overflow" indicator is fitted, then this could also be used as an extra 'half" digit in the display. For example, a 12 kHz signal may be displayed on range 1 by using the display to show "2000", while the overflow indicator provides the missing leading " 1 ".

IC10a is used to detect any over-range indication from IC11 and IC10b is used as a memory latch, causing D3 to be illuminated in the event of overflow. As with the remainder of the circuits, the detector and latch are cleared and reloaded each time the input signal is re-sampled. Correct operation of the display in overflow mode is ensured by using the $\overline{\mathrm{Q}}$ output of IC10b to disable the leading-zero suppression facility whenever an over-range condition is detected.

The decimal point logic required to illuminate the decimal point is arranged for common anode displays having the decimal point to the right of the digit. The negative-AND gates of IC12, together with the discrete OR function provided by D9 to D12, allow S1A to enable the appropriate decimal point cathode synchronously with the related segment cathodes. The steering diodes, D5 to D7, ensure correct operation of the decimal point while illuminating the " kHz " l.e.d. on ranges 1, 2 and 3. An advantage of this circuit configuration is that the complication of a multi-wafer

Fig. 10. P.c.b. design for the D.F.M



Fig. 8. P.s.u. circuit diagram
rotary switch is avoided; only a standard 3-pole 4-way switch is required.

## POWER SUPPLY

The power supply circuit shown in Fig. 8 is a single +5 V d.c. rail at up to approximately 300 mA . This particular arrangement is suitable both battery and mains operation. In portable applications, a 6 V battery pack is connected to SK4 and SK5, otherwise the mains supply is used as shown. In either case the instrument power is switched by S2.

Capacitors C20 and C21 are used to remove high frequency noise from the supply rail which would otherwise adversely affect the performance of the time standard oscillator. D15 is used to bias the regulator, IC 13, to overcome the voltage drop introduced by D16 in the switching circuit. The facility of battery operation may be omitted by the removal of diodes D15 and D16 (replace them with wire links), and the omission of D17, SK4 and SK5.

The Veroboard design for the p.s.u. is shown in Fig. 9.


Fig. 9. Veroboard layout of the p.s.u.


Internal view of the D.F.M.


Fig. 12. Wiring diagram for discrete display


Fig. 13. D.F.M. wiring diagram

## CONSTRUCTION

The p.c.b. design for the D.F.M. is shown in Fig. 10, with the corresponding component layout in Fig. 11. It is recommended that the i.c.s and any thick film resistor networks are mounted in d.i.l. sockets. Constructors should also note that many of the i.c.s are CMOS types, and these should be handled with the usual care.


The p.c.b. has been designed to allow thick film resistor networks to be used for R14 and R15. In the case of R15, the substitution of different networks provides a simple method of adjusting the display brightness. Thus, the use of higher-valued ( $220 \Omega, 270 \Omega$ or higher) or lower-valued
(100』) networks allows the display characteristics to be tailored to the particular application. Lower resistance values will increase the brightness of the display, but will also have the effect of increasing the current consumption correspondingly, and vice versa. The value of R14 should not be varied significantly from the value specified.

The input signal frequency is displayed to the user on a 4digit seven-segment l.e.d. display. The display requires only 12 connections between the p.c.b. and the display hardware; these may conveniently be made with a short length of ribbon cable (with or without $0.1^{1 "}$ pitch plugs and sockets). Alternatively, the multiplexed display may be synthesized from four discrete common anode displays. Fig. 12 shows how four typical displays should be interconnected; the simplification in wiring effort offered by the multiplexed display is self-evident! Whichever type of display is adopted, the operational characteristics will be identical.

The printed circuit board is mounted on the base plate of the case with four pillars, while the remainder of the components and controls are mounted either on the front or on the rear aluminium panels. The interconnection wiring is illustrated in Fig. 13.

The power supply components are mounted on the rear panel, with the regulator attached to the panel (using an insulating kit) to provide the necessary heatsink. The Veroboard circuit is mounted in the case on four small pillars.

## News Briefs

## TELESOFTWARE

ANEW way of using the Oracle Teletext service will enable future teletext receivers to play games, calculate mortgages and tax returns, run educational courses and a lot more.

The electronic signals which are used to carry the teletext information within the TV broadcast are very similar to those used in computer technology, so by replacing pages of written text with pages of a computer program a viewer has simply to select the program of his choice from Oracle. And like the information already on Oracle, these telesoftware programs would be free of charge.

In the future, by adding a microprocessor to the design of TV sets, they will be able to receive, decode and execute telesoftware programs in addition to receiving normal Oracle information. No special technical knowledge will be needed as operating telesoftware will be as easy as selecting pages on Oracle.
The applications of telesoftware on Oracle are almost as varied as the imagination. For example, it will allow future TV sets to play a wide range of video games. And by simply selecting a new program the set can instantly become a highly specialised but simple-to-use calculator. The same TV can become a flexible home educational unit. with a wide range of subjects, which may be learnt as fast or as slowly or as often as required by the individual. It could let you know your social security entitlements or even help detect credit card fraud. Telesoftware will also permit future Oracle receivers to display higher definition graphics and handle different alphabets, such as Russian or Greek-perhaps even still-pictures.

In co-operation with ITV. Mullard Applications Laboratory have designed and built an experimental telesoftware receiver.

Further research is under way already to establish compatible technology standards for a future telesoftware service. ITV is now investigating many areas of application for the system, including its use in education, work on which is being given a high priority. It is hoped that in the future telesoftware will become as integral of the Oracle service as the news headlires or weather forecast are today and will provide the viewer with even more value for money from his television screen.

# Readout... A selection from our Postbag 

Readers requiring a reply to any letter must include a stamped addressed envelope
Opinions expressed in Readout are not necessarily endorsed by the publishers of Practical Electronics.

## AY3 Anybody?

Sir-May I. through your columns. make mention of an anomalous situation that has come to my notice. Following Mr. LentonSmith's article in PE (September 1978) on the TDA 1008 gating/divider i.c.. I was anxious to use these in an organ I am building, only to find that no supplier stocks any of the AY3 series which is necessary for the full range of pitches. I have searched catalogues and many advertisements. but every firm which stocks the 1008 only has the AY1 top octave generator, which is inadequate.

I have enquired of the manufacturers, General Instruments, who tell me that the AY3 series is still made, and it would therefore seem that amateur suppliers do not bother to read data sheets before deciding what to stock.

If I have missed any supplier who does stock the AY3. perlaps they would let me know. Otherwise, I can only advise your readers that they should save their money by not buying the TDA 1008, and go back to the tedious business of hand-building diode gates.
B. D. Arnold,

Worthing.

## Hazard

Sir-I wondered if through your magazine I may draw attention to a small but definite radiation hazard originating in certain exGovernment equipment which was available for a considerable period after the last War.

The specific item which caused me to write this letter is a revolution counter which contains two large moving coil meters with edgewise scales about 10 mm long, scaled 6 -14-18-22-26-30 and marked "Engine Speed Hundreds of r.m.p.". The graduations and numbers are filled with Radium activated luminous paint very thickly applied. From previous experience with an ex-Government watch which had burn marks on the dial from the paint on the fingers I decided recently to do some tests on the meters. (I work at the University of Birmingham). The results confirmed my suspicions. At ( 10 cm 4 inches) from the scales a Geiger counter registered 1000 counts per second. Interposing $1 \frac{1}{2} \mathrm{~mm}$ aluminium sheet to remove beta and alpha emission reduced this to 100 c.p.s. However. since a Geiger counter is only one or two per cent efficient for gamma rays the true rate would be several thousand per second. Although I am not qualified to make an accurate assessment of the activity a rough calculation indicates several millicures which I believe is a quantity that would require a licence if used for teaching purposes.

I therefore suggest that anyone having old
ex-Government equipment with luminous type dials or pointers (warning, it will no longer be luminous because of degeneration of the phospor) should have them properly disposed of-not burned, buried or dumped on the local tip.

Radium is dangerous if ingested and burning will simply spread it about as most readers will realise.
B. Manning,

Kidderminster.

## Coded

Sir-R. W. Coles, in Semiconductor Update. seems less than au fait with codes and cyphers.

There certainly are unbreakable codes despite the best computers. Both in theory, and in practice, there is no way of breaking ciphers based on true random numbers as long as each number sequence is used only once.

The codes that are broken are based on pseudo random numbers, or similar, but even here long sequences of messages are necessary to break into a new sequence of code.

The real gem of modern ciphers is the "trap door" cipher. Each user will publish his own code for anyone to send messages to him. Using a secret second code he will decipher the messages but no one else can succeed in this aim. A further technique is double encoding which gives 100 per cent proof that the message is from the named sender not a fraudulent source. The sender uses his secret code and the others published code. The receiver uses the others published code and his own secret code. No one else can use this combination.

In theory the trap door method can be decoded by computer by well known techniques. The snags are that the biggest and fastest of possible computers would take many millions of years to do what the known code will solve in seconds. So far no one has been able to find a short cut. Those wishing to use their own randon number techniques should beware of most published and commercial methods. These collapse under quite simple analysis. For example many always end in odd or even digits or alternate between them, on a regular basis.

To begin to have any value, even in less esoteric uses, a random series should pass the basic test that, in any base, any one digit will be followed by all possible other digits, and itself, in approximately equal proportion when averaged over a few thousand digits. A simple program, using a two dimensional array, will soon show up any fault here.
R. G. Silson,
Tring.

## Career

Sir-I read with interest your section on industry in the November issuc of Practical Electronics and it seems to me that you could maybe supply me with some information.

Starting in October. 1980, I will be entering university to study one of the following courses, so could you advise me which course of study would lead an honours graduate to the best possible position on entering industry (involved with microprocessor systems, which seem to be playing an ever increasing role in industry) in terms of pay and promotion prospects. Either an honours graduate from Strathclyde University in one of their new degrees, first instituted in 1979. or an honours graduate in physics who has undergone "postgraduate" study in "microprocessors and digital electronics". which I know Glasgow University offers.

Unbiased advice from particular companies and universities is hard to come by.

Vincent Farrelly,
Glasgow.

We asked our Industry Notebook contributor Nexus to offer Mr Farrelly some advice:

You will realise that it is equal!! as difficult for us to forecast emplowment prospects in the mid-80s, when you will have qualified in vour chosen profession, as it is for yourself to do so. What can be said with absolute cerlaim! is that an! degree student in electronics will be in demand, both in the United Kingdom and overseas. There is an acute shortage of such people now and this is likely to continue to the end of this century and bevond. So vou need have no fear of umemplovment in the future, whatever specialised discipline you embark upon.

As to the choice of courses open to you, this in our opinion depends very much on your personal interests. If you read Computer Science and Microprocessor Sustems then you are firmly in the computer sector, admilted! very broad inchuding inciustrial automation but with emphasis on applications.

The course in Electronic and Microprocessor Engineering appears to be more broadly based in electronics with microprocessors and their design and application coming as a speciality later. You will observe that there is a great deal of overlap, the difference between the two courses being one of emphasis, the first towards application, the second towards engineering.

Your third option, a phisics degree, gives vou manly openings for specialisation as a post-graduate including, of course, microprocessors. This would provide, one imagines, much greater flexibility if, for example, lou decided after the first vear or two that microprocessors were rather boring and that you might prefer to be a muclear engineer or enter some other branch of electronic engineering.

Onl! lou can make the choice but any science- based degree will stand pou in good stead for the future. Provided lou have a good grounding in electronics lou will find that most industrial compamies or organisations, if lou prove your capability and are clearly keen to advance, will encourage you in your chosen specialisation at a later date.

Nexus

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The dimensions of the case (with Superboard keyboard cut out) are shown below-case material is approximately 2 mm thick with 4 mm radius corners. We recommend that the power regulator fitted to Compukit boards is mounted on a heatsink and fixed to the outside back of the case.

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