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T UK101 PUK



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statements. Much faster than currently available

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	FULL CONSTRUCTION DETAILS IN P.E. AUGUST 1979 EDITION	Delivery date June 1979 at the 1979 MicroComputer Show Customer orders in strict rotation only.
	SEND ONLY £10.00 DEP	OSIT TO RESERVE ONE
TOP H HILL	A tape of 10 programs on cassette — educational games, etc. will be supplied free of charge with each kit.	NO EXTRAS NEEDED JUST HIT 'RETURN' AND GO.
	Simple Soldering due to clear and consise instructions compiled by Dr.T. Berk, BSc.PhD	Build, understand, and program your own computer for only a small outlay.
	COMMANDS CONT LIST NEW NULL RUN ITATEMENTS CLEAR DATA DEF DIM END FOR SOTO GOSUB IF. GOTO IF. THEN INPUT LET IEXT ON. GOTO ON. GOSUB POKE PRINT READ IEM RESTORE RETURN STOP XPRESSIONS	ONLY £219 + VAT including RF Modulator & Power supply. Absolutely no extras. SPECIAL CHARACTERS @ Erases line being typed, then provides carriage return, line feed.
The the computer of the comput	(l) POS(l) RND(X) SGN(X) SIN(X) X) TAB(l) TAN(X) USR(l) S	 Tertuin, line feed. Terses last character typed. CR Carriage Return — must be at the end of each line. Separates statements on a line. CONTROL/C Execution or printing of a list is interrupted at the end of a line. "BREAK IN LINE XXXX" is printed, indicating line number of next statement to be executed or printed. CONTROL/O No outputs occur until return made to command mode. If an input statement is encountered, either another CONTROL/O is typed, or an error occurs. ? Equivalent to PRINT
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ELECTRONICS

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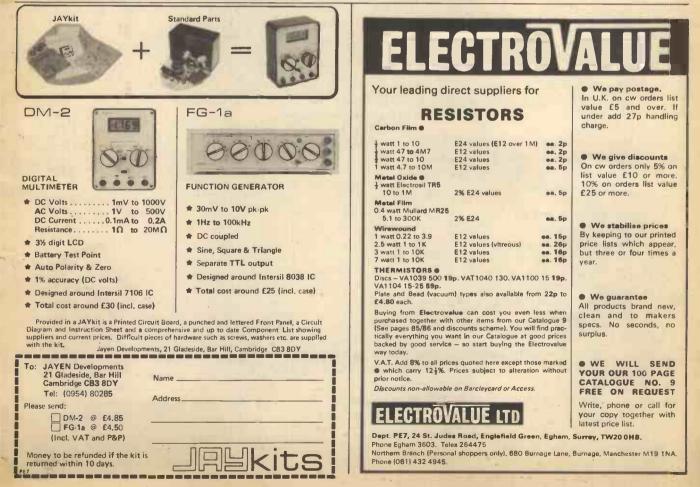
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ELECTROLYTIC CAPACITORS: Axial lead type (Values are in µF) 500V: 10 40p; 47 68p; 250V: 100 65p; 63V: 0.47, 1.0, 1.5, 2.2, 3.3, 4.7, 6.8, 8, 10, 15, 22 8p; 47, 32, 50 11p;	7425 27 74141 56 4015 82 4089 150 CA3020 170 NE562# 411 7426 36 74142 209 4016 45 4093 85 CA3023 170 NE565A# 12 7427 27 74143 314 4017 82 4094 190 CA302BA# 80 NE566# 16
63, 100 279; 50V: 50 11p; 100, 220 25p; 470 32p; 1000 50p; 40V: 22, 33 8p; 100 12p; 2200, 3300 62p; 4700 85p; 35V: 10, 33 9p; 330, 470 32p; 1000 49p; 25V: 10, 22, 47 6p; 80, 100, 160 8p; 220, 250 13p; 470 640 25p; 1000 27p; 1500 30p; 2200 45p; 3300 62p;	7428 35 74144 314 4018 87 4095 105 CA3035 240 NE567# 174 7430 17 74145 65 4019 48 4096 105 CA3036# 110 NE570# 424
ELECTROLYTIC CAPACITORS: Axial lead type (Values are in µF) 500V: 10 40p; 47 68p; 250V: 100 65p; 63V: 047, 10, 15, 22, 33, 47, 68, 8, 10, 15, 22, 8p; 47, 32, 50, 11p; 63, 100 27p; 50V: 50, 11p; 100, 220 25p; 470 32p; 1000 50p; 40V; 27, 33, 32, 300 52p; 470 85p; 320, 350; 32p; 100 95p; 330, 470 32p; 100, 49p; 253, 31p; 100, 22, 47 5p; 300, 100, 160 5p; 220, 250 13p; 470, 640 25p; 1000 27p; 1500 30p; 2000, 450; 250, 475, 475, 475, 475, 475, 475, 475, 475	7433 40 74148 109 4021 95 4098 110 CA3046# 71 RC4136D# 120 7437 30 74150 99 4022 85 4099 145 CA3048 200 SAD1024A# 1350
4700 70p; 15,000 450p; 25V: 4700 68p; 2200 48p; 325V: 32 + 32 175p.	7438 33 74151 64 4023 22 4160 109 CA3075 178 SG3402 29 7440 16 74153 64 4024 66 4161 109 CA3080Etw 80 SN76003N 177 7441 74 74154 96 4025 19 4162 109 CA3080Etw 80 SN76003N 177
POLVESTER CAPACITORS: Axial lead type. (Values are in µF) 400V: 0.001. 0.0015. 0.0022, 0.0033 9p; 0.0047, 0.0068, 0.01, 0.015. 0.018 10p; 0.022, 0.033. 10p; 0.047, 0.068 14p; 0.10 15p; 0.15, 0.22 22p; 0.33, 0.47 39p; 0.68 45p. 160V: 0.039. 0.15. 0.22 11p; 0.33, 0.47 19p; 0.68, 1.0 22p; 1.5 29p; 2.2 32p.	7442 68 74155 53 4026 180 4163 109 CA3085+ 85 SN76023N 144 7443 115 74155 80 4027 45 4174 110 CA3089E 210 SN76033N 174 7444 112 74157 67 4028 81 4175 99 CA3093AQ 398 SN76115N 211
DUBILIEH: 1000V:0.01, 0.015 20p; 0.022 22p; 0.047 26p; 0.1 38p; 10.22, 0.47 48p.	7445 94 74159 185 4029 99 4194 108 CA3123E 200 SN76477# 221 7446 94 74160 82 4030 58 4408 720 CA3130# 85 SN76477# 221 7447 57 74161 92 4031 205 4409 720 CA3130# 85 SN76227 111
POLVESTER RADIAL LEAD (Values are in µF) 250V: 0.01. 0.015. 0.022. 0.027 6p; 0.033. 0.047. 0.068. 0.1 7p; 0.15 11p; 0.22. 0.33 13p; 0.47 15p; 0.66 18p; 1.0 24p; 1.5 27p; 2.2 31p. TU 200 Parting 13	7448 51 74162 92 4032 100 4410 720 ICL7107# 975 TAA960 32 7450 17 74163 92 4033 145 4411 958 ICL7106E# 795 TBA120S 7
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1.0, 2.2 µF, 3.3, 4,7, 6.8; 28V: 1.5, 5000, 1K & 2K (in only). Single 27p 2* Vellow Green 18 10; 20V: 1.5; 16V: 10µF 13p each. 5K0.2M0 single gang 27p 2* Vellow Green 18	7460 17 74187 200 4037 100 4415V 795 ICM721734 790 TBABOO 9 7470 28 74170 185 4038 108 4419 280 ICL7555# 95 TBABOO 9 7470 28 74170 185 4038 108 4412 280 ICL7555# 95 TBABOO 9 7472 25 74172 825 4039 320 4422 545 LD130# 452 TBA820 7
47, 100 40p; 10V: 22µF, 33; 6V: 47, 5KΩ-2MΩ single gang D/P switch 65p Spare clips 65p S	7473 32 74173 120 4040 105 4433 995 LF356# 98 TDA1004# 29 7474 25 74174 87 4041 80 4435 825 LM301A 30 TDA1008# 29 7475 38 74175 87 4042 75 4440 1275 LM304 240 TDA1022# 57
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0.01 µF, 0.2 9p 50V: 0.47 µF 11p 10KQ-500KΩ dual gang 80p LS400 255 CERAMIC CAPACITORS: 50V Self Sick Graduated Basels 22p 7 Seg Displays 7 Seg Displays	7482 69 74180 85 4046 128 4490V 525 LM339* 70 TL064CN* 19 7483 72 74181 165 4047 87 4501 19 LM348* 90 TL081CP* 5
15nF, 22nF, 33nF, 47nF 4p each Vertical & Horizontal TIL307 676	7485 75 74184 135 4049 48 4503 69 LM380 90 TL084CP# 13 7486 31 74185 135 4050 48 4506 51 LM381N 145 UAA170 19
POLYSTYRENE CAPACITORS 0-25W 1000-3-3M0 Honz 10p TIL321 C An 5" 116 0 25W 2000-4-7M0 Vert 10p TIL322 C Cth 5"116	7489 140 74188 275 4051 72 4507 55 LM382. 122 VAA180 19 7490 30 74190 95 4052 72 4508 298 LM1458# 50 ZN414 9 7491 75 74191 95 4053 72 4510 99 LM3900# 60 ZN424 13
SILVER MICA (Values in pF) 3-3, 4-7, 6-8, 10, 12, 18, 22, 33, 47, 50, 68, 75, Minature High Stability, Low noise DL707 CA. 3" 99 DL707 CA. 6" 180	7492 38 74192 98 4054 110 4511 150 LM3909N* 70 ZN425E* 41 7493 32 74193 98 4055 128 4512 98 LM3911* 125 ZN1034E* 20
82,85,100,120,180,180,220,9p es AANGE VAL 199 100 FND357 120 300,330,360,390,600,820,16p es 19 220.47M E24 1.5p 1p MAN3640 175 1000,1200,1800,2000,2200,20p es 19 2-20.47M E12 2p 1.5p LCD 34 digit 875	TRANSISTORS BF184* 38 OC29* 160 ZTX311 17 2N3665 2 AC117* 35 BC169C 12 BF194 12 OC35* 130 ZTX314 24 2N3702 1 AC117* 35 BC169C 12 BF195 12 OC36* 130 ZTX325 30 2N3702 1
MINIATURE TYPE TRIMMERS 220 10M 612 5p 4p 2's Metal Film 100-1M0 6p 4p SWITCMES+ 1's Metal Film 100-1M0 6p 4p TOGGLE 2A 250V	Ac125 # 20 BC170 18 8F196 12 0C41 # 148 ZTX341 20 2N3704 1 AC126 # 20 BC172 11 BF197 14 0C42 # 48 ZTX500 15 2N3705 1 Ac127 # 20 BC177 # 18 BF198 18 0C43 # 55 ZTX501 15 2N3705 1
5 25pf 5 45pf 60pf 88pf 30p COMPRESSION TRIMMERS 100 - price applies to Relations of each type not mixed values 100 - price applies to Relations of each type 100 -	AC128# 20 8C178# 16 8F200# 30 0C44# 31 27X502 19 2N3707 1 AC141# 24 8C179# 18 8F224A 18 0C45# 28 27X503 15 2N3708 1 AC141K# 38 8C182 9 8F244 30 0C46# 28 27X503 15 2N3709 1
3-40pF, 10-80pF, 25-190pF 30p 100-500pF 45p; 1250pF 60p PE Microprocessor Evaluation 3 pole clover 185p	AC142# 24 8C182L 11 8F256# 60 0C70# 28 ZTX531 25 2N3710 1 AC142K# 38 8C183 9 8F257# 30 0C71# 28 ZTX550 25 2N3711 1
SOLDERCON PINS* 100 pins 50p; 500 pins 200p for list. SPST on/off 54p DPDT 6 tags 70p	AC178 22 8C184 9 8F259# 30 0C74# 55 2N696# 35 2N3771#23 AC187# 20 8C184L 11 8F394 27 0C76# 36 2N697# 25 2N3772#198
JACK PLUGS SOCKETS DPDT Games DPDT Games Contre of 79p DPDT Games Control (1997) Screened Plastic conen moulded in line 1A PDT 1450 ADDT 1450	AC188★ 22 BC186 21 8F594 40 0C77★ 76 2N698★ 44 2N373★288 ACY17★ 35 BC187★ 28 BF595 38 0C79★ 76 2N699★ 54 2N3819 22 ACY18★ 40 BC212 10 BFR39 25 0C81★ 50 2N705A★ 19 2N3820 45
2.5mm 13p 10p 8p break 11p A DPDT 13p	ACY19 + 40 BC212L 11 BFR40 28 OC82 + 50 2N707 + 33 2N3823 + 95 ACY20 + 40 BC213 11 BFR41 28 OC83 + 48 2N708 + 19 2N3824 + 70 ACY21 + 35 8C213L 12 BFR79 28 OC84 + 44 2N914 + 32 2N3866 + 90
3.5mm 15p 10p 8p contacts 12p 4 pole c/over 24p MONO 25p 14p 13p 20p 18p PUSH BUTTON STERE(32p 18p 15p 24p 22p Spring loaded	ACY22# 40 BC214 12 BFR80 28 OC122# 48 2N916# 27 2N3903 20 ACY22# 40 BC214L 13 BFR81 28 OC122# 48 2N916# 27 2N3903 20 ACY3# 78 BC3078 20 BFR81 28 OC139# 110 2N920# 51 2N3905 18
DIN PLUGS SOCKETS IN Line SPDT c/over 70p DPDT 6 Tag 85pt	ACY40# 48 BC308 20 BFX81# 45 OC140# 110 2N930# 18 2N3906 17 ACY44# 39 BC328 15 BFX84# 26 OC141# 110 2N961# 61 2N4037# 52
2 PIN Loudspeaker 10p 8p 20p SWITCHES # Ministure Non-Locking 3 4 5 Pin Audio 13p 10p 20p Push to Make 15p Push to Break 25c	AD149* 70 8C338 12 8FX86* 28 0C171* 40 2N1132* 22 2N4061 17
Co-extal (plastic) 10p 12p 12p (metal) 18p 12p 22p ROCKER: SPST On/off 250V 23p PHONO - Be lingte PHONO - Be lingte	AF114# 40 BC477# 25 BFY18# 50 OC203# 85 2N1304# 50 2N4289 20 AF115# 40 BC547 12 BFY50# 20 OC204# 85 2N1305# 28 2N4859 65
PHONO assorted colours 10p 15p 8p elangle plotuble 15p	
BANANA 4mm 11p 12p — ROTARY: "Make a switch". Make your own 2mm 12p 12p — multiway switch. Adjustable stop shafting	AF124# 35 BCY30# 57 BSY95# 30 11930C 55 2N1670#150 2N5180# 38
WANDER 3mm 7p 7p 1p/12W. 2p/6W. 3p/4W, 4p/3W, 6p/2W 7p	AF139# 30 BCY40W 78 B0205W 190 11931CW 66 2N1986W 60 2N5457 32
TTL 74LS* (TEXAS) DIL SOCKETS*	AF186# 50 8CY59# 90 E113 /5 119326# 76 2N2217# 43 2N8459 32 AF186# 50 8CY59# 90 E176 116 T1P32C# 75 2N2218A#34 2N5485 38 AF239# 42 8CY70# 18 F421 150 T1P33A# 80 2N2219A#20 2N8777 4
LS00 11 LS63 150 LS153 76 LS240 236 LS346 185 LS01 11 LS73 46 LS155 96 LS241 231 LS347 48 LS02 12 LS24 1231 LS347 48 Bpin 10p 25p Trace	BC107 9 BCY78 25 ME6002 10 TIP33C 110 2N2221 23 2N6109 50
LS03 12 LS75 48 LS157 76 LS243 232 LS352 228 14pin 12p 35p Diodes	BC108* 9 80121* 78 MJ491* 160 TIP34C* 110 2N2303* 45 40311* 60
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I I S14 751 I S93 891 I S168 1581 I S258 1101 I S377 717 I COM PUTER/VIDE	80115 20 80137# 36 MJE3055#70 TIP428# 82 2N2846#140 40348# 105 80116 20 80138# 50 MPE102 66 TIP2055# 85 2N2804# 30 40348# 105
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Part of the P.E. Minisonic now released as an independent kit for use with other synthesisers. Component set (incl. PCB) (Order as Kit 60-1) £3-64

This unit has its own voltage controlled amplifier and has full manual control over attack, decay, sustain and release functions. Component set (incl. PCB)

TRANSIENT GENERATOR (P.E. Apr. 77) An envelope shaper, without VCA, having the usual attack, decay, sustain and release functions, and in addition it also provides a "Repeat Effect "enabling a synthesiser to be programmed to imitate such instruments as a mandolin or

A slightly modified version of the circuit published in Elektor'. December 1976, and includes manual and automatic control over the rate of phasing and vibrato. Component set £17.38 Printed circuit board £2.33

PHASING UNIT (P.E. Sept. 73) A simple but effective manually controlled unit for introducing the "phasing" sound into live or recorded

PHASING CONTROL UNIT (P.E. Oct. 74) For use with the above Phasing Unit to automatically control the rate of phasing. Component set (incl. PCB) £474

SOPHISTICATED PHASING AND VIBRATO UNIT

ENVELOPE SHAPER WITHOUT VCA (P.E. Oct. 75) Provides full manual control over stack, decay, sustain and release functions, and is for use with an existing voltage controlled amplifier, Component set (incl. PCB) £477

ENVELOPE SHAPER WITH VCA (P.E. Apr. 76)

TO

VOLTAGE CONTROLLED FILTER (P.E. Dec. 74)



P.E. MINISONIC Mk. 2 SYNTHESISER A portable mains-operated Miniature Sound Synthesiser, with keyboard circuits. Although having slightly fewer facilities than the large P.E. Synthesiser the functions offered by this design give it great scope and versatility. Consists of 2 log VCOs, VCF, 2 envelope shapers, 2 voltage controlled amps, keyboard hold and control circuits. HF oscillator and detector, ring modulator, noise generator. mixer, power supply.

Set of basic component kits (excl. KBD R's and tuning pots - see list for options available). from £61.00 Set of printed circuit boards £8-99

P.E. SYNTHESISER (P.E. Feb. 73 to Feb. 74) The well acclaimed and highly versatile large-scale mains-operated Sound Synthesiser complete with keyboard circuits. Other circuits in our lists may be used with the Synthesiser to good advantage. Details in our lists.

FORMANT SYNTHESISER (Elektor 1977/78)

Very sophisticated music synthesiser for the advanced con-structor who puts performance before price. Details in our lists.

128-NOTE TUNE-PROGRAMMABLE SEQUENCER

(P.E. Nov/Dec 77)

Enables a voltage controlled synthesiser to automatically play pre-programmed tunes of up to 32 pitches and 128 notes long. Programs are keyboard initiated and note length and rhythmic pattern are externally variable. (Please use order codes quoted in brackets.)

Main Circuit (Nov) excl. sw's (KIT 76-1)	£18-03
Power Supply (KIT 76-3)	£4-72
Trigger Inverter and Alt. Output (KIT 76-2)	£1-15
LED Counter (KIT 76-4)	£2.10
PCB (as published) for KITS 76-1 & 3 (PCB 76A)	£2-61
PCB for KITS 76-2 & 4 (PCB 768)	£2.54

P.E. STRING ENSEMBLE (PE Mar-July 78)

The new keyboard string-instrument synthesiser.

Basic component sets:	
Power Supply (KIT 77-1)	£8-77
Tone Generator (KIT 77-2)	£14-66
Diode Gates (KIT 77-3)	£18-81
Chorus Generator (KIT 77-4)	£19-08
Voicing System (KIT 77-5)	£7-38
Printed Circuit Boards:	
Double-sided PCB for Power Supply, Tone Generator &	Diode
Gates with most of the Matrix wiring as printed trac	king
(PCB 77L/R)	£18.40
PCB for Chorus Generator (PCB 77C)	£2.65
PCB for Voicing System (PCB 77D)	£2.62
Fuller details of kits & PCBs are in our lists.	

P.E. JOANNA PLUS ORGAN VOICING

P.E. JUANNA PLUS UNBAN VUILING The basic five octave electronic plano (P.E. May/Sept 75 and Sound Design) has switchable alternative voicings for Honky-Tonk, ordinary plano, and Harpschord or a mixture of any of these three, together with facilities including fast and slow tremolo, loud and soft pedal switching, and sustain pedal switching. The modification retains all the circuitry associated with the plane but in addition provides an organ-voice envelope facility with 5 switchable pitches, variable attack and sustain, phasing and vibrato.

modification retents all the circulity associated with the plano but in addition provides an organ-voice envelope facility with 5 switchable pitches, variable attack and sustain, phasing and vibrato. Set of components (excl switches) for PSU, Frequency generator, Pitch and Note Divider, Envelope Shapers, Volcings, and Control circulities. (Order as KIT 71-5) £99-25 Set of PCBs (Order as PCB SET 71-6) £29-18	switchable controls affecting the fuzz quality whist retaining the attack and decay, and also providing filtering. Does no duplicate the effects from the Guitar Effects Pedal and car be used with it and with other electronic instruments. Component set using dual silder pot £7-51 Component set using dual rotary pot £6-83 Printed circuit board £1-65	WAH-WAH UNIT (P.E. Apr. 76) The Wah-Wah effect produced by this unit can be controlled manually or by the integral automatic controller. Component set (Incl. PCB) £3-63
GUITAR EFFECTS PEDAL (P.E. July 75) Modulates the attack, decay and filter characteristics of an audio signal not only from a guitar but from any audio source, producing 8 different switchable effects that can be further modified by manual controls, Possibly the most	FUZZ UNIT Simple Fuzz unit based upon P.E. "Sound Design" circuit Component set (Incl. PCB) £2:09 TREMOLO UNIT	
Interesting of all the low-priced sound effects units in our range. Circuit does not duplicate effects from the Guitar Overdrive Unit. Component set with special foot operated switches Alternative component set with panel switches Printed circuit board £7.69 £1.43	Based upon P.E. 'Sound Design' circuit. Component set (InCl. PCB) £2.94 TREBLE BOOST UNIT (P.E. Apr. 76) Gives a much shriller quality to audio signals fed through it The depth of boost is manually adjustable. Component set (incl. PCB) £2.5	VOICE OPERATED FADER (P.E. Dec. 73) For automatically reducing music volume during "talk-over"—particularly useful for Disco work or for home-movie shows.
TERMS: Goods in current adverts & lists VAT. Recomm	Keyboards add £2-00 each plus VAT. Other 15 add 25p plus VAT, over £15 add 50p plus hended: optional insurance against postal 50p for cover up to £50, £1-00 for £100	If changed), o full total of r, post & national Money Order or through an English Bank. To

oover, etc. pro-rate. N.B. Eire, C.I., B.F.P.O. and other countries are subject to higher export postage rates.

TERMS: Goods in current adverts & lists over £50 goods value (excl P&P & VAT). Correctly costed, C.W.O., U.K. orders only. This voucher must accompany order. Valid until end of month on cover of P.E.

COMPONENTS SETS include all necessary resistors. capacitors, semiconductors, potentiometers and transformers. Hardware such as cases, sockets, knobs, keyboards, etc. are not included but most of these may bought separately. Fuller details of kits, PCBs and parts are shown in our lists.

CIRCUIT AND LAYOUT DIAGRAMS are supplied free with all PCBs unless "as published

PHOTOCOPIES of P.E. texts for most of the kits are available-prices in our lists

ELEKTOR ELECTRONIC PIANO (Elektor Sept 78)

A touch-sensitive, multiple-voicing 5 catave piano using the latest integrated-circuit techniques for the keying and envelope shaping and virtually eliminating "be-hive" noise hitherto inherent in previous electronic pianos. Details in our lists.

DIGITAL REVERBERATION UNIT (Elektor May 78) A very advanced unit using sophisticated i.c. techniq instead of mechanical spring-lines. The basic delay range 24 to 90mS can be extended up to 450mS using extension unit. Further delays can be obtained using m extensions.

Main component set (KIT 78- Extension component set (KIT PCB for Kit 78-1 (PCB 78A) PCB for Kit 78-2 (PCB 78B)		£45-45 £43-36 £2-86 £1-06
ANALOGUE REVERBERATIO	IN UNIT (Elektor O	ct 78)
Using i.c.s instead of spring- maximum delay of up to 100 extends this up to 200mS. Mar stereo mode.	mS, and the additi	onal set
Main component set (KIT 83- Additional Delay Set (KIT 83- PCB (as published) to hold bo	-2)	£26-18 £18-25
kits (PCB 9973)		£4-31
RESONANCE FILTER (Elektor	r Oct 78)	
This filter module has been des to produce a more realistic sin	igned to allow a syr mulation of natural	nthesiser musical
instruments. Basic component set (KIT 82 PCB (as published) (PCB 995		£15-10 £3-29
SYNTHESISER EXTERNAL I	NPUT INTERFACE	
(P.E. Oct 78)		
This unit allows external inpu phones etc. to be processed synthesiser.		
Basic component set (inc) PC	B) (KIT 81-1)	€2·94
GUITAR MULTIPROCESSOR An extremely versatile sound producing, for example, Flangin Tremolo as well as other fascir with most electronic instrument	processing unit ca g, Vibrato, Reverb, I ating sounds. May	be used
RHYTHM GENERATOR KITS	· · · · · · · · · · · · · · · · · · ·	
Several available - details in our	lists.	_

GUITAR FREQUENCY DOUBLER (P.E. Aug. 77)

modified and extended version of the circuit publishe Component set and PCB £4-52

GUITAR SUSTAIN (P.E. Oct 77)

Maintains the natural attack whilst extending note duration. Component set, PCB and foot switches £5-13 Component set, PCB and panel switches £3.71

WIND AND RAIN UNIT

manually controlled unit for producing the above-named sounds Component set (incl. PCB) £4-26

GUITAR OVERDRIVE UNIT (P.E. Aug. 76) Sophisticated, versatlle Fuzz unit, including variable and switchable controls affecting the fuzz quality whilst retaining the attack and decay, and also providing filtering. Dees not

duplicate the effects from the Guitar Effects Pedal	and ca
be used with it and with other electronic instrument	
Component set using dual silder pot	£7.5
Component set using dual rotary pot	£6-8
Printed circuit board	£1-6
FUZZ UNIT Simple Fuzz unit based upon P.E. "Sound Design" Component set (incl. PCB)	circui £2-0

Nust be added to full total of goods, discount, post & handling, on all U.K. orders. Does not apply to Exports.

EXPORT ORDERS ARE WELCOME but to avoid delay we advise you to see our list for postage rates. All payments must be cash-with-order, in Sterling by Inter-national Morey Order or through an English Bank. To obtain list – Europe send 20p, other countries send 50p.

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AND OTHER PROJECTS

PHOTOGRAPHS in this advertisement show two of our unlis containing some of the P.E. projects built from our kils and PCBs. The cases were built by ourselves and are not for sale, though a small selection of other cases is available.

LIST—Send stamped addressed envelope with all U.K. requests for free list giving fuller details of PCBs, kits and other components.

OVERSEAS enquiries for list: Europe-send 20p; other countries—send 50p.



KIMBER-ALLEN KEYBOARDS AND CONTACTS

Kimber-Allen Keyboards as required for many published circuits. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C, the keys are plastic, spring-loaded, fitted with actuators, and mounted on a robust aluminium frame.

3 Octave (37 notes)	£25.50
4 Octave (49 notes)	£32-25
5 Octave (61 notes)	£39-75
Contact Assemblies (gold-clad wire) for use with the above KBDS (1 for each n	
Type GJ: Single-pole change-over	each 251p
Type GA: 1 pair of contacts, normally open	each 24p
Type GB: 2 pairs of contacts, each pair normally open	each 28 p
Type GC: 3 pairs of contacts, each pair normally open	each 374p
Type GE: 4 pairs of contacts, each pair normally open	each 481p
Type GH: 5 pairs of contacts, each pair normally open	each 581p
Type 4PS: 3 pairs of contacts plus single-pole changeover	each 57p

Printed Circuit Boards for use with most contacts (thus eliminating much interwiring) are

available. Details in our lists.	
	NEW
P.E. TUNING FORK (P.E. Nov. 75) Produces 84 switch-selected frequency-accurate tones. A	
LED monitor clearly displays all beat note adjustments. Ideal	PCB
for tuning acoustic or electronic musical instruments. Main component set (incl. PCB) £14-93	CEDVICE
Power supply set (incl. PCB) £6:28	SERVICE
SYNTHESISER TUNING INDICATOR (P.E. July 77)	PCBS FDR ALL NEW
A simple 4-octave frequency comparator for use with synthesisers and other instruments where the full versatility	P.E. & E.E. PRDJECTS
synthesisers and other instruments, where the full versatility of the P.E. Tuning Fork Is not regulred.	FOR WHICH PCB LAY-
Component and PCB (but excl sw.) £7.45	OUTS HAVE BEEN
	PUBLISHED AND FOR WHICH FULL COPY-
CONSTANT DISPLAY FREQUENCY METER (PE AUG 78)	RIGHT CLEARANCE IS
A 5-digit frequency counter for 1Hz to 99999Hz with a 1Hz	AVAILABLE.
sampling rate. Readout does not count visibly or flicker due to	LIMITED QUANTITIES
display blanking. Component set £24-05*	ONLY FOR AN EXPERI-
Printed circuit board £3-03*	MENTAL PERIOD.
•This kit & PCB are at 8% VAT (all others are 121%)	LET US KNOW YOUR
	NEEDS AND WE WILL ADVISE YOU OF
TAPE NOISE LIMITER	AVAILABILITY AND
Very effective circuit for reducing the hiss found in most tape recordings. All kits include PCBs	PRICES.
Standard tolerance set of components £2.96 Superior tolerance set of components £3.76	
Regulated power supply (will drive 2 sets) £4-69	INTEGRATED CIRCUITS
	301 8-pin DIL 48p 318 8-pin DIL 220p
DYNAMIC RANGE LIMITER (P.E. Apr. 77)	320-15 195p 324 14-pin DIL 87p
Automatically controls sound output to within a preset level.	341-15 87p 709 8-pin DIL 48p
Component set (incl. PCB) £4-58	723 T05 87p
	726 TO5 1005p
	749 9 nin DII 57n
DISCOSTROBE (P.E. Nov. 76)	4007 14-pin DIL 17+p 4011 14-pin DIL 17+p 4024 14-pin DIL 46+p 4069 14-pin DIL 18p
4-channel light-show controller giving a choice of sequential, random, or full strobe mode of operation.	4024 14-pin DIL 46-p 4069 14-pin DIL 18p 4136 14-pin DIL 126p
Basic component set £18-19 Printed circuit board £345	
Printed circuit board £3-45	AM2833 8-pin DIL 360p AY10212 16-pin DIL617p AY16721/6 188p
	CA3046 14-pin DIL 71p
BIDLOGICAL AMPLIFIER (P.E. Jan/Feb. 73)	CA3084 14-pin DIL209p
Multi-function circuits that, with the use of other external	FX209 16-pin DIL729p LM323 562p
equipment, can serve as lie-detector, alphaphone, cardiophone	M252 16-pin DIL680p MC3340 8-pin Dil 150p
etc. Pre-Amp Module Components set (incl. PCB) £3.95	M252 16-pin DIL680p MC3340 8-pin DIL 150p MCM6810 24-pin DIL670p SG3402N 14-pin DIL262p
Basic Output Circuits-combined component set	31KU25 380p
with PCBs, for alphaphone, cardiophone, frequency meter and visual feed-back lamodriver circuits. £6-59	TDA1022 16-pin DIL582p XR2207 14-pin DIL420p ZN425E 16-pin DIL375p
Audio Amplifier Module Type PC7 £7-75	ZN425E 16-pin DIL375p
	TRANSISTORS
SOUND BENDER (P.E. May 74)	AC128
A multi-purpose sound controller, the functions of which include envelope shaper, tremolo, voice-operated fader.	BC107
automatic fader and frequency-doubler.	AC128 34P AC176 28P BC107 13P BC108 13P BC109 15P BC109 15P BC109C 16P BC177 18P BC184 11P BC184 11P
Details in lists.	BC177
CODUCTIONTED DOWED CUDDI IEC	
SOPHISTICATED POWER SUPPLIES A wide range of highly stabilised low noise power supply kits	BC204 10p BC209C 13p
is available-details in our lists.	BC213 11p
PRICES ARE CORRECT AT TIME OF PRESS.	PHONOSONICS
E. & D. E. DELIVERY SUBJECT TO AVAILABILITY.	PHUNUSUNICS

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K117 – IG 37 ISLNS 7021, FILS, BUDS 702, FILS, GEN K117 – Turpose K117 – IG ME JOIN PN 60V Jow noise K124 – 500 02 J/F Disc Caramica K126 – 500 IE 5% + Vett CF Resistors All Backs £1 each Incl. VAI: CF Resistors Istest Bargaln list, which lists 55 more packs.

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sealed can 88p Send SAE for our relay list - 84 types listed and illustrated.

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Any FLIFTER NT L1-75 Mono gen, purpose amp with tone and Vol/on-off controls. Utilizes sim, circuitry to above amp. Output 2W into 8 ohms. Input matched for crystal caritidge. 4 transistor circuit, Simple to build on PCB provided. Can be either battery or mains operated. (For mains powered version add £2:20 for suit-able transformer.) Blue vinyl covered aluminium case to suit (W372) £1-30

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MISCELLANCOUSICS Supplied with data if requested. MC3302 quad comp. 120p; 710 diff comp. (T099) 40p; ZN1034E precision timer £2/25; LM711 Dual diff comp 65p; LM1303 dual stereo pre-amp 75p; MC1469R veitage reg 51-50; UPC1025H audio £3-50; 5-75C2 audio £2-88; TDA2640 audio £2-92; SN75110 dual line driver 70p; MC8500 CRCC gen POA

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KITSI KITSI Send S.A.E. for lists of kits from a simple Amplifier to a Reaction Tester, etc.

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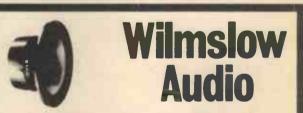
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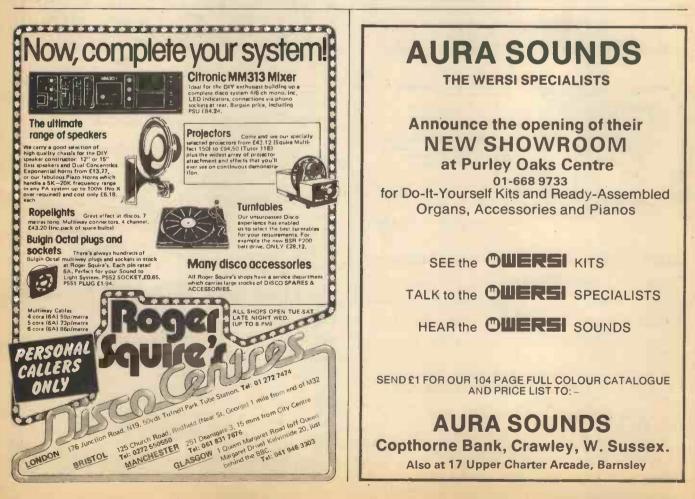
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1503 MIL23321 (ID2/17,A) 3mm (125) GREE 1503 MIL2331 (OP/212A) 3mm (125) YELLO 1504 ARL4880 (FLV117) .5mm (2 RED 1505 MIL5251 (III222) .5mm (2 GREE 1506 MIL5251 (IIV5353) .5mm (2 YELLO 1509 FLV111 .5mm (2 YELLO	E0.15 0.15 0.11
1509 FLV111 .5mm (.2) CLEAR	£0.11
SUPER 'Hi-Brite' Type (III, Re	0)
1521 MIL32 .3mm (.125) RED	£0.10
1522 MIL52 .5mm (.2) RED	£0.10
1514 ORP12 Light dependent resistor	£0.55
1520 OCP71 Photo transistor	£0.35
LED CLIPS	60.4E
1508/125 pack of 5 125 clips 1508/2 pack of 5 2 clips	£0.15 £0.18
1000/2 pack of 5 2 clips	20.10
DISPLAYS	
DL703 7 segment D.P. left (.30" height) Comr	non Anode
NEU Single Digit UNC	. 1523 £0.70
DL707 7 segment D.P. left (.0.3" height) Comr	non Anode
	. 1510 £0.95 mon Anode
RED Two-Digit Reflector O/NC). 1524 £1.70
DL727 7 segment D.P. right (.510" height) Comr	non Anode
RED Single Digit Light Pipe O/NC	. 1511 £1.70
OPTO-ISOLATORS	
Isolation Breakdown-Voltage 1500-continuous	s fwd current
100mA	
CIL74 Single-Channel 6 pin DIP standard	
	itter and NPN
	IO. 1497 £0.50
	lated Channels
	IO. 1498 £1.00
CILQ74 Multi-Channel 16 pin DIP Four Iso	lated Channels
	O. 1499 £2.20

SWITCHES

the second se			
Description	No.		Price
DPDT miniature slide	1973		15p
DPDT standard slide	1974		16p
Toggle switch SPST			
1 amp 250V a.c.	1975		37p
Toggle switch DPDT	4070		
1 amp 250V a.c.	1976		47p
Rotary on-off mains switch	1977		56p
Push switch - Push to make	1978 1979		15p
Push switch – Push to break	1978		20p
Rocker Switch	Colour	No.	Price
A range of rocker	RED	1980	33p
switches SPST - moulded	BLACK	1981	33p
In high insulation	WHITE	1982	33p
material available in a	BLUE	1983	33p
choice of colours ideal	YELLOW	1984	33p
for small apparatus	LUMINOUS	1985	33p
Description		No.	Price
Miniature SPST toggle 2 amp	250V a.c.	1958	78p
Miniature SPST toggle 2 amp	250V a.c.	1959	84p
Miniature DPDT toggle 2 amp	250V a.c.	1960	90p
Miniature DPDT toggle centre	oπ 2 amp 2 50V	a.c. 1961	£1.06
Push-button SPST 2 amp 250 Push-button SPST 2 amp 250	v a.c.	1962 1963	£1.01 £1.06
Push-button DPDT 2 amp 250		1964	£1.35
a and a control of Dr Dr Z amp 200		1304	11.30
MIDGET WAFER SWITCH	FS		
the search and a second and a second se			

Single bank wafer type – suitable for switching at 250V a.c. 100mA or 150V d.c. in non-reactive loads make-before-break contacts. These switches have a spindle 0.25 in dia and 30 Index-

ing.		
Description 1 pole 12 way 2 pole 6 way 3 pole 4 way 4 pole 3 way	No. 1965 1966 1967 1968	Price 54p 54p 54p 54p
MICRO SWITCHES Plastic button gives simple 1 pole	No.	Price
change over action Rating 10 amp 250V a.c.	1970	27p

AUDIO LEADS

No. 107	Type FM indoor Ribbon Aerial	Price 67p
113	3.5mm Jack plug to 3.5mm Jack plug.	
114	5 pin DIN plug to 3.5mm Jack connected to	84p
	pins 3 & 5. Length 1.5m	96p
115	5 pin DIN plug to 3.5mm Jack connected to pins 1 & 4, Length 1,5m	96p
116	Car aerial extension. Screened Insulated	
117	lead. Fitted plug & skt. AC mains connecting lead for cassette	£1.41
	recorders and radios. 2 metres	77p
118	5 pin DIN phono plug to stereo headphone Jack socket	£1.18
119	2 + 2 pin DIN plugs to stereo Jack socket	£1.10
	with attenuation network for stereo	
120	headphones. Length 0.2m Car stereo connector. Variable geometry plug	£1.01
	to fit most car cassettes, 8-track cartridge &	
	combination units. Supplied with inlined fuse power lead and instructions	67p
123	6.6m Coiled Guitar Lead Mono Jack plug	
124	to Mono Jack plug BLACK 3 pin DIN plug to 3 pin DIN plug.	£ 1,69
	Length 1.5m	84p
125	5 pin DIN plug to 5 pin DIN plug. Length 1.5m	840
126	5 pin DIN plug to Tinned open end.	
127	Length 1.5m 5 pin DIN plug to 4 Phono Plugs.	84p
	All colour coded. Length 1.5m	£1,48
128	5 pin DIN plug to 5 pin DIN socket. Length 1.5m	900
129	5 pin DIN plug to 5 pin DIN plug mirror	•
130	image. Length 1.5m 2 pin DIN plug to 2 pin DIN inline socket.	£1.18
	Length 5m	77p
131	5 pin DIN plug to 3 pln DIN plug 1 & 4 and 3 & 5. Length 1.5m	93p
132	2 pin DIN plug to 2 pin DIN socket.	
133	Length 10m 5 pin DIN plug to 2 phono plugs.	£1.10
	Connected pins 3 & 5. Length 1.5m	84p
134	5 pin DIN plug to 2 phono sockets. Connected pins 3 & 5. Length 23cm	77p
135	5 pin DIN socket to 2 phono plugs.	
136	Connected pins 3 & 5. Length 23cm Colled stereo headphone extension lead	77p
	Black, Length 6m	£1.97
178	AC mains lead for calculators etc.	51p

CASES AND BOXES

INSTRUMENT CASES. In two sections vinyl covered top

and sid	es, aluminium	bottom, fre	ant and back	
No. 155 156 1 57 158	Length Bln 1 1 in 6 in 9 in	Width 51in 6in 41in 51in	Height 2In 3in 1≩in 2≩in	Price £1.35 £2.29 £1.40 £1.90
ALUM	INIUM BOXE	ES. Made	from bright a	li., folded
		a complete	e with helf inch de	eep lid and
SCIEWS.	k			
No.	Length	Width	Height	Price
159	5 lin	21in	14in	67p
160	4in	4in	1 in	67p
161.	4in	2 lin	1 in	67p
62	5 lin	4in	14in	76p
163	4in	2 lin	2in	69p
64	3in	210	lin	48p
165	7in	Sin	2+In	
				£1.12
66	8in	6in	3៣	£1.43
167	61n	4in	2in	93p
-		_		

NUTS AND BOLTS

BA BOLTS - cheese head. Supplies in m		of BA threaded ca	dmlum piated	screws	slotted
Туре	No.	Price	Туре	No.	Price
1in OBA 1in OBA	839 840	£1.29 81p	in 4BA	848 847	34p 27p
1in 2BA	842	70p	In 6BA	848	43p
tin 2BA	843	48p	1 in 6BA	849	22p
in 2BA	844 845	56p 47p	 in 6₿A	850	27p
		cadmium plated fu			
Type OBA	855	Price 77p	4BA	No. 857	Price 32p
28A	856	61p	6BA	858	25p
	PC 4	lat cadmium plat	ted olain star	mood u	nahara
supplied in m			teo pan sta	npeu w	0311013
Туре	No.	Price	Туре	No.	Price
08A 28A	859 860	15p 13p	48A 68A	861 862	13p 13p
200	000	1.0P	ODA	002	rab
		t tinned supplied h			
OBA	No. 851	Price 43p	Type 4BA	No. 853	Price 23p
2BA	852	300	6BA	854	23p

FUSE HOLDERS AND FUSES

Descrip 20mm x 11 x 1 in 11 in cari Panel mo Panel mo	5mm c chassis inline ty ounting	mountin pe 20mm			No. 506 507 508 509 510		1	3p 3p 2p 36
QUICK	BLOW	20mm						
Type 150mA	No. 611	6p	Туре	No. 615	5p	Туре	No. 619	6p
250mA 550mA 800mA		5p 5p 8p	1.5A 2A 2.5A	616 617 618	6p 5p 6p	4A 5A	620 621	10p 5p
ANTI-S								
100mA 250mA			1A 2A	No. 628 626	5	2.5A 3.15A	No. 628 629	
500mA	624		1.6A All 8p	627 each	·	5A	630	
QUICK-	BLOW	11 in.						
250mA	No. 831		500mA All 8p	No 632 each		BOOM	No. 4634	
Туре	No. 635		7 уре 2.5А	No 838		Type 4A	No. 641	
2A	637		3A	639)	5A	642	
			All 6p	marcili.				

TRANSFORMERS

MINIATUR No. 2021 2022 2023	TE MAINS Primary 240 Secondary 6V-0-8V 9V-0-9V 12V-0-12V	00mA 00mA	Price £1.01 £1.01 £1.26
	Te MAINS Primary 240 decendent secondary win Type MT280-0-6V 0-6V R MT150-0-12V 0-12V	dings MS	Price £1.80 £1.80
1 AMP MA No. 2026 2027 2028 2029 2030	INS Primary 240V Secondary 6V-0-6V1 amp 9V-0-9V1 amp 12V-0-12V1 amp 15V-0-15V1 amp 30V-0-30V1 amp	Price £2.70 £2.16 £2.80 £2.97 £3.67	P.& P.45p P.& P.45p P.& P.55p P.& P.86p P.& P.86p
Multi-tappe	D MAINS Primary 240 d secondary mains trans mp current rating. Second	formers availa	ble In } amp, 1 -19-25-33-40-
Voltagesavt 4,7,8,10,1 No. 2031 2032 2033	Illable by use of taps. 4, 15, 17, 19, 25, 31, 33, Rating 3 amp 1 amp 2 amp	40,25-0-25V Price £3.82 £4.95 £6.13	P. & P. 86p P. & P. 86p P. & P. 80p P. & P. £1.00
	40v Primary 0-55v • 2A S		+£1.00 P. & P.
tu	OFFER 40v Primary 0-20v e 2/ pros for each volt from oltage up to 20v e 2A is ea leal for the experimenter	the secondar sily obtainable	y winding any

Type	No.	Price
T03 – single heat sink	873	20.29
T03 – double heat sink	874	21.29 + 30p P.& P.
T04 – single de heat sink	875	21.56 + 35p P. & P.
T05/39 heat sink	876	20.15
T018 – heat sink	877	20.15
T01 – heat sink	878	20.09
MINIDRILL 12v hand held battery operated Ideal for drilling printed circuits of	mini drill. 7,50 or model maki	00 r.p.m. Collet chuck.

MEL 11 (TIL81) NPN Light Detector:

Silicon Photo Darlington Amplifier – VCBO: 40v, VCEO 30v, VECO 10, lc 100mA. Ptot: 300mW IL Min 0.5 Typ. 2mA ID 100mA nA. 26p

SEMICONDUCTORS

TRAN	ISISTORS	74 SERIES TTL IC'S
Type Price Type Price Type AC107 £0.23 BC134 £0.20 BD190 AC113 £0.21 BC135 £0.77 BD195 AC115 £0.22 BC137 £0.20 BD195 AC117 £0.23 BC137 £0.20 BD195 AC117 £0.23 BC137 £0.20 BD195 AC117 £0.32 BC137 £0.20 BD195 AC117 £0.32 BC137 £0.20 BD195 AC127 £0.31 BC140 £0.25 BD195 AC127 £0.21 BC140 £0.24 BD201 AC126 £0.15 BC142 £0.24 BD201 AC126 £0.26 BC1201 BC145 £0.25 BD201/200 AC127 E0.26 BC1203 BC1203 BD203 AC132 BD203 AC132 £0.21 BC150 £0.28 BD203/200 AC137 E0.21 BC203 A	Price Type Price Type Price Type C0.84 BSY28 £0.17 UT48 £0.22 2N2924 C0.97 BSY38 £0.17 2N2925 2N2926 2N2926 C1.03 BSY38 £0.21 2N2926 2N2926 2N2926 C1.03 BSY40 £0.31 ZTX107 £0.11 2N29260 2N29260 E1.07 BSY40 £0.31 ZTX108 £0.11 2N29266 2N29266 C0.86 BSY951 £0.21 ZTX108 £0.11 2N29266 2N3021 C0.86 BSY954 £0.14 ZTX300 £0.14 2N3011 2N29266 C0.86 BU105 £1.51 ZTX302 £0.18 2N3054 2N3054 C0.86 BU205 £1.51 ZTX500 £0.18 2N3054 C0.86 BU206 £2.05 ZTX500 £0.14 2N3391 4 BU205 £1.51 ZTX500 £0.14 2N3391 4.184	60.09 7404 60.12 7437 60.32 7476 60.32 7476 60.32 7476 60.32 7476 60.32 7476 60.32 7476 60.32 74118 60.32 74118 60.32 74118 60.32 74118 60.32 74118 60.32 74118 60.32 74118 60.32 74117 74117 60.63 60.09 7405 60.24 7433 60.23 7481 60.33 74121 60.42 74117 60.63 60.09 7405 60.14 7440 60.13 7482 60.37 74122 60.42 74177 760.63 60.10 7408 60.14 7443 60.55 74136 60.55 74136 60.63 60.11 7415 7443 7443 60.56 74141 60.55 74136 60.63 60.43 7411 60.16 74490 60.76 7486 60.76 74141 60.55 74182 60.76
AC155 £0.21 BC167 £0.14 BD234 AC156 £0.21 BC168 £0.14 BD235 AC157 £0.27 BC168 £0.10 BD235 AC165 £0.21 BC176 £0.10 BD237 AC166 £0.21 BC170 £0.10 BD239A AC168 £0.27 BC171 £0.10 BD239A AC168 £0.27 BC173 £0.10 BD249A AC168 £0.27 BC173 £0.10 BD249A AC168 £0.27 BC173 £0.10 BD249A AC176 £0.28 BC173 £0.17 BDY11 AC176 £0.28 BC175 £0.17 BDY12 AC176 £0.28 BC176 £0.17 BDY12 AC178 £0.28 BC179 £0.17 BDY12 AC178 £0.28 BC179 £0.17 BF115 AC180 £0.21 BC182 £0.10 BF118 AC181 £0.21 BC182 £0.10 BF118	20.89 MJ480 C1.03 2N3415 20.59 MJ481 C1.03 2G301 2N3416 20.61 MJ490 C1.03 2G301 2N3416 20.59 MJ490 C1.03 2G301 20.24 2N3416 20.59 MJ490 C1.03 2G301 20.24 2N3614 20.60 MJ491 C1.24 2G302 C0.24 2N3614 20.61 MJ430 C1.24 2G303 C0.24 2N3616 20.64 MJ2370 C0.65 2G304 C0.32 2N3616 20.38 MJ2571 C0.65 2G306 C0.32 2N3702 21.34 MJ2521 C0.49 2G308 C0.33 2N3702 21.44 MJ2535 C0.37 2G309 C0.38 2N3702 20.3706 MJ2535 C0.37 2G370 C0.37 2N3706 20.3706 MJ2535 C0.37 2G371 C0.19 2N3708 20.34 MPF104 C0.3	Type Price Type Price <t< td=""></t<>
AC187K C0.30 BC183L C0.10 BF125 AC188 C0.19 BC184L C0.10 BF127 AC188K C0.30 BC184L C0.10 BF127 AC188K C0.30 BC184L C0.10 BF127 ACY17 C0.37 BC187L C0.24 BF153 ACY19 C0.37 BC207 C0.24 BF154 ACY19 C0.37 BC208 C0.12 BF156 ACY21 C0.37 BC202 C0.14 BF157 ACY22 C0.37 BC212 C0.10 BF158 ACY27 C0.37 BC212 C0.10 BF158 ACY27 C0.37 BC212 C0.10 BF158 ACY28 C0.37 BC213 C0.10 BF160 ACY29 C0.37 BC213 C0.10 BF160 ACY29 C0.37 BC213 C0.10 BF162 ACY29 C0.37 BC213 C0.10 BF162 ACY30	£0.56 MPSA55 £0.23 2G378 £0.19 2N3772 £0.68 MPSA56 £0.23 2G381 £0.19 2N3773 £0.28 2G381 £0.19 2N3819 2N3819 2N3819 £0.27 2G401 £0.35 2N3820 2N3820 2N3820	E173 ELINEAR IC's 62.38 Fried Type Price Type Type Price Type Type<
AD143 C0.81 BC304 C0.41 BF182 AD149 C0.84 BC327 C0.18 BF183 AD161 C0.37 BC327 C0.18 BF183 AD162 C0.37 BC327 C0.17 BF184 AD162 C0.37 BC327 C0.17 BF185 AD161/162 BC337 EC.17 BF185 AD1740 C0.56 BC441 EO.32 BF187 AF114 EO.27 BC460 EO.41 BF194 AF115 EO.27 BC477 EO.22 BF195 AF115 EO.27 BC477 EO.22 BF197 AF117 EO.32 BC543 EO.22 BF197 AF124 EO.32 BC543 EO.21 BF198 AF124 EO.32 BC544 EO.11 BF200 AF126 EO.32 BC549 EO.11 BF200 AF126 EO.32 BC550 EO.16 BF224 AF179 EO.64 <td>CD 32 CC71 E0.16 2N706 E0.15 2N4292 CD 32 CC72 E0.26 2N711 E0.32 2N4293 E0.22 CC76 E0.32 2N718 E0.32 2N4923 E0.22 OC76 E0.32 2N718 E0.37 2N5136 E0.28 OC77 E0.54 2N726 E0.31 2N5136 E0.32 OC76 E0.34 2N727 E0.31 2N5136 E0.43 OC81 E0.26 2N74 E0.22 2N5136 E0.31 OC81 E0.26 2N74 E0.22 2N5146 E0.11 OC82 C9.32 2N918 E0.32 2N5457 E0.11 OC82 C9.32 2N914 E0.16 2N5296 E0.16 OC139 E0.32 2N5459 E0.32 2N5459 E0.32 OC140 C9.46 2N113 C9.19 2N5459 E0.37 OC170 E0.38 2N1132 E0.19 2N5459</td> <td>C0.20 C0.20 C0.20 C0.20 C0.20 C0.45 C</td>	CD 32 CC71 E0.16 2N706 E0.15 2N4292 CD 32 CC72 E0.26 2N711 E0.32 2N4293 E0.22 CC76 E0.32 2N718 E0.32 2N4923 E0.22 OC76 E0.32 2N718 E0.37 2N5136 E0.28 OC77 E0.54 2N726 E0.31 2N5136 E0.32 OC76 E0.34 2N727 E0.31 2N5136 E0.43 OC81 E0.26 2N74 E0.22 2N5136 E0.31 OC81 E0.26 2N74 E0.22 2N5146 E0.11 OC82 C9.32 2N918 E0.32 2N5457 E0.11 OC82 C9.32 2N914 E0.16 2N5296 E0.16 OC139 E0.32 2N5459 E0.32 2N5459 E0.32 OC140 C9.46 2N113 C9.19 2N5459 E0.37 OC170 E0.38 2N1132 E0.19 2N5459	C0.20 C0.20 C0.20 C0.20 C0.20 C0.45 C
AF239 C0.41 BCV33 C0.59 BF259 AL102 C1.29 BCV34 C0.66 BF262 AL103 c1.27 BCV70 C0.16 BF263 ASY26 C0.41 BCV71 C0.16 BF270 ASY27 C0.43 BCV72 C0.16 BF271 ASY28 C0.41 BC210 C0.65 BF271 ASY29 C0.41 BC210 C0.65 BF273 ASY29 C0.42 BC15 C0.65 BF274 ASY50 C0.32 BC15 C0.56 BF274 ASY55 C0.32 BD121 C0.70 BF337 ASY55 C0.32 BD121 C0.70 BF337 ASY55 C0.32 BD124 C0.70 BF337 ASY55 C0.32 BD124 C0.76 BF457 ASY57 C0.32 BD131 C0.38 BF458	Co.38 CO.205 F1.24 2 N1308 CO.32 2 2303 Co.88 2N1309 CO.38 2 2304 2 2304 Co.39 P346A CO.38 2 2307 2 2304 Co.39 P397 CO.49 2 N1619 CO.38 2 2307 Co.34 P397 CO.49 2 N1619 CO.42 2 2307 Co.41 2 N1819 CO.49 2 N1819 CO.49 2 2322 Co.37 R20008B C2.70 2 N2147 CO.32 2 2322 Co.34 R20008B C2.70 2 N2148 C1.06 2 2324 Co.34 R2010B C2.71 2 N2148 C1.06 2 2324 Co.34 R2010B C2.71 2 N2148 C1.06 2 2325 Co.34 R2010B C2.71 2 N2148 C1.06 2 2324 Co.34 R2010B C2.81 2 N2192 C0.41 2 2325 Co.40 T140 C0.23 2 N2217 C0.41 2 5 327	C0.60 E0.77 E0.86 E0.86 E0.86 E0.86 E0.86 E0.46 E0.46 E0.46 E0.46 E0.46 E0.47 E0.47 E0.77 TOS Case No. E0.48 E0.46 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.40 E0.77 Do amp Voits E0.33 E0.33 E0.35 E0.62 E0.62 E0.62 E0.62 E0.77 Price E0.83 E0.85 E0.62 E0.62 E0.77 Price E0.83 E0.85 E0.62 E0.77 Price E0.83 E0.85 E0.62 E0.77 Price E0.83 E0.80 E0.40 E0.99 E1.21 Price E0.83 E0.82 E0.82 E1.21 C0.77 E0.77 E0.77 6 amp E1.21 E0.65 E0.65 E0.65 E0.68 E0.68 E0.68 E0.68 E0.65 E0.68 E0.65 E0.62 E0.22 Price E1.21 E1.21
AU1104 C1.81 BD133 C0.48 BF396 AU113 C1.51 BD133 C0.43 BFR33 BC107 C0.99 BD136 C0.38 BFR49 BC107A C0.99 BD136 C0.38 BFR49 BC107A C0.99 BD136 C0.38 BFR49 BC107B C0.10 BD139 C0.39 BFx84 BC107B C0.09 BD139 C0.39 BFx84 BC108B C0.09 BD139 C0.39 BFx84 BC108B C0.09 BD139/140 BFx85 BC108B C0.09 BD139/140 BFx85 BC108B C0.10 BD155 C0.86 BFx87 BC108B C0.10 BD155 C0.86 BFx87 BC108B C0.10 BD175 C0.86 BFx89 BC108B C0.10 BD175 C0.72 BFx50 BC108C C0.11 BD177 C0.72 BFx50	C0.34 2N2218 & 60.24 2N2218 & 60.22 C0.32 Co.24 2N2219 & 60.22 Co.24 2N2219 & 60.22 C0.26 Co.27 Co.28 Co.24 C0.28 TIC44 C0.39 2N2219 & 60.22 Co.24 C0.32 TIC45 E0.39 2N2219 & f0.22 Co.32 Co.34 C0.32 TIC94 & f0.43 2N2214 f0.22 Co.32 Co.34 Co.35 TIC94 & f0.43 2N2214 f0.24 Co.34 TIP394 & f0.43 2N2214 & f0.22 f0.32 Co.34 TIP394 & f0.43 2N2214 & f0.22 f0.24 40327 f0.32 TIP308 & f0.45 2N2224 & f0.22 f0.32 40347 f0.27 TIP308 & f0.45 2N2369 & f0.16 40348 f0.36 f0.32 TIP314 & f0.43 2N2369 & f0.16 40346 f0.36 f0.24 TIP314 & f0.43 2N2369 & f0.16 40360 f0.62 TIP314 & f0.43 2N2464 f0.51 40406 f0.51 40406 f0.51 40407 C0.18 TIP328 f0.43 2N2646 f0.51 40407	c0.41 c0.43 c0.44 c0.44 <td< td=""></td<>
BC113 C0.18 BD179 C0.21 BFV51 BC114 C0.18 BD179 C0.81 BFV52 BC115 C0.21 BD180 C0.81 BFV52 BC116 C0.21 BD181 C0.92 BIP19 BC116 C0.21 BD183 C1.03 BIP19 BC116 C0.21 BD183 C1.03 BIP19/20 BC117 C0.23 BD185 C0.73 BSX19 BC120 £0.43 BD186 C0.73 BSX25 BC125 £0.19 BD186 C0.73 BSX20 BC126 £0.19 BD187 C0.81 BSV25 BC126 £0.25 BD186 C0.81 BSV25 BC126 £0.26 BD188 c0.81 BSV26 BC132 £0.20 BD188 c0.81 BSV27	£0.18 TIP32C £0.48 2N2712 £0.24 40408 £0.18 TIP41A £0.48 2N2714 £0.24 40409 £0.41 TIP41B £0.50 2N2904 £0.19 40410 £0.41 TIP41C £0.52 2N2904A £0.23 40411	Bandwissing Bandwissing C0.56 C0.81 C0.81 C0.76 C0.86 C0.76 C0.76 DEPT. PE7, P.O. BOX 6, WARE, HERTS. SHOP 18 BALDOCK ST., WARE, HERTS. AT OPEN 9 to 5.30 MON-SAT.



Logically laid out to accept both 0.3" and 0.6" pitch DIL packages as well as Capacitors, Resistors, LED's, Transistors and components with leads up to .85mm dia.

500 individual connections in the central breadboarding area, spaced to accept all sizes of DIL package without running out of connection points, plus 4 Integral Power Bus Strips around all edges for minimum inter-connection lengths.

All connection rows and columns are now numbered or lettered enabling exact location indexing.

Double-sided nickel silver contacts for long life (10K Insertions) and low contact resistance (<10m. ohms).

Easily removable, non-slip rubber backing allows damaged contacts to be rapidly replaced.

No other breadboard has as many individual contacts, offers all these features and costs only £5.80 each or £11.00 for 2 - inclusive of VAT and P.P.

Snip out and Post

David George Sales, r/o 74 Crayford High St., Crayford, Kent DA1 4EF

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Please send		derSucker @ £6.80	Please
		ead Board @ £5.80 ead Boards @ £11.0	
(All prices i overseas or		d P.P., but add 159	% for
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Company.			
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Tel. No.		<mark></mark>	
Please make	cheque/P.O.'s	payable to David C	Seorge Sales

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AFFORDABLE-JUSTIFIABLE?

THE AGE of affordable personal

computing has finally arrived", so runs the slogan of one American supplier and, if you were paying their price and earning an American salary then it quite definitely has. However, most of us have to pay the same figures in £'s rather than \$'s, making the products about twice the price and, of course, most people in the UK do not earn in relative terms - the salaries paid across the pond.

So, has "the age . . , arrived" in the UK, well of course it depends on what you can afford! It also depends on the justification behind buying a home computer and that is still, and probably will continue to be, uppermost in the minds of many potential consumers (or their wives). Can you justify the expense? Will the computer be worth the £200 plus in time saved? Will it enable you to manage your money --- if you have any left - so much more efficiently that it will pay for itself inside a couple of years? Can you use it to control your heating, hot water, burglar alarm, to wake you up with a cup of tea and help with the cooking? The answer to all these questions and to many more similar ones is, probably, not yet.

Why then does anyone other than the small business man buy a personal computer? The answer in 9 out of 10 cases is because computing is a hobby. It must be the fastest growing hobby in this country, probably in the world. It is probably one hobby which "outsiders" totally fail to understand, to them a computer must be justifed on the terms above, no doubt one day soon it will be, but at present it is used for the interest, entertainment, education and to tax the logical dexterity of the individual.

OUR SYSTEM

Most hobbyists, having bought a system quickly find out what they really need and many find that they have purchased, what turns out to be the wrong system for their needs. With this and the growing interest in computing of our readers in mind (a small survey at last year's Breadboard showed more than 35% of you listed microprocessors and allied technology as your basic interest - a greater percentage interest than in any other area of electronics) we have reviewed a different system each month for four months and will continue in this way.

Having got to grips with many

systems we now have an excellent understanding of what most people require in a hobby computer and how they need it to expand to meet their developing requirements. That knowledge has been used in deciding to publish our own single board computer of which you will find more information on page 63. While other systems, developed in this country and sold in small numbers, have been limited by the lack of peripherals or lack of interest and development of software, we feel that we have overcome these problems by making our computer compatible with all Ohio Scientific Superboard accessories etc.

We believe that Compukit UK101 will be a significant step nearer "the age of affordable personal computing" in the U.K.

We also expect next month's issue to be in great demand and since distribution problems are by no means unknown at the present time, we urge you to place an order with your newsagent to ensure your copy. We believe our single board computer is in a class of its own when price and performance are compared.

Mike Kenward

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We regret that lengthy technical enquiries cannot be answered over the telephone (see below).

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Technical Queries

We are unable to offer any advice on the use or purchase of commercial equipment or the incorporation or modification of designs published in Practical Electronics.

All letters requiring a reply should be accompanied by a stamped, self addressed envelope and each letter should relate to one published project only.

Components are usually available from advertisers; where we anticipate supply difficulties a source will be suggested.

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Cheques and postal orders should be made payable to IPC Magazines Limited.

Sound Triggered ELASH

R.A. PENFOLD

Winth the aid of this sound triggered flash unit it is possible to take numerous interesting and unusual action photographs which probably could not be obtained by other means. The circuit is really just a form of sound activated switch, but it has a very fast operating speed so that it can, for example, be triggered by the sound from a balloon as it bursts, causing the flashgun to fire and take a photograph to freeze the balloon mid-burst.

Units of this type are usually only designed to provide instant triggering of the flash, and this makes it difficult to obtain pictures of shortly after the triggering event has taken place. Such photographs are often the most spectacular and interesting.

Previous experience with this type of equipment has shown that it is possible to obtain a specific delay by positioning the microphone away from the object to be photographed, the time taken for the sound to reach the microphone (due to the distance between the microphone and the object) determining the delay. Drawbacks of this system are that it can easily lead to frequent accidental triggering due to the necessary use of a high sensitivity. Also, only a fairly short maximum delay can normally be obtained, and it may not be practical at all if the event to be photographed only produces a low sound level.

Despite the slight increase in cost and complexity, it was thought to be well worthwhile incorporating a variable delay facility into the present design. The unit can be set for nominal delays of 0.5, 1, 2, 5, and 10ms, or for instant operation. However, the delay times can easily be varied to suit individual requirements, and the unit can be modified for a continuously variable delay if desired.

THE CIRCUIT

The block diagram of Fig. 1 shows the basic arrangement employed in the circuit. A low impedance dynamic (cassette recorder) type microphone is used as the transducer, and this feeds a common base amplifier stage which provides a suitably low matching input impedance. Low impedance dynamic microphones have very low output signal levels, usually less than a millivolt, and so a further stage of amplification is needed in order to boost the signal to a reasonable level.

This amplified signal is used to trigger a monostable multivibrator, and it is the length of the pulse produced by this circuit which determines the delay given by the unit. For instant operation the timing capacitor is switched out of circuit so that there is no significant delay through the monostable, although like every other part of the circuit, it obviously has a finite operating speed and there is still a very small delay.

The flash must be triggered on the falling edge of the monostable output waveform, and so the output from this circuit is fed to the input of a second negative edge triggered monostable. When triggered, this produces a brief (1ms.) output pulse which triggers a thyristor and fires the flashgun.

The full circuit diagram of the Sound Triggered Flash Unit is shown in Fig. 2.

Transistor TR1 is used as a fairly conventional common base amplifier with the microphone connecting directly into its input (emitter) circuit. This results in a small d.c. current flow through the microphone, but this is of no practical consequence.

In the interest of avoiding premature triggering it is advisable to use a sensitivity only marginally higher than is absolutely necessary. VR1 is therefore used as a simple volume control type sensitivity adjuster at the output of the preamplifier.

Transistor TR2 is used as the basis of the common emitter amplifier stage, and VR2 is adjusted to bias TR2s collector to a little more than one third of the supply voltage.

The output of TR2 is direct coupled to the input of the first monostable which is based on an NE555 l.c. This will be triggered when a signal from the microphone causes the voltage at TR2 collector to fall below one third of the supply potential. S1a selects one of five timing resistors, and these provide the five delay times.

For *instant* operation the lowest value timing resistor (R10) is left in circuit by S1a, and S1b switches the timing capacitor, C5, out of circuit. The unit, in effect, simply





INSTANT

STRIKE A LIGHT!

10ms DELAY

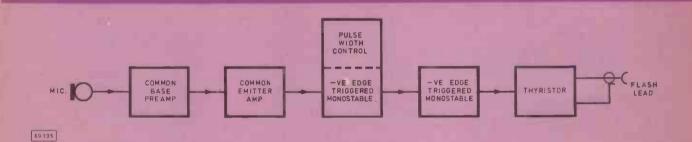


Fig. 1. Block diagram

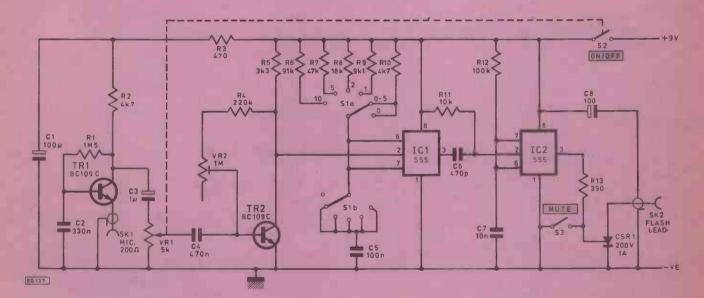


Fig. 2. Full circuit diagram

SOUND TRIGGER

WITH

VARIABLE DELAY



couples the signal at TR2 collector through to the second monostable circuit.

The latter is a second NE555 based circuit using IC2. The input of the second monostable must be high under quiescent conditions or the output will assume the high quiescent state, and switch on CSR1 via current limiting resistor R13. R11 holds the input in the high quiescent state and capacitive coupling is used to the normally low output of IC1.

Timing components R12 and C7 produce a nominal output pulse length of just over 1ms., but the pulse must merely be long enough to reliably trigger the thyristor and the pulse length of the second monostable is otherwise of no importance.

Switch S3 can be used to short circuit the gate of CSR1 to the negative supply rail and thus prevent the flashgun from being triggered by the unit. This is a very useful feature in practice which can help to avoid spurious triggering while preparing to take a photograph, but is not an essential part of the unit. S2 is the ordinary on/off switch and can be ganged with VR1.

Current consumption of the unit is about 18mA from a 9 volt (PP3 of PP6) battery.

CONSTRUCTION

It is advisable to house the project in a metal case earthed to the negative supply rail, so that the circuit is screened from stray pick-up which could cause spurious triggering of the flashgun.

The small components are assembled on a 0.1in. matrix stripboard having 17 strips by 41 holes using the layout illustrated in Fig. 3, except R6 to R10, which are mounted on the tags of S1. This and all the other point to point wiring is detailed in Fig. 4. The unit has a high overall level of gain and care to avoid earth loops must be taken if the wiring is altered in some way. Sockets to suit the plugs fitted to flashguns are available from a few sources, but it will probably be more convenient to connect the unit to the flashgun via a flash extension cable. This has the plug (which would normally fit into the camera's flash socket) removed, so that the lead can be threaded through a hole in the rear of the case and connected to the component panel.

It is essential that the flash-lead is connected with the correct polarity if the unit is to function. Normally the black lead or outer braiding is the negative lead, and the white one is the positive lead. This may not always be the case though, and if necessary, trial and error can be used to find the correct polarity.

Note that many cassette type dynamic microphones have provision for a remote on/off control facility, and are fitted with a combined $3 \cdot 5/2 \cdot 5$ mm jack plug. Either the plug can be changed for an ordinary single type, or a $2 \cdot 5$ mm socket can be fitted to the unit to accommodate the unused part of the plug, as on the prototype.

USING THE UNIT

With VR2 adjusted for maximum resistance (set fully anticlockwise) the unit will probably work, but with very low sensitivity. Adjusting VR2 for decreased resistance will increase sensitivity, up to the point where the collector voltage of TR2 becomes virtually equal to the trigger voltage of IC1. The unit may then fail to operate, or having triggered once it will latch and fail to operate subsequently. VR2 should be very slightly backed off from this setting.

The steps involved when taking a photograph are listed below. It is suprisingly easy to make an error in this procedure which will ruin the photograph, and it is a good idea to make a few "dummy runs" before actually attempting some exposures.

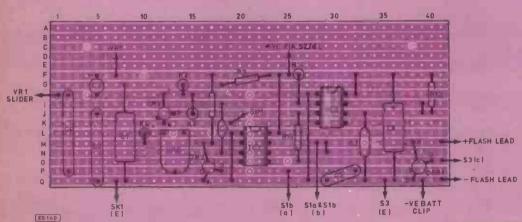
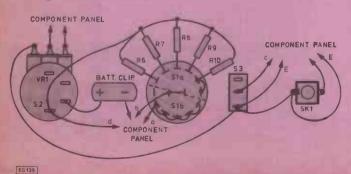
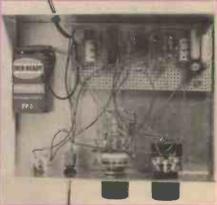


Fig. 3. Stripboard layout of Sound Triggered Flash







Practical Electronics July 1979

COMPONENTS

Resistors

R1	1M5
R2, R10	4k7 (2 off)
R3	470
R4	220k
R5	3k3
R6	91k
R7	47k
R8	18k
R9	9k1
R11	10k
R12	100k
R13	390
Potentiomete	rs
VR1	5k log + switch (S2)
VB2	1M preset

Capa	citors	
01	00	

C1, C8	100µ/10∨ (2 off)
C2	330n (C280)
C3	1µ/10V

(a) Set up the subject, flashgun, microphone, and camera, and adjust the camera's controls for correct focus, etc.

(b) Close the mute switch, turn the unit on, and advance the sensitivity control to the appropriate point (a little experimentation with the unit will show how far the control must be advanced in order to give reliable triggering for various types of shot).

(c) Check that the flashgun is fully charged ready to fire, and that S1 is set for the required delay.

(d) Switch off the light, open the camera's shutter (using the B setting), open the mute switch, and activate the subject so as to fire the flash and take the picture.

(e) Close the shutter before switching on the light, and close the mute switch.

It is not necessary for there to be total darkness when the main lighting is switched off, but there should obviously be a minimal light level and the shutter should be opened for no longer than is absolutely necessary. Where flying objects that represent a hazard to the photographer or camera are to be photographed, adequate safety precautions should be taken, such as eye protection.



C4	470n (C280)
C5	100n mylar 10%
C6	470p ceramic plate
C7	10n (C280)
Semiconductor	s and a straight of the second se
IC1, IC2	555 timer (2 off)
TR1, TR2	BC109C (2 off)
CSR1	Any 200V (p.i.v.) 1A (TO5 case)
Switches	
S1	2-pole 6-way rotary
S3	1-pole 1-way toggle
Miscellaneous	
Metal instrumen	t case approx. 152 x 114 x 50mm
SK1, 3·5mm jac	k socket
2.5mm jack soc	ket (see text)
Flash extension	ead or flash socket (SK2)
Stripboard (0.1ii	n. matrix)
PP3 battery and	connector studs
Two control kno	bs
Wire, solder etc.	

Water cascades from a plastic container shot by an airgun. Non-gimmick applications might include "snapping" nocturnal wildlife, or "freezing" extensometer experiments at the breaking-point



Electronic flashguns usually have a flash duration of less than 1ms and will freeze most action. A computer flashgun used close to the subject or an adjustable flash duration type can produce very much more brief flashes which will freeze virtually any action.

DELAY TIME

All the delays provided by the unit as described here are fairly short, but it is an easy matter to obtain longer delays if desired, by altering the timing resistor values. The delay time in ms. is approximately equal to 0.11R where R is the value of the timing resistor in kilohms (20M Ω max.).

If a continuously variable delay is preferred, S1 can be replaced by a SPST switch so that C5 (and the delay) can be switched out, and the timing resistance can consist of a variable resistor and a 1k resistor in series. The value of the potentiometer is chosen to give the maximum delay that is required.

Market Place

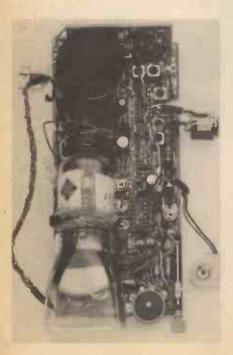
Items mentioned are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

by Alan Turpin

MICROVISION

As you might expect, it's not very big, the screen is only 2in. across the diagonal. The case is $3\frac{1}{2} \times 2\frac{1}{4} \times 7in$. Weight 21oz. including batteries (4 × 1.5V manganese alkaline).

Surprisingly, a fair picture was receivable inside the P.E. office building. In fact the Microvision did not appreciate a very strong signal from our boosted rooftop aerial. The dislike of being connected to permanent



and David Shortland

aerials was noticed by several testers of the set. This did not matter too much as the Microvision's own telescopic aerial proved sensitive enough in most reasonably open places. In one countryside test area, however, it failed to tune in anything in one house but in another, a level half mile away, reception was excellent. UHF reception can be like that.

One point to consider if thinking of buying one of these marvels of the micro age is, how mobile do you hope to be with it? Directional sensitivity of the aerial (in tests around Poole) was such that although undoubtedly portable, even pocketable, the Microvision could not be used easily "on the move". Once set down and directionally tuned, reception was OK.

Now to the question of what you see on a screen so small. While the set was under test the Miss England contest was on. Head and shoulders shots of the girls showed their facial beauty adequately. But a full length line up of the seven girls selected for the final vote showed them so small as to render them unstimulating. On another channel a two person interview was under way. Again, head and shoulders shots were OK but over shoulder two shots were getting visually dull. A football match could be followed but the players' numbers were only just readable.

So, given that technically the Microvision is an eye opener, the important consideration is, how much might you see of your particular televisual interest?

Microvision sells for around £100, with these accessories—a soft carrying case, shoulder strap, earphone, screen hood. An optional accessory pack contains a mains adaptor, external aerial connector, external 12V d.c. battery connector.

Sinclair Radionics Ltd., St. Ives, Cambs. PE17 4HJ. (0480 64646).

TWEETERS TO THE TRADE

Mullard have announced to the trade new lin. dome tweeter loudspeakers. They have a frequency range of 2kHz to 22kHz, high sensitivity and low distortion.

The tweeters have a non-exposed paper dome, and a diffuser within the cover. Horn loading of the dome gives high on-axis sensitivity. The minimum recommended crossover frequency is 2kHz at which the power handling capacity is 50W measured at the input to the filter (6W at the tweeter). At 4kHz, power handling rises to 70W (8W at the tweeter). The operating power (sound pressure level of 96dB at 1m) is 2W. The tweeters, which have a ceramic magnet system, are available with 8Ω and 15Ω impedances.

Trade enquiries to Mullard Ltd., Mullard House, Torrington Place, London WC1E 7HD. (01-580 6633).

MACROVISION

One of the main problems with large screen television has been the low efficiency of the screen resulting in the need to sit in darkened conditions for viewing. The new VS500 television from Mitsubishi has a 50in highefficiency aluminium coated screen which is both angled and concave, concentrating the light and allowing the screen to be viewed in full daylight.



The unit has a pull down front panel on which is fitted a precisely located optical mirror. This mirror reflects the image onto the screen in three primary colours from three separate picture tubes and lenses which are mounted in the consul. The lenses are factory set and require no adjustment by the user.

Remote control of on/off, colour contrast and volume is available as well as normal channel access. On changing channels visual confirmation of the programme selection is flashed onto the corner of the screen for a short period as well as a permanent l.e.d. indicator on the consul of the unit lighting up.

Although the unit is of primary interest to schools, colleges, hotels, conference centres and clubs, the manufacturers also expect large sales in the domestic market where the $\pounds 3,500$ price tag which includes VAT should make it a must for *every* home?

Mitsubishi Electric (UK) Ltd., Otterspool Way, Watford, Herts. WD2 8LD. (0923 40566).

JVC VIDEO SYSTEM

The Video Home System (VHS) which was developed mainly by JVC is the basic system used by many of the major Japanese companies (i.e. JVC, Akai, Hitachi, National Panasonic, Mitsubishi and Sharp). Now JVC have taken their VHS design into the portable market with the HR4100 cassette recorder.

This new unit is fully compatible with all VHS recorders, weighs only 9.3kg (4.2lbs) complete with a cassette, battery pack and RF converter. Also available for use with the HR4100 is a new colour video camera (GC4100) and a separate TV41 tuner-timer which when linked to the HR4100 will provide exactly the same basic recording and playback facilities as deck type VHS units.

The price of the HR4100 is \pounds 799.92, the GC4100 is \pounds 934.20 and the TV41 is \pounds 240.75 (all prices include VAT).

JVC (UK) Limited, Eldonwall Trading Estate, Staples Corner, 6-8 Priestley Way, London NW2 7AF (01-450 2621).

SINCLAIR DFM

The latest instrument to be announced by Sinclair is a digital frequency meter.

The PFM 200 has an 8 digit l.e.d. display with an adjustable accumulation period which gives high resolution from low audio frequencies right up to VHF, without the need for range changing. The guaranteed range is 20Hz to 200 MHz with frequency resolution down to 0.1Hz.

Other applications include, transmitter, audio, digital and RF circuit checking and on video equipment applications include monitoring scanning frequencies and video bandwidths.

The unit is powered from a 9V battery and is supplied complete with test leads and prods, protective wallet and operators manual. Optional extras include a.c. adaptors for 117V, 220V or 240V, de-luxe padded carrying case with lead storage compartment and a connector pack comprising BNC, co-ax, DIN and phono adaptors, plus telescopic aerial for direct signal pick-up from nearby transmitter.

The PFM 200 is priced at £49.80 plus VAT. Sinclair Radionics Limited, London Road, St. Ives, Huntingdon, Cambs, PE17 4HJ (0480 64646).





MINIATURE REED RELAY

Despite a body size of only $28 \cdot 1 \text{mm} \times 7 \cdot 5 \text{mm}$, the Erg AXM1 high reliability reed relay can switch inductive loads of up to 20W. Two versions of this axial leaded, miniature component are available: Form A-s.p.s.t. (normally open), and Form C-s.p.d.t. (changeover). Operational life at full inductive load switching rating is >1 million operations. At low levels, switching life is extended to >100 million operations.

A 10W version in the range offers coil options of 5, 12, or 24V d.c. The Form A standard version has a switching voltage capability of 200V d.c. or a.c. peak at 0.5A (resistive load).

Optional extras include external magnetic shielding and ruggedised mounting clip. Prices from $\pounds 1.08$ (100 rate), delivery usually from stock.

Erg Industrial Corp. Ltd., Luton Road, Dunstable, Beds LU5 4LJ. (0582 62241).



PROTECTING THE INQUISITIVE

Shuttered 13A sockets, manufactured to British Standards, are apparently safe from young prying fingers, but when a three-yearold is inquisitive enough to use a knitting needle to operate the shutter opening mechanism in the earth hole, then perhaps there is a need for extra protection.

Eighteen months ago two worried fathers got together and designed a simple socket cover and showed it to The Boots Company who were sufficiently impressed to market it under their own brand name.

The designers claim that the covers can not be opened by young hands.

As well as protection of the very young the covers are also used where it is important that the supply is not interrupted. On switched sockets the covers do also prevent access to the switch.

The manufacturers, Cima Products, are now producing twin socket covers retailing at around $\pounds 1.25$, single socket covers are around 75p.

Cima Products, 62 Queen Square, Bristol 1. Tel. (0272 28831).

NEW SCOPE

Scopex Instruments have introduced a new dual trace oscilloscope to succeed their 4D10A range. The 4D10B retains the accuracy (\pm 3 per cent) and the d.c.-10MHz bandwidth of its predecessor, but has an enhanced specification made possible by the incorporation of CMOS technology.



In the XY mode Channel "A" is switched into the horizontal deflection system giving fully matched sensitivities for both X and Y axes over the entire 10mV to 50V/cm range. When used in the conventional mode, the vertical amplifiers are complemented by a 16 range timebase of 1µs to 100ms/cm.

Priced at around £188 (ex. VAT) the 4D10B is available from Scopex Sales, Pixmore Avenue, Letchworth, Herts. SG6 1JJ.





FOR the dynamic study of rotating and oscillating mechanical systems, the strobe light is invaluable. It plays a role equivalent to the cathode-ray oscilloscope in electronics, and the principle of both instruments is the same. In the oscilloscope, a periodic input signal is observed for a time corresponding to the sweep of the timebase and at a frequency equal to, or a sub-multiple of, that of the input signal. Similarly, the strobe light illuminates a rotating or oscillating system with very short flashes at a frequency equal to, or a sub-multiple of, that of the system. To the observer, the moving system appears stationary. Unlike the oscilloscope, the strobe light is most often used at a frequency very slightly different from that of the system under investigation, so that the system appears to move very slowly.

A simple, robust and accurate strobe light is described, which operates over the frequency range 22–360Hz with a pulse length variable up to about 400μ S. The use of the instrument is described, and details are given for the construction of strobe discs for any combination of rotational and flash frequencies.

CIRCUIT DESCRIPTION

Fig. 1 shows the complete circuit diagram of the strobe light, and illustrates the simplicity of the system.

The master oscillator is an astable multivibrator using two of the four NAND gates of the CD4011AE connected as inverters. If pins 5 and 6 are at logic "0" (zero volts), then pins 4, 1 and 2 must be at logic "1" (15 volts) with pin 3 at logic "0". Capacitor C1 thus charges through VR1 and R2 until the voltage at point X rises to about 7.5 volts, causing the inverter IC1a to change state. IC1b changes state also, and C1 now discharges, producing the second half of the cycle.

CMOS data books usually quote the formula T = 1.41RC for calculating the period, T, of this circuit where, in this case, R = VR1 + R2 and C = C1. However, the author has never succeeded in verifying this relation experimentally, and instead has used the modified equation:

$$T = 1.96RC + 7.98 \times 10^{-4}$$
 seconds, (1)

which has been determined empirically in the circuit of Fig. 1 by the method of linear regression.

Consideration of the form of equation (1) shows that the frequency variation for large values of R is much less than that at small values of R. In other words, if R is to be a calibrated potentiometer, the frequency scale would be very cramped at the high frequency end. This difficulty is partly overcome by using a potentiometer, VR1, having a logarithmic law. The calibration is still slightly compressed towards the upper frequency end, but not drastically so. The fixed resistor, R2, is used to set the high frequency limit of the oscillator.

The pulse generator is formed by the two remaining gates of the CD4011AE, again connected as inverters. The incoming square-wave from pin 3 is differentiated by C2, R3 and VR2, and the resulting spikes have their negative components removed by D1 to protect the input to IC1c. This inverter produces short pulses to logic "0", the lengths of which are equal to the widths of the input spikes measured at half their maximum amplitude. This can be understood by referring to Fig. 2(a). A positive voltage step is applied at the input: because the capacitor cannot change its charge instantly, the fast-rising step is faithfully reproduced at the inverter input, and the output is thus a voltage step from "1" to "0". As soon as the input step is complete, the capacitor begins to charge up through R and the input voltage to the inverter begins to fall. When this falls to about half of its initial value,

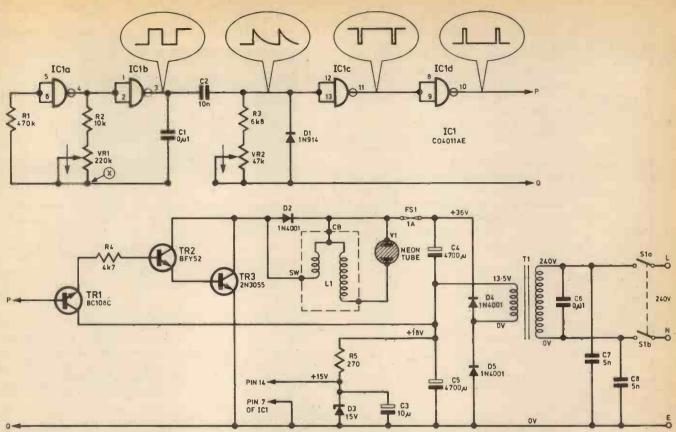


Fig. 1. Circuit of Stroboscope. The arrows on the potentiometers indicate clockwise rotation

the inverter changes state and the output returns to "1". Mathematically, the decay curve of the spike, shown in

detail in Fig. 2(b), is described by

$$v = V e^{-t/RC}, \qquad (2)$$

where e = 2.718, V is the voltage step, and v is the instantaneous voltage after t seconds. Given that the inverter will change state at V/2, this defines a pulse length, t_p , and the equation becomes

$$V/2 = V e^{-t} \rho^{/RC}. \tag{3}$$

$$e^{t_p/RC} = 2. (4)$$

Taking natural logarithms of both sides:

Rearranging gives

$$t_p = \text{RC.log}_e 2, \dots \dots (5)$$

e.e., $t_p = 0.693 \text{RC}$ seconds. (6)

A glance at Fig. 1 shows that the maximum pulse length corresponds to a resistance R = 53.8 kilohms and the minimum pulse length to a resistance R = 6.8 kilohms. C is 10 nanofarad and the extreme values of t_p are:

Maximum $t_p = 0.373$ milliseconds for R = 53.8 kilohms Minimum $t_p = 0.047$ milliseconds for R = 6.8 kilohms

VR2 can, if required, be calibrated by linear interpolation from these extremes.

The pulses are inverted by IC1d and are used to turn on the transistors in the neon drive circuit.

NEON DRIVE CIRCUIT

The neon tube used is the high-intensity type designed for ignition timing applications, and it requires some 3 kilovolts to strike the discharge. This voltage is derived from a standard car ignition coil having a primary resistance of $3 \cdot 19$ ohms and designed for a 12 volt system. Although the cost

of a new ignition coil can be between £6 and £9, a brief visit to a breaker's yard or second-hand shop can secure a working coil for a few pence only!

In this circuit, the coil is energised by the Darlington pair TR2 and TR3 operating as a switch, these transistors being turned on by a current pulse from TR1. The duration of the flash of light from the neon tube is governed by the length of the pulse from the CMOS circuitry. This is made as short as possible to avoid producing blurred images of high-speed movement.

When TR3 switches off, there is a large back-e.m.f. across the primary of the coil which, if ignored, would prolong the flash and increase the dissipation in TR3. To prevent this, the diode D2 is connected across the primary to short-circuit the back-e.m.f.

POWER SUPPLY

The power supply may appear unconventional at first sight, but it is a simple voltage-doubler arrangement which operates in the following way. During one half-cycle, D4 charges up C4 to 18 volts; during the next half-cycle, D5 charges up C5 to 18 volts. Because C4 and C5 are in series, the maximum voltage is +36 volts, with a second supply at +18 volts.

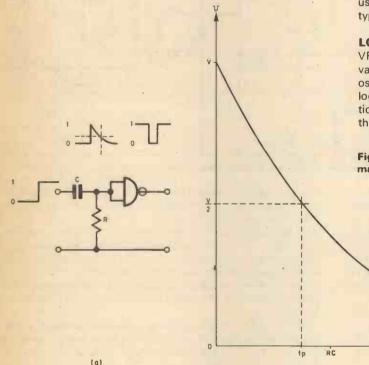
The reason for using a 36 volt line for the coil supply is explained in the next section. The 18 volt supply feeds TR1 and a simple Zener diode stabiliser for the 15 volt CMOS supply. C6, C7 and C8 act as a suppressor to limit mains-borne interference.

SPECIAL POINTS

Ignition coils are very robust—they have to be in order to operate successfully in the automobile environment: some ballasted types dissipate about 80 per cent more power than normal during starting; a capacitive discharge ignition system charges up a 0.5 microfarad capacitor to about 500 volts and then discharges it across the 12 volt coil primary.

In this strobe light, the coil is being treated very gently, although the use of a 36 volt line to energise the coil may raise a few eyebrows! It will be shown that all is quite normal, however.

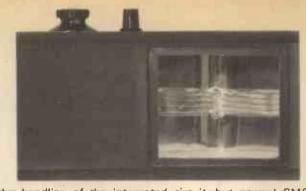
It is often difficult for the amateur constructor to measure the parameters of a pulse circuit such as this, operating at a low duty cycle. The use of a reactive load complicates the issue even more, so this article concludes with calculations of circuit parameters, and proves that the coil is not being over-run by the 36 volt supply. While the instrument can be set up with an ordinary multimeter capable of measuring d.c. up to 1 ampere, calibration is not quite as straightforward. Three methods of calibration are described in the Appendix, one requiring no test equipment other than a record-player turntable and a strobe disc, the construction of which is described later.



The voltage induced in the secondary of the ignition coil is proportional to the rate of change of current in the primary. Under normal circumstances, a mechanical switch (the contact-breaker) provides the current steps from a lowresistance source (the car battery). Using a power transistor as an electronic switch is the major limitation in producing a high e.h.t. The switching time is relatively slow and, with a supply of 12 volts, the e.h.t. is not high enough to strike the neon discharge, i.e., it must be less than 3 kilovolts. However, it is roughly true to say that the switching time is independent of the supply voltage, so that if the supply voltage is trebled, so is the rate of rise of primary current, and thus the e.h.t. is trebled also. Using a 36 volt supply produces a peak e.h.t. of about 4 kilovolts. Such voltages could prove dangerous and proper care should be taken if taking measurements.

CONSTRUCTION AND SETTING UP

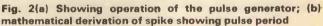
Layout of the components is shown in Fig. 4. Fig. 5 gives board assembly. Using a 14-pin d.i.l. socket will minimise

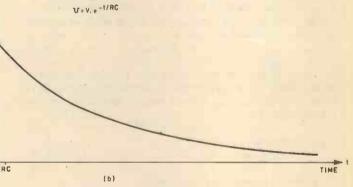


the handling of the integrated circuit, but normal CMOS handling precautions should still be taken. The power supply should be tested first, together with the 15 volt supply for the CD4011AE. If more than 15 volts are present on pin 14 of the d.i.l. socket, the Zener diode should be replaced by one giving slightly less than 15 volts. If a CD4011BE is used, the replacement is unnecessary because this device type is rated at 20 volts, not 15 volts.

LOG LAW

VR1 is a logarithmic law potentiometer and is used as a variable resistor to control the frequency of the master oscillator. It must be connected correctly to make use of the logarithmic law. The arrows on the sliders of the potentiometers in Fig. 1 indicate clockwise rotation, and enable the constructor to wire these correctly. As VR1 is rotated

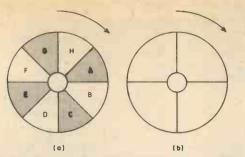


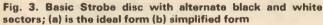


clockwise, the frequency decreases; as VR2 is rotated clockwise, the pulse length increases.

It is important to ensure that the 2N3055 transistor is being driven fully on by each pulse. If this check is not made, it may be found that this transistor runs very hot and that the e.h.t. is low. Put a multimeter (1 ampere d.c. range) in series with the coil primary and record the current at any frequency setting but at the maximum pulse width. Replace R4 by a 2·7 kilohm resistor and record the current again. If there is no change, the original value of R4 is correct. If the current is higher in the second case, increase R4 from 2·7 kilohms until the current begins to fall. The correct value is the highest resistance that will give the maximum current. This adjustment is necessary to compensate for the gain spreads of TR2 and TR3.

Although the voltages used in this circuit are relatively low, due respect must be paid to the e.h.t. on the neon tube. A simple, flat, parabolic mirror can be made from polished metal to direct the light, and a glass or Perspex front cover protects the fragile neon tube.





USES

Uses of the strobe can be enumerated as:

1. Almost any rotating system can be studied in motion using the strobe light. Any irregularity of motion is instantly revealed.

2. The speed of rotation of a system can be measured directly with the strobe light, subject to the precautions mentioned later governing multiple-order solutions to equation (10). If the speed of rotation is inconveniently slow, a stroboscope can be used on the system to enable a much higher pulse frequency to be used.

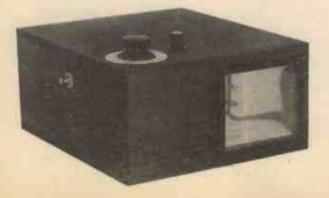
3. Vibrating systems, too, are interesting to study: a very common system which can be very spectacular when strobed is the loudspeaker cone. Excessive excursions are easily visible, and the loading effect of various waddings in a bass enclosure can be simply judged.

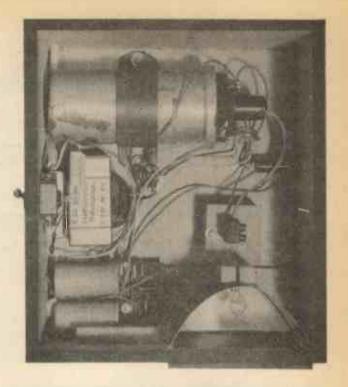
4. The use for which this strobe was originally designed was the study of a suspected defective claw action in an 8mm cine camera. A flash frequency of 16 hertz was used initially, but this was too slow to be observed comfortably. Instead, an integral multiple of 16 hertz was used: this produced a multiple image of the claw, which was much easier to study.

5. There is an unlimited number of uses for the strobe light, but as an example of the capability of the instrument, it was used to illuminate an electric power drill, the chuck of which was rotating at 3,600 r.p.m. With a flash frequency of 60 hertz and a narrow pulse width, the engravings on the chuck appeared indistinguishable from those on the stationary chuck!

CONSTRUCTION OF A STROBE DISC

Fig. 3 shows a basic strobe disc, a circular card with alternate black and white sectors. (a) is the ideal form, having equal black and white sectors; (b) is a simplified form which can be used successfully with the strobe light because of its





very short pulse duration. The artistic effort involved in (b) is considerably less, however! The strobe disc is attached to a rotating system and, if illuminated by a strobe light, will appear stationary if the speed of rotation bears a simple relationship to the strobe frequency. (If the strobe disc is intended for subsequent use under a.c. mains light. the type shown in Fig. 3(a) must be made.) To calculate the number of black lines (or sectors) required, the following procedure is very simple.

Suppose the strobe frequency is f hertz, that the strobe disc rotates at n revolutions per second and that there are p black lines (sectors) on the strobe disc.

Time between consecutive flashes = 1/f seconds. In 1/fseconds there are n/f revolutions and np/f black lines (sectors) pass a given point in this time.

To obtain a stationary display, sector A must exactly replace sector C in 1/f seconds; in other words, one black line (sector) must pass a fixed point in 1/f seconds.

i.e.,
$$\frac{np}{f} = 1$$
 for a stationary display. (7)

Therefore the number of black lines or sectors, p, is given by $p = \frac{1}{n}$

The strobe disc used in the Appendix for calibration purposes is designed to appear stationary on a 331 r.p.m. turntable viewed under 50 hertz mains lighting. This will now be used as an example to calculate p.

A trap for the unwary is immediately revealed in deciding what value of f to use as a.c. mains light has a flicker component of 100 hertz (one peak and one trough per cycle producing two equivalent light peaks), so this is the value of f to use.

f = 100 hertz and $n = 33\frac{1}{3}/60 = 100/180$ revolutions per second.

 $p = 100 \times \frac{180}{100} = 180$ black lines or sectors.

Many people will have such a strobe disc already, but for those who have not, and to whom the artistic effort involved seems daunting, most record shops will be able to provide one.

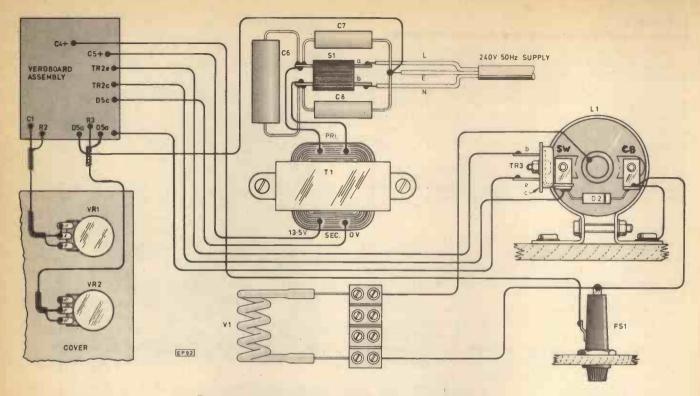


Fig. 4. Component layout and connections to Veroboard

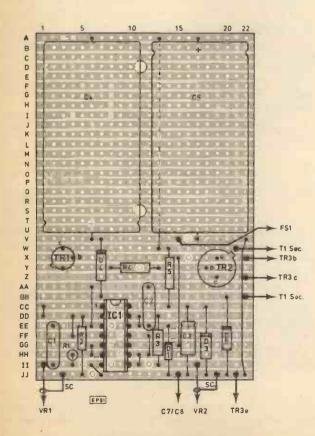
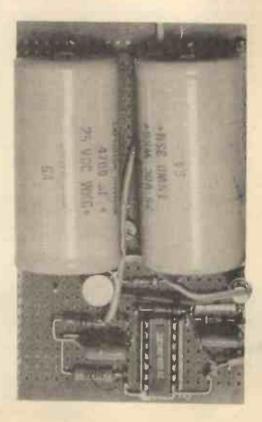


Fig. 5. Veroboard layout



APPENDIX

Measurement of peak current and power dissipation

The only item necessary for these measurements is a noninductive resistor. Copper wire, having a resistance of less than one ohm, is folded back on itself so that the two ends are together and is wound in this configuration on any convenient former. The resistance is measured by averaging a series of voltage and current readings. The current must not be so large that it heats the wire, as it is the "cold" resistance which is required. It is then soldered in series with the coil primary.

Resistance of non-inductive resistor= 0.73 ohmsResistance of coil primary= 3.19 ohms = RPeak voltage across non-inductive resistor = 3.30 volts(measured on oscilloscope)

Peak current = $I = \frac{3 \cdot 30}{0 \cdot 73} = 4 \cdot 52$ amps during a pulse. Peak power dissipated in coil = $I^2 R$

= 65.2 watts during a pulse.Peak current through 2N3055 = 4.52 amps.

Collector-emitter saturation voltage (from tables) = $1 \cdot 1$ volts at 4 amps

> Peak power dissipated in 2N3055 = 4.52×1.1 = 5.0 watts during a pulse.

COMPONENTS ...

Resistors

R1	470 k
R2	10 k
R3	6k8
R 4	4k7
85	270

Capacitors

C1	Oµ1, polyester
C2	10n, polyester
C3	10µ, 25V elect.
C4, 5	4,700µ, 25V elec
C6	0µ1, 1,000V
C7,8	5n, 1,000V

Potentiometers

VR1	220k,	logarithmic
VR2	47k	linear

Semiconductors

IC1	CD4011AE
TR1	BC108C
TR2	BFY52
TR3	2N3055
D1	1N914
D2	1N4001
D3	BZY88 C15V Zene
D4,5	1N4001

Miscellaneous

L1	Car ignition coil, 12V type, 3.19 ohms primary	
	resistance.	
V1	Neon flash tube (type FT3, Service Trading Co.).	
FS1	1A fuse with holder.	
T1	Mains transformer. Secondary 13.5V (or 15V)	
	r.m.s., 2A.	
S1	Double pole mains switch.	

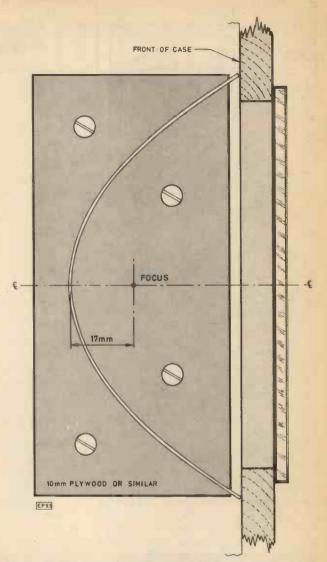


Fig. 6. Template for forming plywood parabolic profile in which reflector is placed. The lid has similarly shaped pieces attached

The average dissipations and current are derived from the peak figures by multiplying by the duty cycle. Obviously this is variable, so the maximum figure is used, corresponding to the highest frequency and the maximum pulse length.

From equation (1), minimum

period	$= 2.76 \times 10^{-3}$ seconds
From earlier calculation,	
maximum pulse length	$= 3.73 \times 10^{-4}$ seconds
Maximum duty cycle	$=\frac{3.73 \times 10^{-4}}{2.76 \times 10^{-3}}=0.135.$
Maximum average current	$= 4.52 \times 0.135 = 0.61$ amps
Maximum average coil	
dissipation	$= 65.2 \times 0.135 = 8.8$ watts
Maximum average	
2N3055 dissipation	$= 5.0 \times 0.135 = 0.68$ watts

These figures show how conservative the ratings really are, and why the power transistor does not require a heat sink. It runs barely warm at maximum dissipation: under normal conditions of use, the maximum duty cycle is only required in the very brightest ambient lighting conditions.

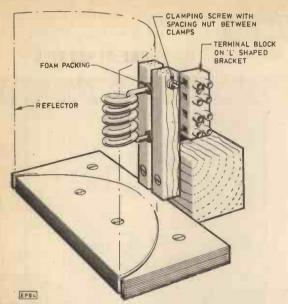


Fig. 7. Showing neon fixing. The centre of the helix forms the reflector focus

Calibration of the strobe light

Three methods of frequency calibration are given here so that all constructors can calibrate their instrument, whatever test equipment they possess.

1. Using a frequency counter. This method is trivial when such an instrument is available. A rectangular 15 volt pulse is present at pin 10 of IC1. Some frequency counters do not perform well when counting narrow pulses, so a second measurement may be worthwhile on the 50:50 square wave on pin 3 of IC1.

2. Using an oscilloscope. Lock the timebase externally to the 50 hertz mains frequency, and set the sweep duration to 40 milliseconds. The timebase will now be running at 25 hertz. With the master oscillator of the strobe at minimum frequency, feed the signal from pin 3 of IC1 into the Yamplifier and slowly increase the strobe frequency. The first stationary waveform obtained should be one cycle of a square wave. The frequency control can now be marked 25 hertz. Increase the frequency until the next stationary waveform appears. There should now be two cycles on the trace. Mark this point 50 hertz. Proceed in this manner, marking the points 75, 100, 125, 150, 175 hertz, and so on. The strobe is now calibrated.

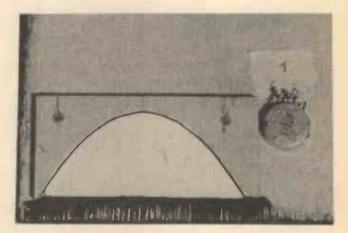
3. Using a strobe disc. This method requires no apparatus other than the turntable of a record player and a strobe disc for $33\frac{1}{3}$ r.p.m., described earlier. The calibration is just as accurate as that obtained in method 2; the only drawback is that the calibrated spot frequencies are rather strange values and not the regular series produced by the oscilloscope method. Nevertheless, to be able to calibrate a scientific instrument without recourse to any standard measuring instrument is quite a rarity, and will be appreciated by many constructors.

With the strobe disc on the turntable rotating at $33\frac{1}{3}$ r.p.m., switch on the strobe light at maximum frequency and adjust the pulse length to give a comfortable level of illumination. Reduce the frequency slowly, while watching the strobe disc, until a stationary display is seen. Mark this position of VR1 100 hertz, the design frequency of the stroboscope. Reduce the frequency further until another stationary display is seen: mark this position 50 hertz. Repeat twice more, marking out 33.3 hertz and 25 hertz.

 Table 1

 A list of the calibration frequencies in hertz

33 ¹ / ₃	45	78
100.0	135.0	234.0
50.0	67.5	117·Q
33.3	45.0	78.0
25.0	33.8	58.5
	27.0	46.8
	· · · · · · · · · · · · · · · · · · ·	39.0
_		33.4
		29.3
		26.0
		23.4
	50·0 33·3	50.0 67.5 33.3 45.0 25.0 33.8



Parabolic groove in lid to take top of reflector

There are several stationary displays because the rigorous solution to equation (7) is

$$\frac{np}{s} = 1, 2, 3, \dots$$
 (9)

This can be seen by reference to Fig. 3(a). The first order solution corresponds to sector A replacing sector C in 1/f seconds. The second order solution corresponds to sector G replacing sector C in 1/f seconds, and so on. Because f is the only variable in this experiment, the frequencies for stationary displays are given by the formula

$$f = \frac{np}{1, 2, 3, \dots}$$
 hertz, (10)

hence the four calibration points above. Two further series of calibration points can be provided by running the turntable at 45 and 78 r.p.m. with the same stroboscope. A full list of calibration frequencies is given in Table 1.

A fourth method of calibration, which the author found surprisingly successful, is to use equation (1) together with the logarithmic law of VR1 to produce a theoretical frequency scale for VR1. However, anyone attracted by this method should first derive his own form of equation (1), because the linear regression method absorbs any deviation of C1 from 0.1 microfarads into the "constant" term, 1.96.

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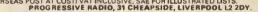
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Transducer 79 + Testamex 79—June 19–21. Wembley Conference Centre. Details: Trident.

Great British Electronics Bazaar-June 28-29, 1979. Alexandra Palace. Details: 0799-22612.

1979 Microcomputer Show (incorporating the DIY Computer Fair)— July 5–7. Bloomsbury Centre Hotel, London. Will include seminars. Further details: Online Conferences Ltd. Tel: Uxbridge (0895) 39262.

Consumer Electronics Symposium—July 8–11, 1979. University of Essex. Organised by the Society of Electronic and Radio Technicians. Details: The Symposium Office (CE), SERT, Faraday house, 8-10 Charing Cross Road, London WC2 0HP.

The International Word Processing exhibition and conference will take place July 10–13, 1979, at the Wembley Conference Centre.

Details: BETA Exhibitions, Business Equipment Trade Association, 109 Kingsway, London WC2B 6PU.

B.A.E.C. Amateur Electronics Exhibition—July 21-28. Held at the shelter at the centre of the Esplanade, Penarth, South Glamorgan. The exhibition is to display a large number of projects and electronic games made by members, and will be open every evening from 7 p.m. (except Sunday, July 22), and also on the afternoons of July 21, 22 and 28. Proceeds go to the Cancer Research Campaign.

Harrogate International Festival of Sound—August 18–19 (public), August 20–21 (trade) 1979. The Exhibition Centre + hotels. Details: Exhibition and Conference Services Ltd., Tel: 0423-62677.

Telecom '79—September 20–26. Palais des Expositions, Geneve. Details: Secretariat Telecom '79, Orgexpo, 18 Quai Ernest-Ansermet, Case Postale 65, CH-1211, Geneve 4 (Suisse).

Eltro Hobby '79—October 3-7. Killesberg Exhibition Grounds, Stuttgart. Details: 01-236 0911.

Compec—November 6-8, 1979. Grand Hall, Olympia, London. Details: Iliffe Promotions Ltd. Tel: 01-261 8437/8.

Electronics 79-November 20-23. Olympia, London. Details: 021-705 6707.

Breadboard 79—December 4-8. Royal Horticultural Halls, Westminster. Details: Trident International Exhibitions. Tel: 0822 4671.

IEA/Electrex—February 25-29, 1980. National Exhibition Centre, Birmingham. Details: Industrial and Trade Fairs Ltd. Tel: 021-705 6707.

Communications '80—April 14–18. National Exhibition Centre, Birmingham. Details: ITF Exhibitions. Tel: 021-705 6707.

All-Electronics Show (1980)—April 29-May 1. Grosvenor House, London. Details: 0799-22612.

ADVANCED CTV CHASSIS—THE TX9

N developing the TX9 single board chassis, Ferguson (Thorn Group) believes it has achieved a major breakthrough in colour television technology.

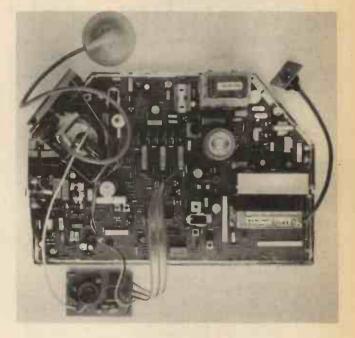
The chassis will drive a new range of 14, 16, 18 and 20in. screen sizes without the need for any component or electrical changes.

The five circuit elements which contribute most in reducing component count consist of a tiny surface acoustic wave filter which replaces most of the conventional i.f. coils; a luma chroma processor single chip which alone amplifies and decodes picture control and colour information; an e.h.t. diode split transformer acting for horizontal output, focus and e.h.t. rectification; an integrated field time base i.c. and an efficient, compact thyristor p.s.u.

Designed from the outset to be assembled automatically, the TX9 chassis is made up with 70 per cent of its components tested, inserted, and tested again on board. This ensures a reliability and subsequent high performance, which is further endorsed by the considerable reduction in preset adjustments required.

Servicing the TX9 will inevitably result in savings since component replacements are common across a range of receivers meaning that less money need be tied up in spares.

The first television featuring this chassis in a new series (which will replace the 9000 family) is the 14in. model 3755 portable which is expected to be released to the trade in September, 1979. No price has yet been fixed.



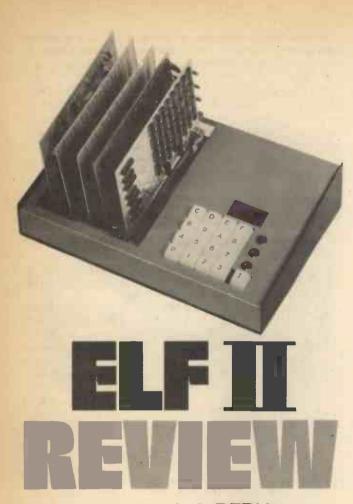
TELEPHONES 1984 STYLE

A FRENCH company called Norex produce a telephone accounting system which has now arrived in this country via Rack Data Ltd. of Bourne End, Bucks. It's a case of "watch out!" on those personal telephone calls made at work, because this system is the answer to a prayer for companies who suffer from indiscriminate or excessively liberal use of telephones by employees. On the other hand, if you like ringing up your aunt in Australia at the company's expense, it could be the end of the road. The system can monitor details of every call on every extension. The picture below says it all!

The "Teleprintex" comprises a CPU which can be connected to any PABX telephone; it is self-testing, and in the event of a failure can indicate the location of the fault by printout. The printer documents recorded data on up to 15,000 telephone calls from a floppy disc, at the request of a separate keyboard unit.

A system costing £6,500 can support 20 lines and 128 extensions.





A.A.BERK B.Sc. Ph.D.

The Cosmac Elf, from Netronics R & D Ltd, has been one of the most popular and fastselling small systems produced for the hobbyist in the U.S.A. It is based around the RCA 1802 MPU which has some remarkable features. H.L. Audio (advertising in this issue) are marketing the machine in this country and they kindly lent one to our contributor for review.

THE exciting feature of this system is its modularity. This implies at least two important advantages. Firstly, the buyer can start with a very basic, and hence cheap, system and slowly build it up as interest and financial outlay permit. Secondly such a system is usually more expandable, fundamentally, than a oneboard computer. Both of these advantages are most admirably inherent in the Elf, and while there are disadvantages described below, these two points should be borne in mind throughout.

I shall describe the various parts and levels of the machine in the order they would normally be purchased.

An interface to the P.E. VDU is also described.

BASIC MACHINE

This consists of a double-sided, plated-through p.c.b. of excellent quality in every way, containing a hexadecimal key-pad, five spaces for expansion sockets, the MPU, clock and support logic plus four other switches labelled: I, RUN, LOAD and M/P.

The 1802 is an 8-bit processor with sixteen 16-bit, two 8-bit, four 4-bit and three single-bit internal registers. Any of the 16-bit registers $(R(1), \ldots, R(F))$ may be used as the Program Counter to tell the MPU where to find the next instruction. In addition to holding addresses during the execution of a program, each Rregister may be regarded as two separate 8-bit registers, thereby providing, on-chip, 32 bytes of scratchpad RAM, selectable by unique machine-code instructions.

Referring to the 1802-architecture diagram, R(0).1 denotes the high byte of register R(0) and R(0).0 the low byte, and similarly for the other registers. The diagram shows all the other registers provided with the number, in brackets, of bits in each.

D (8 bits) is the most important of these and is a pretty typical accumulator through which all logical and arithmetic commands must be routed as well as all load and store instructions. Operations such as "LOAD", "STORE", "ADD" etc automatically refer to D and may not be used directly for the R-registers. This creates the restriction, for instance, that R-registers must be loaded with data (or even cleared) by loading D first and then one half of R, followed by D again and the other half.

The greatest restriction is the lack of ability to call subroutines by an 1802 instruction code. This does not restrict the power fundamentally, but makes it harder to perform the same activity. Despite such restrictions (as well as having only one accumulator) the 1802 is very rich in conditional branch and skip instructions and includes some branches which depend upon the state of a set of external flag lines ($\overline{EF1}, \ldots, \overline{EF4}$). These may be set by any external hardware from simple switches to the complex workings of one's kitchen appliances.

There is an I/O flip-flop (QFF) whose state may be set and checked by machine code instructions and is connected to a l.e.d. on the Cosmac Elf. It is this line which is used for the serial communications such as cassette. The 1802 is perfectly fitted for hardware control purposes and it is surely in this area that its best use is realised. There are so few pins left after the above is taken care of, that the 16 Address-Bus bits have to be multiplexed onto 8 address lines and must be latched at the appropriate time by external devices to address more than 256 bytes of addressable memory. Such latching gives the usual 64K maximum memory addressing.

Direct Memory Access (DMA) and interrupts are very well catered for on the 1802 and are used to their maximum advantage by the Elf system. The hex keyboard uses this structure as follows.

Each time a byte (2 hex digits) is to be loaded into the

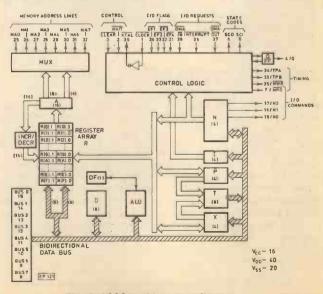


Fig. 1. 1802 architecture diagram

machine, the appropriate two hex keys are pressed, one after the other, followed by the I (input) button. The two key-pressings are latched into two 4-bit buffers, and if more than two keystrokes are made, only the final two strokes are remembered, allowing the user to change his mind before I is pressed.

Pressing I causes a DMA request to pass the latched byte into whichever address the machine is set up to access at that instant (stored in R(0)) and causes the 2 hex characters to be displayed on the 7-segment l.e.d. displays.

In order to load a program into any computing machine, the correct address block must be accessed (location by location) as the program is fed in, and a Program Counter set-up with the start-address of that block. This will force execution of the program "from the top". This procedure is technically termed "loading" and is normally performed on a computer by some resident program—which has to remain in ROM to prevent its demise upon switch-off—and is termed a Loader.

It is provision of such firmware for a machine which causes many of the headaches associated with system design. Does one provide a very low-cost and simple firmware monitor, of which the user will tire very easily, or a complex, sophisticated and expensive program which influences and demands a great deal of the hardware design of the entire system?

The interesting point about the 1802 is that there is a simple loader on the chip itself and this is used by the basic Elf board. It has no ROM, only RAM (256 bytes).

Access to the loader- which only loads sequentially in memory from address location 0000 is created by a pulse on the LOAD input to the MPU provided by the load switch next to the keyboard. Also included on the chip is the ability to run the program thus loaded.

The only other switch provided is a memory protect switch (M/P), which protects RAM from being overwritten and is used during viewing of the information loaded into the memory block based at 0000 without destroying it. M/P is part of the Elf's keyboard and display circuit and is not a part of the 1802's structure. In this way, also, any other program block in memory may be executed—from a hardware control routine to a BASIC interpreter—simply by loading into 0000 the instruction CO (a jump command), followed by the address of the start of that program block. This is exactly how the expansions to the basic system—right up to BASIC and beyond—are implemented.

TV CHIP

The other feature of the basic board is the 1861 chip, which is a TV Controller and may be used to display on a TV, the bit patterns (as short black and white lines) stored in a block of 256 bytes of memory. Letters and characters may be displayed on the screen by placing the right 1s and 0s in a block of memory. In this way, the 1861 claims to provide a video "graphics" interface to the computer and, while going part of the way, it is rather cumbersome and crude and provides characters too large, and too slowly, to be of more than educational use. However, the TINY BASIC language, described later, does use this aspect of the Elf for its output and gives an excellent introduction to BASIC in general.

As a consequence of the simple nature of the TV chip, the bandwidth necessary for its display, through a u.h.f. modulator, is quite restricted. It was thus found that the cheaper version of the ASTEC modulator provided an excellent output to the TV's aerial socket, even when working on +5V. The power supply on the basic board is set up to accept a.c. or a raw +8V d.c. supply and H-L Audio sell an excellent p.s.u. which will deliver +8Vplus all the other voltages needed for the various expansions available. The regulator and smoothing circuit of the basic p.c.b. provides power for the basic board itself plus one of the expansion connectors—other expansion boards must have their own regulators and smoothing capacitors, as on any well-designed motherboard system.

In order to fully appreciate and use the 1802 machine code, a book is very important—Tom Pittman's "A Short Course In Programming" is an excellent example and is available through H-L Audio.

The basic board provides the first-time buyer—perhaps having little or no technical background—with an excellent introduction to everything from basic soldering to the full complexity of machine-code programming. However, it is very restricted and fiddly to program and one very soon needs to access parts of memory randomly with the ability to store on and load from cassette. This takes the user to the next level of the machine.

GIANT BOARD

As mentioned above, one of the five expansion sockets is fed from the basic board's regulated +5V supply—this is the far left socket which allows one to plug in Netronics' Giant Board.

I would certainly say that the full potential of the machine cannot be appreciated without having expanded at least to this level, and many may decide to go no further.

Included in the board is, first and foremost, a 256 byte block of ROM containing a system monitor which is stated to occupy the memory locations F000 \rightarrow FOFF. In fact, only the upper 4 bits and the lower 8 bits of the 16 address-bus bits are demultiplexed off for decoding and the monitor is thus resident as *copies* in sixteen pages of memory and may be contacted in blocks starting at any of the following addresses: F000, F100— FF00. The Giant Board also contains the all-important cassette interface. Other components of the Giant Board are as follows.

One 8-bit parallel input port is provided into which an ASCII keyboard fits for the use of BASIC.

One 8-bit parallel output port is provided for userconfiguration as well as a set of 14 lines used to communicate with I/O devices via dedicated machine-code instructions within the 1802. This lends further weight to the argument that the system is excellently fitted for hardware control applications.

An RS232C interface and a 20mA loop are also included, and these interfaces, of course, need supplies in addition to the standard +5V provided.

The System Monitor allows the user six commands for program development and cassette storage. To place the machine in a mode whereby the System Monitor has control, the user must perform the following actions.

Run, Load and M/P must be set off—this is a system reset normally given by a reset button on other machines. Load is then set on which begins the 1802's loader function at the address 0000. A jump instruction (CO) is then typed, followed by the Input button. CO appears on the display and the 1802 is ready to load address 0001. In this address is loaded the upper byte of the start address of the monitor (FO, normally, though F1, F2 etc, will do), then the lower byte (00) and Load is set off. This little block of 3 bytes is a small program in itself and is executed by setting Run on. It causes a jump to the system monitor which then waits for one of its six commands:—00, 01, 02, 03, 04, 05. They have the following meanings—

00 followed by an address will run a program starting at that address.

01 plus an address allows examination of the contents of that address and pressing "I", repeatedly, displays the subsequent locations' contents.

02 is similar but allows modification of contents-for loading and editing programs etc.

03 and 04 allow cassette writing and reading of programs.

05 gives the facility for searching memory for a given byte.

It is at this point that one has a real computer at ones disposal. Each time a command has been used, the Run switch is turned off and then back on and the same jump is executed to

the Monitor.

To execute any program, either a jump from 0000 or the 00 command in the monitor can be used.

The cassette interface was found to be very fast—it timed at around 1200 baud and will write or read a 1K block of memory in around 8 seconds. This speed is essential for the Elf as all its software expansions are provided on tape and would be very cumbersome on a slow interface.

4K RAM

To run any of the software expansions such as BASIC, Elfbug (a machine-code monitor using the TV interface) etc, or to write larger, more complex programs, more memory is essential. There are two main ways to expand the memory. The "doit-yourself" man could buy one of the prototype boards, which plug into the expansion sockets and wire up his own memory. He would need some address decoding to make it sit in the correct place in the main memory map of the machine, some latches to demultiplex off the upper byte of the address and an on-board +5V regulator.

However, standard 4K memory boards, using 2102s, are available with latches, address decoding, regulator and jumpers to allow the user to choose their memory positions.

One such board of 4K RAM is necessary for BASIC and very useful for Elfbug, which is described below.

The main limitation of the basic l.e.d. display is that full addresses plus contents cannot be displayed. Elfbug uses the TV interface to display a block of 24 locations, plus addresses, which may be modified on the screen. The hex characters are very large and extremely readable. Elfbug also allows the registers to be displayed, execution of a program and the setting of breakpoints.

The monitor itself takes up $\frac{3}{4}$ K of storage but requires 416 bytes workspace, in addition, to run the TV interface. It is loaded from cassette and is fully relocatable-though its position in relation to user-workspace is critical.

BASIC

The TINY BASIC interpreter available is described below and requires a minimum of 4K RAM normally configured to lie from address 0000.

The space taken up by the interpreter is from 0000 to 0F3F and includes the video display buffer (contents of the TV screen at any instant), various flags and 26 variable contents (variables are capital letters only from A to Z). This, then, is a RAM based BASIC and may not reside in ROM or EPROM.

In addition to 4K RAM, an ASCII keyboard is required and is available to plug directly into the Giant Board—H-L Audio's Power Supply Unit is fitted out to supply the requirements of this keyboard.

With the original 256 bytes of RAM on the basic board (which is relocated above the 4K RAM), the user has 384 bytes available for BASIC programs—quite adequate for many simple routines.

The BASIC is an integer version allowing calculations in the range -32768 to +32767 and has no scientific functions or exponentiation: +, -, X and \div are all that are provided.

PEEK and POKE exist, as does a call to a machine-code routine via the USR function. No FOR - - - NEXT statement is provided which is a great restriction; and, of course, no strings or arrays.

PRINT, IF --- THEN, GOTO, LET (which is optional), **INPUT, GOSUB, SAVE** and LOAD (for cassettes), etc. are included and will provide an excellent introduction to this high level language.

In order to gain the most from the language, Tom Pittman has written an excellent book "Introduction To Programming In TINY BASIC" and is essential for the beginner.

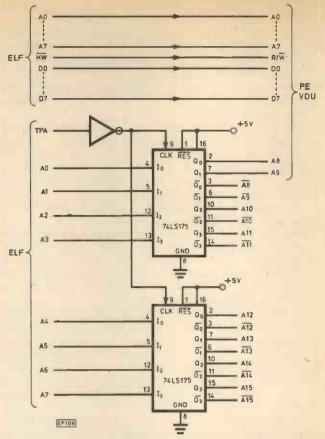


Fig. 2. Simple interface

The ASCII keyboard used with the BASIC is of good quality and when mounted in a cabinet looks pleasant and handles very professionally. The photograph shows the complete system mounted in the available cabinets which, with their blue IBM colours, look most attractive.

The BASIC ASCII characters, as displayed on the TV via the 1861 TV chip, are, in some cases, a little difficult to read at first—very few of these large letters can be fitted on the screen comfortably and hence only a small portion of one's program is available for viewing. However, the display is adequate and will provide an excellent start into the field of computing.

As usual with American machines, the TV frame frequency is 60Hz and, though the majority of UK TVs have the flexibility to adapt, I had trouble with a Philips TV which had no adjustment for frame hold or line hold.

PE VDU INTERFACE

In order to circumvent the problems of the video interface, or to provide a powerful addition to the machine, a more professional VDU may be added. The asynchronous VDUs on the market which communicate via a RS232 interface are quite appropriate at this point but a cheaper, and more flexible, alternative is to use the memory mapped PE VDU. The interface is quite simple and provides a 1K block of RAM for display on a TV screen. See Fig. 2.

The VDU requires 10 address lines (A0, ..., A9), a read/write line (R/\overline{W}), two select lines (\overline{ENABLE} and \overline{VDU} SELECT) and eight data lines (D0, ..., D7). The data lines are connected straight through and R/\overline{W} is taken from \overline{MW} on the Elf. A0, ..., A7 are also connected straight through, while the upper address lines (A8 and A9) and the address decoding have to come from latches as shown in the diagram. Here, two 74LS175s are used to demultiplex off A8, ..., A15 as well as



(Above) Elf II with 16K RAM on board, professional ASCII keyboard and power supply

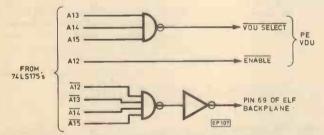


Fig. 3. Circuit for fitting in RAM and VDU. Addresses are PE VDU from EX00, RAM from OX00 (for any X)

their inverted values—this makes address decoding a little simpler. The rising edge of TPA is the signal for the high bits of the address on A0, \ldots , A7 to be latched and this is converted to a falling edge for the 74LS175s and fed to their clock inputs. The outputs from the 74LS175s are used to decode the VDU RAM into the desired position.

The 256 bytes of RAM on the basic board must be decoded too, to prevent them 'from interfering.

If the VDU is to be used with the Giant Board only, the diode CR4 on the Giant Board should be lifted to free the 256 bytes of RAM for external decoding. Pin 69 of the backplane of the motherboard is then the enable (active high) for this block of RAM.

FX00 (for X anything) is the enable address for the Monitor and there is a large variety of circuitry which will fit the RAM and VDU in without conflict. One is suggested in Fig. 3.

Here, 0s on A12, ..., A15 give 1s on $\overline{A12}$, ..., $\overline{A15}$ and hence enable a four-input NAND gate to give a zero which is inverted to enable the RAM block. This is addressed, therefore, from 0X00 (any X). The VDU is enabled by A13, A14, A15 high and A12 low—giving EX00 as base address. This circuit should provide enough information for any adaptations to be made for the addition of further Elf family devices, such as 4K RAM boards; and, at the same time, provides all 16 address lines for any other use.

Fitting the *PE VDU* into the system will add greatly to the potential of the machine—perhaps someone will have a go at adapting the TINY BASIC to run via the VDU instead of through the 1861 chip.

DOCUMENTATION

Each board and expansion has its own details and manual included and each hardware addition has carefully written constructional details of a very adequate nature. The documentation with the software tapes, I found excellent, though, perhaps, for the absolute beginner, should be supplemented by one of the books, as mentioned above.

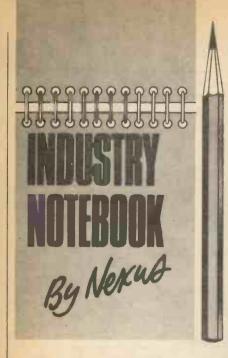
The hardware, I feel, is rather inadequately described. The absolute beginner would find it difficult to learn very much about hardware from the circuit diagrams included in the manual—the knowledgeable hobbyist will, of course, have little trouble, if used in company with the 1802 design data and some CMOS data books.

CONCLUSION

This kit, rather than forming a sophisticated personal computer, is ideal for the person who wishes to start without a large outlay, but wants to keep the options open for expansion. It is also excellent for anyone wishing to evaluate the 1802 for dedicated control purposes—though to write a long control program, an assembler would be very useful.

This last is, apparently, on its way, along with an assembler and a new video display board. All in all, most compatible with the tradition of expandability set up by the basic Elf.

The PE VDU is available solely from Technomatic, 17 Burnley Rd, London NW10.



A wareness

Last month I reported that schools and universities in France were to receive 10,000 microcomputers and teacher training in their use to help ease the young into the MPU era. Now the Americans have launched their Basic Computer Literacy Programme. Under this scheme the U.S. Federal Government will buy up to half a dozen microcomputers for any schoool class at any level of education.

In the U.K., computers in schools are not a new innovation. I recall that in the early 1960s Elliott Automation was encouraging their use in education but of course not on the gigantic scale now thought to be necessary. Today the Science Research Council has been addressing itself to the new challenge and has published a report "Proposed New Initiatives in Computing and Computer Applications". Half a dozen fiveyear projects are envlsaged which include school and undergraduate training as well as post-graduate training and research.

Although our educational budgets are still under strain, cash should be found for improving microelectronics literacy in our schools. If the young are provided with awareness of the benefits of the new technology fear will be dispelled and attitudes will change.

At adult level there are so many 'awareness' programmes that it is hard to keep up with them. Apart from the Governmentsponsored programmes hardly a week passes without a conference, seminar or computer "workshop" or "teach-in" being organised.

Intel Anniversary

Ten years ago most industry observers were beginning to believe that the semiconductor industry would shrink in number of companies down to three or four huge volume suppliers and-perhaps a handful of specialised companies on the fringe of the industry which could survive on relatively small quantities of more unusual devices. After all, no other type of business had such high commercial risks or needed so much capital investment. Many companies of real substance had moved into the semiconductor business with high expectations only to withdraw later in a spirit of humiliation and sadly depleted bank balances.

So when, in 1969, a small break-away group founded Intel there seemed little hope of success. In fact net income for the first two years was a loss of nearly \$3 million. Then revenue started to come in, first at a trickle and later, apart from a dip in 1975, at a sharp upward rate. This was achieved on the founding philosophy of the necessity of a new company having a unique product and the investment power of contemporary big timers.

Intel celebrated their 10th birthday with 11,000 employees generating \$400 million turnover. This year \$40 million is being spent on R & D. Intel also has a policy of constant replacement with up to date production equipment, investment this year being \$120 million. That these policies are paying off is evidenced by the fact that IBM is now Intel's largest customer with orders of \$25 million.

The business has been built on solid state memories and the MPU. Longest running line was the type 1103 RAM, now honourably retired after a production run of 35 million units. In the last recorded twelve month period Intel shipped a fantastic total of 200 billion bits of memory. Co-founder Dr. Robert Noyce certainly deserves the Faraday Medal which he was awarded on a recent trip to London.

Recognition

British semiconductor manufacturers seldom get adequate recognition even when they do well. Plessey, for example, has come in for a lot of criticism lately so it is only fair to record that their new remote control chips, a bipolar transmitter and MOS receiver, have already attracted an order for 500,000 sets from a European TV manufacturer. And, as they cost 30 per cent less than the cheapest competitive set of chips, there is a growing interest from toy companies who see a modestly priced remote control (without wires) as a good future prospect.

Let's cast our minds back to 1975 when Racal-Dana (then Racal Instruments) introduced in September of that year the 99 Series of counter/timer and frequency meters with the sales tag "Simply Light Years Ahead". The centre piece of all the instruments in the range was a Racaldesigned chip containing some 5,000 components fabricated by Ferranti in the CDI process.

Now Racal has launched a successor range, the 9900 Series, of 13 instruments with an overall frequency range from d.c. to 3GHz. Specifications are improved, they are simpler to use, digital readouts are larger and clearer and styling has become more international. But the chip is the same.

John Ceresa, Racal-Dana's managing director told me that there is still nothing to beat the Ferranti-built CDI chip for this application. There are 10,000 of them in the 99 Series in use throughout the world, some of them with well over three years' hard service, with not a single case of chip failure. So all the models in the 9900 Series will carry a lifetime guarantee on the chip and you can't ask for better than that.

Madley

It seems unlikely that a modest little name like Madley will ever have the same aura as Goonhilly, at least to old-timers who remember the thrill of the first TV transmissions via satellite in the early 1960s. The original Goonhilly I earth terminal is still there and working but now supplemented by Goonhilly II, III and IV with still the intention of another four dishes on the site.

Meantime Madley, near Hereford, has come on stream to handle traffic through the Indian Ocean Satellite. A second dish has already been contracted for and is due for service next year and Madley III should come in 1981, with IV, V and VI to follow.

Prime contractor to the Post Office was Marconi Communications Systems whose share of the work was £6 million out of the £10 million so far invested in the Madley site.

An interesting sidelight on inflation is that a three-minute telephone call to Australia in 1930 then cost £6, equivalent to £84 today. Now you can directly dial to Australia via Madley at a rate of £3.15 for three minutes, less than 5/- in 1930 money.

And if the commonly accepted yardstick that a nation's prosperity is in direct proportion to its population of telephones and general communications capability is still correct, then the U.K. surely cannot be in decline.

Exports

Despite hard times good products still sell well. Five years ago Racal-Milgo in data communications had a full-year turnover of £6 million. In a recent 4-week trading period the company took £6.3 million worth of orders of which 40 per cent were for export. The record month was entirely at the U.K. end of the business and does not include the business in the same period achieved by the Miami, Florida, company which is also Racal-owned.

Redifon, in the flight simulator business, has clocked up £37 million of business in the past year from overseas buyers in the U.S.A., Germany, Japan and the Middle East.

But BSR, whose enormous output of record players goes 70 per cent to North America, could be hit by the relative weakness of the U.S. dollar against the pound sterling. Even so, BSR inched up sales to North America last year by 5 per cent and had a total turnover of just under £160 million.



CASUAL COMPUTERS

Now open in London's Tottenham Court Road, is The Byte Shop, whose Gants Hill address may already be familiar to computer users. Demonstrations and over the counter sales of personal and business computers are the theme. Casual "lunchtime" visits by London's business men and the general public are expected, and experienced staff will be on hand to put the machines through their paces. Systems will range in price from a few hundred pounds to around £15,000.

GLASS KEYBOARD

W HEN personnel who are not totally familiar with computerised equipment, are confronted with rows of switches or a keyboard, they very often "can't see for looking", and a typical situation brought down to the home computer level could be this:

The VDU says: Further instructions? Key Y for yes, Key N for no.

The user's eyes and "prodding" finger then scan frantically up and down the keyboard in an attempt to find those keys. Wouldn't it be more convenient to have a keyboard with rows of buttons which have nothing written on them? And then suddenly, in the situation described above, two keys become labelled with illuminated letters; one as Y and the other as N. Or even better, one as YES and the other as NO. With Digilux by Marconi Radar Systems, you can do just this. Software alone allocates key functions, and the VDU screen labels them!

The Digilux *keyboard*, or anything like it, is usually to be found on home computers for a long time, but at a high level of sophistication there are self evident advantages. The transparent panel uses 8 vertical and 4 horizontal light beams covering the lower half of the screen.

The 400mm Digilux touch sensitive panel uses 64 horizontal and 64 vertical light beams spaced 6mm apart, so that a finger placed anywhere on the screen will interrupt at least one beam in each axis. When two beams in any one direction are blocked, decoding assumes that the finger is at the centre of the two, giving an effective resolution of 3mm. The generated 14-digit binary code identifying the touch position, is used to produce an electronic video-marker, with which the finger can be manœuvred to obtain coincidence with the target. As the photograph shows, this system can be quicker at addressing an image on a screen, than requesting from a conventional keyboard.



BAEC

NCE upon a time there was a local electronics hobby group with a membership of twenty or so. By May 1966 things were going well and the group decided to spread their wings, and the hobby, by adopting a constitution, electing a committee, and renaming the group *The British Amateur Electronics Club.* Followed up by a newsletter proper, and publicity in the electronics magazines, the group subsequently became international.

The aim of the organisation is simple. It is to help hobbyists avoid pitfalls and bugs in their pursuit of constructing, often sophisticated, electronic equipment. The Newsletter contains letters to the editor of this nature, and generally promotes cross-pollination of ideas, exchanges and sales of related items. Reviews of the various electronics magazines and their projects are included, from which we at P.E. do not escape. A Members List also enables members to discover BAEC: "brothers" who might live in the locality.

There is a Beginners Section, run by Bill Stilling, a technical library operated by Robert McBride, and, of course, a number of local groups. Lapel badges are obtainable, and BAEC labels. Use of these labels on orders to many of the suppliers found advertising in electronics magazines, will give the member concessionary prices.

The Chairman and Editor of BAEC is Mr. C. Bogod, Dickens, 26 Forrest Road, Penarth, Glam. Annual subscription is £3.50 for UK members, and £4.50 for overseas (surface post), which, to avoid bank commission should be paid in sterling. Address subscriptions to the Treasurer, Mr. R. G. Voisey, 7 Twyn-y-Fedwen, Whitchurch, Cardiff.

FINANCE FOR INNOVATION

THE NATIONAL Research Development Corporation (NRDC) has published a new brochure called *Finance for Innovation* which describes the various kinds of finance offered by the NRDC.

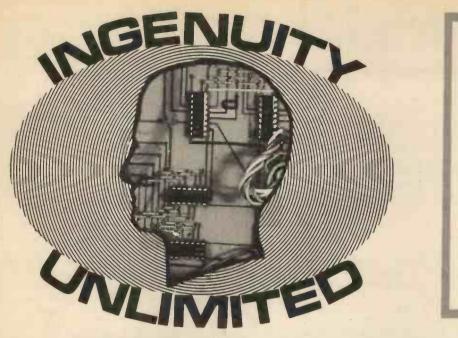
The brochure explains three forms of finance: Joint venture finance, recirculating loans, and equity and loan capital.

NRDC was set up in 1949 to promote the exploitation and development of new technology, with amounts invested being as small as £5,000, and as large as £5m. The word "exploitation" is included deliberately because the joint venture finance can apply not only to development work, but tooling up for production. The NRDC also claim to have in-house technical and patent expertise which is not generally available from other sources of finance.

A video marker follows the operator's finger across the screen, to assist referencing of the target



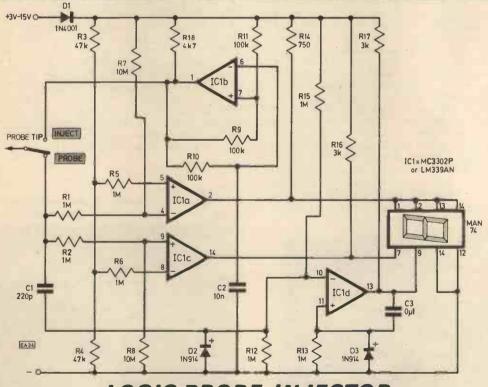
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A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought. Why not submit your idea? Any idea published will be awarded payment according to its merits.

Will be awarded payment according to its merits. Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.



LOGIC PROBE INJECTOR

THE circuit employs a low power quad comparator feeding a common cathode seven segment l.e.d. display. The display is mounted horizontally, so that the figure eight is on its side. A partially functional unit may be used since two of the segments are not fused.

IC 1a compares the input voltage with a reference voltage of $\frac{1}{2}V_{cc}$. If input is at logic 0 level, a 0 lights on the right of the

display. Similarly, IC1c lights a 1 if a logic 1 is detected. A negative pulse is stretched by IC1d and lights the decimal point. If the input is floating, the display is blanked.

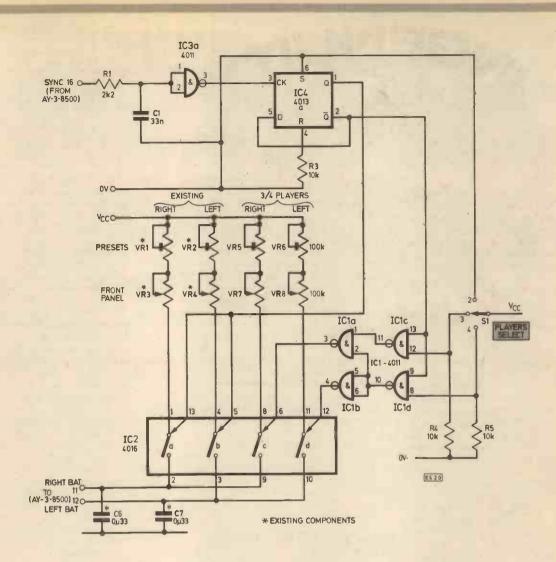
S1 selects either the "Logic Probe" or "Signal Inject" modes. IC1b is connected as a square-wave generator, and oscillates at around 1kHz. Rise and fall times are around 100nS.

The supply current, which is virtually

independent of the state of the l.e.d. is around 9mA at 5V, to 25mA at 15V. The probe may be used with both CMOS and TTL since it has an input current of around 2μ A, and a sink current from the injector of 16mA.

The supply voltage may range from 3V and 15V.

D. P. Akerman, Coventry.



MODIFICATION FOR THE PE SPORTSCENTRE

THE circuit shown extends the **PE** Sportscentre game for use by 2, 3 or 4 players. The 3 player option gives 2 players on one side against one player on the other. The 4 player option gives two players on each side.

The basic AY-3-8500 chip only has two player inputs. This is extended to four by using the 4016 multiplexer chip to switch the four players bat inputs in pairs on alternate TV frames.

Sync pulses from pin 16 on the games chip are integrated by R1, C1 and IC3a to separate out the frame sync pulses. These clock the D type flip flop IC4a in the 3 and 4 player mode. The Q output is connected to a, b control inputs of the 4016 multiplexer chip IC2. When Q is high, pin 11 of the game chip is therefore connected to VR3, and pin 12 to VR4.

The \overline{Q} output is gated by IC1, hence the c control input on the 4016 (for 3 players) and the c, d control inputs (four players) go high when \overline{Q} goes high. Pin 11 of the games chip is now connected to VR1 and pin 12 to VR8.

The right hand bat input is thus controlled by VR3 and VR7 on alternate frames and the left bat by VR4 and VR8. The coincidence logic in the chip for detecting "bat and ball" does not notice this multiplexing of the bat signal. VR3, VR7 are thus the right team; VR4, VR8 are the left team. In the two player option the D type flip flop is permanently set, and the a, b gates in IC2 are permanently made. The bats are then controlled solely by VR3, VR4.

The 3,4 player modification works best on the tennis. On the football and squash games the court becomes a bit cluttered.

E. A. Parr, Carluke, Lanarkshire.

Semiconductor UPDATE FLATURING : ICL 8063 HA 5195 BB 212 R.W. Coles

TEAM WORK

When you need to put together a power amplifier, for a hi-fi application or even to drive a d.c. motor in a servo system, there are quite a few different ways of setting about the job. You could be lucky enough to find an integrated power amplifier with an adequate specification so that your problem is solved in a very simple way. Generally speaking, complete monolithic power amplifiers can handle most jobs up to about 10W, but watch those THD specs at the higher power levels!

If you need a lot more than 10W, you will need to consider a discrete transistor design with a couple of 2N3055's or some other power-pair in a push-pull configuration. You will find it better to jack up the supply voltage to 60V or so for the 50W applications, and a centre tapped supply makes life easier. The pre-amplifier and driver circuits may be a bit of a headache unless you use an existing design, but suppose you want to go it alone, wouldn't it be possible to use a couple of 741 op-amps in the pre-amp and driver stages? After all, getting 741's to do what you want is easy, isn't it? Unfortunately you soon find out that there is a "credibility gap" between the puny 24V swing at 5mA or so which the op-amp can supply, and the tens of milliamps at 60V which the "power-pair" may require. You can add a couple of intermediate driver transistors of course, but then the problem of protecting the power devices against short circuits and the

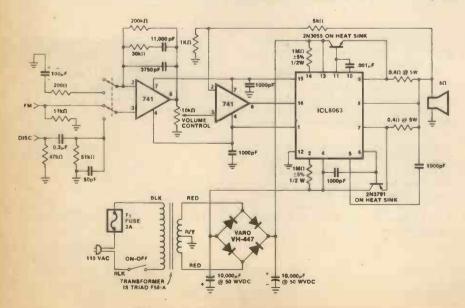
dreaded "second-breakdown" rears its ugly head. Oh well, didn't P.E. publish a pretty good power amp circuit back in April '75, or was it '76...?

If resigning yourself to someone else's design goes against the grain you can now fill the "credibility gap" with a new device from the electronic equivalent of the 7th Cavalry, Intersil. The ICL 8063 is a sixteen pin d.i.l. package which contains, a complete driver and protection circuit system which can be put between, say, a 741 and a pair of power transistors. You can build the '8063 into all kinds of power amplifier designs, 50W hi-fi systems, 1A servomotor drivers, beefy function generators, and so on. It's up to you. With the '8063 you don't have to accept someone else's integrated public address chip, or someone else's discrete design, you can now get into the power design scene yourself, without being a specialist!

To see what this chip can do, take a look at the example circuit reproduced here. This is a complete audio hi-fi amplifier delivering 50W r.m.s. using the '8063 in combination with a couple of 741's and a 2N3055/2N3791 power pair. That's teamwork.l

FASTEST THING ON FOUR-TEEN LEGS

Don't get me wrong, 741's are great, but with a bandwidth that starts to droop alarmingly from 20kHz or so (in most applications) it's hardly the stuff of which great



oscilloscope Y amplifiers are made—now is it? If you like the ease of use, accurate gain setting, and precise d.c. performance of opamps like the 741, but need just a teenyweeny bit more bandwidth—try the Harris HA 5195.

This incredible new monolithic op-amp has a gain-bandwidth product of 150MHz(yes, *MHz*) and it slews at an amazing 200V per microsecond to provide an 11 ns pulse response risetime which rivals TTL for speed. Down at d.c. it's almost as good as a 741 with a 20K voltage gain (open loop) and an input offset voltage of 5.0mV or so. The full power bandwidth (200 ohm load, plus and minus 5V swing) is 6.5MHz, which makes this chip the fastest monolithic op-amp around.

I always dreamed about building an oscilloscope using i.c.s throughout, and some readers may remember my Digiscope using a matrix of 80 I.e.d.s as a display screen, which was my first attempt at this. I spent ages searching for a simple but fast Y amplifier chip which could work at d.c., and eventually settled for an R.C.A. device which managed a bandwidth of almost 2MHz. I thought that was pretty slick at the time but the HA 5195 could probably manage more than 10MHz in a similar application so if anyone would like to build a "Son of Digiscope ..." "We have the technology—we can rebuild it!"

HANDY-CAP

Remember those beautiful air-spaced 500pF tuning capacitors (condensers, actually I) which we used to use for tuning in Radio Luxembourg or the Home Service? Well, I'm showing my age again I suppose, but when you had one of those at the other end of the dial-cord, you really knew what "radio" was all about. These days you would use a device like the BB 212 variable capacitance double diode and some cissy pot. The BB 212 seems to do all that those aluminium marvels used to do, only it goes in a boring, plastic, TO92 transistor package. You can swing its capacitance from 20 to 500pF, and you probably won't need beehlve trimmers or padders to match the two halves because they're pretty well matched by Mullard, You don't need dial-cord of course, or flywheels or anti-backlash couplings, just a d.c. voltage which you can shift between 0 and 9V. There is one thing though, you won't be able to tune in the Home Service, or the Light Programme; you'll have to settle for Radio 1. Serves you right!

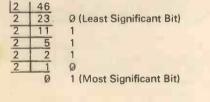
Microprocessor Evaluation System D.S.COUTTS

Part 3

E have now reached the point where we can start programming, but first a few preliminary notes might be useful.

NUMBER SYSTEMS

Each digit in the binary system can take the value of Ø or 1 with the least significant digit on the right. To convert decimal 46 to binary, divide the number continually by 2 as shown in Fig. 3.1 together with the binary to decimal conversion.



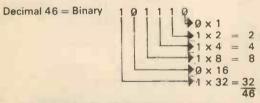


Fig. 3.1. Binary and decimal conversions

The front panel l.e.d.s on the CP1610 can display That is decimal Q to decimal 65535.

OCTAL NUMBERS

Binary numbers are very clumsy to work with when they are large and so the octal system is often used. The binary number is divided into groups of 3 digits from the right. As 3 binary digits can give values from Ø to 7 there is no 8 or 9 in the octal system. For example:

DECIMAL	46	becomes	BINARY	101	110	
			or OCTAL	5	6	
DECIMAL	125	becomes	BINARY	001	111	101
			or OCTAL	1	7	5

and the front panel l.e.d.s on the 1610 can display Octal Ø to Octal 177777.

Table 1 shows the values from Ø to 17 in decimal, binary and octal.

DECIMAL	BINARY	OCTAL
0	00000	0
1	00001	1
2	00010	2
3	00011	3
4	00100	4
5	00101	5
6	00110	6
7	00111	7
8	01000	10
9	01001	11
10	01010	12
11	01011	13
12	01100	14
13	01101	15
14	01110	16
15	01111	17
16	10000	20
17	10001	21
	TABLEA	

TABLE 1

Programs will be written in octal and with leading zeros, omitted unless stated.

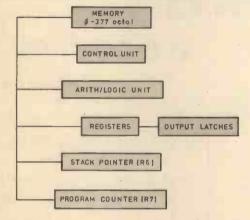
RUNNING PROGRAMS

When writing a program into memory that program will contain instructions for the processor, and data for the processor to work on with addresses to tell the machine where to put the results, etc. As the information is all stored in memory as binary numbers how does the machine differentiate between instructions, data and addresses? The answer is the program must be written in such a manner that the information in memory is accessed in the correct order. In Fig. 3.2 there is a simplified block diagram of the processor. At the top we have the memory sitting between addresses Ø to 377. This holds our program. Next we have the control unit. This accepts instructions from the program and controls the operation of the Arith/Logic Unit (ALU). Next we have the 8 registers: Ø, 1, 2 and 3 are general purpose, being used for computation, address pointers, etc. Registers 4 and 5 are also general purpose but have an automatic increment feature. Register 6 is the Stack Pointer (SP). This register points to an address space in memory where return addresses are stored when subroutines are used. Register 7 is the all important Program Counter (PC) which points to the next address in memory from which we will obtain data.

FLOWCHARTS

When writing programs it is useful to construct a flowchart first. Flowcharts are a means of representing programs in simplified form using standard graphical symbols. This allows you to follow your program through and check that the various jumps go to the correct places.

Rather than explain what direct and indirect addressing and immediate data, etc., mean here, it is probably more instructive to look at a few flowcharts and programs with notes. We are more interested in making the machine do something than memorising a dictionary of terms.



EG 121

Fig. 3.2. Simplified block diagram of the processor

PROGRAM 1

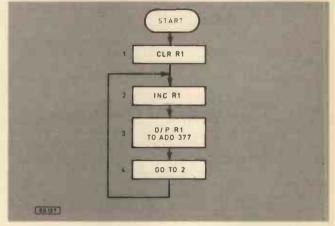
This program increments register 1 and outputs it to the l.e.d.s. The flowchart and listing for the program are shown in Fig. 3.3. It starts at box 1 which says clear register 1. After that the contents of register 1 are output to the output latches and l.e.d.s by storing it in memory address 377, the first l.e.d. lights up. From box 3 we go to box 4. This box contains a jump instruction telling us to go to box 2 again. R1 is again incremented and output to the l.e.d.s.

The program will loop around boxes 2, 3 and 4 until it is stopped by putting the load/run switch back to load.

The last operation in the program was to enter Ø into address 376, therefore the address latches are pointing to address 376. When the load run switch is put to run, the first thing the processor does is to load the data from address 376 (in this case Ø) into the program counter. This tells the machine where the next item of information is located. The processor now loads the contents of address Ø into the control unit. This word must be an instruction as the machine has been told only where to fetch the information from (ADD (0) but has not been told to do anything yet. The word in address Ø tells the machine to clear register 1. The machine increments the PC and clears R1. The PC is now pointing to address 1 in memory. As the first operation is completed the word in location 1 must again be an instruction. This is loaded into the control unit, the PC is incremented and R1 is cleared. The PC is now pointing to address 2 and as the last operation has been completed the word in location 2 must be another instruction. The word from location 2 is loaded into the control unit and the PC is incremented to point to location 3. The word in location 2 tells the machine to output the contents of register 1 but the machine has not yet been told where to send it. The word in location 3 must therefore be an address. This word is loaded into the

377 is now pointed to by the address latch and the data in R1 is written into address 377 (the output latches). Meanwhile the PC has been incremented again and now points to location 4 which must be another instruction as the previous operation has been completed. Locations 4, 5 and 6 contain 3 words telling the Control Unit to make a jump back to location 1 forming a program loop. The PC will increment to point to location 5 then to location 6 and finally it will be set to point to location 1. The instruction in location 1 is loaded into the Control Unit and the PC is incremented to point to location 2 and R1 is again incremented. We go round this loop until we stop the machine. The instructions in locations Ø and 1 are one word instructions. Locations 2 and 3 contain a 2-word instruction using Direct Addressing, i.e. the address immediately follows the instruction in memory. Locations 4, 5 and 6 contain a 3-word instruction

processor and written into the address latch so that address



ADD	DATA	OPERATION	INSTRUCTION CYCLES
Ø	711	CLR R1	6
1	11	INC R1	6)
2	1101	OUTPUT R1	11
3	377	TO 377	} · · · }+
4	4	JUMP	
5	1400	то	12
6	1	ADD 1)
376	Ø	STADD	

*** 29 INSTRUCTION CYCLES**

1 INST CYCLE = 4 MICROSECONDS

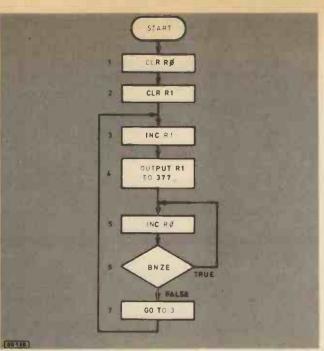
Fig. 3.3. Flowchart and listing for program 1

again using Direct Addressing. The loops formed by locations 1 to 6 takes 29 instruction cycles or 116 microseconds, therefore, R1 and the l.e.d.s are incremented once every 116 microseconds.

PROGRAM 2

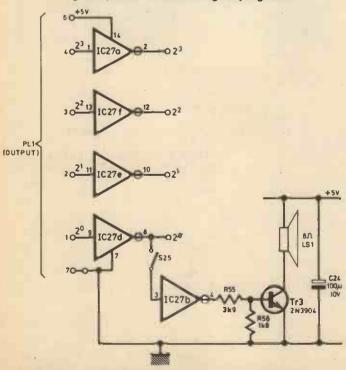
The Flowchart and listing for program 2 are shown in Fig. 3.4.

In this program after R1 is output, register RØ is incremented and tested to check that it is not zero. As it has been previously cleared to zero and then incremented, the register does not contain zero, and we leave box 6 by the TRUE exit and go to box 5. The program keeps incrementing RØ and testing it. Eventually RØ will be filled with 16 ones and next time we increment it, it will go to all zeros. When this happens box 6 is left by the FALSE exit to box 7. Box 7 loops to box 3, where R1 is incremented and the loop is repeated again. When box 6 is executed, if the test is TRUE



ADD	DATA	OPERATION	INSTRUCTION CYCLES
0	700	CLR RØ	6
1	711	CLR R1	6
2	11	INCR R1	6
3	1101	O/P R1	11
4	377	TO ADD 377	
5	10	INCR RØ	6
6	1054	BNZE	12 -10
7	2	BACK 2	7/9
10	4	JUMP	13
11	1400	ТО	12
12	2	ADD 2	1
376	0	STADD	

Fig. 3.4. Flowchart and listing for program 2



EA32

Fig. 3.5a Circuit diagram of output unit

the box takes 9 machine cycles to execute, and if the test is FALSE the box takes 7 machine cycles to execute. This is because a TRUE result requires a branch back to the previous box, whereas a FALSE result allows the program to go straight on to box 7. As boxes 5 and 6 are executed 65536 times, this takes (65536 x 15) -2 = (983040 - 2)x 4µs = 3.932152 microseconds. Boxes 3, 4 and 7 are executed once each time R1 is incremented, therefore, R1 in-

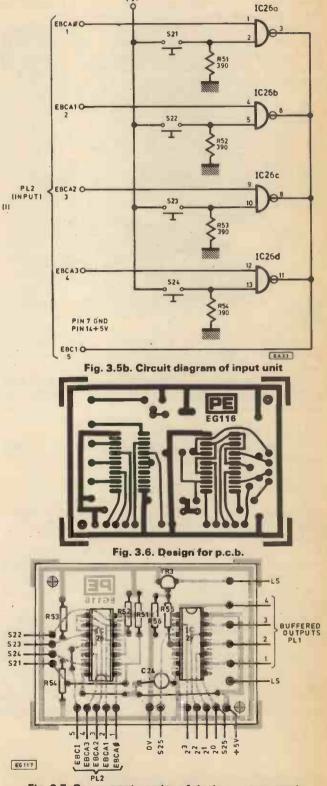


Fig. 3.7. Component overlay of the input-output unit

crements once every 11 + 6 + 3932152 + 12 = 3932181microseconds (≈ 4 seconds).

INPUT-OUTPUT UNIT

The circuit diagram of the output unit is shown in Fig. 3.5a. It is used to buffer the 2° to 2³ outputs and enables an audio output to be obtained via speaker LS1, which can be switched into the circuit by S25. The input circuit (Fig. 3.5b) uses a 7403 quad NAND gate i.c. to buffer the four push button input switches.



Input-Output Unit

Both the input and output circuits were mounted on the p.c.b. shown in Fig. 3.6 with the component overlay shown in Fig. 3.7.

The input-output unit is mounted in a separate $150 \times 80 \times 40$ mm case and is connected to the 1610 via PL1 and PL2.

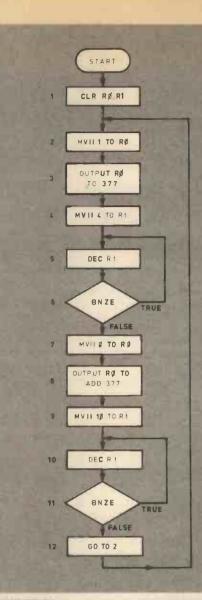
PROGRAM 3

This program enables the system to be used as a variable mark-space pulse generator. The flowchart and listing are shown in Fig. 3.8.

Registers Ø and 1 are cleared. Then 1 is loaded into RØ and this is output to ADD 377, setting the 2° output to a '1', which is inverted by the 7404 buffer (IC27d) in the inputoutput unit. Next a number is moved into R1 (4 in this instance) and R1 is then decremented until it reaches zero. When R1 reaches zero a 'Ø' is loaded into RØ and output to ADD 377, setting the 2° output back to zero. We then move a number into R1 (1Ø in this instance) and decrement R1 until it reaches zero. When R1 reaches zero, we jump back to load a '1' into RØ and output it again.

It can be seen that the data in location 7 determines the width of the mark and the data in location 20 determines the width of the space. With the values of 4 and 10 used in the program the mark is 340 microseconds and the space is 628 microseconds. Increasing or decreasing the number in location 7 by 1 will increase or decrease the width of the mark by 60 microseconds, and similarly for the space. This is because the boxes 5, 6 and 10, 11 take 15 machine cycles of 4 microseconds.

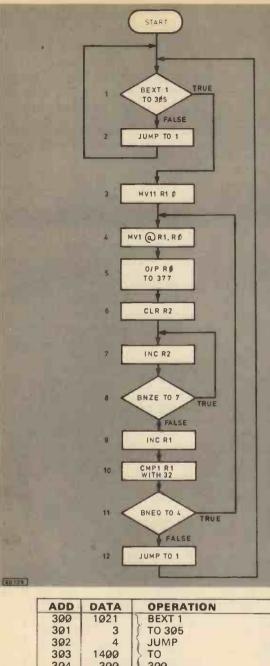
The instruction in locations 2 and 3 loads immediate data from location 3 into register Ø. Locations 6 and 7, 13 and 14, 17 and 2Ø are similar. This program illustrates "nested loops", the loops formed by flowchart boxes 5, 6 and 1Ø, 11 are nested within the loop formed by boxes 2 to 12.



				INSTRUCTION
	ADD	DATA	OPERATION	CYCLES
	Ø	700	CLR RØ	6
	1	711	CLR R1	6
	2	1270	MVII	} 8
1	3	1	1 TO RØ	5 0
	4	1100	O/P RØ TO	} 11
	5.	377	ADD 377	
	6	1271	MVII 4	8
	7	4	TO R1 (MARK)	ls °
	10	21	DEC R1	6
	11	1054	BNZE	7/9
	12	2	BACK 2]]
	13	1270	MVII	} 8
	14	Ø	ØTORØ	
	15	1100	O/P RØ TO	} 11
	16	377	ADD 377	12
	17	1271	MVII 19 TO	8
	20	10	R1 (SPACE)	5
	21	21	DEC R1	6
	22	1054	BNZE	} 7/9
	23	2	BACK 2	1
	24	4	JUMP	
	25 .	1400	то	12
	26	2	ADD 2)
	376	Ø	STADD	ľ

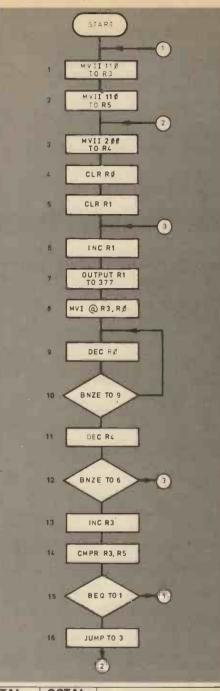
(68 131)

Fig. 3.8. Flowchart and listing for program 3



~~~	PAIA	AL PRIMI AU
300	1021	D BEXT 1
301	3	TO 305
302	4	JUMP
303	1400	ТО
304	300	300
305	1271	1 MVILB1
.306	Ø	Q (ST ADD)
307	1210	MVI@R1,R
310	1100	) O/P RØ
311	377	TO 377
312	722	CLR R2
313	12	INC R2
314	1054	BNZE
315	2	BACK 2
316	11	INC R1
317	1571	CMPLR1
		5
320	32	WITH 32 (END ADD + 1)
321	1054	BNEQ
322	13	BACK 13
323	4	JUMP
324	1400	> TO
325	300	) 300
326	300	STADD

Fig. 3.9. Flowchart and listing for program 4



OCTAL	OCTAL	
ADDRESS	DATA	OPERATION
Ø	1273	LOAD START OF TABLE
1	100	INTO R3
2	1275	1 LOAD END OF
3	110	TABLE + 1 INTO R5
4	1274	LOAD TONE DURATION
5	400	INTO R4
6	700	CLR RØ
7	711	CLR R1
10	11	INC R1
11	1101	) O/P R1
12	377	j TO 377
13	1230	MVI@R3,RØ
14	20	DEC RØ
15	1054	BNZE
16	2	∫ BACK 2
17	24	DEC R4
20	1054	BNZE

EG 128

BACK 11
INC R3
COMPARE R3 WITH R5
BRANCH IF EQUAL
BACK 25
) JUMP
ТО
ADD 4
TABLE
DETERMINES
TONETO
BEOUTPUT
000000
STADD
STADD

Fig. 3.10. Flowchart and listing for program 5

#### **PROGRAM 4**

When you have entered a program into memory, it is useful to be able to check that you have entered it correctly. Program 4 (Fig. 3.9) will output a block of memory to the Le.d.s, stepping through addresses at 4 second intervals, which is long enough to check the data entered. Address **306** holds the start address of the block to be read out and address **320** holds the last address plus 1 of the block. Address **326** holds 300, the start address of the routine. To run the routine enter the start and end address + 1, then set the switches to **326** and push the address enter switch. Put all switches to their centre "OFF" position and put the load/run switch to run. When you press the **BEXT** 1 switch on the input-output unit, the block of memory will be output to the Le.d.s.

When the load/run switch is put to run, the program loops through the flowchart boxes 1 and 2 examining the BEXT 1 switch. When the BEXT 1 switch is pressed, the



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Varitone Stylophone (May 1979) R1 should be 27k. TR2 is *pnp* but the type number is correct. Pins 2 and 7 on IC2 should be reversed. program jumps to box 3. The start address of the block is moved into register 1. Box 4 moves the data from the address pointed to by the contents of register 1. This data is then output to the l.e.d.s. Boxes 6, 7 and 8 are used to form a 4 second delay. Box 9 then increments register 1 and boxes 10 and 11 compare register 1 with the last address plus 1 and if the end has been reached the FALSE exit of box 11 leads to a jump back to box 1 to examine the BEXT 1 switch again. If the end has not been reached, the TRUE output of box 11 leads back to box 4 to load and output the next memory location. With the numbers 0 and 32 in addresses 306 and 320 the block 0 to 31 will be output.

#### **PROGRAM 5**

This program (Fig. 3.10) is an example of table look up. The values in locations 100 to 107 are used to determine the tone to be output.

Boxes 1, 2 and 3 load the start of the table, end of table and tone duration into registers 3, 5 and 4 respectively. Register RØ and R1 are then cleared and R1 is incremented and output to 377. Box 8 contains an indirect address instruction. It says load register Ø with the contents of the address pointed to by R3. R3 has been previously loaded with 100, therefore R0 is loaded with the data contained in location 100, which is 200. R0 is then decremented until it reaches Ø. When RØ reaches Ø we go to box 11. R4 is decremented and checked to see if it has reached Q. If it hasn't, we branch back to box 6 to increment R1 and output it again. We keep going round the loop formed by boxes 6 to 12 until R4 reaches Ø. When this happens we go to box 13 and increment R3 to point to the next address in the table. Box 14 compares R3 with R5 to check if we have reached the end of the table. If we have reached the end we go back to box 1 and start again. If we have not reached the end, we jump to box 3 to load the next tone and output it.

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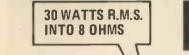
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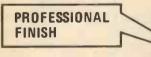
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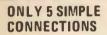
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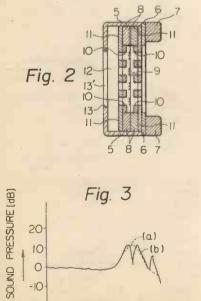
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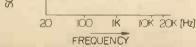
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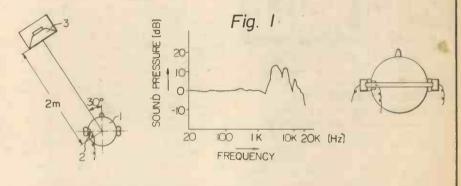
#### **PEAK LISTENING TIME**

In recently published British patent application GB 2 000 941 A, Matsushita Electric of Osaka, Japan, seem to contradict their train of thought on improved headphone reproduction recently reported in our feature on *Binaural Stereo Patents* (Practical Electronics, March 1979). In fact Matsushita appear now to be following the line previously adopted by Sony and also reported in the same feature.

In BP 1 517 938 Matsushita originally patented a system for notching out an unnatural peak at around 3kHz allegedly Introduced by headphone listening while Sony referred in apparent contradiction to similar peaks introduced by loudspeaker listening. The Matsushita research engineers are however now seeking to patent a headphone which introduces just those







peaks they were previously trying to notch out. The new patent application (which dates back to July 1977) explains that Matsushita's Investigations using a test loudspeaker and a replica human head which "listens" to the loudspeaker, have shown two resonant peaks in the frequency response curve at the head ears, one due to resonances in the ear canal and the other due to distractions from the head and ear lobes. These peaks, shown graphically in Fig. 1, are missing when the same sound is coupled direct to the head ears via headbhones.

Matsushita propose several ideas for artificially introducing the peaks Into sound heard via headphones. Fig. 2 shows a conventional headphone, with a transfer curve to the listener's ear as indicated by the broken line (a) of Fig. 3. To provide the required, unbroken line (b) the headphone is modified as shown in Fig. 4. In both old

> 11 10

14

10

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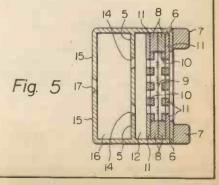
12

5 8

Fig. 4

and new headphones the diaphragm 9 between magnets 10 couples with the ear through pad 7. But the open mesh 13 which backs the diaphragm of the conventional headphone is in the new Matsushita 'phone replaced by a pair of bores 14. These function as acoustic tubes with reactance calculated to create parallel resonances and put the required twin peaks into the transfer curve Fig. 3(b).

A more sophisticated approach, which is claimed to replicate the phase transfer characteristic as well as the frequency transfer characteristic of loudspeaker listening, is shown in Fig. 5. An additional cavity 16 is formed on the outside of the phone with a central bore opening 17. This tunes the lower frequency peak more abruptly. Alternatively the extra cavity 16 may be omitted and the same effect achieved by Introducing an electrical phase advance circuit into the headphone system.





#### JUPITER AND VOYAGER 1

Fiscal problems are a constant hazard for space funding and there are inevitable delays and disappointments to the designers of new missions. With so much to be done for the good of the world as a whole the priorities create grave difficulties. However the mission of Voyager 1 and Voyager 2 has more than justified the decision to 'Go'. Data and pictures have already caused considerable and unashamed excitement among astronomers. The great Red Spot which has been the source of much controversy has revealed itself as having an anticlockwise rotation. The number of papers that may be expected from this one thing alone will justify the continuous observations that amateurs have made over many years.

One of the surprises came from the pictures. of the massive planet's own satellites. One of these, Ganymede, is larger than the planet Mercury, for it has a diameter of 5,270km. The surface of this satellite is covered in craters, many of them with rays of ejected matter. A close-up picture shows grooves and ridges with craters at junction points. Indeed some of these close-up views, where the resolution allows the smallest recognisable feature to be of the order of 3km across, look very much like roundabouts with roads leading off in many directions. Since the density of this moon is low and the satellite is thought to be largely composed of water, these ridges and grooves may well be deformations of the surface by tectonic forces.

Callisto is the outermost satellite of the four, first recognised by Galileo and carrying his name to this day. Voyager 1 photographed Callisto from a distance of 202,000km. The surface is literally peppered with craters caused by meteorite impacts. Some of the craters have rays brightly visible and giving the surface a somewhat speckled look. One very clear feature is visible and looks like a giant bull's eye with surrounding rings. The centre is a deep crater several thousand kilometres across. The rings give the impression of ripples that have spread outwards as if a stone had been dropped in a pool. This is a unique discovery and Callisto is the most cratered body in the solar system. One question that arises when looking at these pictures is the very evenness of the size of the craters almost as though similar units were dropped on the surface at the same time.

The innermost of Jupiter's twelve satellites is a tiny body called Amalthea. It lies at a distance of some 160,000km from Jupiter. Amalthea is a practical demonstration of strength of the giant planet's gravitational pull. This tiny satellite is elongated so that it is twice as long as it is wide, with one end directed towards its parent body. It too is covered with craters and has a low reflectivity (the Albedo). The long dimension of the satellite is about 290km.

Another remarkable discovery made after passing closer to Jupiter was that there is a ring similar to those found round Saturn and Uranus. This ring is some 55,000km from the 'cloud tops' of Jupiter, 8,000km wide and about 30km thick consisting of scattered boulders. Again this is a discovery unforseen and was due to the fact that the spacecraft oscillated slowly as it moved along, also long exposure in use at the time (11.2 minutes) with Voyager's narrow angle camera.

The spacecraft moved on toward the innermost of the Galilean moons, Io, which is about the size of the Earth's Moon. This is a most interesting body. It is some 420,000km from Jupiter and has at least eight active volcanoes. One of these can be seen in the photographs in eruption. The satellite has a surface of broad plains, thought to be salty, having fault lines, channels, depressions and highlands. The surface colour seems to be a yellowish brown. Another special thing is that there are no noticeable craters to mar the surface. There are however large lava fields and it could be that the constantly erupting volcanoes reform the surface continuously. Clouds of ionised suphur which surround Jupiter are perhaps from these eruptions. This does have a link with the decametre radiations which are detected from the vicinity of Jupiter and were originally thought to be from Jupiter itself. Io was very much involved in this matter.

Europa is the fourth of the Galilean satellites and is a brown coloured body a little smaller than the Moon. It is suspected that this satellite may also have volcances. This no doubt will be settled when Voyager 2 passes much closer than has Voyager 1. This is expected to be at the beginning of July.

Voyager will now go on its way toward Saturn which it will reach in November 1980. The discoveries by Voyager 1 have already been used to formulate the exact activities within the controllable limits of the spacecraft Voyager 2.

#### JUPITER SLING SHOT

The final go-ahead has been given for the solar mission. The details of this were given last year in *Spacewatch*. Now an agreement between NASA and the European Space Agency has been signed for the Solar Polar Mission. This is a joint Euro-American project. The European Space Agency will supply one of the probes and the National Aeronautical and Space Administration will supply the other probe and the facilities for launching including the Shuttle. Launch is expected in 1983, possibly February. The two probes will be directed towards Jupiter. The gravity of Jupiter will then give the necessary impetus so that one probe passes over the northern pole of the Sun and the other over the southern pole. The two probes will therefore go though the upper and lower levels of the solar system where no spacecraft has yet penetrated. A new set of data with polar views of the Sun will become available. No doubt this will open new vistas of knowledge.

#### MARS

At one time there was a programme of missions to Mars with a manned landing expected in the early 1980's. This hope has for one reason and another slipped back in time. However as the Viking mission was such a success there is now every hope that the main theme will be kept in mind. Mars is more like our own world and is the primary candidate for colonisation.

The Martian atmosphere is nearly all carbon dioxide, with some traces of nitrogen, argon and oxygen. The surface pressure of the Martian atmosphere is about one hundredth of that of the Earth. The temperature varies between -130° and 10°C. As shown on the pictures returned from the Viking mission the surface is mountainous with vast plains which are desert like in appearance. The plains are often covered with rocks and craters. There is water on Mars but it seems to be concentrated at the poles. The measured average wind speed was about 15km/hr. There are however areas where speeds as high as 60km/hr have been recorded. Dust storms are also a hazard and can last for many days. The mechanisms of these dust storms are being studied closely. Since these storms are usually found in specific areas it would be possible to avoid activity in dangerous places.

A study has been made for a vehicle which would be able to deal adequately with the rocky terrain and still be controllable from Earth. Since the distance of the planet from the Earth varies considerably as the two bodies follow their orbits round the Sun, the matter of time delay in control signals is important. To this end a study was made of the various types of vehicle to see which would best serve the purpose. A vehicle moving over the surface would be subject to many hazards. A vehicle in orbit would not be able to fulfil the needs of the mission. It was therefore decided that an aeroplane was the best solution. An intense study of the possibility was made by the Jet Propulsion Laboratories at Pasadena, California. The team who were responsible were, V. C. Clarke Jr., A. Kerem and R. Lewis. This design team studied two types of aeroplane, a cruiser and a lander. The design required that both vehicles would be deployed in the atmosphere. The lander however would be designed for both landing and subsequent take-off. Basically the cruiser, which would be hydrazine powered, was designed to fly at an altitude of 1km and be capable of sustaining aloft a 40kg experiment package for up to 15 hours duration. A maximum range of 4,000km was decided upon so that the 'airspeed' would be 200 to 300km/hr.

In order to achieve a satisfactory environmental survey the height at which the vehicle should fly would be 35km.

The tenuous atmosphere dictates the choice of a 'non air breathing' unit for propulsion. Because of this another weight penalty is introduced. A scaled down version of the Akkerman hydrazine engine was developed by the Dryden Flight Research Centre. The power output is a little over 11kW. Electric power was considered but present units that were available did not have good enough performances. Batteries are being developed with mission is ready this might be an alternative. A low speed variable pitch propeller was adopted.

The aeroplane is designed to carry a 100kg payload together with 50kg of control systems. This places considerable demands, for an efficient structure, on the designer. The total weight of the airframe engine and fuel is limited to 150kg. The team decided on composite material so a carbon reinforced plastic with some Kelvar-reinforced plastic was adopted. An important point in these matters of design was the need for sterilisation in order to prevent contamination by Earth germs. Since the sterilisation would have to be carried out at 100°C distortion could be a problem. This was solved by specifying a high temperature cure of the composites. The wing structure was hinged with six joints and the skin is made up of carbon fibre reinforced plastic.

Since there is the time lag in communication the aeroplane would need to be autonomous, the flying and landing would be under the control of a computer. Since the surface of Mars is not yet very accurately determined it would be necessary to have some avoidance radar. In order to limit weight it was décided on an elementary system which had limited avoidance capability and Earth guidance would be used to control which areas needed complete avoidance. The landing and take off conditions imposed demanding technical skill so it was decided to use some basic approaches only. A vertical landing powered by retro-rockets was chosen. The aeroplane would commence its descent at an altitude of 1km, with a power pull-up to place the craft in a stall. Once in the stable attitude the aeroplane would fall vertically, reaching a terminal speed of 60m/sec. Hydrazine rockets would then fire, slowing the vehicle to 1-2m/sec.

Take-off would be by power rocket ascent to an altitude of 1km, followed by a dive to gain speed and then a gentle pull out.

The mission proposed by JPL calls for 12 aeroplanes. This would ensure a satisfactory chance of success even if half the number crashed. They would be transported to Mars in groups of four, each cluster being placed on an interplanetary trajectory by a twin-stage Inertial Upper Stage. Three Space Shuttle flights would be required, each delivering an IUS and four packaged aeroplanes in low orbit. Each of the planes would be in its own capsule. A braking parachute would be deployed at an altitude of 9.5km, reducing the descent speed to 60m/sec. The parachute would be jettisonned at 7.5km and the aeroplane would unfold itself and begin flying.

The areas in which the aeroplanes would operate from the point of view of the experiments would be:

Atmospheric Dynamics and Composition Electromagnetic Sounding Gamma-ray spectroscopy Gravity Field Infra-red Reflectance Spectrometer Magnetic Field and Photography.



#### **ON COURSE**

#### by Mike Abbott

Microcomputer courses brought to our attention. Texas Instruments---Bedford. Range of "hands on" courses available at T.I.'s new purpose-built centre. Details: (0234) 67466.

Bleasdale Computer Systems—London. Range of "hands on" courses for engineers and management. Details: 01-540 8611.

Membership of the **Computer Education Group** may interest some readers. The edition of *Computer Education* which we received contained much interesting information, including details of microprocessor courses.

#### NEW RAM

A NEW 16K dynamic RAM which operates from a single 5V supply is now available from Intel. The 2118 is a 16-pin device with access time in the 100ns region. The -5V requirement is generated automatically on-chip, thus being transparent to the user. Power consumption is 130mW operating, and 15mW on standby.

#### **COSMAC CLUB**

T is proposed to set up a club in Britain for users of the RCA 1802 microprocessor; Cosmac, Elf, Elf II, Super Elf etc. Unofficial assistance has been promised by RCA and HL Audio.

Those interested are requested to contact James Cunningham at: 7 Harrowden Court, Harrowden Road, Luton LU2 0SR, enclosing SAE.

#### **VOICES IN THE SKY**

THE Civil Aviation Authority have purchased a new automatic broadcast system developed by Marconi Space and Defence Systems (MSDS), to relay weather reports to airline pilots. Weather reports which are received every half hour by telex at the Civil Aviation Communication Centre at Heathrow, are automatically converted into human voice and transmitted continuously and simultaneously on up to four frequencies. How is it done? A recording of standard weather report phrases, words and figures has been digitised and stored in a computer memory, to be electronically re-constructed as required by the telex messages.

#### A TERMINAL CASE

THE degree to which microprocessors will irreversibly change our lives is perhaps impalpable at this stage, but when a visiting business or salesman slips open the executive case illustrated below, keys in questions, and stock/delivery details come flying out of the printer, you'll know that the iconoclastic microprocessor has brought the mysterious world of computers down to everyday level.

The Microlink, manufactured by The Micronics Company, is claimed to be the lowest cost portable intelligent terminal on the market, giving the user rapid access to a central database, from perhaps your office, an engineering site, or hotel room. The V820 Microlink contains a CPU, acoustic coupler, expandable ROM and RAM, quiet printer, and solid state keyboard; some typical users being quoted as mobile engineers, reporters and audit teams. A range of plug-in firmware configures the terminal to the requirements of the user.

Interfaces are included for high speed cassette transfer, and VDU or TV monitoring. So, executives be warned, your visitors in future may place one of these cases on your desk and say: "Have you got a VDU?".





#### ... a selection from our postbag

Readers regulring a reply to any letter must include a stamped addressed envelope. Opinions expressed in Readout are not necessarily endorsed by the publishers of Practical Electronics.

#### Best on the market

Sir—In response to your column (April issue), the P.E. is indeed the best magazine on the market.

The greatest fun, or satisfaction, from electronics is being able to improvise on a given "theme" and to experiment with different ideas. Your particular format is suitable for both first time attempts in a new area (where it is necessary to follow the spec. fairly closely), while the design is versatile enough to allow for changes and innovations when the subject is a little more familiar.

Your magazine also includes some unusual and unique ideas and circuits that make a refreshing change and provide even new or further applications for electronics.

#### Possible Improvements?

- 1. The "On The Fringe" column you ran at one time was always interesting to read and, where possible, it did give just enough detail to allow an experimenter to try different ideas. For a single page it did provide a great deal of "gen", and many ideas mentioned in that column several years ago are now being presented (on T.V. etc.) as "The very latest". "Air ionisation" and "plant activity" were two good examples that come to mind. Presenting factual details in electronics years before those ideas are mentioned elsewhere is indeed a feat worthy of praise. Perhaps the best format would be a combination of "On The Fringe" subjects, together with technical tips/bright ideas, etc.
- 2. Data: On (semiconductors and i.c.s.) type numbers, especially those that can't be found in most data books, would be a benefit to all constructors.

Very often one's stock of devices always includes:

a) A glut of one or two particular types (which *can* often be used as an alternative to the specified type number).

b) A "hoard" of marked devices whose type numbers are difficult to find in most Data/Equivalent books. Only the most basic parameters and leadout configurations would be required.

I hope the above is of some use to you, in the meanwhile carry on the good work and many thanks for being *the* mag. that does "get it right".

E. Wilcockson, Moed,

Clwyd.

#### I.c.s Unlimited

Sir—Ingenuity Unlimited, April '79, Mr. J. Hogarth of Guiseley presents his "Programmable Divider", and an excellent example of how to use 50 per cent more i.c.s than necessary. This comment, when applied to more elaborate devices, may help other readers realise worthwhile economies when devising circuits.

Rather than using a 7430 8-input NAND gate and then inverting its output with 1/6 of a 7404 hex inverter, I believe he might just as well have used a 7420 dual 4-input NAND gate. One quad-input set would have done for the divider; the other 4 inputs wired together would accomplish the inversion.

As it stands 5/6ths or, in fact, 6/6ths of the 7404 are wasted, as well as 1/2 of the 7430.

This situation arises so frequently, where the odd gate on an i.c. is not used, I have found it always worthwhile to keep track of them; need for additional i.c.s can often be avoided.

L. Proctor, Charlottenlund, Denmark.

#### **EPROM** praise

Sir—I note with interest your "announcement" of the Intel 2758 EPROM, which I can say I have had great success in using, both building a programmer, and using them in quite a few items of equipment during the past year.

I would like to take this opportunity to confirm your enthusiasm for the use of these devices, and additionally to point out a few things which might be of interest to other readers.

Firstly, although the 2758 does allow single byte programming, I have not found much use for this facility for "patching" of existing code, as it is only possible to program bits within the **PROM** that are at a logic 1 down to a logic 0, and only total erasure of the **PROM** can return any bits at logic 0 to logic 1. The implication of this is that, although it is relatively easy to program in an entirely new piece of code, it is probably impossible to program in a branch from the old code to the new code, thereby making the patch inaccessible to the old code.

Secondly, the use of the PD/PGM pin on the 2758 as a power saving measure will cause current surges of around 100mA—it is therefore essential to decouple the PROM by the use of substantial decoupling capacitors at the PROM to reduce the effects of the current surges on electrically adjacent circuitry.

Thirdly, for those interested in expansion, there is a pin compatible version of the 2758, also manufactured by Intel as the 2716, which holds  $2k \times 8bit$  bytes. Care must be taken if it is intended to use these devices, as Texas also manufacture a  $2k \times 8bit$  EPROM called the 2716 which is the 2k version of the 2708, with the 2708's attendant disadvantageous requirement for 3 power supplies. The Texas equivalent of the Intel 2716 is known as the 2516. Hitachi also manufacture a 2716 which appears to be equivalent to the Intel 2716.

J. McCarthy, East Dulwich, London.

#### **Envelope Shaper**

Sir—I have just successfully completed building the A.D.S.R. Envelope Shaper described in "Practical Electronics", January 1979 and believe that the comments below will be of use to other constructors.

Firstly, chip IC1 (74C00 quad 2-input NAND) is shown with only three gates being used, the remaining one being left floating. This is unwise because floating gates of CMOS chips often burst into very high frequency oscillation which can seriously disturb the other operating gates in the chip. This parasitic oscillation can be completely removed by grounding (earthing) both of the unused gate inputs.

Secondly, a systematic search through all of the excellent mail-order suppliers who advertise in "Practical Electronics" failed to locate a stockist of the 74C00 chip. I therefore used the 4011 which is another 2-input NAND which is both cheap and readily available. However, use of this chip requires the different pin connections listed below.

74 <b>C</b> 00	4011
1	1
2	2
3	3
4	5
5	6
6	4
7	7
8	10
9	8 spare pins which
10	9 should be earthed
11	11
12	12
13	13
14	14
	P. McChesney
	Bromborough,
	Wirral,
	Merseyside.

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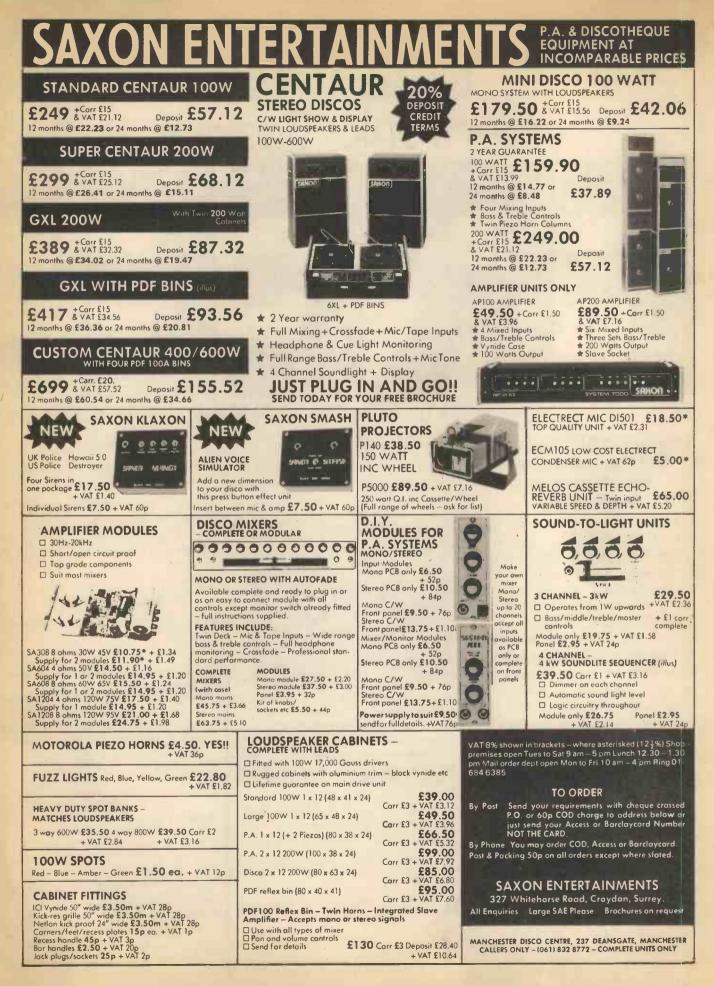
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The Chinaglia Dolomiti is a very impressive, solidly constructed multimeter with a large clear scale, giving  $\pm 2\%$  accuracy on d.c., and +2.5% on the a.c. and  $\Omega$  ranges. Input sensitivity is 20k $\Omega/V$ , and the movement has a "luxurious" damped action which inspires more confidence than the cheap "flicky" type. The meter also has an active cut-out which isolates one probe within milliseconds of overload, and the probe itself houses a quick blow fuse.

The f.s.d. ranges are as follows: current: 50µA-5A d.c., 5mA-5A a.c. Voltage: 150mV-1:5kV d.c., 5V-1.5kV a.c. The a.c. bandwidth is 20Hz-20kHz.

Resistance is accurately readable from about 50m $\Omega$  up to 50M $\Omega$ , and capacitance too can be measured (ballastic method) with reasonable precision. A capacitive reactance facility also exists. The meter can read dB in the range -10 to +65 in six ranges (0dB = 1mW into 600 $\Omega$ ).

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4

# **ALLENNAL P.R. SPURGEO**

THIS HIGH IMPEDANCE UNIT WILL INCREASE THE EXISTING RANGES OF MANY LOW COST MULTIMETERS

HOW many of us have wished for a multimeter that has negligible effect on circuitry, with low d.c. and a.c. ranges? Such instruments are usually priced well beyond most constructors' means but the unit to be described here will convert an existing multimeter into an instrument of medium accuracy with wide ranges which are rarely found on economically priced meters. No modifications are required to the multimeter as the unit is simply inserted between the test prods and the meter.

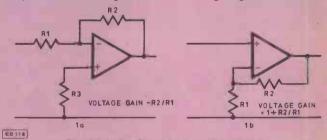
The ranges shown in the specification cover those needed in semiconductor work and the accuracy depends upon the resistors used, but it should be remembered that most multimeters have scales giving errors of more than 2 per



cent of full scale deflection, so high accuracy in the unit is usually unjustified. In use Metermate is as simple as any mutimeter, and measurements on transistor circuitry can be made with confidence that the circuit under test is not affected by the meter. Measurements on CMOS, unijunction and other high impedance circuits are possible, provided the loading effect of the input resistor chain is allowed for. Measurements of a.c. at audio frequencies give valuable information about signal gain; by using the unit in conjunction with a simple sine wave oscillator frequency response curves for amplifiers, filters, etc. can be measured. The higher sensitivity ranges are useful for measuring transducer outputs, and as a null detector in bridge circuits.

#### **DESIGN THEORY**

Op amp circuit design is very simple, as gain depends only on passive components. Two basic op amp configurations are possible, inverting and non-inverting (Figs. 1a and 1b).

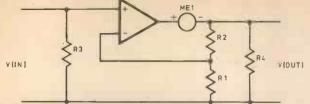


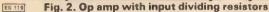
#### Fig. 1a, b. Basic op amp configurations

The inverting circuit gives an input impedance dependent upon R1, R2 and R3, but the non-inverting circuit has a high input impedance (about  $1M\Omega$  for the 741) independent of R1 or R2. By fixing the gain of this circuit accurately it can be used with input dividing resistors to feed a meter, as

#### SPECIFICATION

D.c. and a.c. voltage (7 ranges): 20V, 10V, 5V, 1V, 100mV, 10mV, 5mV. D.c. and a.c. current (5 ranges): 1 $\mu$ A, 10 $\mu$ A, 100 $\mu$ A, 1mA, 10mA. Input impedance: equivalent to 1M $\Omega$ /V. Accuracy: d.c. and a.c. ranges 2 per cent (depending on resistors and meter used). Frequency response: 20Hz to 30kHz. Resistance (4 ranges): 1 $\kappa$ Ω, 10 $\kappa$ Ω, 100 $\kappa$ Ω, 1MΩ. Forward reading, linear scale. Current consumption: 10mA. shown in Fig. 2. This circuit operates as follows. If an input current of i(in) flows through R3 (negligible current flows





into the op amp due to its high input impedance) developing an input voltage V(in):

 $V(in) = i(in) \times R3$ 

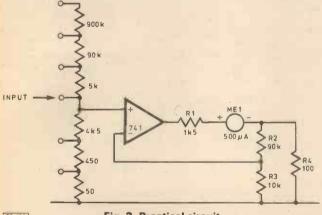
Gain is fixed by R1 and R2 at 1-R2/R1, so the output voltage V(out) is given by:

V(out) = i(in). R3(1-R2/R1)

This voltage is developed across R4 by current flowing from the op amp through the meter. If a current of i(m) is needed to give f.s.d. on the meter, then:

$$R4 = V(out)/V(in) = i(in)/i(m)$$
.  $R3(1-R2/R1)$ 

Fig. 3 shows a practical circuit with values suitable for meters with 0.5mA f.s.d. The gain is fixed at 10, so with

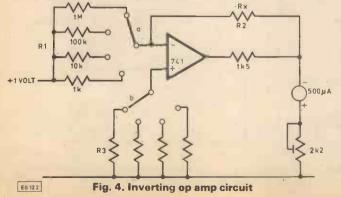


E0 120

#### Fig. 3. Practical circuit

5mV at the op amp input (developed across the 5k $\Omega$  chain by 1µA) the output voltage is 50mV. As R4 is 100 $\Omega$  the meter current i(m) will be 0.5mA. Higher current ranges are made by reducing the input resistance from 5k $\Omega$  so that 5mV is still fed to the op amp. Series resistors drop higher input voltages to 5mV.

It is not possible to use the non-inverting configuration to give a linear scale for resistance, so Metermate switches the circuitry around IC1 to give an inverting circuit with the unknown resistance in the position of R2 in Fig. 1a. R1 is a switched range of reference resistors, and a 1V stabilised supply is fed to the input. When R1 = R2 the output is 1V



(as the gain is then 1) and is fed to the meter with a  $2 \cdot 2k\Omega$  resistor in series, as in Fig. 4. R3 is switched so that the impedance at both op amp inputs are approximately equal.

When measuring a.c. voltage or current with Metermate IC1 functions in exactly the same way as for d.c. except its output is not fed to the meter but to a full wave rectifier (Fig.

COMPONENTS	
Resistors R1 R2 R3, R12, R16, R32, R3 R4, R9, R10, R17 R5 R6 R7	1 M 100k 33 10k (5 off) 1k (4 off) 470k 47k 47k
R8 R11 R13, R34 R14, R18, R35 R15 R19 R20 R21	470 1k5 68k (2 off) 22k (3 off) 100 390 6k2 180
R22 R23, R24 R25 R26 R27 R28 R29 R30 R31 R36 R37	220 1Ω0 (2 off) 4Ω7 12 33 120 330 1k2 3k3 680k 220k
R37 R38 R39 R40, R41, R42 R43	1M8 2M2 10M (3 off) 5k6
Potentiometers VR1, VR3, VR4 VR2 All sub min hor.	10k (3 off) 2k2
Capacitor C1	1μ elect.
Semiconductors D1, D2 D3 D4, D5, D6, D7 D8 TR1 IC1, IC2	1N4004 (2 off) BZY88 3V Zener 1N914 (4 off) TIL 209 I.e.d. BC109 741 (2 off)
Switches S1 S2 S3 S4	1 pole 12 way rotary 2 pole 6 way rotary 3 pole 3 way rotary 2 pole slide
Miscellaneous 6 off 3 off 2 off 2 off Veroboard Case	2mm plugs and sockets knobs PP3 batteries battery clips
Resistors R23 to R42 se	e Table 2 and text

Resistors R23 to R42 see Table 2 and text.

5). Gain is R2/R1, so with an input of 50mV from IC1, 222mV will appear across the 400 $\Omega$  resistor, giving a current of 0.555mA. The overall gain is therefore 1.11. This factor is necessary as we wish the meter to read true r.m.s. values, not average full wave rectified a.c.

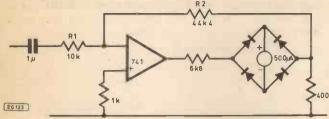


Fig. 5. Circuit for a.c. voltage and current tests

#### THE CIRCUIT

The full circuit, including meter switching arrangements, stabilised 1V supply, and a complete input resistor chain, is shown in Fig. 6. Two diodes protect the input of IC1 from excessively high voltages. The last position of S2 is used to make IC1 available as an op amp. SK1 is connected to the non-inverting input, with S1 selecting the resistance to earth, SK3 is connected directly to the inverting input, and the meter may be left in circuit or the op amp output taken directly from SK5 with the meter disconnected. The effective circuit is shown in Fig. 7.

#### CONSTRUCTION AND CALIBRATION

DC/AC 1MO2

O S2a,b

O \$ 30,b,c

100 my RH

100k03

10 kOL 1k05

DCO1

ACO2

no

06 DPAMP

The prototype unit was built on Veroboard, giving the compact layout shown in Fig. 8. The constructor must decide whether to use 1 per cent resistors or an accurate bridge to select values. The former is costly and the latter can be time consuming. If it is decided to buy close tolerance resistors use the components list to select values, and assemble the entire circuit, taking care with both the i.c. and diode orientations. The connection of the chain resistors is shown at Table 1 and the link wiring for the switches is shown in Table 2. Otherwise leave off the board all the chain resistors R23 to R42 and connect temporary resistors for R12, R13, R14 and R15.

Wire a temporary resistor of around  $4.7k\Omega$  across the

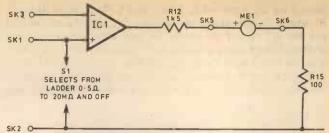


Fig. 7. Effective circuit of the Metermate EG 124

unit's input, and switch S1 to 1µA, S2 to d.c./a.c. and S3 to d.c. Metermate can now be used as a sensitive detector in a simple bridge circuit (Fig. 9) once a meter of appropriate

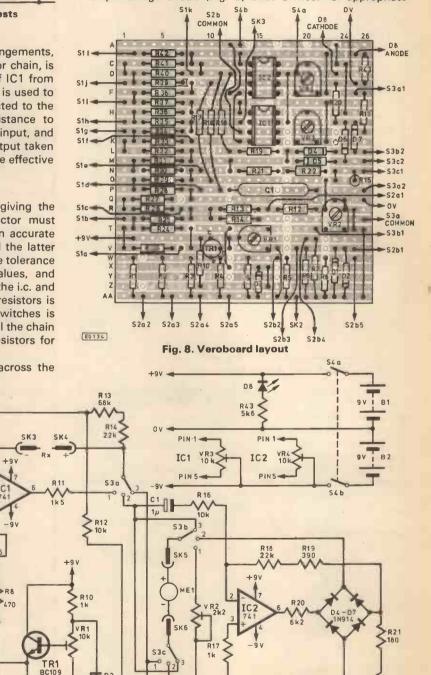


Fig. 6. Complete circuit diagram of the Metermate

R9

03

BZ Y88

R15

S2b

E O

0

>R6 SR7

D1 D2 1N4004 1N4004

R22

220

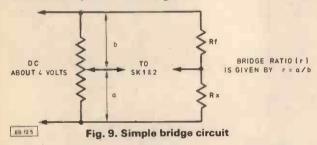
SK2

EG 126

sensitivity is connected to SKs 5 and 6. At balance Rx = r xRf where r is the ratio of the bridge arms. Use the off-set null pot for IC1 to set the meter at zero with the input leads shorted before using the unit. Accurate fixed resistors within a factor of 10 from Rx should be used for Rf. Replace R12, R13, R14 and R15 with accurately measured resistors first. Assemble the input resistor chain by measuring R23 and

SWITCH 1	COMBINED RESISTANCE VALUE	CONNECTION OF COMBINED RESISTORS		
Ra = R23, R24 Rb = R25 Rc = R26, R27 Rd = R28, R29 Re = R30, R31 Rf = R32, R33 Rg = R34, R35 Rh = R36, R37 Ri = R38, R39	0Ω5 4Ω5 45 450 4k5 5k 90k 900k 4M	In parallel In series In series In series In parallel In series In series In series		
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$				

R24 first, then adding R25 and measuring the total resistance. Repeat for R26 to R42, checking the overall resistance each time. If an accurate meter is available the 10mA, 1mA and higher voltage ranges can be checked, and the resistors adjusted accordingly.



R1 to R4, R16, R18, R19, R21 and R22 should also be accurately measured and added to the board. The resistance ranges are calibrated after setting the stabilised  $1\vee$  supply. To do this connect SK 1 to the emitter of TR1 and switch the unit to read  $1\vee$  d.c. f.s.d. Adjust VR2 to give a reading of  $1\vee$ . Connect a known accurate resistor across the Rx sockets, and adjust VR3 to give a true resistance reading on the meter. It only remains to set the off-set null pot on IC2 with the unit switched to a.c. and the input shorted, and setting up is complete.

#### **USING METERMATE**

Once set up Metermate is used in exactly the same way as a conventional multimeter. When making voltage or current readings, always start with S1 set to the highest range to avoid overloading the meter, and return to the off position after use. For resistance measurements, switch the unit off before setting S2 and S3, connect the resistance, and then switch on. If the unit is switched on without a resistance equal to, or less than, the range selected by S2, then the meter may be damaged as the i.c. will pass up to 10mA. The same will happen if S2 is turned to the op amp position unless external resistors are connected. Do not leave resistors connected to Rx when using the d.c. or a.c. ranges as readings will be upset.

Metermate's input impedance on voltage ranges is

approximately the resistance of the input chain selected; on the 20V range it is  $20M\Omega$  and on the 5mV range only  $5k\Omega$ . When measuring voltages, ensure that the input impedance is at least ten times the impedance of the voltage source. In practice this means that voltages over 1V can be accurately measured in circuits with impedances of up to  $500k\Omega$ , as in most transistor circuits, TTL, many linear i.c.s and some CMOS.

-				
FR	MO	то		
S1f		S2b1		
SK	1	S1 common		
S2t	o6	SK1		
OV		S1a		
OV		SK2		
S2a	a common	SK3		
S3I	b <b>3</b>	S3a2		
S30	c3	S3b3		
\$3	a <b>3</b>	S3c1		
SK	4	S3a common		
SK	5	S3b common		
SK		S3c common		
	TABL	E 2		

If a.c. voltages or currents with a d.c. component over 0.5V are to be measured then a blocking capacitor should be used at the input, 1µf being suitable. True r.m.s. readings will only be obtained when measuring a pure sine wave. Frequency response is substantially flat from 30Hz to 30kHz, dropping by 3dB at 10Hz and 50kHz.



#### MODIFICATIONS

Resistance values can readily be changed to suit different meter sensitivities, or to provide different voltage or current ranges (useful if the meter scale is 0 to 3 instead of 0 to 10). For a meter (or multimeter) of sensitivity i mA.

R15 = 50/i (Ω) R30 = 200/i (Ω) 
$$\vee$$
 R3 = 1/i (kΩ)

To give a voltage range of V millivolts the resistance R between IC1's non-inverting input and the voltage source is given by:

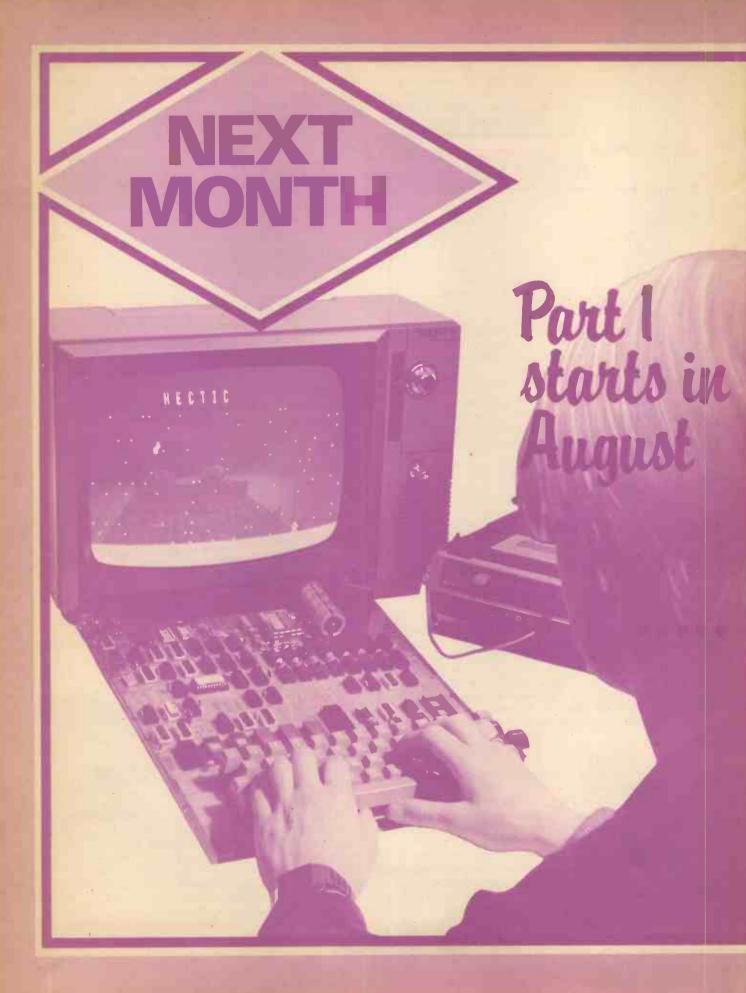
#### $R = V - 5 (k\Omega)$

For a current range with a f.s.d. of i mA the resistance to earth (i.e. across which the voltage is developed) is:

 $R = 5/i (\Omega)$ 

A useful feature sometimes found on good quality multimeters with decade ranges is a scale  $\div 2$  switch. If a  $100k\Omega$ 1 per cent resistor is put in series with R14 with a normally closed switch wired across it, the switch will change the gain of IC1 from 10 to 20, doubling the sensitivity of Metermate on every range (or dividing the f.s.d. by 2).

Ingenious constructors can doubtless think of other modifications to Metermate's circuit. Although the unit was designed to uprate an existing multimeter, by building the circuit in a case with its own self-contained meter, an accurate multimeter could be constructed.



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# ELECTRONICS

OUR AUGUST ISSUE WILL BE ON SALE FRIDAY, 13 JULY 1979

#### FOUR TONE CHANNELS SUBJECTIVELY BALANCED FOR LOUDNESS

T IS from personal experience that I appreciate how little time the performing musician has available in which to alter settings on equipment. This circuit was originally made up with this in mind, and allows rapid, click-free, switch selection of four preset tonal responses which are subjectively balanced against each other for loudness. The controls are simply two subminiature DPDT toggles, which in combination allow the four tones to be selected, and if mounted close together as Fig. 1, permit easy single handed operation, and a quick visual indication of status.

#### **THE CIRCUITRY**

The tone forming section is basically a phase splitter with a low value emitter bypass capacitor giving treble boost at the collector, the signal from the latter being the basis of the two types of treble tones. A single supply rail is used, with d.c. bias for TR2 and IC1 being derived from a single resistive divider. Switching clicks are eliminated by ensuring that no side of a capacitor unless at a d.c. level is ever left without an earth reference path. See Fig. 2.

In an early prototype, the input was simply fed, as in typical commercial treble boosters, to the base of, in this case, TR2. But this did not give good results with guitars, since (a), the input impedance was quite low, becoming lower still at higher frequencies, and (b) internal noise was a problem in treble boost settings. Adding the FET source follower as an input buffer solves both of these problems." The input Impedance Is now uniformly high (by instrument standards), and noise is greatly reduced by presenting a low a.c. Impedance to the base of TR2. The overall noise performance now depends almost entirely on the 741, and so it is recommended that for such as recording work, a 741N be used; for stage use a 741C is more than adequate.

> Switched Tone Treble Boost

The Settings (Switch positions refer to Fig. 1) Normal setting (2/4) Signal is routed direct to the output amp.

**Soft** setting (1/4) Modified treble cut and a restricted bass response. Gives a useful accordianlike tone to chords played on Brass ensemble type keyboards, which usually produce sawtooth waveforms. Gives a rounded tone with Fender bass.

**Sharp** setting (1/3) Treble boost, but with extreme treble (from 3kHz) rolled off and also some cancellation of mid frequencies due to antiphase mixing with emitter signal. Brightens a "muddy" electronic piano without sounding tinny, gives a good "church" organ tone with string ensembles, and gives presence boost with guitars.

**Bright** setting (2/3) Treble boost without upper high roll off. Gives a really crisp, "tingly" guitar treble boost, while still retaining good bass response.

#### **THE CONSTRUCTION**

The unit is best built in a diecast aluminium box which is earthed via *one* only of the jack sockets. Internal interwiring could be greatly reduced by rearranging the suggested stripboard layout to accept direct p.c.b. mounting switches. The only internal connection that needs to be made with screened cable is that between the Input jack socket and the p.c.b., the screen should be earthed at one end only. A PP3 is adequate for the power supply (drain is approx. 3mA). If any unwanted distortion is evident when using very high output instruments, two PP3s can be used in series to give +18V. This allows a higher input before overload, but



obviously there is a limit, after which series resistance can be added at the input to attenuate the signal. At + 18V, R5. needs to be reduced to about 15k to preserve electrically silent switching.

Apart from being made up as a self-contained unit, the circuit could also find use built into the front end of an instrument amplifier, in which case the supply can be anywhere from 9 to 18V, and extra inputs can be added using the 741 as a virtual earth mixer with gain, see Fig. 4. If the series resistance Rs is not less than 220k in each case, then up to about four inputs can be added, with or without their own volume/tone controls to suit.

#### SOME GENERAL POINTS

The following are notes on treble boost effects and some notes for guitarists.

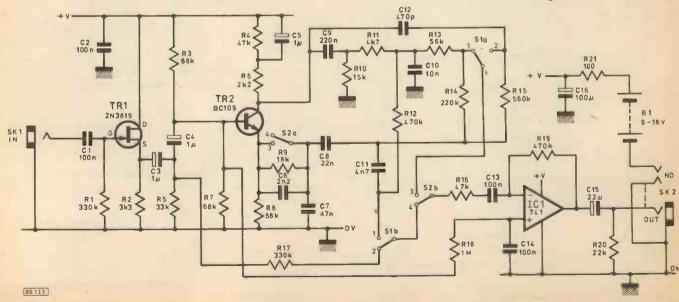
(1) The unit complements Gibson and similar pickups well, while Fenders tend to already have a surfeit of treble and do not need further treble boost.

(2) As with any signal processing unit, certain styles of playing will benefit, others will not suit the effect at all. The result you get depends to a large degree on how it is used. With guitars, for example, using the plectrum at different positions along the strings gives widely different timbres and the difference this makes to the sound becomes more noticeable when the harmonics, or "overtones" above the fundamental, are emphasised by treble boosting. For example, try selecting main pickup and playing with the plectrum hand higher up than usual, around the 16th fret. Using gentle plectrum strokes here can give a nice effect on chords with the treble boost settings.

(3) The Sharp setting can be used to heighten the audible effect of phasers, which being swept multiple notch filters, work by selectively atténuating varying components of the audio signal. With guitars, treble boost at about 1 to 4kHz

#### Fig. 1. Switch settings

Fig. 2. Circuit diagram



gives them more harmonics to work on. Above 4 to 6kHz however, the notched responses of many phasers flatten out, and so the Bright setting, which boosts mainly in this region, while still altering the overall phased tone, may hardly affect the apparent "movement" in the phasing effect, as it is boosting in a region of static frequency response. *True* phasing, which is now known as flanging, operates by modulation of time delay, as opposed to phase delay, and produces a greater number of notches. These extend above 6kHz to as high as the particular flanging method used allows, and so boosting of any part of the high end of the audio frequency spectrum can heighten the subjective effects.

(4) The effect of the treble boost on Bass guitars is to help give the often heard "click" to the attack transient at the beginning of each note, an effect which may bass players today favour.

(5) A high impedance input, as on this unit, gives smoother working of guitar volume controls and a generally improved responsiveness to the pickups. Some guitars however, are still wired as Fig. 5, where turning down the volume control puts more resistive load on the pickup, affecting tone and

sustain qualities. This rather defeats the object of a high impedance input, and so it is both simple and worthwhile to rewire as Fig. 6.

(6) No matter how a guitar's innards are wired, signal degradation will often occur by mains hum pick up, by noise/interference pick up, by the lead being microphonic (crackling when jostled or kicked), and by loss of high frequencies due to the capacitance of the screened cable. The longer the lead you use between guitar and amp, the more noticeable these all become, and turning down a guitars, volume control under these conditions will cause even more high frequency losses. An effects unit with a low impedance output, such as this design, can be used to avoid these problems. Use a reasonably short, *not* coily, screened cable from guitar to unit, then any long stretch of cable can be used between unit and power amp without signal degradation.

#### Fig. 3. Stripboard layout

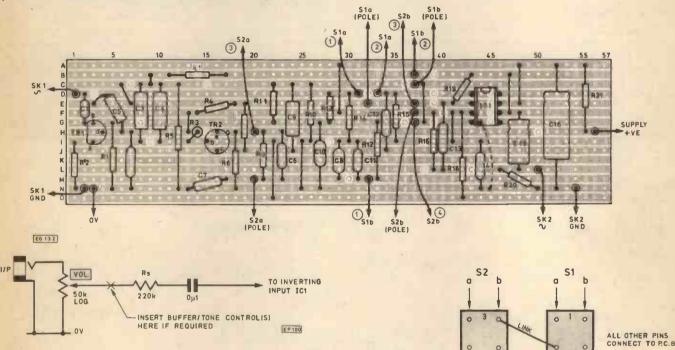
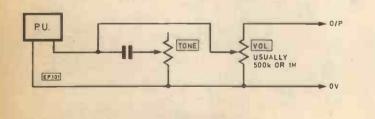


Fig. 4. Suggested arrangement for extra input



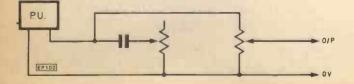


Fig. 5. Some guitars are wired like this, and can be rewired as shown in Fig. 6.

0

0

Fig. 3a. Tone switch wiring

Fig. 8. Guitar rewired to maintain a more constant load across the P.U.

EP95

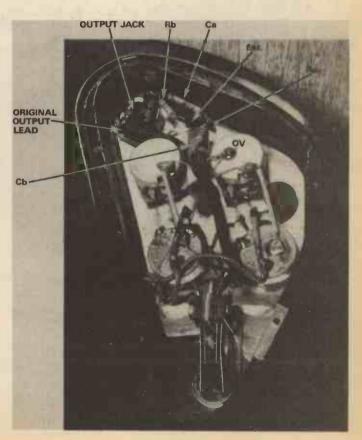
Resistors		C9	220n
R1, R17	330k (2 off)	C10	10n
R2	3k3	C11	4n7
R3, R7, R8	68k (3 off)	C12	470p
R4, R16	47k (2 off)	C15	22u
R5	33k	C16	100u
R6	2k2		
R9	18k		
R10	15k	Transistors	
R11	4k7	TR1	2N3819
R12, R19	470k (2 off)	TR2	BC109
R13	56k		
R14	220k	A DESCRIPTION OF THE OWNER OWNER OF THE OWNER OWNER OF THE OWNER OWNER OF THE OWNER	
R15	560k	Integrated Circuits	
R18	1M	IC1	741
R20	22k		
R21	100		
		Miscellaneous	
		SK1	Standard Jack socket
Capacitors		*SK2	Switched jack socket
C1, C2, C13, C14	100n	Veroboard, suitable	case, 9V battery, d.p.d.t. toggle
C3, C4, C5	14	switches (2 off)	
C6	2n2		
C7	47n		
C8	22n	*Bulgin type J21 (Home	radio)
Las Street	No. of Street,		S. S. S. S.

The photograph shows the f.e.t. preamplifier installed in the author's guitar, using the non-battery system

(7) An alternative way of avoiding the signal degradations mentioned is to have a low impedance output buffer stage inside the guitar itself. A few guitar manufacturers are now using FET preamps with batteries installed in the guitar. This works extremely well, and much fuss is made of it in sales write-ups. The increase in the price of the guitar, however, is usually far more than is justified by the inclusion of the few extra components.

I have found an interesting way to add an internal FET preamp to any guitar without having to install batteries, for which there is often no space without cutting into and enlarging the electrics cavity (not recommended with something that may have cost the owner a few hundred pounds). This circuit utilises the power supply of an existing effects unit or amplifier with a 9 to 18V power line, it costs very little, is simple to install and can be removed just as easily, leaving the guitar exactly as it was. The simplicity of using an ordinary guitar jack lead is retained, by using the central conductor to carry both the power supply from the unit or amplifier to the FET and the signal from the guitar back to the amplifier *simultaneously*.

The advantages obtained by having the preamp are identical to those outlined in notes 5 and 6. The FET is a source follower, obtaining its power supply via R*, in the effects unit or amplifier, and then Rb, the drain resistor. The drain is heavily decoupled by C2, to prevent the signal from the source being cancelled out to zero. A sub-min toggle can be added for restoring the output of the guitar to completely normal operation, or if there is room, a second



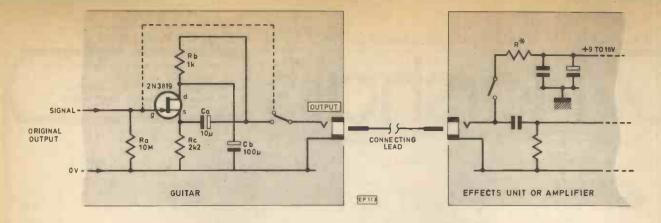


Fig. 7. Installation of a preamp in the guitar, without the need of an internal battery. No isolating capacitor is necessary at the preamp input. The only extra components required in the Treble Boost unit are the switch and 4k7 resistor R^e



jack socket can be wired direct to the FET gate. The only modification to the effect unit/amplifier is to add a single 4k7 resistor from the power line (which should be, and usually is, well decoupled) to the input jack socket, before the input capacitor. The guitar toggle switch changes the input back for normal operation.

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JA7         C010           056         C010           250V-7mm         .05           Value         Price           J011         C081           L0015         C007           J015         C007           J015         C008           J015         C007           J012         C007           J012         C007	0.88         0.17           1.0         0.17           1.5         0.17           2.2         0.17           3.3         0.17           4.7         0.17	203152 Mitri: MC33/µ 4Mit: MC25/µ (2.35 1.2766 Mit: MC25/µ (2.35 4.134394 Mitri MC25/µ (2.35 4.433319 Mitri MC25/µ (2.35 5.00 Mitr: MC23/µ 5.00 Mitri MC25/µ
0033 r066 22 c016 0047 c007 033 c010 27 c023 0088 c007 047 c010 33 c010 c007 048 c011 39 c030 400+100mm 1 c016 c010 400 c007	68         C0.21         C0.23           10         £6.17         £0.21         £0.23           15         £0.21         £0.21         £0.23           22         £0.21         £0.21         £0.23           33         £0.21         £0.23         £0.36	8 00 MHr HC25/µ 10 MHr HC25/µ £2.85 10.7 MHr HC25/µ £2.85 18 MHr HC25/µ £3.15 Sectarts Proce 20 MHr HC25/µ £3.15 HC5/µ £8.15
15 C0.16 56 C0.30 022 C0.07 .22 C0.16 68 C0.30 047 C0.09 Alto syntable 1000 and 4000	47         6021         £023         £075           68         68.23         £0.75         £0.75           150         £0.95         £0.95         £0.75           220         £1.00         £1.00         £1.00	48 MHr HC25/µ £2.96 HC13/µ £0.15 100 MHr HC25/µ £3.86 HC25/µ £8.10
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CA3028         C0.70         CA3080         feast         TBA5700         E2.20         TDA520AD0         C4.50           CA3028A         C4.90         CA308A         C2.10         TBA651         C1.00         TDA5252         C3.50           CA3028A         C4.90         CA306A         Fe486         TBA651         C1.20         TDA5250         C2.50           CA3028A         C4.96         FBA651         C1.20         TDA5250         C2.50         C3.50           CA3028A         C9.96         CA30989         C1.96         FBA700         E2.20         TDA2540         C3.40           CA3029A         C9.96         CA30989         C1.90         FBA700         E2.20         TDA2540         C1.84           CA3030A         C2.40         TBA700         C2.45         TDA2540         C1.46           CA3030A         C2.70         CA3140         C1.40         FBA7500         C1.76         TDA2501         C2.45         TDA2	NEW TELETEXT CHIPS         from Mullard – available now from Marshall's – Sole UK distributor to the hobbyist market.         SAA Series, for the reception, display and control of television based text display systems. Suitable for 'CEFAX and ORACLE' Remote Control.         SAA 5010 Infrared/Ultrasonic       £3.48 £8.13 SAA 5012 Binary Input Tuners         P.D.A.	SN74474 C10 7451274         C0.50 7452300         C1.65 SN74547347124           SN74474 C100 7451274         C0.50 7452330         C1.65 SN74547447134           SN74456 72.62 74512374         C0.50 7452330         C1.65 SN74547447134           SN74456 72.62 74512374         C0.50 7452330         C1.60 SN745044         C0.30           SN74456 72.62 74512384         C0.57 7452394         C1.00 SN745044         C0.30           SN74594 72.00 7451384         C0.66 7452394         C1.00 SN745044         C0.30           745004         C2.7451384         C0.67 7452394         C1.35 SN74534         C0.30           745004         C2.7451384         C1.00 7452394         C1.35 SN74534         C0.30           745004         C2.7451384         C1.30 7452394         C1.35 SN74534         C0.30           745004         C2.7451349         C1.30 7452394         C3.35 SN74544         C0.40           745044         C2.7451549         C1.30 7452394         C3.55 SN74544         C0.40           745044         C2.7451549         C1.30 7452394         C3.55 SN74544         C0.40           745044         C2.7451549         C1.30 7452394         C3.55 SN74544         C0.40           745044         C2.7451549         C1.30 74527394         C3.55 SN74544         C0.40<
CA3040         CA376         TAA621         CE300         TRA9000         C1300         TIOBICP         C0420           CA3044         CL85         TAA6818         CL45         TAA105         CL400         TIOBICP         C0420           CA3044         CL85         TAA6818         CL45         TAA105         CL40         TIOBICP         C0420           CA3043         CL20         TAA6518         CL45         TACA106         CL40         TIOBICP         C0420           CA3043         CL20         TAA900         CL36         TCA220         CL40         TIOBICP         C0420           CA3045         CL30         TAA930A         CL36         TCA220         CL40         SN76001N         CL30           CA3045         CL22         TAA930A         CL45         TCA2705         CL40         SN78000N         CL30           CA3047         CL22         TAA930B         CL45         TCA2705         CL40         SN78000N         CL30           CA3047         CL27         TAA930B         CL45         TCA2705         CL45         SN78000N         CL30           CA3047         CL27         TAA930B         CL45         TCA400         CL45         SN78000N         <	TELETEXT (Dedicated I.C.'s)       £8.71         SAA 5020 TIC (Timing Chain)       £11.61         SAA 5020 VIP (Video Input Processor)       £11.61         SAA 5040 TAC (Teletext Data Acquisition and Control       £31.82         SAA 5050 TROM (Teletext Read-Only Memory)       £18.86         TIC, TAC & TROM are NMOS — VIP is linear bipolar, we recommend using DIL sockets with these chips.       \$1000 Notest and	741527W         C020         741517W         C030         741528W         C030         741518W         C030         741528W         C035         SV74123W         C036         SV74123W         C036         SV74124W         C037         741521W         C030         741521W         C030 <th< td=""></th<>
TRANSISTORS (For full range see catalogue)           201736         C136 201902         77.00 204/235         C136 204/235	C1.35         C0.466A         C0.70         ECI097A/B         C0.16         ST15         C0.36         TP29A         C0.44         TS34         C1.65           C0.86         40513         C1.86         ECI097A/B         ECI18         ST155         C1.06         TP29A         C0.44         TS34         C0.65           C0.86         40514         C1.86         ECI097A/B         ECI18         ST155         C1.16         TP29A         C0.44         TS34         C0.67           C0.86         40514         C1.86         ECI097A/B         EC118         ST122         C0.66         TP29A         C0.44         TS34         C0.97           C0.86         AC151         ECA3         ECI13         C222         EF123         C0.66         TP29A         C0.44         TS34         C0.67           C0.86         AC151         ECA3         EC116         C022         EF127         C040         TP29A         C0.84         TS35         C0.66           C0.86         AC153         EGA6         EC116         C022         EF127         C022         TP33A         C0.87         TS357         C0.82         TS357         C0.66         E0.66         E0.16         E0.67         E1.66         <	7445287         0227         74457100         01.00         7445398         0228         557413394         0128         5584         64         57441374         0107         7445734         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         7445744         0107         74
20324         C030         244661         C010         244288         C0.22         24159         C1.15         60374           20325         C1.10         244664         C1.02         24428         C0.22         34159         C1.15         60374           203326         C1.02         244264         C1.25         244216         C0.22         34201         C1.36         60391           203331         C2.20         244274         C2.26         74201         C1.36         60391           203355         C0.30         244051         C1.25         744274         C0.27         40051         C1.70         60382           203355         C0.30         244051         C1.02         244274         C0.27         40051         C1.70         60382           203355         C0.30         244051         C1.02         24427         C0.31         40323         C0.70         6030         604051           2034556         C0.22         244121         C0.27         244021         C0.31         6034         604050         604051         6040         60354         604050         604051         C1.84         60405         74414         C0.34         6034         604050         6034 <td>C185         AD149         C285         GC136         C213         GF17         C627         TM42C         C106         TG51         C023           C105         AD160         C110         GC136         GC24         BF176         GC27         TM450         C602         TS52         C033           C106         AD161         C1.00         GC138         GC44         BF176         GC37         TM470         C643         TS53         C033           C106         AD162         C1.00         GC138         GC44         BF176         GC37         TP1110         C037         TD170         C646           G10         AF106         G60         GC141         GC32         BF181         GC37         TP1110         C037         TD110         C037         TD130         C037</td> <td>New Low Price "KIM" The Ready to Use Microprocessor System Not a kin but supplied fully tested, wird and guaranteed Expandable Memory that graves with poor system not just an evolution kin. Starting with KIM 1 at E107 95 VAT inclusive you get immediate capability which can be expanded to a complete system capable of addressing up to 55K BYTES of Memory. KIM IS SERAIDABLE Expand as not learn up to 55K KIM 4 – Mathemater Ham SerVice 13 e-poors upp) The Service of the system capability which all a poors upport KIM 4 – Statuc Addre and plags and motioned KIM 4 – Mothemater Ham SerVIC 13 e-poors upport CT655</td>	C185         AD149         C285         GC136         C213         GF17         C627         TM42C         C106         TG51         C023           C105         AD160         C110         GC136         GC24         BF176         GC27         TM450         C602         TS52         C033           C106         AD161         C1.00         GC138         GC44         BF176         GC37         TM470         C643         TS53         C033           C106         AD162         C1.00         GC138         GC44         BF176         GC37         TP1110         C037         TD170         C646           G10         AF106         G60         GC141         GC32         BF181         GC37         TP1110         C037         TD110         C037         TD130         C037	New Low Price "KIM" The Ready to Use Microprocessor System Not a kin but supplied fully tested, wird and guaranteed Expandable Memory that graves with poor system not just an evolution kin. Starting with KIM 1 at E107 95 VAT inclusive you get immediate capability which can be expanded to a complete system capable of addressing up to 55K BYTES of Memory. KIM IS SERAIDABLE Expand as not learn up to 55K KIM 4 – Mathemater Ham SerVice 13 e-poors upp) The Service of the system capability which all a poors upport KIM 4 – Statuc Addre and plags and motioned KIM 4 – Mothemater Ham SerVIC 13 e-poors upport CT655

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2.5"×6" 61p		7414 45p	74173 BOp	4511 700	BD131 36p TIP32 46p
3.75"×5" 60p	BRIDGE	7416 24p	74174 <b>80</b> p	4516 84p	B0132 35p TIP33 86p
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POTENTIQ/	1N916 5p	7440 10	74193 88	AC128 17p	BF179 26p 2N706 10p
METERS	1N4001 4m		74194 560	128/178 MP 36p	BF180 200 2N1131 100
1-Kohm to 2Mohm	1N4004 Be	7441 48p 7442 48p	74195 500	AC142 18p	8F181 8p 2N1132 24p
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220	1N5400 13p	7445 84	74197 32# 74198 100#	AC176 10p	BF183 20p 201304 iOp
	1N5404 29p	7446 500	74198 1007	AC187 23p AC188 20p	BF184 20p 2N1305 40p BF185 20p 2N1308 30p
CERAMIC		7447 500	74133 144	AD149 850	BF185 20p 2N1308 38p BF194 8p 2N1308 j0p
CAP.		7448 500		AD161 35p	8F196 8a 2N1613 18a
(50V)	VOLTAGE	7450 100		AD162 35	8F197 00 2N1711 20p
22pf to 50nf 3p	REGULATORS	7451 128	CMOS	AF125 228	BF198 80 2N1893 260
	7805 60p	7453 12#	4000 12#	AF128 22p	8F200 33 2N2217 24e
POLYESTER	7812 800	7454 100	4001 12p	AF127 22m	8F224 10 2W2219 21p
CAP	7815 800	7460 14	4002 120	AF139 32m	BF257 14m 2N2389 10m
(260V)	7818 60p	7470 100	4008 689	AF186 54e	BF258 280 2N2484 180
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-033047;	7905 80p	7473 12#	4008 84#	ASY53 33p	BFR39 100 2N2906 10p
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-15, -22 -33µF	7915 BOp	7475 260	4010 35p	A\$Y55 33p	BFR79 18p 2N2928 10p
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-4788µF 12p	7924 80p	7480 289	4012 1Zp	BC108 6p	BFX29 20p 2N3054 50p
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8814, 10014 7p		7495 269	4019 400	BC157 6	BSX20 10p 2N3710 8p
1500F Ba		7496 289	4020 500	BC158 Bp	BU205 130p 2N3711 8p
220µ/f 0p	CA3028A 86p CA3046 76p	74105 400	4021 000	BC159 bp	BU208 150p 2N3772 100p
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40 pin 40p	7400 100	74145 300	4047 88	BC214 10m	
	7401 100	74151 450	4048 500	BC303 25m	UNIT 4
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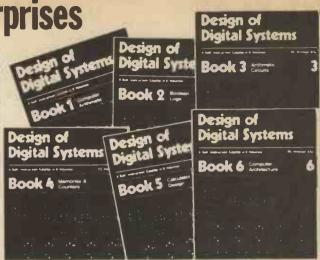
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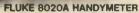
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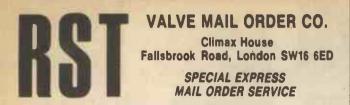
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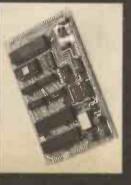
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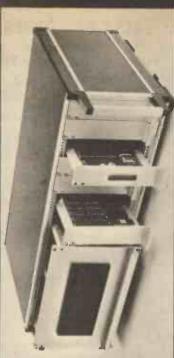
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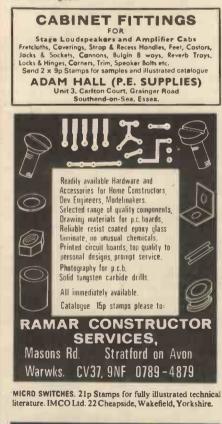
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