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6/9V. D.C. Miniature Geared Motor, precision built incredibly powerful for size
approx speed (a 6 V .- 60 rpm 40 ma.
approx. speed ( G . 9 V . 80 rpm 50 ma .
approx. speed (G $9 V .-80 \mathrm{rpm} 50 \mathrm{ma}$.
27 mm dia.. 38 mm length. 55 gr . weight.
drive spindle $5 \times 3 \mathrm{~mm}$. dia.
$\mathbf{f 2 . 5 0}$ post paid ( $\mathbf{~} 2.70$ inclus. VAT).
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230/240V. a.c. 1 rpm Synchronous geared Motor, mf. HAYDON. $230 / 240 V$. a.c. 2 rpm Synchronous geared Mator, mf.
CROUZET. Price above 2 Motors: $£ 2.90+30 \mathrm{p}$. P. \& P. ( $\mathbf{3} 3.46$ inclus. VAT). N.M.S.
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A portable mains-operated Miniature Sound Synihesiser, with keyboard crrcuits. Although having slightly fower facitities than the large P.E. Synthesiser the functions offered by this design give it great scope and versatility Consists of 2 log VCOs. VCF. 2 envelope shapers, 2 vollage onirolled amps. keyboard hold and coniol circuls. mixer power supply.
Set of basic component kits (excl. KBD R's
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Very sophisticated music synthesiser for the advanced con, pho puts performance betore price. Dotais in our lists.

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(P.E. Nov/Dec 77)

Enables a voltage controlled synthesiser to automatically play pre-programmed tunes of up to 32 pitches and 128 notes long. Programs are keyboard initiated and note length and rhythmic pattern are externally variable. (Please use order codes quoted in brackets. 1

Main Circuit (Nov) excl sw's (KIT 76-1) 18.03
Power Supply (KIT 76-3)
Trigger Inverter and Alt. Output (KIT 76-2)
LED Counter (KIT 76-4)
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## P.E. STRING ENSEMBLE (PE Mar-July 78)

Basic component st
Power Supply (KIT 77-1)
Tone Generator (KIT 77-2)
Diode Gates (KIT 77-3)
Chorus Generator (KIT 77-4)
Voicing System (KIT 77-5)
$\qquad$
Double-sided PCB ior Power Supply, Tone Generator \& Diode Gates with most of the Matrix wiring as printed tracking (PCB 77L/R)
CB for Chorus Generator IPCB 77C)
PCB for Voicing System (PCB 77D)

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The basic five octave electronic piano (P.E. May/Sept 75 and Sound Design) has switchable alternative voicings for Honky Tonk, ordinary piano, and Harpsichord or a mixture of any of these three, together with facilities including fast and slow tremolo loud and soft pedal switching. and sustain pedal switching. The oodifation relains all the circuiry associated with the piano ut in addition provides an organ-voice envelope facilliy with 5 witchable pitches, variable attack and sustain, phasing and Sti.

Set of components (excl switches) for PSU, Frequency generator, Pitch and Note Divider. Envelope Shapers, Voicings Set of PCB (Order as PCB SET 71-6)

GUITAR EFFECTS PEDAL (P.E. July 75)
Modulates the attack, decay and titer characteristics of an audio signal not only from a guiter but from any audio source. producing of different switchable effects that can be further modified by manual controls. Possibly the most interesting of all the low-priced sound effects units in our Onge. Circunt

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& \text { Printed clrcuit board }
\end{aligned}
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$£ 7.69$
£5.05
$£ 1.43$
parts are shown in our lists.
CIRCUIT AND LAYOUT DIAGRAMS are supplied frce with all PCBs unless "as published"
PHOTOCOPIES of P.E. texts for most of the kits are avallable-prices in our lisis.

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## elektor electronic piano (Elektor Sept 78)

 A touch-sensitive. multiple-voicing 5 octave piano using thelatest Integrated-circuit techniques for latest integrated-circuit techniques for the keving and
envelope shaping and virtually eliminating bee hive noise envere shaping, and evinus electronic pianos. Details in our
hitherto inherent in previo lists.
digital heverberation unit (Elektor May 78)
A very advanced unit using sophisticated i.c. techniques instead of mechanlcai spring-lines. The basic delay range of
24 to 90 mS can be extended up to 450 mS using the 24 to 90 ms can be extended up to 450 mS using the
extension unir. Further delays can be obtained using more exiensions.

Main component set (KIT 78-1)

$\mathbf{C 4 5} .45$
$£ 43.36$
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$£ 1.06$
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Using i.c.s instead of spring-Illes, the main unit has a maximum delay of up to 100 ms , and the additional set extends this
sterea mode.
Main component set (KIT 83-1)
Additional Delay Set (KIT 83-2)
£26.18
$\mathbf{£ 1 8 . 2 5}$
CB (as published) to hold both ahove
£4.31

## RESONANCE FILTER (Elektor Oct 78)

This filter module has been designed to allow a synthesiser to produce a more realistic simulation of natural musical instruments.
$\begin{array}{lr}\text { Basic component set (K1T 82-1) } & \text { £15.10 } \\ \text { PCB (as published) (PCB 9951) } & \text { £3.29 }\end{array}$

## SYNTHESISER EXTERNAL INPUT INTERFACE

(P.E. Oct 78)

This unit allows externai inputs, such as guhtars. microphones etc. to be processed by the circuits within a
synthesiser. Basic component set (incl PCB) (KIT 81-1) £2.94 GUITAR MULTIPROCESSOR (P.E. Oec/Feb 78)
An extremely versatile sound processing unit capable of producing, for example, Flanging, Vibrato, Reveri, Fuzz and Tremolo as well as other fascinating sounds. May be used with most electronic instruments. Details in our lists. RHYTHM GENERATOR KITS
Several avallable - details in our lists.
GUITAR FAEOUENCY DOUBLER (P.E. Aug. 77)
A modified and extended version of the circuit published
Component set and PCB $£ 4.52$

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Maintains the natural attack whilst extending note duration,
Component set, PCB and foot switches
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## WIND AND RAIN UNIT

A manually controlled unit for producing the abovernamed
sounds
Component set (licl. PCB)

## GUITAR OVERDRIVE UNIT (P.E. Aug. 76)

Sophisticated, versatule Fuzz unit, including variable and switchable controls affecting the fuzz quality whilst petaining Tuplicate the effects from also providing filtering. Does no duplicate he allects from the Guitar Elfects Pedal and can Component and with other electronic instruments.

Component set using dual slider pol
Component set using dual potary pot
Pimped circuit board

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Simple Fuzz unit based upon P.E. "Sound Design" circuit, $\begin{array}{r}\text { E2.05 }\end{array}$

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Based upon P.E. Sound Design' circuil Component set (incl. PCB) $£ 2.94$ TRE日LE BOOST UNIT (P.E. Apr. 76)
Gives a much shriller quality to audlo signals fad through it
The depth of boost is manually adjustable.
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## WAVEFORM CONVERTER

Slightly modified from a circult published in "Elektor". Converts a saw-tooth waveform into four different waveforms: sine-wave, mark-space saw-tooth, regular triangle form, and squarewave with an externally variable mark-space ratio.

Component set (incl. PCB but excl. sw/sl

## VOLTAGE CONTROLLED FILTER (P.E. Dec. 74)

Part of the P.E Minisonic now refeased as an independen
kit for use with other synihesisers.
Component set (Incl. PCB) (Order as Kit 65-1)
AING MODULATOR (P.E. Jan. 75)
Part of the P.E. Minisonic now released as an independen Component set (incl. PCB) (Ord

## NOISE GENERATOR (P.E. Jan. 75)

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it for use with other synthesisers.
Component set (incl. PCB) (Order as Kit 60.1) £3.84
ENVELOPE SHAPEA WITHOUT VCA (P.E. Oct. 75)
Provides full manual control over attack. decay. sustain and release functions. and is for use with an existing voltage ontrolled amplifier.
Component set (incl. PCB)
ENVELOPE SHAPEA WITH VCA (P.E. Apr. 76)
This unlt has its own voltage controlled amplifier and has full This unit has its own voltage conroled amplifier and release functions.

## TRANSIENT GENERATOR (P.E. Apr. 77)

an envelope shaper, wihout VCA, having the usual attack decay, sustain and release functions. and in addition it also provides a "Repeal Effect enabling a synthesiser to be programmed to imitate such instruments as a mandolin of banjo.
Component set
£4.87

## SOPHISTICATED PHASING AND VIBRATO UNIT

Elightly modified version of the circuit published in ciektor. December 1976. and includes manual and utomatic control over the rate of phasing and vibrato Component sel

PHASING UNIT (P.E. Sept. 73)
A simple but effective manually controlled unit for introducing the "phasing" sound inio live or recorded music.
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PHASING CONTROL UNIT (P.E. Oct. 74)
For use with the above Phasing Unit to automatically control Component set (inct. PCB)

WAH-WAH UNIT (P.E. Apr. 76)
The Wah-Wah effect produced by this unit can be controlled
manually or by the integral automatic controller.
£3. 63

AUTOWAH UNIT (P.E. Mar. 77)
Automatically produces Wah-pedal and Swell-pedal sounds
uach time a new note is played.
Component sel. PCB, special foot switches
Component set and PCB. with panel switches
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For automatically reducing music volume during
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## AND OTHER PROJECTS

PHOTOGRAPHS In this advertisement
show two of our units containing some of the P.E. prolects buitt from our kits and CBs. The cases were buill by ourselves and are not for sale. though a small selection of other cases is avallable.
UISt-Send stamped addressed envelope with ail U.K. requests for free ist giving fuller details of PCBs, kits and

OVEASEAS enqulries for lis!: Europe-


## KIMBER-ALLEN

 KEYBOARDS AND CONTACTSKimber-Allen Keyboards as required for nany published circuits. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C, the keys are

| plastic, spring-loaded, fitted with actuators, and mounted on a robust aluminium frame. |  |
| :--- | :--- |
| 3 Octave ( 37 notes) | $\mathbf{C 2 5 . 5 0}$ |
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Contact Assemblies (gold-clad wire) for use with the above KBDS (1 for each note): Type GJ: Single-pole change-over
Type GJ: Single-pole change-over
Type GA: 1 pair of contacts, normally open
Type GB: 2 pairs of contacts, each pair normally open Type GE: 4 pairs of contacts, each pair normally open Type GH: 5 palrs of contacts, each pair normally open each $37 \frac{1}{2}$ p

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Printed Circuit Boards for use with most contacts (thus eliminating much interwiring) are available. Details in our lists.
P.E. TUNING FORK (P.E. Nov. 75)

Produces 84 swhith-selected frequency-accurate tones. A LED monitor clearly displays all beat note adjustments. Ideal or tuning acoustic or electronic musical instruments.

Main component set (incl. PCB)
14.93
C6.28

YNTHESISER TUNING INOICATOR (P.E. July T7)
a simple 4octave trequency comparator for use with
synthesisers and other instruments where the full versatitity
of the P.E. Tuning Fork ts not required.
Component and PCB (but excl sw.)
f7. 45

CONSTANT DISPLAY FREQUENCY METER (PE AUG 78)
A-digit frequency counter for 1 Hz to 99999 Hz with a 1 Hz sampling rate. Readout does not count visibly or flicker due to display blanking.
Component set
-This kit \& PCB are at 8\% VAT (all others are $12 \frac{1}{2} \%$ )

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Very effective circuit for reducing the hiss found In mosi tape Standard tolerance set PCBs
Superior tolerance set of components
Regulated power supply (will drive 2 sets)

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Automatically controls sound output io within a preset
Component set (incl. PCB)

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4-channel light-show controller giving a chalce of sequential, random, or full strobe mode of operation. Basic component set

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Pre-Amp Module Components set lincl. PCB) $£ 3.95$ Basic Output Circuits-combined component set with PCBs, for alphaphone, cardiophone, frequency meter and visual feed-back lampdriver circuits. Audio Amplifier Module Type PC7

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Details in lists.

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$0.068,0.1,0.15,0.22,0.33,0.47 \mu F, 110$ altogether for $£ 4.95$
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. 160 V . Total 370 for $£ 12.30$
K 006 Tantalum bead capacitors. 10 each of the following: $0.1,0.15,0.22,0.33,0.47$ $\begin{array}{llllll}0 \cdot 68, & 1 & 2 \cdot 2 & 3 \cdot 3 & 4 \cdot 7,6 \cdot 8, \text { all } 35 \mathrm{~V}: & 10 / 25 \\ 15 / 16 & 22 / 16 & 33 / 10 & 47 / 6100 / 3 \text {. Total } 170\end{array}$ ants for £14-20 small physical size. 10 each of these popular values: $, 2 \cdot 2,4 \cdot 7,10,22,47,100 \mathrm{pF}$. Tota 70 for 83.50
ing 220,470 and 1000 abse also includ ing 220,470 and $1000 \mu \mathrm{~F}$. Torat 100 for
KO21 Miniature carbon film 5\% resistors. CR25 or similar. 10 of each value from 10 A O TM. E12 series. Yotal 610 resistors, E8.00 from in to $10 \mathrm{M} \mathrm{C8.30}$
K041 Zener diades, $400 \mathrm{~mW} 5 \%$ 8ZY8B, e1c 10 of each value from 2.7 V to 36 V . E24 series. Total 280 for £15.30.


## TRANSFORMERS

All mains primary: $12-0-12 \mathrm{~V} 50 \mathrm{~mA} 85 \mathrm{p}$ ${ }^{85} \mathrm{p}$; $1 \frac{1}{2} \mathrm{~A}$ ¢2.40. $9-0-9 \mathrm{~V} \quad 75 \mathrm{~mA} 85 \mathrm{p}$; 1 A 30 V , iA Multitapped type 0 O-12-15-20-24


PAICES ARE CORRECT AT TIME OF PRESS
E. O. E. DELIVERY SUBJECT TO AVALLABILITY

## SEMICONDUCTORS POTS \& IRONS



## ZENER DIODES

400 mw
sulated
(Bay 88 )
DO
Glass encap sulated range of voltages avall-
able. $1 \cdot 3 v, 2 \cdot 2 v, 2 \cdot 7 v, 3 \cdot 3 v, 3 \cdot 9 v$, 4.3v, 4.7v, 5.1v, 5. 6v, 6.2v, 6.8v, $1.5 \mathrm{v}, 8 \cdot 2 \mathrm{v}, 9.1 \mathrm{v}, 10 \mathrm{v}, 11 \mathrm{v}, 12 \mathrm{v}, 13 \mathrm{v}$,
$15 \mathrm{v}, 18 \mathrm{v}, 18 \mathrm{v}, 20 \mathrm{v}, 22 \mathrm{v}, 24 \mathrm{v}, 27 \mathrm{v}$, $30 \mathrm{v}, 33 \mathrm{v}$, 39v, No . $\mathbf{2 4} \mathrm{Bp}$ ea.
1w-1. 5 w P/ast/c and metal encap-
sutaled Range of
avaliable $1.3 v$ of
voltages avallable. $1 \cdot 3 v, 2 \cdot 2 v, 2 \cdot 7 v, 3 \cdot 3 v$,
$3 \cdot 9 v, 4.3 v, 4.7 v, 5 \cdot 9 v, 5 \cdot 6 v, 6 \cdot 2 v$,



10w Melal stud type solo case Range of voltages avallable, 1.3


\&iv, 100v. No. Z10 35p ea.

| SILICON RECTIFIERS |  |
| :---: | :---: |
| 200 mA |  |
| 15920 50v | c0.06 |
| IS921 100v | c0.07 |
| IS922 150v | E0.08 |
| 15923 200v | 80.09 |
| 15924 300 | c0. 10 |
| 1 Amp |  |
| IN4001 50v | ¢0.04i |
| IN4002 100v | co. 05 |
| in4003 200\% | c0.06 |
| IN4004 400v | E0.07 |
| in4005 600v |  |
| IN4006 800 v | E0.09 |
| IN4007 1000v | ¢0. 10 |
| 1.5 Amp |  |
|  | ${ }_{5}$ |
| 15020100 v | co. 10 |
| IS021 200 V | c. 11 |
| 15023 400v | E0. 13 |
| IS025 600v | c0. 14 |
| IS027 800\% | c0. 16 |
| iS029 1000v | c0. 20 |
| is031 1200v | c0. 25 |
| 3 Amp |  |
|  |  |
| IN5401 100v | c0.15 |
| INS402200v |  |
| IN5404 400\% |  |
| INS406600v | c0. 21 |
| IN5407800v | 80.25 80.30 |
| IN5408 1000v | c0. 30 |
| 10 Amp |  |
| IS10/50 50 y | c0. 19 |
| 1S10/100 100 | c0. 21 |
| IS10/200 200 | ce. 23 |
| IS10/400 400v | E0. 35 |
| IS10/600 600v | c0. 42 |
| IS101800 8000 | co. 51 |
| IS10/1000 1000 | c.0.60 |
| IS10/1200 1200w | c0. 69 |
| 3 Amp |  |
| IS30/50 50v | E0. 56 |
| IS30100 100V | c0. 69 |
| 1530/200 200v | c0.93 |
| IS301400 400v | E1. 25 |
| 15301600600v | E1. 76 |
| IS30,800 800v | c1. 94 |
| 1530/1000 1000 | c2. 31 |
| IS30/1200 1280v | C2.88 |
| 60 Amp |  |
| 15715050 v |  |
| 1S70100 100v | c0. 84 |
| 15701200200 v | 81.20 |
| IS70/400 400v | E1. 75 |
| IS701600 600v | c.2. 25 |
| IS70/800 800v | c.2. 50 |
| 157010001000 v | E.3.00 |
| BYX $38 / 300064300 \mathrm{~V}$ | c0. 45 |
| BY X38/600 6A 600V | ${ }^{2} 0.60$ |
| BYX38/300 Rev 6A 300v | E0.45 |
| BYX38/600 Rev 6A 600v | ¢0.60 |

## POTENTIOMETERS

CAREON POTS (Linear Track)
Single gang with wire end terminations, $6 \mathrm{~mm} \times 50 \mathrm{~mm}$ plastlc shaft 10 mm bushes supplied wilh shake proof washer \& nut. Tolerance $\pm 20 \%$ of resistance.
1831 ik ohms $£ 0.26^{*} 1836$ 47kohms $£ 0.25^{\circ}$ $18322 \mathrm{~h} 2 \mathrm{hms} £ 0.26$ - 1837 100hohms $£ 0.26^{\circ}$ 18334 k 7 ohms $£ 0.20^{\circ}$. 1838220 hohms $50 \cdot 26^{*}$ 1834 10kohms $£ 0.26^{\circ} 1839$ 470kohms $£ 0.26^{*}$ 1835 22kohms $£ 0.26^{*} 18401$ Meg

CARBON POTS (Log Track) 1842 4h7ohms $£ 0.26^{\circ} 1846100 \mathrm{kohms} £ 0 \cdot 26^{\circ}$ 1843 10kohms £0.26* 1817 220kohms £0.26*


DUAL CARBON POTS (LIn Track) These high quallty dual gang pots are fitted whith wire end terminations and $6 \mathrm{~mm} \times$ 50 mm plastic shaft 10 mm , bush ind suptolerance $+20 \%$ but matched to within 2 db of each other. VC3
$18514 \mathrm{k7} \quad\left[0.96^{\circ} \quad 1855100 \mathrm{kohms} \quad \mathrm{c} 0.86^{\circ}\right.$



## DUAL CARBON POTS (Log Law)

 18604 Chohms $\mathrm{Eb} .86^{\circ} \quad 1864$ 100kohms $\$ 0.86^{\circ}$ 1861 10kohms $50.86^{\circ}$. $1865220 \mathrm{kchms} 50.85^{\circ}$


SINGLE GANG SWITCHED (LIN Law) These potentiometers are fitted with incorporated within the rotary action of the pot. Specification of pot is as VC1.
Switch rating 1.5 mps at 250 V .
1870467 thms $60.655^{\circ}-1974100$ kahms $50-65^{\circ}$




SWITCHED POT (Log Track)
Specificatlon as VC2 but rack having (lo0) 1879 4k7ohms $\quad$ [0.65. 1833100 kghms [0.65*




DUAL GANG LOG-ANTI-LOG POT
1888 rrack specification as dual gang pots VC3, but tracks mounted to log-anti-log action 100 kohms E0.75*
A miniature 16 mm type replacement volume control incorporating single pole on-off switch. Reslstance value
Tolerance $t 20 \% ~ / 8 w a t t ~ r a t i n g ~$ 1889
1889 E0.27* VC8 MINIATURE ROTARY VOL CONTROL
$5 k$ hms log law with on $/ 0$ fl swlich. 20 mm grooved spindle. Tag connectlons 17 mm for replacement $\begin{array}{ll}\text { for replacement. } \\ 1890 & \text { E0.54 }\end{array}$
WIRE WOUND POTS
A range of wire wound single gang pots with 10 mm bush and supplled with shakeproof washer and nut.
1891 100hms $£ 0.80 \quad 1895$ 2200hms $£ 0.80$




## PRE-SET POTS <br> HURIZDNTAL MOUNTINO

Miniature type for transistor circults. The wiper or ine driveset is provided with a slot the preset wlll fit printed wiring boards with a pitch of 2.54 mm . All tracks are llnear law. $\begin{array}{lllll}1801 & 100 \text { ohms } & \mathrm{f0.09*} & 1808 & 22 \text { kohms } \\ 1802 & \mathbf{~} 220.09^{*}\end{array}$
 18041 kohms $\quad \mathbf{~} 0.09^{\circ}, 1811220 \mathrm{kohms} \mathrm{f0.09}^{\circ}$

 1807 tokohms 18154 M 7 ohms $£ 0.09^{\circ}$

## PRE-SET POTS

VERTICAL MOUNTINC
Minlature type for transistor circults. Wloer adjustment is made by a screw board. All tracks are llnear law. VC7
18161 1000hms $\quad$ C0.09* 182322 kohms $\quad \mathbf{~} 0.09^{\circ}$ $1817220 \mathrm{ohms} 5 \mathrm{f0.09}{ }^{\circ}$. $182447 \mathrm{kohms} 50.09^{\circ}$




## ANTEX IRONS

| O/No. 1943. 15 watt high quallty soldering | O/No. 1931. Highly popular $\times 2525$ watt |
| :--- | :--- | :--- |
| Iron totally enclosed element in a ceramic | quality soldering fron ceramic shafts to | Iron tofally enclosed element in a ceramic shaft fitted with $3 / 32^{\prime \prime}$ bit. $£ 3-80$ O/No. 1947. Replacement element for 1943

 iron. 1944. Iron coated bit $3 / 32^{\prime \prime}$ for 1943 O/No. 1945. Iron coated bit $1 / 8^{\prime \prime}$ for 1943

Iron. $80-46$ O/No. 1946. Iron coated blt $3 / 16^{\prime \prime}$ for 1943 O/No. 1948. Generak purpose 18 watt Iron flted with iron coated bit. $\quad$ E3.60 | O/No. 1952. Replacement element for 1948 |
| :--- |
| Iron. |
| 1.90 | O/No. 1949. Iron coated blt $3 / 32^{\prime \prime}$ for 9940 Iron.

$£ 0.46$ O/No. 1950. Iron coated blt $1 / 8^{\prime \prime}$ for 1948 O/No. 1951. Iron coated bit $3 / 46^{\prime \prime}$ for 4948

## 0800 0800080 80080800 <br> 0080 0008000 00808080 <br> Ulaw yuul UAh budus will the new Blon the board, rub over with a soft penell The transler will adhere to the board. Then complete the circult with your BI-PAK

 provide near perfect Insulatlon break-down voltage of 1500 volts $A C$ and a leakape current of only $3-5 \mathrm{uA}$ and another shaft ofstainless steel to ensure strength. $£ 3.60$ stainless steel to ensure strength. $\begin{aligned} & \mathbf{8 3 . 6 0} \\ & \text { O/No. 1935. Replacement element for } 1931\end{aligned}$ O/No. 1932. Iron coated blt $1 / 8^{\prime \prime}$ for 1931 iron.
O/No. 1933. Iron coated blt $3 / 16^{\prime \prime}$ for 1931

for 19.50 | O/No. 1933. Iron |
| :--- |
| Iron. |
| $\begin{array}{l}\text { O/NO.50 }\end{array}$ | O/No. 1934. Iron coated bli $3 / 32^{\prime \prime}$ for 1931

$£ 0.50$
O/No. 1953. SK1 soldering kit-this hit conO/No. 1953 . SK1 soldering kit-this hit con$3 / 16^{\prime \prime}$ bit plus two spare blts, a reel of solder, heat-sink and a booklet 'how to
solder'. In presentatlon display box. $£ 5.65$ O/No. 1939. ST3 soldering Iron stand. Siand made from hloh orade bakelite materlal chromium plated strong steel spring, suitable for all models, includes accommodation for slix spare bits and wo
sponges which serve to keep the soldering sponges when
iron bits clean
£1.50

## PRINTED CIRCUIT BOARD TRANSFERS

| BRIDGE RECTIFIERS | SILICON 1 amp Type $50 V$ RMS 100V RMS 400 V RMS | BR1/50 <br> BR1/100 <br> BR1/200 <br> BR1/400 | $\begin{aligned} & \text { Price } \\ & £ 0.20 \\ & £ 0.22 \\ & £ 0.25 \\ & £ 0.35 \end{aligned}$ |
| :---: | :---: | :---: | :---: |
|  | SILICON 2 amp SOV RMS 100V RMS 400 V RMS 1000 V RMS | $\begin{aligned} & \text { BR2150 } \\ & \text { BR21100 } \\ & \text { BR21200 } \\ & \text { BR2400 } \\ & \text { BR2/1000 } \end{aligned}$ |  |

## OPTOELECTRONICS

NEW INCREASED RANGE - ALL 1 ST QUALITY LED S (diffused)
O/no. Type
1501 AR
1502 ATIL209) 1502 M1L2322TILL2111
1503 MLI 3331 (OPL212A 1503 M1 3331 10PL212A)
1504 ARL4850(FLV117)
150 1505 MIL5251(TIL222)
$1506 \mathrm{ML5351}(\mathrm{MV5} 553)$
supep •HI Brite Type
1521 MI32
1522 MIL52


$.3 \mathrm{~mm}(.125)$ RED

0.10
0.10

LEO CLIPS
$\begin{array}{ll}1508 / 125 & \text { pack of } 5 \\ 1508 / 2 & 125 \text { clips } \\ \text { pack of } 5 & 2 \text { clips } \\ & \text { ALL } 8 \% \text { V.A.T. }\end{array}$
$\begin{array}{ll}1508 / 125 \text { pack of } 5 & 125 \text { clips } \\ 1508 / 2 & \text { pack of } 5 \\ 2 \text { clips } \\ \text { ALL } 08 \% \text { V.A.T. }\end{array}$
60.55
60.35
60.15
60.18

## DISPLAYS

$\begin{array}{ll}\text { DL303 } 7 \text { segment O.P. Ieft ( } 13^{\circ} \text { height) Common Anode } \\ \text { RED Single Digit } & \text { O/NO:1523 } \\ \text { RO.70 }\end{array}$ RED Single Digit
DL707 7 segment $0 . P$. left ( $.0 .3^{\circ}$ helght) RED Single Digit $\quad$ DL527 7 segment D.P. left (.50" height) DL527 7 segment D.P. left ( $.50^{\prime \prime}$ height) DL727 7 segment D.P. right ( $.510^{* \prime}$ height)
RED Two-Digit Light Pipe
 DL747 7 segment D.P. Lef (.630" height) Common Anode
RED Single-Digit Light Pipe

## OPTO-ISOLATORS

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CIL74 Single-Channel 6 pin DIP standard type - optically coupled pair with infra-red LEO Emitter and NPN
Silicon Photo Transistor.
O/NO: 1497 EO.5D CILD74 Multi-Channel 8 pin DIP Two Isolated Channels. CILO74 Multi-Channel 16 pin DIP Four Isolated Channels. ALL (a) 8\% V.A.T.

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Low Loss Co-axial Cable
15 Way Multi Coloured Ribbon Cable

| O/NO | PRICE/ |
| :---: | :---: |
|  |  |
| 3127 | ¢0.20 |
| 3128 | c0.15 |
| 3129 | ¢0.30 |
| 3130 | c0.22 |
| 3131 | ¢0.18 |
| 3132 | c0.10 |
| 3133 | ¢0.09 |
| 3134 | ¢0.07 |
| 3135 | ¢0.22 |
| 3136 | £0.40 |



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## THE BIG GUNS!

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AST month in the leader we looked at - one aspect of the manufacturers and industrial distributors involvement in component supply. Although many companies are not interested in supplying one-off components on a mail order retail basis they are now becoming more interested in encouraging hobbyist use of their products and in making the hobbyist aware of their company name.
The fact that a good percentage of our readers are employed in industry, does, of course, not escape them and they also realise that the spending power of the hobbyists as a collective unit is considerable. We are gradually finding more companies that are willing to spend some time and effort wooing the hobbyist.

This issue carries the second part of our Microprocessor Evaluation System, it will have been noted by many readers that this system is based on a General Instruments device and the article is written by one of their engineers. G.I. actively encourage their employees to develop ideas for publication and it is likely that more projects will come from this stable. We are also in contact with Ferranti, Mul-
lard and Texas and expect these contacts to also bear fruit.

It is true to say that this is not something new and we have had similar tie-ups in the past. What is interesting is that at the present time, more large organisations are becoming aware of the potential in this market, and, because of this, we should all benefit. Obviously the knowledge and industrial backup that such companies provide can lead to the best possible projects, often developed by the very engineers involved in the design of the i.c.s. employed. We can thus offer readers projects developed with the aid of test gear and facilities that are out of reach of a small company, let alone most hobbyists.

This factor is probably not significant when related to a sound to light system or remote control but when a high quality amplifier is to be described, immense benefit in the performance of a project constructed at home can result. We believe that this involvement by such companies can only do good to our hobby, however, we will not overlook the fact that it is often possible to improve on commercial equipment.

## COMPUTER

If you read the review of Superboard II in this issue, that long awaited product of Ohio Scientific Industries, you will realise that it is possible to improve on the design and, even for its extremely competitive price, the Superboard, although very versatile, is not the best possible product. It follows that although a great deal of time and company effort has been expended in the design of this product, the final result could still be improved on; fantastic though that may sound when related to such an excellent computer for less than $£ 300$.

As we have noted at the end of the review, improvements are being incorporated in a design based on Superboard and we expect a full series to be published in P.E., starting in the August issue, describing what we honestly believe will be a computer to outstrip all others in the price range - kit or complete.

## EAR PLUGS?

It seems that the bit of trumpet blowing we did a couple of months ago has had no effect on any readers as, to date, we have not received a single comment on it!

Mike Kenward

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It is not often, even in the lightning-fast field of Microcomputers, that we have a chance to review a system quite as up-to-the-minute as the one described below. The machine lent to us by COMP Components (14 Station Road. New Barnet, Herts. Tel: 01-441 2922) was perhaps one of the only two in the country at that time. Supply problems have been immense for Ohio Scientific Industries who make the machine. It can only be hoped that, by the time this review appears, the Superboard will be available ex-stock, or on very short delivery, not only from COMP but from the many stockists who started taking orders before Xmas on a full cash basis from the hundreds of hobbyists who have shown interest in this remarkable machine.

The photographs in this article show the compact form the Superboard takes-along with the type of video output which can be expected. Prices range from just over $£ 260$ to around $£ 280$-see current adverts in this issue for the latest information.

THE machine comes as a single p.c.b. which includes the keyboard section, plus a set of manuals and cassette-based demonstration programmes.

## P.C.B. LAYOUT

The p.c.b. is extremely well laid-out, as the photograph shows, with the keyboard deciding the total width of the board which is approximately 370 by 305 mm ( $14 \frac{1}{2} \mathrm{in} . \times 12 \mathrm{in}$.) in size. The quantity of room and uncramped nature of the assembly would make this a very acceptable kit with few of the usual problems associated with highly dense component layouts. Plenty of high frequency by-passing is also provided as can be seen from the number of disc-ceramic capacitors dotted about the board.

Referring to the p.c.b. photograph: just above, and to the left of the keyboard, is a line of six 24 -pin d.i.l. sockets holding, from left to right, one ACIA (6850) for serial communication devices such as cassette and teletype, the monitor ROM ( 2 K Bytes) and four BASIC ROMs ( 2 K Bytes each). These are followed by the 6502 MPU which is at the heart of this machine. Further to the right is a pair of two-way data bus buffers-using 8 T 28 chipsand some logic for running the keyboard. Finally, at extreme right, is a 40 -pin d.i.l. expansion socket labelled J1. The 8 T 28 's are used purely for expansion via J 1 .

The next couple of rows of i.c.s are for various logic functions and include some unoccupied i.c. pads labelled PROTO on the circuit diagram in the manual and not connected to any p.c.b. tracks. The two rows above these form the next two main blocks
of the system-VDU and RAM sections. The RAM is seen implemented by two half-rows of 2114 ( 1 K by 4 BITs ) RAM i.c.s over to the right. Eight of these sockets contain i.c.s in the basic system giving 4 K bytes of memory and eight more are provided for 4 K of expansion simply by plugging in the extra chips.

Only 7 K of memory is shown in the photograph. Further to the left is another pair of 2114 s forming a further 1 K of RAM for the VDU. Next to these is the 24 -pin character-generator chip and switching i.c.s for the VDU which is of the memorymapped type for speed and complete flexibility of use. Over to the extreme left is the crystal-controlled clock generator from which the whole computer, from MPU to cassette interface, derives its timing information.

At the top of the board is the remaining support logic and cassette interface (over to the left), along with various unoccupied holes and pads-the components for which may be added later to complete an asynchronous communications interface including RS232. The top right of the board has provision for power supply unit components, and in the photograph houses an efficient little u.h.f. modulator (ASTEC) which allows direct connection to the aerial socket of a TV (tuning around Channel 36) and gives the excellent results shown in the photographs in this article.

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## SUPPLY

The Superboard is a single supply device requiring a regulated +5 volt source. The current depends heavily on the number and type of 2114 employed on the board. OHIO supplies the lowpower versions, and with the full 8 K the p.s.u. will have to deliver 2.5 to 3 amps . There are also two sockets (with plugs included!) on the top edge of the board. The most important of these is $\mathbf{J} 2$-to the left-as this supplies inputs and outputs to cassette and video monitor (or u.h.f. modulator). The other, J3, is for asynchronous communications.

The various "links" printed on the p.c.b. may be cut and connected differently for a variety of uses, and a further edge connector, J4, next to the keyboard (left) may be used to expand the key-functions for a separate programmable application.

The ambitious user will certainly find the p.c.b. construction and layout more flexible than most single-board computers on the market, not least for the reason of spaciousness of design.

## KEYBOARD

The integral keyboard gains its best "touch" when the whole p.c.b. is screwed firmly to a base-board of some rigid material such as chip-board. It is pleasant to operate and apparently instantaneous in use. There are 53 keys which form a familiarly spaced type-writer format with legends properly moulded-in for durability and excellent readability.

One's unconscious emotional reaction to any computer is almost certainly heavily dependent upon three things:
(a) The keyboard.
(b) The display.
(c) The software monitor and programming language employed.

These three areas are described overleaf.

To the computer, the keyboard is an array of 8 rows and 8 columns occupying a single address location (DFOO). One location is enough as the 8 rows are write-only and the columns read-only. The keyboard routine, stored in the monitor ROM, constantly sends "walking-bit" information to the rowslatched by two 4-bit latches (74LS75s)-and reads the columns looking for the result of a key-closure. The keyboard is thus read for information and decoded by the monitor-this obviates the necessity for a hardware keyboard-encoder chip and ensures speed and accuracy with a low (and hence cheap) chip count. Multiple key readings are prevented by carefully written software and, indeed, this keyboard was considerably less prone to "doubling" than that of some other personal computers.

One or two idiosyncrasies of the keyboard are worth mentioning. The BREAK key to the far right of the keyboard, is simply a RESET button and will be described later. RUBOUT and ESC seem to have no effect for normal operation and the REPEAT key appears to do the exact opposite. If any key is pressed for more than about half a second, that character or function starts repeating continuously (a very useful feature) until the key is released-pressing REPEAT stops this process!

The character generator in the VDU section contains upper and lower case letters plus all the necessary special characters in the ASCII set. These are accessable via the keyboard but not all are indicated. For instance, "UP ARROW"-here implemented by $\wedge$-is an essential part of BASIC programming and is given by SHIFT N, a fact which the experienced programmer will already know, but not so the first time user. There are several other "hidden" special characters, and the complete list, which I discovered, is shown in Table I along with any function they perform.


Normal operation of the keyboard is in upper-case mode and for this, the SHIFT LOCK must be set in its "down" or "locked" position. My first mistake was to miss this point in an initial enthusiasm to see the BASIC working! Here the manual has its own poignant, if ungrammatical, reminder to the user not to be guilty of the adage about-"reading the instructions when all else fails".

As BASIC text is entered into the machine each line may be corrected or deleted before the computer reads the line for execution or filing. When such a line is complete, RETURN is pressed to transfer the line into the machine.

Two control characters are used on the keyboard: CONTROL O and CONTROL C. The first suspends the VDU display during command and program entry, the second suspends program execution. CONTROL $O$ is very useful, for instance, for running from a clear screen-the command RUN, typed after CONTROL O, will not appear.

## DISPLAY

As explained above, the VDU section is of the memory mapped type and hence each character on the screen occupies a specific memory location. The Hex address of the "screen" can be found on the memory map diagram (Table 2) and will be seen
to occupy 1024 (1K) locations. These are arranged as 32 characters by 32 lines on the screen, but many of them remain undisplayed by the majority of TV sets. The reason for this is quite simple and rests on the fact that a 4 MHz (approx.) crystal is employed to generate the "dot" frequency for the characters. Each character is formed from an $8 \times 8$ dot matrix and the width of a dot on a TV screen is inversely proportional to the dot frequency; 4 MHz is simply too low (and hence the dots are too large) to accommodate much more than 24 characters on a horizontal line.

I could only fit 23 on the Philips set illustrated. It is possible to adjust some sets to accommodate more characters per line, but the machine is meant for the average domestic TV, and the majority of rental companies will show a remarkable lack of enthusiasm for your rearrangement of the internals of their $£ 300$ colour sets.

In setting up the machine for initial use, the SHIFT LOCK is pressed to lock it into the "down" position and then the BREAK key operated. BREAK may be pressed at any time during use causing the display to clear and present the letters D/C/W/M? on the lowest line used by the monitor. These letters, as with all entries to the machine under monitor control, scroll upwards on the display as new entries take their place on the lowest line. BREAK places the machine in its command mode and only one of the above four command letters will be accepted at this point.

The letter D is for disk operation and is not applicable here. C clears the memory of the machine and causes the words MEMORY SIZE? to appear. At this point the user may reserve memory space to hold machine code routines or binary data blocks. Typing just RETURN reserves no memory space, while typing any number greater than 769 (the minimum number of memory bytes needed as workspace for the monitor and BASIC interpreter) will restrict the RAM available for writing in BASIC. This prevents subsequent programming from using the reserved space and destroying its contents.

The next message to appear is TERMINAL WIDTH? Typing RETURN assumes the number 24 , while typing any number from 16 to 23 restricts the use by the BASIC interpreter to that number of characters per line VDU display. Any number greater than 24 is assumed to mean 24 , and any number less than 16 is ignored causing TERMINAL WIDTH? to reappear. When other output devices are used, the width may be specified up to 72 characters.

After terminal width has been specified, the Superboard enters its BASIC function by printing up a quick advert for itself and giving the number of free bytes of RAM available, which it determines using a memory test. Any RAM malfunction may be noted at this point.

Table 2. Memory Map of the Superboard II

| Mex Address | Function |
| :---: | :---: |
| 0000-02FF | This block of RAM is used by the monitor and BASIC ROMs as scratch-pad-included here is the stack and all necessary vectors and flags |
| 0300-OFFF | Rest of basic 4 K system RAM |
| 2000-1FFF | Optional extra on-board 4K RAM |
| A000-BFFF | BASIC ROMs (8K) |
| D000-D3FF | Video RAM |
| DFOO | Keyboard |
| F000-F001 | ACIA for serial communications |
| F800-FFFF | Monitor ROM including: floppy disk bootstrap, keyboard routine, 65 V machine code monitor, BASIC support and hardware vectors for NMI, RESET and IRQ |

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The other two command letters are used in the following way. W allows a re-entry after the BREAK without clearing memory. A most thoughtful addition-if only for the reason that the BREAK key is placed far out to the right of the keyboard and very exposed. It is easy, after setting up a BASIC program to brush the BREAK key causing the screen to clear and D/C/W/M? to appear-pressing C at this point would delete the program completely. $M$ is used to enter the machine code monitor and will be described later.

During text entry, the monitor moves a "dash" symbol (the cursor) across the screen to keep the user updated with his current writing position. "SHIFT O" may be used at any time to delete the previous character written-this causes a dash to appear permanently at that point to remind the user that, as far as the machine is concerned, the previous character has been deleted. Several such dashes imply the deletion of that number of previous characters; and SHIFT P deletes the whole line, writing an @ symbol and moving the cursor to a new line.

In this way editing may be performed on a line adequately but slightly awkwardly. A better way would be to actually remove the deleted character from the display and backstep the cursor.

As can be seen from the photographs, the display provides very large characters separated vertically by very little space; this tends to make the text less readable than the majority of VDU pictures. However, it allows a very cheap system of "character slot" graphics to be implemented on the system. 128 of the character-generator ROM's patterns are graphic characters-inaccessable from the keyboard, incidentally. This includes a rich variety of tanks, guns, gaming characters, playing-card suits, lines, blocks and many more, to be displayed during program execution. Due to the lack of space between the character rows, these join up vertically as well as horizontally to produce continuous patterns. Separate dots within the $8 \times 8$ matrix of each character slot cannot be lit separately, and hence the term "character slot" graphics as opposed to "point graphics".

In addition, the top few TV lines of the top row of characters are rather indistinct as there is no delay and blanking at the start of each frame.

Most TVs find it easy to synchronise with the Superboard even though it produces 60 Hz frame information for the US market: some adjustment of the vertical hold is often necessary. The 60 Hz frame frequency (non-interlaced) provides one other slight drawback. Most TVs have badly smoothed d.c. rails, and a little 50 Hz hum is always present-this tends to beat with the 60 Hz frame frequency causing a constant jitter of the screen contents-more noticeable as the brightness control is increased.

As the display is memory mapped, the user may access it simply by "POKE"ing information into the RAM block (1K) dedicated to storing the screen's contents. As explained, many of these slots are undisplayed on an average TV screen. The monitor carefully starts each line of BASIC text 5 spaces in from the left on each line and uses only the top 26 lines in order to ensure that no information is lost during program development. The full screen is 32 characters by 32 lines, and most British TVs will show all the lines though not all the characters on each line. However, much of the software supplied for the Superboard uses all 32 lines of the display which actually covers less than the normal 625 lines of UK TVs.

It is clear that some considerable thought has been employed in providing a display capable of use in gaming and "animation" applications. This has been successful up to a point given the extremely low price of this powerful system-the designers have even included a monostable circuit to blank the video display each time the MPU accesses the VDU. This helps to prevent spurious noise from appearing on the screen during continuous use of the VDU.

## SOFTWARE MONITOR AND PROGRAMMING

The above section contains a full description of the commands available to the user and below is described use of the two languages employed in programming the SuperboardBASIC and 6502 machine code.

The fact that the system offers the language BASIC to the user will undoubtedly be the prime reason for his buying this machine. It should be emphasised that this is no Tiny BASIC or in any sense a minimal version. It is a full 8 K Microsoft BASIC and even includes the very useful DEF FN.

It was clear from the start that program execution was fast, but I was unprepared for the remarkable speed of calculation which became apparent. When reviewing the TRS80, I ran some standard (Benchmark) programs and carefully timed them for later comparison. Programs not concerned with arithmetic or scientific calculation ran in about 55 per cent of these times on the Superboard-not surprising as the TRS80 is known to have a slightly slow clock. However, the 6502 on the Superboard was clocked at 1 MHz , and the 2 MHz version would halve these times again. This is still interesting but unremarkable.

I then tried my scientific calculation program. A large number of intermediate results are required, and the TRS80 had run this program in 117 seconds. I typed RUN on the Superboard and pressed return. There was no time to notice the reading on my watch-it ran in 7 seconds! I tried re-running the program 20 times and even juggled it about-still 7 seconds. The original program had no print-out so I made it print all 500 intermediate results on the VDU-the time shot up to just on 15 seconds. At this point, I noticed that the actual chip on the board was the "A" version of the 6502 -able to run at 2 MHz .

Swopping a few wires around increased the MPU clock to 2 MHz , and I was fascinated to watch the Benchmark program run in $3 \frac{1}{2}$ seconds-about $1 \frac{1}{2}$ seconds slower than the time quoted for its running on an IBM 370/115 mainframe computer. The speed of the Superboard rests entirely with the version of the BASIC interpreter included in its design, and not to any special feature of the 6502 for example-presumably Microsoft could do just as well on any other system, if they tried.

The BASIC statements may be typed in for immediate execution or, by giving them line numbers, may be written as part of a larger program for running later. In immediate mode, the computer acts like a super calculator-with responses to Sine and Cosine markedly faster than those of my Texas SR 56 calculator. Any level of parenthesis is available along with all the usual BASIC scientific functions to a floating-point accuracy of $6 \frac{1}{2}$ digits and including the random number function.

The only omission I discovered in the repertoire worth noting was the PRINT USING statement. Otherwise, everything is there, including numeric and string arrays plus an excellent set of string-handling functions.

Memory efficiency and speedy programming are aided by omitting "LET" in assignments and using "?" for PRINT. Multiple statements are allowed on a single line by the use of a colon as separator. Variables may be two characters in length (the first must be alphabetic) and, with array variables, there are as many as could be wished for by the most voracious scientist.

The BASIC manual, as implied in the documentation, was not written as a primer. It is a quick-reference list for the knowledgeable user. In parts, it is a little too quick and even now I am unable to fathom out the use of the USR function which has the useful capability of being able to call machine code routines from a BASIC program.

LIST is one of the most important BASIC editing features. On the Superboard it is very versatile. "LIST 50-" and "LIST $50-100$ " will give a listing of a program from line 50 onwards and between 50 and 100 respectively; also, "LIST -50" gives the list up to line 50 , and "LIST 50 " gives line 50 alone. This is all very useful and appears nowhere in the manual!

The BASIC interpreter also supplies 17 stated error codes to assist in the debugging of programs. Each code is composed of a letter plus a graphic symbol for some reason. SYNTAX ERROR for instance, is denoted by S__ as opposed to the more usual SN. I also discovered one unlisted error code-B $\boldsymbol{B}$. This turned up after typing (in immediate mode) $\mathrm{A}(1000)$, among other things. Perhaps someone should run a competition to discover how many other codes, plus their access statements, exist-could be interesting!

It is certain that the first time user will find the documentation on BASIC, supplied with the kit, inadequate, and he is well advised to seek out one of the excellent primers on this subject best found from a local library or by browsing in the technical section of most sizeable bookshops. The language is easy to learn and will enable programming to quite a sophisticated level in an hour or two.

## MACHINE CODE

The fact that the Superboard can also be programmed in machine code, via the machine code monitor, impresses me very strongly. It adds that touch of flexibility which is prevalent throughout the system's design. The machine code is accessed by typing " $M$ " when $D / C / W / M$ ? appears on the screen after BREAK. This causes six characters to appear at top left of the screen-four hex characters for address, followed by a space, and two characters for the data contained at that location.

The commands available for program writing are rudimentary but adequate. Typing a full stop allows subsequent hex characters to load the address field of the display. The data field is kept updated during this process allowing the user to view the contents of any memory location. To load the data field, a "/" must be typed. "L" allows loading from the cassette and "G" causes execution of a machine code program starting at the current address displayed on the screen. The manual also gives the starting address, in the monitor, of several useful routines, thus allowing a very low level utilisation of the machine.

A user who wishes to write many machine code routines, perhaps for control purposes, would do well to write a sophisticated BASIC program, using PEEK and POKE, to aid the loading and construction of such routines.

## CASSETTE INTERFACE

I had a little trouble loading both the standard and my own saved programs, at first-nothing serious, however, and a little experience with the tape-recorder's controls gave acceptable results most of the time. The cassette interface runs at about 300 BAUD and depends upon the constant crystal-controlled clock

of the whole system for both recording and retrieving data. This means that changes in tape speed will diminish performance more severely than would be the case if the retrieval clock were derived from the recorded data itself. Data is stored on the CUTS (Kansas City) standard of recording a low pitch tone for a logic zero and high for a one. Decoding one's and zero's from the tape depends upon the decay time of a monostable to distinguish between high and low tones-its time constant is adjustable, for setting up, by a subminiature pot on the board.

The software supplied with the board, on cassette, formed a strange mixture of games, educational programs, calculations and financial packages. Some of the tapes actually seemed to contain software errors-if the tape interface can be trusted-and one, at least, was complete rubbish. One or two of the games were good-particularly "New York Taxi" where the player has to hail a taxi without being run over; and "Hectic"a game well outside my powers of speed and co-ordination-the display left me staring fascinated by the mayhem-like destruction implied by a spinning bomb which hurtles from random points in the sky and removes bits of the landscape.

There is also a Ratio Analysis program giving an idea of the machine's business capabilities, and one or two educationally useful teaching programs.

As a very rough guide to the speed of loading and size of programs, "New York Taxi" takes up about 2.7 K of memory, occupies 81 lines of program and takes 2 minutes 20 seconds to load.

## MANUAL AND EXPANSIONS

The user's manual is not bad but could do with re-writing in a more concise and logical form. Very complete circuit diagrams are included which appear, on the whole. accurate and well drawn; however, a little verbal explanation in this area would help the user to appreciate some of the finer points of design more easily.

The short section on BASIC and two pages describing the whole of microprocessor theory will leave the beginner mystified and the expert bored. Most of the facts appear somewhere in the quite lengthy text but are either laboured or under explained.

The final section is devoted to the 6500 series of MPU chips and their machine code. This is most useful for reference, but is entirely for the experienced.

The Superboard II, to give its full title, is the name given to the OSI 600 board, and may be incorporated into a special metal case with power supply to form the CHALLENGER IP. The 610 board is an expansion p.c.b. containing 24K RAM and a floppy disk controller. Sockets are provided for dual mini floppy drives as well as a d.i.l.-plug terminated ribbon cable to plug into J 1 on the Superboard. The 610 also interfaces to the 620 expansion board designed to run the OSI 48 -line bus for the full range of OSI add-ons.

Expansion capabilities and flexibility of the basic p.c.b. are very varied-for instance, there is a link option on the board to enable the future use of a single 64 K bit ROM for the BASIC interpreter instead of the four 16 K bit ROMs supplied. This would free three 2716 -pin-compatible sockets for other things. This type of detail will remain unused by the majority of users but tends to imply a high degree of thoughtful system design.

## CONCLUSION

The machine described here is certainly one of the most exciting on the present market for both the hobbyist and anyone needing an introduction to computers in general and microcomputers in particular.

The video output is probably not fitted for business applications though the power of the machine may well be. The addition of an asynchronous video terminal and printer would, however, solve the problem quite quickly.

In a short time, the home computer man will learn the limitations of cassette storage in general and will begin to hanker after the yet largely unappreciated advantages of disk. Here again the Superboard provides the answer with a cheap plug-in floppy disk system.

The price of the Superboard puts it into a class of its own which is going to be very hard for the current purely machinecode systems to match. Its BASIC is nearly as powerful as, and faster than, that of the Tandy TRS80, for instance. It is, of
course, written by the same firm and takes up the same amount of ROM. The monitor on the other hand (half the size of the, TRS80's) is less powerful in many ways but better in othersallowing machine code, for instance. The graphics and VDU, though more than adequate, are not up to the standard of the more expensive systems, however.

It is perhaps here, while discussing its comparison with a computer three times its price, that the finest compliment is to be found for this excellent innovation into the UK market.

As a consequence of the remarks in this article, COMP Components have designed and are producing a computer-The Compukit UK 101 finkit form for E219 +VAT)-based on the Superboard but with many enhancements. These include up to 48 characters per line-with much clearer type and a superior character font with many useful technical symbols. The VDU will generate 50 Hz frame information with a faster "dot" clock to produce a rock steady picture even through a modulator-which will be included on the board, as well as a regulated p.s.u. just requiring a transformer to make the system fully operational. Keyboard management will be much improved and include all the missing characters mentioned in Table 1.

Our contributor Dr. Berk will be writing a series of articles fully describing the design and construction of Compukit UK 101 and these should be published in P.E. commencing in the August issue, thus providing readers with a better Superboard at even lower cost.

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## FRED

To me, Fred will always be Fred Bennett, the previous Editor of our favourite electronics mag, and, incidentally, its founder. Well Fred, if you are reading this you'll be pleased to know that at long last your services to electronics have been officially recognised. They have named a series of semiconductor devices after you, so you can join the ranks of Hertz, Ohm, Ampere and all the rest!

Hang on a minute though, leafing through the FRED data sheet I find no unsolicited testimonial, only a bleak paragraph which informs me that FRED stands for Fast Recovery Epitaxial Diode.

Joking aside, the FRED is a device worth a second look. The name has been coined by Thomson-C.S.F., the French semiconductor house, and they have made a whole series of diodes available under the FRED banner. The diodes are intended for use as rectifiers in switching power supplies and the like, and they represent a big improvement on the traditional silicon diode design.

The need for these devices has been emphasised by the new power-supply-wonder-of-the-age, the D.O.L. (Direct-OffLine) switcher. These power supplies get rid of the heavy iron transformer of traditional désigns and make possible small, lightweight, regulated power packs in a fraction of the normal space:

The way they work is simple enough. Take a 230 V a.c. input and half-wave rectify it to give a high d.c. voltage. Now chop up this d.c. supply into a high frequency pulse train using transistors and then pass it through a small, lightweight, high frequency isolating and step down transformer. Finally, rectify in the traditional way, only use FREDs. To achieve voltage regulation, monitor the output voltage and use it to control the width of the pulses produced by the chopper transistors.

Easy isn't it, but as you can imagine the "high frequency" bit causes problems not only for the choppers, but also for the rectifiers. Old fashioned rectifiers are fine at 50 Hz but at 20 kHz ? forget it. FREDs on the other hand have a low forward voltage drop, low conduction losses, high surge current rating, low reverse recovery time, low forward recovery time and are great at 20 kHz . The range includes the BYW80 at 7 Amps, the BYW 81 at 12 Amps, the BYW 77 at 20 Amps, the BYW 92 at 35 Amps, and the BYW 78 at 50 Amps. Better luck next time FredI

## COUNT ON IT

You have probably seen the delectable Intersil D.V.M. chips, the ICM 7106 and 7107. Well now Intersil have done it again with a whole family of counters in the same image. Once again you can have a choice of l.e.d. or l.c.d. displays, but in addition you can choose either a 19999 full scale count or the 15959 timer variety. As usual, the chips drive their displays directly, and they"ll run from five volt supplies too. CMOS technology is used, and so the IM7224 l.c.d. version draws only 10 microamps at 1 kHz and a miserly 1 milliamp at a full 10 MHz ! The $\mathbf{7 2 2 5}$ l.e.d. version takes a lot more of course, or rather, the displays do. To get the 15959 versions, just add an " $A$ " suffix. This new chip is going to be a big success, you can count on it!

## DOTTY CHIP

Anyone who studied the excellent P.E. VDU SYSTEM series must have been impressed by the capabilities of the SFF 96364 TV controller chip which helped to make that low cost system possible. You may have noticed that in addition to the control chip, the system used NMOS RAMs, several TTL packages and a 24 pin
character generator ROM which contained the character dot matrix information for the raster scan display. If you are thinking of building this sort of system into, say, a home computer, you may be interested to learn that you can save quite a lot of board space and trade three packages for one if you substitute a National DM 8678 for the existing 2513 ROM and two of its TTL support chips.

In the existing system, 6 bit ASCII character data from the screen RAM memory area is latched in a TTL 74174 register before being used to control the character select lines of the 2513 ROM. Output data from the ROM appears in parallel a row at a time, and has to be serialised in another TTL package, a 74165 parallel in/serial out shift register, before being applied to the modulator.

The DM 8678 device replaces the two 16 pin TTL devices and the 24 pin ROM with a single 16 pin package. Not a bad trade! The pin reduction is possible because whereas the 2513 ROM had parallel inputs and outputs, the DM 8678 has parallel inputs but a serial output. The speed requirements of such a fast system can be met by the National device because it is a bipolar (nót MOS) chip which is fully TTL compatible. The DM 8678 chip comes in a variety of optional forms which each contain a different character font. For compatibility with the P.E. system, the DM 8678 CAB is required because it has a $7 \times 5$ character dot matrix in upper case. Of course it would be very easy to add a lower case character facility to the system by using two DM 8678 s, with some simple extra gating. In this case a DM 8678 CAH would be requíred.

If you really were cramped for space, another saving might be to substitute a single MK $4118 \mathrm{IK} \times 8$ RAM for the eight 2102 S currently specified.


Fig. 6. Signal processing board


Fig. 8. Circuitry directly mounted to channel triac. Layout is similar for both channels

Fig. 7. Zero voltage switch board
with modifications to the signal processing circuitry. Steep bandpass filters, possibly based on cascaded low and high pass Chebyshev filters are required for an unambiguous display. In both cases, a pro-rata change in the rating of the power supply transformer and rectifiers is the only modification necessary to the remainder of the unit. The unit may be wired into 30A mains feeds with complete confidence. Higher current feeds are suitable provided the prospective short circuit current is under 900A. This value may be derived from the supply impedance. A simple method of measuring the latter parameter is to connect the largest allowable lpad to the feed. The difference between
loaded and open circuit supply voltage will indicate the supply impedance reasonably accurately.

The use of loads in excess of 6.2A in the design version is limited primarily by lamp surge current. On certain mains supplies, where impedance is relatively high, one may well get away with greater loads. Under no circumstances however should the high speed fuses be subjected to currents in excess of 11.5 A . This implies the use of a 7 A fuse (If $1 \frac{1}{4} \mathrm{in}$. glass) to protect the high speed fuse. As such a fuse is not available, and its use would significantly reduce triac protection it is recommended that the point where 5A $1 \frac{1}{4} \mathrm{in}$. fuses start to go 'pop' is taken to be the safe limit.


Fig. 9. Theile network

## R.F.I. PROBLEMS

If r.f.i. is troublesome, assuming that the r.f.i. elimination routine has been dutifully followed, then the best course of action is to suppress at the point of pickup. This is usually sensitive audio circuitry. The use of a Theile network in power amplifiers prevents r.f.i. picked up in speaker cables being fed back to the input via feedback networks (Fig. 9). If unbalanced lines are used for interconnection, the use of balanced line cable is helpful. One of the inner cores is used for the return, and the screen is connected at one end only.

Earthing arrangements should be under suspicion, particulary mains earths. The SLM should ideally have its own, low impedance cable to the mains socket. In exceptionally difficult cases, copper screening may be employed. This should hermetically encase the power control circuitry. All mains cables should be screened with copper braid.

Audio circuitry power supplies should incorporate suppression chokes of the bifilar variety.

Normally however, no problems will be experienced provided speaker cables are kept well away from mains cables connected to the SLM.

## LAMPS

In order to minimise lamp filament failure rate and thereby avoid unnecessary loss of high speed fuses it is expedient to use single coil lamps, having longer life and more robust filaments. Inherently heavy duty lamps such as PAR 38 types are strongly recommended. Wherever possible, 240 or 250 V lamps should be specified. If, say 230 V lamps were used at a venue with a 250 volt supply, the 9 per cent increase in voltage over the nominal will shorten lamp life by 60 per cent. Using 250 V lamps will of course entail a small loss in potential light output in most cases; 240 V lamps are the best compromise.

Zero voltage switching, applying intermittent power (as in this application) and good ventilation also increase filament life expectancy. 100W lamps are the compromise between a large number of small lamps with low thermal inertia giving a responsive display but with high hardware costs, and a smaller number of high power lamps exhibiting a sluggish response. Thermal lag will significantly affect the character of a display, so experimentation is urged.

Stage lighting gelatines (Cinemoid) are a cheaper colour medium than coloured lamps and provide a wide range of colours of differing densities. No's 11, 18, 34, 41 and 46 are typically of optimum density for efficient projection. It has been stressed that good display technique is the crucial factor in successful discotheque lighting; operators who are seriously concerned with lighting display techniques should study stage lighting and carry out extensive experiments. Whilst the difference between theatrical (stage) and discotheque lighting in the applied sense is wide, the basic principles are similar.


Fig. 10. Power supply board


## DISPLAYS

Displays are dichotomised by the two classes of music: those which are viewed and those which are experienced, appertaining to cerebral and physical music respectively. For the former, suitable "viewed" displays consist of lamps faced by coloured and patterned diffusion screens. Unfortunately, this display mode, in the form of "lightboxes" is common in discotheques, yet is totally unsuited to the enhancement of physical music.

A "physical display", in the same manner as physical music must "cry out for physical participation", hence a display which is "experienced". To achieve this, light must be projected. Of course, "lightboxes" project light, but the effect is weak and ineffectual, because they are not usually designed to act as an efficient projector they have a multiplicity of close seated colours project a mixed light which tends towards pale hues or even white and the light source is often in the line of sight and acts as a distraction, hence the rule of physical displays, (a) use efficient light projectors (b) project a minimal number of bold colours from discrete sources.

Strategic placing of bass and treble displays causes violent oscillation which is stimulating. Remove the light source form the audience's line of sight. Ideally it should be overhead, but close to the floor is an acceptable compromise.



An idealised overhead display system based on rock concert techniques would consist of lighting units in steel boxes with hook clamps and parabolic reflector lamps, which in this case should be 100W PAR 38s instead of PAR 56s.

The direct opposition of bass and treble lamps gives rise to an exciting "cross-fire" effect. Equipment of this calibre requires large capital investment, but it is certainly worth



Fig. 11. Some arrangements for overhead displays (OHD). Directly above shows how beams from areas $P$ and $R$ will mutually interact. Lamps are maintained at 45 degrees and colours chosen to minimise this problem


Fig. 12. Low cost projection box

hiring it from PA hire contractors for special occasions.
A compromise arrangement suitable for small venues is shown in Fig. 11; setting up is rapid. Note again the arrangement of bass and treble lamps in order to give a cross-fire effect. The compromise between a large number of discrete sources which are costly and take a long time to set up and a small number which concentrate the light sources unduly is around 500 W per box, made up of $5 \times$ 100W PAR 38s.

## PROJECTION BOX

The key details of a simple projection box are shown in Fig. 12 or 50 mm grid $\times 8$ or 12 gauge "Weldmesh" serves to protect the lamps and gelatine sheet which is pinned behind it; PAR 38s are however extremely tough and it may be omitted if desired. The lamps are mounted on a centre batten. Local power connection should be of butyl cable and brass holders should be used in order to ensure a long service life at high temperatures.

A fine zinc or aluminium mesh, matt black painted flush with the aforementioned batten will provide reasonable ventilation with minimal light leakage. When setting up an SLM or judging the aesthetic effect of a display, study the effect of the lighting on people rather than viewing the light source. The use of too many colours will only create an ambiguous, confusing pattern which defeats the object of using a two channel SLM. Stray white light can be troublesome and if elimination is impossible, exchanging the offending light source for a 60W "Fireglow" lamp is a reasonably diplomatic solution. Bear in mind that the location of floor mounted sources often defines the nucleus of the dance area in discotheque applications.

Finally, remember that the discotheque is primarily concerned with music and dancing. A good sound system is the first prerequisite; lighting is a secondary consideration, but lighting which stimulates dancing must be the foremost choice.

# PATEMT REMIW 

Copies of Patents can be obtained from: the Patent Office Sales, St. Mary Cray, Orpington, Kent Price 95p each

## TOUCH CONTROL

Fidelity Radio Ltd. of London NW10 in BP 1513562 propose a simple and cheap touch or tuning control for an amplifier or radio. It is suggested that the device can also be used to control lighting or power The patent was applied for in early 1975 and is granted under the old laws.


A control panel carries two parallel strips, 2,3, of conductive ink, which are printed by silk screen techniques. The strip 2 serves as a resistor and is connected at one end to a low voltage supply, e.g. 5 volts. The strip 3 offers negligible resistance. When a finger is placed as a bridge at any position along the length of the parallel strips, the potential at that position on strip 2 is applied to strip 3, i.e. the finger acts as the movable contact of a potential divider.


Strip 3 is connected to the circuit under control by resistor 4 which is incorporated to protect the circuit components against static discharge. Amplifer 5 connects to signal processing device 6 which removes any residual a.c. and supplies one input of a comparator 8.

The second comparator input is derived from output 9 and the comparator feeds an up-down signal to digital counter 10 so that the count changes as the potential at 6 varies with respect to the potential at 9 .

Clock pulses for counter 10 are sourced from pulse generator 12.

When the potential at strip 3 is very low, i.e. when there is no finger contact, the output of amplifier 5 drops to a value which operates switching device 13 to connect the amplifier input to a fixed potential source (e.g. -10 volts). This drops the output of amplifier 5 further to establish a positive closed position of the switch 13 and de-activate the clock pulse generator 12. This ensures that spurious charges on strip 3 cannot achieve a control function. Counter 10 and an analogue converter 11 at the counter output together act as a store for the last sensed finger position on strip 3.

The output at 9 is used as a control signal, for instance to vary receiver tuning or amplifier volume.

## ALARM HORN

General Signal Corporation of New York in BP 1523909 describes a simple but efficient horn design, suitable for generating an alarm tone. The patent was first applied for in late 1974 and thus is granted under the old laws.


The horn has a two part coil, inner or primary coil 107 being wound inside secondary coil 108 on the same core 106.

Primary coil 107 has five or six times the number of turns in coil 108 and the two coils are wound in opposite sense and in series. The magnetic fluxes generated by a common current are thus in opposition and the overall flux is equal to the difference between the two fluxes.

As the circuit shows the lower end of primary coil 107 is coupled to the collector

130 of a control transistor 128 and the upper end of coil 107 is coupled to the transistor base 129 via resistor 132.

The upper end of secondary coil 108 is coupled to the transistor emitter 131 and the lower end of coil 108 is coupled to transistor base 129 via resistor 133. Resistor 132 is approximately 10 times the value of resistor 133, the two resistors thus defining a potentiometer chain connected in parallel with the series connected coils. Capacitor 134 bridges coil 107.


When control switch 141 is closed d.c. flows from battery source 140 through the potentiometer chain to put transistor base 129 slightly positive with respect to the emitter. Transistor 128 turns on initiating conduction from collector 130 to emitter 131. Current now flows and capacitor 134 charges.

As a consequence of the turns ratio and sense of the coils a positive voltage is induced in coil 108 which reverse biases transistor 128. But current continues to circulate in the series RC circuit 107, 134 to dissipate stored energy. As the current and flux from coil 107 reduces, transistor 128 switches on and the whole cycle repeats.

A diaphragm 105 adjacent to the coils and core 106 will move with the flux to produce sound. The pitch of the sound produced will depend on the diaphragm resonance and component values. The patent suggests values suitable for generating a piercing tone of 2.2 kHz .

difficulties. Since its launch on January 30 a power supply has malfunctioned and one of the transmitters gave trouble. A back-up instrument was brought into service but the data signals received back on Earth were not strong enough. The mission engineers felt that they could overcome the problem but this meant waiting for the period March/April when the vehicle was eclipsed. However this cannot help the "Light lon" mass spectrometer which seems to have developed a short-circuit on one of the load resistors.

So far Scatha has had the experience of three charge conditions. One of these built up to -300 V . These charges are thought to be related to geomagnetic storms stirred up by the Sun. The eclipse was not detected by Scatha. This was the total eclipse of February 26. The Field Detector experiment uses an antenna of 100 feet span. It consists of two fine wires attached at one end to the satellite and having a small mass at the other end. The rotation of the satellite, about 1 rpm , deploys the wires by "centrifugal" reaction.

The orbital characteristics are: apogee $43,225 \mathrm{~km}$., perigee $27,780 \mathrm{~km}$, an inclination of $8.3^{\circ}$. There is a drift rate of $6^{\circ}$ east daily. The vehicle has a designed lifetime of one year.

## VENUS 11 AND VENUS 12

The two Russian Probes released from Venus 11 and Venus 12 landed on the surface of the planet at points some 500 miles apart. Venus 11 was launched from Earth first and then four days later Venus 12 was also launched. However the flight path differed so that although Venus 11 was launched first it was sent along a different and rather longer path. Consequently Venus 12 was the first vehicle to arrive at Venus and release its probe. Four days later the second probe was on its way to the surface of the planet.

Both probes commenced a regular stream of data. The report of the temperature and pressure at the surface was to the effect that the pressure was of the order of 88 times that of the Earth's atmosphere, and the temperature in excess of $450^{\circ} \mathrm{C}$. Each probe, during its descent, relayed information about the state of the Venusian atmosphere. Nine samples were taken and the result has enabled the profile to be modelled. There is close agreement with that of the American probes. The chromatographs have confirmed that the major constituents are carbon dioxide and nitrogen. The chromatographs at the surface have also detected carbon monoxide.

There is a significant point of interest about these chromatographs. Usually they are somewhat bulky pieces of equipment. In this case the "sigma" units were small and weighed only two pounds. With the data from the Russian units and that from the American mission the knowledge of the bright planet should be increased considerably.

## TROUBLE FOR SCATHA

The United States Air Force satellite, which was designed for a project of special importance, the investigation of the electrical charges built up on spacecraft, has run into

## UK-6 BRITAIN'S SPECIAL STARGAZER

UK - 6 has the launch date of May 24 this year. A Scout vehicle will launch the satellite which will go into a high circular orbit. Originally it was intended that the altitude should be 550 km . In fact the altitude will be 625 km with an inclined orbit of $55^{\circ}$. This situation has been achieved by improvements in the Scout vehicle. In addition to the achieving of the greater height the lifetime of the satellite will be increased. It is expected that its life will be 3 years, but if the same nursing by the team who kept UK-5 in operation is available, the life will be extended.

It is predicted that there will be a period of intense solar activity at the end of this year resulting in the raising of the top limit of the Earth's atmosphere. There will therefore be the possibility of detecting particles which come from the Sun's corona. The inclination of the orbit was chosen in the interest of the cosmic ray detector. At the inclination of $55^{\circ}$ the instrument will be able to scan through the Earth's magnetic field. This position however does reduce slightly the best achievable measurements by X-ray apparatus.

## ALTITUDE CONTROL

The mass of the UK-6 is 155 kg . The payload is about 40 per cent of the total, which is a high proportion. The satellite is spin stabilised. The altitude control is rather unusual, being based on a magnetic coil which encircles the base of the satellite body. When a current is passed through the coil a force is set up which enables the whole satellite to be precessed to another position. There is provision for passive thermal control and the solar array will be able to deliver 95 W continuously to the end of its life. Batteries are available to provide power when the satellite is eclipsed.

The X-ray sensors are directed away from the Sun so that simultaneous observations may be made from Earth.
It is hoped that UK-6 will provide more data so that an even better assessment may be made of the manner in which stars are formed. There are three principal experiments on board and these will provide information on variable X-ray sources, low energy X-ray sources, such as the possible Black Hole Cygnus X-1 or the Crab nebula. There will also be information on the heavier nuclei of cosmic rays. These are suspected to come from the remnants of super-novae. These data will enable astrophysicists to understand more about how the radiation is produced and perhaps provide a clue as to formation or collapse of stars.

## COSMIC-RAY DETECTOR ON UK-6

The cosmic-ray detector was supplied by a team from Bristol University. It is the largest of the experiments. It consists of a pair of concentric spheres 75 cm in diameter. They are filled with a mixture of neon, helium and argon with traces of nitrogen. The mixture is maintained at a pressure of a little over one atmosphere. A battery of photomultiplier tubes surround the chamber and these observe any events taking place within. The outer sphere acts as a supporting structure. The inner which is made of plastic and contains a wavelength "shifter" is the working part of the detector. The designers believe that this will be the first instrument to accurately measure the rays formed from elements over a wide range, specifically from iron to uranium and possibly beyond. The action of the cosmic-ray detector is such that when a cosmic-ray, which is an atom that has lost its electrons, passes through the chamber it produces flashes of visible radiation, one each time it passes through the plastic sphere and one each time it passes through the gas mixture. This is the Cherekov effect which is observed by the photomultipliers. The detector is arranged so that several of the multipliers "see" each flash. The charge on the nuclei is measured as it passes through.

The second experiment which is designed for the investigation of low energy $\mathbf{X}$-rays is a joint contribution by the University of Birmingham and the University College, London. The experiment has four gazing incidence Xray reflectors which lead to proportional counters. These are intended to be pointed at possible X-ray sources, to allow detailed observations to be made. These studies will work in cooperation with ground based observation to ensure the accurate position fixing of the sources.
Leicester University supplied the third principal experiment and it is designed to observe the variable $\mathbf{X}$-ray sources. The apparatus is similar to the University College and Bristol University experiment with the ability to observe rapidly varying sources.

Two minor experiments from the Royal Aircraft Establishment are aboard the satellite. One is a collection of new types of solar cells which are to be tested under space conditions and the other investigates how space effects metal oxide semiconductors.


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The pulses from the Schmitt trigger are fed into the two monostables via the leading edge trigger inputs. The time constants of the two monostables define the lower and upper limits of the passband. With the respective R set high, output pulses appear at $Q$ for input pulses below the crossover frequency. For higher frequencies $Q$ remains high. With R set low, the output at Q remains low.

IC1(a) defines the lower limit of the passband, so that below this band its Q output varies at the input frequency, setting $R$ on the (b) circuit low for the leading edge of each new pulse, and therefore holding "Qb" low. Within the passband Oa remains high and Qb varies at the input frequency. Above the passband $Q b$ remains high.

Components D1, D2, C3, C4, R8, R9 form low pass filters, so that the inputs to the switches can only be set high when Q and $\overline{\mathrm{Q}}$ are in steady states outside the passband.

The electronic switches are provided by one half of a 4016 CMOS quad bilateral switch i.c. The switches cause the capacitor C5 to charge or discharge according to the frequency of the input pulses, and the voltage is applied to the gate of the 2 N3819 FET. The very high gate impedance ensures that the charge remains constant until the next operation of one of the switches. The l.e.d. is driven by TR 5.

The LDR of the opto-isolator varies the current drawn by TR6 and TR7, and hence the current through the bridge rectifier. These components take the place of the variable resistor in the normal triac control circuit.

The control circuit is connected in series with the projector motor and its normal variable speed control resistor.

## DESIGN PARAMETERS

For stable speed control and rapid response at switch-on without overshoot, an appropriate value for C5 must be selected. If C5 is too small, the motor speed will hunt about the desired value, since the effective time constant of the
speed controller is smaller than the reaction time of the motor. If C 5 is too large the motor will take too long to reach the correct speed when switched on. The optimum value of C5 will depend upon the characteristics of the motor and the load presented by the projector, and is easily found by experiment.

## CONSTRUCTION

The lamp and photo-darlington are mounted on a small piece of veroboard which is supported by a conveniently placed bracket adjacent to the projector shutter blades. The low voltage power supply is derived from the 8 V a.c. projector lamp supply. All the leads, including those for the motor control circuit, are taken out of the base of the projector body to a separate box, mounted behind the projector on a common baseboard. This form of construction was chosen so that no physical modification of the projector would be necessary.

The low voltage circuits are mounted on two pieces of veroboard approximately $45 \times 120 \mathrm{~mm}$. A third plain matrix board of the same size holds the a.c. circuitry and optoisolator, and the three boards are mounted as a sandwich, using 6BA studding and spacing nuts, inside an aluminium box.

A single toggle switch provides switching for 16 and 24 f.p.s. Slower speeds may be obtained using the normal projector speed control. The control circuit is energised by switching on the projector and no other controls are necessary.

The layout and form of construction are not critical, apart from the need to ensure that the high voltage triac circuit is mounted separately from the rest of the components.

Using a variable speed cine projector can be very frustrating. Continuous adjustment may be necessary to keep the speed of the projector constant as the machine warms up, and the load on the take up spool varies.



Fig. 1. Block diagram of Cine Speed Controller. Solid state switches are used to transfer the digltal filter output to the integrating capacitor.

| Resistors |  |
| :---: | :--- |
| R1 | $100 k$ |
| R2 | $1 k$ |
| R3 | $47 k$ |
| R4 | $100 k$ |
| R5 | $3 k 3$ |
| R6, R7 | $270 k$ (2 off) |
| R8-R10 | $4 M 7$ (3 off) |
| R11 | $2 M$ |
| R12 | $3 k 9$ |
| R13 | $1 k 2$ |
| R14 | $22 k$ |
| R15 | 390 |
| R16 | $100 k$ |
| R17 | $200 k$ |
| R18 | 22 |
| R19 | $1 k 5$ |
| R20 | 82 |
| R21 | 47 |
| R22 | ORP12 LDR |
|  |  |

Potentiometers
VR1-VR4 250k presets (4 off)
Capacitors

| C1-C4 | 100 n (4 off) |
| :--- | :--- |
| C5 | $4 \mu 7$ |
| C6,C7 | $100 \mathrm{n} / 400 \mathrm{~V}$ (2 off) |
| C8,C9 | $100 \mu$ (2 off) |

Transistors and Diodes

| TR1 | 2N5777 |
| :--- | :--- |
| TR2, TR3 | BC147 (2 off) |
| TR4 | 2N3819 |
| TR5 | BC148 |
| TR6, TR7 | BF259 (2 off) |
| D1,D2 | 1N914 (2 off) |
| D3 | TIL209 |
| D4 | BR100 |
| D5-D8 | full wave rectifier |
| D9-D12 | 400V p.i.v. |
| D13 | BZY88C6V8 |
| CSR1 | T2800D |

## Integrated Circuits

## IC1 4528 <br> IC2 4016

Miscellaneous
L1, L2 55 Turns 28 s.w.g. $1 \frac{1}{4} \times \frac{1}{4}$ inch ferrite rod. LP1 miniature 5 V incandescent bulb.
S1 DPDT toggle switch.

Fig. 2. Full circuit diagram.



EGT13

Fig. 3. Supply Board.


Fig. 4. Main Board.


E0 115

Fig. 5. Mains Voltage Board.


## SETTING UP PROCEDURE

Apart from the selection of an appropriate value for C5, the only other adjustments required are the tuning of the digital filter. VR1 and VR3 have to be set to define the lower and upper limits of the passband respectively.

Some method must be used to indicate when the projector is running at the correct speed. A strobe disc on a convenient shaft provides a very accurate method of measurement.

With S1 set to 16 f.p.s., the passband of the digital filter is first set to its maximum by adjusting VR1 to its maximum, and VR3 to its minimum value. The normal speed control is set to fast and when the projector is switched on, C5 will be uncharged and the motor speed will rise to the top of the passband. VR3 is now increased, bringing down the motor speed until it reaches a value fractionally above 16 f.p.s. The

## News Briaf's

by Mike Abbott

## COUNTDOWN

Muirhead '79 -May 15-16. Muirhead is a company which specialises in communications technology and servo control systems, and the exhibition and symposium will take place at the Glaziers Hall in London. Further details: Mrs. P. Boreham on 01-650 4888.
The first All-Electronics Show/Seminex, Scotland, will take place on May 15-17, 1979, at Edinburgh's Assembly Rooms. Some 68 exhibitors were listed at the time of writing. Details: Saffron Walden 22612.
Welsh Amateur Radio, TV, Electronics and Computer ExhibitionSunday, May 20. Barry Memorial Hall. There will be trade, and bring-and-buy stands. Further details: Reg Knowles on 0222-565656.
Midlands Breadboard-May 23-26. Bingley Hall, Birmingham. Details: Trident.
Intel Fair-June 11, 1979. Wembley Conference Centre. Details to be announced.
Tranducer $79+$ Testmex 79-June 19-21. Wembley Conference Centre. Details: Trident.
Great British Electronics Bazaar-June 28-29, 1979. Alexandra Palace, London. Details: 0799-22612.
1979 Microcomputer Show (incorporating the DIY Computer Fair)July 5-7. Bloomsbury Centre Hotel, London. Will include seminars. Further details: Online Conferences Ltd. Tel: Uxbridge (0895) 39262.

top of the passband is now set. If a load is now applied to the projector by grasping the take-up spindle, the speed will drop. VR1 is decreased until the motor speed has risen to a value fractionally below 16 f.p.s. The bottom of the passband is now set. Further fine adjustment of the two resistors may now be made as necessary to achieve the minimum passband. S1 is then set to 24 f.p.s. and the procedure repeated with VR2 and VR4.

## CONCLUSIONS

There is no simple electronic solution to the accurate.control of a variable speed motor, but with the right time constants and the band width correctly set up, this solution works extremely well! It provides a stable projector speed which is totally independent of motor temperature or load. No modification of the projector was necessary and no additional power supplies are required. With a common base board, the projector and speed control are completely portable. At last the projectionist can watch the film instead of the projector.

Consumer Electronics Symposium -July 8-11, 1979. University of Essex. Organised by the Society of Electronic and Radio Technicians. Details: The Symposium Office (CE), SERT, Faraday House, 8-10 Charing Cross Road, London, WC2 OHP.
The International Word Processing exhibition and conference will take place July 10-13, 1979, at the Wembley Conference Centre.

Word processing is having a dramatic effect on business procedures, and this event, which is claimed to be the largest in Europe, will display "virtually every available system and piece of equipment".

Details: BETA Exhibitions, Business Equipment Trade Association, 109 Kingsway, London WC2B 6PU.
Harrogate International Festival of Sound-August 18-19 (public), August 20-21 (trade) 1979. The Exhibition Centre + hotels. Details: Exhibition and Conference Services Ltd., Tel. 0423-62677.
Telecom '79-September 20-26. Palais des Expositions, Geneve. Details: Secretariat Telecom '79, Orgexpo, 18 Quai Ernest-Ansermet, Case Postale 65. CH-1211, Geneve 4 (Suisse).
Eltro Hobby '79-October 3-7. Killesberg Exhibition Grounds, Stuttgart. Details: 01-236 0911.
Compec-November 6-8, 1979. Grand Hall Olympia, London. Details: Iliffe Promotions Ltd. Tel: 01-261 8437/8.
Electronics 79-November 20-23. Olympia, London. Details: 021-705 6707.

Breadboard 79-December 4-8. Royal Horticultural Halls, Westminster. Details: Trident International Exhibitions. Tel 08224671. IEA/Electrex-February 25-29, 1980. National Exhibition Centre, Birmingham. Details: Industrial and Trade Fairs Ltd. Tel: 021-705 6707.
All-Electronics Show (1980)-April 29-May 1. Grosvenor House, London. Details: 0799-22612.

## News Briefs

## BRITISH SATELLITE

by Mike Abbott

THE FIRST all British amateur satellite is to be built at the University of Surrey, co-ordinated by the Telecommunications Research Group within the Department of Electronics and Electrical Engineering. The satellite's purpose and proposed features are a departure from the international OSCAR series, in that it will provide practical experience in developing an inexpensive UK spacecraft programme, and will feature a series of high frequency beacons, enabling radio amateurs worldwide to study the changing effects of the ionosphere on radio signals.

Collaboration will involve the university's Electronics and Amateur Radio Society (EARS), the Amateur Satellite Corporation (AMSAT), AMSAT-UK, and the Radio Society of Great Britain. Many companies such as Racal are to give active support.

Priority will be given to telecommand and other fundamental services, but complex experiments are anticipated, to be undertaken either by the university or amateur groups in this country. With a possible launch opportunity in early 1981 , the sateliite is intended for a polar orbit at an altitude of 900 km .

These satelites house VHF and UHF receiver/transmitters which allow radio amateurs to extend the range of their transmissions in the same way television programmes are relayed around the globe.

With the fault which developed in OSCAR-6 causing it to switch on and off unexpectedly, extra command stations were set up in Canada and Australia to control its use. Lacking in a similar facility, indiscriminate use by European radio enthusiasts threatened the satellite with drained batteries, and to counteract this EARS set up the only command station in the world, run (to this day) by students.

The EARS command station has since 1974 commanded successive OSCAR satellites while in orbit over Europe.

## MARSHALL'S BRISTOL MOVE

Due to expansion, Marshall's have now moved from Fishponds Road, Bristol, to 108A Stoke's Croft, which is approximately five minutes' walk from the main shopping centre.


The command station at Surrey University has a steerable aerial complex capable of tracking to an accuracy of 0.5 deg , using an ex-Admiralty 2-metre paraboloid, and a tracking mount which was originally part of a Bofors anti-aircraft gun.


## POIIIIS REISIIT

## TRS 80 REVIEW (April 1979)

Unfortunately some errors appeared in the programs given in the TRS 80 review. The programs should read:

PRINT( $389 * 14.761$ ) $\uparrow 8.7) * \operatorname{SIN}(0.87)$
10 CLS
20 FOR $X=129$ TO 191
30 PRINT X; " '"; CHR\$ (X);
40 /F INT $((X-128) / 9)=(X-128) / 9$ THEN PRINT: PRINT 50 NEXT

## METRONOME (September 1978)

If the unit does not operate correctly, swap the two gates IC3d and IC1d so that IC3c is driven from IC3d. D3 should be 7.5 V .

PHASER (April 1979)

The pin connections for the BF244B were incorrectly given in Fig. 2. The correct connections are shown above.

## CHAMP-PROG (April 1978)

Fig. 8.2. D1 should go to pin 15 on IC1, and C2 should go to pin 14, IC1. Also, pin 2 should connect to pin 12 of IC1 and not pin 5 . The timing capacitor of IC3 (C6) should be 47 nF . TR20 configuration should conform to that of TR 19-26. The connections to pins 1 and 3 of the PROM socket should be interchanged.

Fig. 8.5. These waveforms may seem to be upside-down, but are in fact correct. See Readout September 1978.

If the supply over-voltage protection circuit triggers unnecessarily due to the regulator's slow response at switchon, reduce the unregulated input voltage to 72 volts.

The data bus should not be taken from the transistors of CHAMP, since they perform an inversion, but direct from the CHAMP data bus.

The circuit around D22 and S1 should be wired as below.

## AUTORANGING

MULTIMETER (May 1979)
A printing error has placed the green wiring overlays in Figs. 2.8 and 2.9 about 5 mm "North West" of their intended positions. Correct registration can be recognised by noting that wires 1 to 5 should go to the wafer poles (below mounting lugs).



## STORING AND DISPENSING

If you are not a smoker who stores components in 2 oz . tobacco'tins you may be interested in one of Verospeed's smart multidrawer units. They are available in 24 and 48 drawer sizes. The drawers are easily labelled and dividers are available in packs of ten.


Also shown is a component dispenser for small production runs. It has three rows of picking bins of varying widths.

Multi-drawer units are $£ 16.80$, plus VAT, inc. p\&p. The component dispenser is $£ 5.52$, plus VAT, inc. p\&p. Bins, from 38 p to 92 p.

Verospeed, Barton Park Industrial Estate, Eastleigh, Hampshire, SO5 5RR. (0703 618525).

## IRONS AT EXCLUSIVE PRICES

A company offering Litesold irons at reduced prices has just been set up. The company, called Future Electronics, is advertising the irons exclusively to P.E. readers.

The Litesold Model 15, 12 W iron, fitted with a $1 / 16 \mathrm{in}$. bit, operating at $360^{\circ} \mathrm{C}$ and weighing 14.9 gms is available at $£ 3.90$ inc. VAT and p\&p from Dept. MA1, Unit BI, Park Hall Trading Estate, Martell Road, London SEI. (01-761 3919).

## MINI CASSETTE DECK

The CM600 miniature cassette system is a completely self contained unit measuring only $76 \times 76 \times 64 \mathrm{~mm}$ and has been designed specifically for digital applications. With the drive motor, read/write amplifiers and control circuitry all included in the unit it only requires a 5 V power supply for operation.
The CM600 has a two track recording head which produces a maximum recording density of 800 bits per in. and a data rate of 2,400 baud. Data capacity on a standard 100 ft miniature cassette is 1.6 M bits. The reel to reel drive system has a forward search speed of 5 i.p.s. with a rewind speed of 15 i.p.s. and a stop/start time of 150 ms .


Operation of the system is controlled entirely by external logic signals. Typically these represent tape direction (forward/release), tape motion (stop/go), tape speed (fast/slow), select read/write and data input. Output lines carry data and indicate which side of the cassette is being used.

The price of the CM600 is $£ 110$ plus VAT and $p \& p$.
BFI Electronics Limited, 516 Walton Road, West Molesey, Surrey KT8 0QF. (01941 4066).

## DISPLAY CONSOLES

The new range of easy access display consoles from Boss Industrial Mouldings offers the option of either a satin black aluminium display panel or red, green or neutral grey, translucent filter windows if illuminated displays are to be used.

The contoured sides of the BIM7500 series are 12.7 mm thick, solid oiled walnut, with textured sand finished exterior panels, the top one being 1.2 mm thick steel and the keyboard panels being 1.6 mm thick aluminium.


The consoles are available in nine sizes offering the combination of 4 keyboard panel widths and overall sizes ranging from $250 \times$ $260 \times 112 \mathrm{~mm}$ high to $500 \times 431 \times 200 \mathrm{~mm}$ high with the larger sizes having a fully hinged upper section.

The exterior panels of all models are quickly detachable on removal of two or three concealed screws to enable rapid component accessibility.

The consoles vary in price from $£ 38.44$ to £81.54 excluding VAT and p\&p. For further information contact Boss Industrial Mouldings Ltd., Higgs Industrial Estate, 2 Herne Hill Road, London SE24 0AU.

## P.C.B. TERMINAL BLOCKS

A range of p.c.b. mountable terminal blocks from two to 12 ways is available from Carrier Electronics. The pins are on a 5 mm pitch or two pitches of 0 lin matrix board. Wires up to $1.5 \mathrm{~mm}^{2}$ are accepted in a- nickel plated brass bush with screws clamping on to captive phosphor bronze wire protectors. The terminals have a 13 A continuous rating.


Prices range from 18 p for two way to 74 p for 12 way, plus, VAT at eight per cent, and postage.

Carrier Electronics Ltd., 48 Chester Street, Wrexham, Clwyd. (0978 5667I).

## ELFII

The Elf II microcomputer kit is based on the RCA 18028 bit microprocessor. The basic system includes a 256 bit RAM expandable up to 64 K , a fully decoded hex keyboard and a 5 slot plug-in expansion bus (less the 86 pin connectors). The system is mounted on a double sided plated through p.c.b. A 1861 video i.c. is also supplied to enable segments of the memory to be displayed on a TV screen.


Also included in the basic kit is an owner's manual and complete instructions for assembly and testing. A power supply is available for $£ 5.00$ together with a complete range of expansion modules, including a fully encoded ASCII keyboard.

The basic Elf II kit is available from $£ 99.95$ with a steel cabinet and plexiglas dust cover for an extra $£ 29.95$. All prices are exc. VAT and p\&ep. P.E. will soon be reviewing this kit.

HL Audio, 138 Kingsland Road, London E2 8BY. (01-739 1582).

## KEYBOARD KIT

This keyboard kit has a 9 bit parallel ASCII encoded output with dual polarity strobe edges and it can generate both upper and lower case ASCII codes. The unit has a power requirement of 5 V 120 mA and features long keystroke ( 3 mm ) keys throughout. All the keytops have removable transparent caps suitable for special legends if required.


The overall size of the keyboard is $290 \times$ $140 \times 25 \mathrm{~mm}$ and it is priced at $£ 28.50$ plus VAT and p\&p.

Video Terminals, 197 Hornbeams, Harlow, Essex. (0279 30132).

## PSUs

The TPS range of precision bench power supplies consists six high accuracy dual and single output units from $0-30 \mathrm{~V}, 1 \mathrm{~A}$ up to $0-60 \mathrm{~V}, 2 \mathrm{~A}$.


Although they are similar, the six units in the range offer different specific features: Three of the range are dual output units, with the facility to connect in series and parallel to effectively double the voltage or current ratings and all the outputs have a regulation of less than 0.5 mV variation. With a ripple and noise figure of less than 0.5 mV r.m.s.

Four of the units have an additional $5 \mathrm{~V}(1$ or 3 A ) output facility, allowing users to power logic circuits without connecting to the principal output terminals. The separate 5 V feeds may be finely adjusted by an internally mounted trimpot.

Output voltage on all types is adjusted by a 10 -turn potentiometer mounted on the front panel, and this allows setting to be carried out to within 5 mV . Similarly, current trip adjustment is made using a single turn potentiometer. The moving-coil meters provide reading accuracy better than 2 per cent f.s.d., and on one model-the TPS 21D-these are replaced by l.e.d. digital readouts.

The prices of the TPS range are from $£ 108.55$ to $£ 254.42$ plus VAT and $\mathrm{p} \& \mathrm{p}$.

Gresham Lion Limited, Gresham House. Twickenham Road, Feltham, Middlesex TW 13 6HA. (01-894 5511 ).

## CASIO CALENDAR

The Calendar 200 from Casio is a liquid crystal quartz digital wristwatch that displays the time, date or calender page for any month of the year. The calendar display is a new idea from Casio whereby the information is presented like a conventional calender page accompanied by a large digit representing the number of the month and an indicator showing the position of the Sundays. Automatic leap year conversion is programmed in.

 cluding VAT. For further information contact Casio Electronics Co. Lid., 28 Scrutton Street, London, EC2A 4TY). (01-377 9087).

## CHILTERN IMPACT PRINTER

The new type 150 Numeric Impact Printer has been specifically designed for the digital instrument market. The instrument has eight active columns as standard with facility to expand to 15 columns if required. Each column has 12 characters with 0 to 9 with a dash and dot format and two extra columns have assorted symbols.


The standard interface is parallel TTL CMOS DTL compatible with optional inverted logic by selection and manual or remote control of all functions. An optional bit parallel character serial interface is available as is a line buffer giving up to four lines of storage.
There are optional clock/calendar and serial event counter cards available with the unit and for specialised print functions an IEEE/488 interface.
For further information contact Chiltern Data Systems Lid., Stoke Row, Henley-onThames, Oxon, RG9 5RB. (049-17 549).

## PLAY IT AGAIN CASIO

What more could you want a calculator to do than calculate, tell the time and ring alarms? Casio think a tune playing function may fulfil a need. Their Melody Card, M-80, is just slightly larger than their recent creditcard size series.

Digits I to 8 are labelled Do to Dó, digit 9 gives one note above that octave, and the decimal and zero play the two notes below it. Thus you can play the one note samba and more.


A digit "key" will play its note as long as the key is pressed. Tunes of up to eight notes can be stored in memory. Even the timer alarms can be made melodious.

The recommended retail price of this threat to the Stylophone is $£ 29.95$. It can be bought by post at $£ 25.95$, inc. VAT and $p \& p$. from Timetron, The Beaumont Suite, 164-167 East Road, Cambridge CBI IDB. (0223 67503).

# Microprocessor Evaluation System <br> PART TWO 

THE complete development system was mounted into a $380 \times 200 \times 100 \mathrm{~mm}$ case which was drilled as shown in Fig. 2.2 and then covered with fablon. The switches and l.e.d.s were fitted to the front panel taking care to place the anode pin of each l.e.d. at the top. The on/off switch, two DIN sockets, fuse holder and a grommet for the cable entry hole were fitted next with a 6BA earth tag fitted close to the grommet.

The outside positions of switches S4 to S 19 were joined together using two $16 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. tinned copper wire bus bars with the anodes of D1 to D16 joined to the third bus bar via resistors R34 to R49. (Fig 2.1).


Fig. 2.1. Front panel wiring diagram
The track layouts for the double sided p.c.b. are shown in Figs. 2.3 and 2.4 with the component overlay in Fig. 2.5. After the board has been made it should be placed in the case along with the transformer. Check that the transformer is clear of the p.c.b. and then mark and drill the three p.c.b. mounting holes.

Extra holes should be drilled in the rear and bottom of the case for ventilation.

## POWER SUPPLY UNIT

The circuit diagram of the p.s.u. is shown in Fig. 2.6 with the p.c.b. design and component overlay shown in Fig. 2.7 and 2.8. After the p.c.b. has been assembled and checked ensuring that the two regulators (IC25, IC26) are soldered with long leads, the p.c.b. should then be mounted on the rear of the case and the two regulators mounting holes drilled. Both regulators should then be mounted using the insulated mounting kits supplied with the devices.

An ohmmeter should be used to check that the regulators
are not shurted to the case and then the p.s.u. can be wired up to the secondary winding of the transformer. The earth lead from the $O V$ of the p.s.u. should also be connected to 6BA solder tag under one of the mounting screws of SK1.

The mains cable to the primary of the transformer can be wired next via the fuse holder and mains switch. The mains earth lead should also be connected to the case.

Recheck the p.s.u. wiring and then switch on and monitor the output voltages. They should be approximately $-3 \cdot 3 \mathrm{~V}$, +5 V and +11.2 V . If the p.s.u. is functioning correctly switch off and disconnect the unit from the mains.


EA28
Fig. 2.6. Power supply circuit diagram

## MAIN PRINTED CIRCUIT BOARD

The main processor board can be soldered next and it is strongly advised that i.c. sockets are used for all the integrated circuits. A suggested soldering sequence is to fit the i.c. sockets first, checking their orientation carefully, then the resistors and capacitors, TR1, TR2, D17, to D20 and 1 MHz . crystal. Vero-pins can be used for the power input wires, S1-S19 and the I.e.d.s D1 to D16. Also fit pins for the five branch external output and input lines and fit pins for IC19 pins 9, 10, 15 and 16 (the 4 bottom $Q$ lines of the output latches). Fit all the through board links, ensuring that they are soldered both sides. Also check that all capacitors and resistors are soldered both sides of the board where necessary. Do not plug any i.c.s in yet.

Clean the flux from the board and examine it very carefully for bad joints and solder bridges between tracks. At this point take an ohmmeter and check the p.c.b. out against the circuit diagram. An hour spent doing this could save many hours later.


PL1 (Output)

| Pin | From | Signal |
| :---: | :---: | :---: |
| 1 | IC19 pin 16 | O/P latch $2^{\circ}$ |
| 2 | IC19 pin 15 | O/P latch $2^{\prime}$ |
| 3 | CC19 pin 10 | $0 / P$ latch $2^{2}$ |
| 4 | IC19 pin 9 | O/P latch $2^{3}$ |
| 5 | n.c. |  |
| 6 | PSU +5 V | $+5 \mathrm{~V}$ |
| 7 | GND | OV |
| PL2 (Input) |  |  |
| Pin | From | Signal |
| 1 | IC23 pin 25 | EBCA ${ }^{\text {d }}$ |
| 2 | IC23 pin 24 | EBCA1 |
| 3 | IC23 pin 23 | EBCA2 |
| 4 | IC23 pin 22 | EBCA3 |
| 5 | IC6 pin 1 | EBC1 |
| 6 | PSU + 5 V | +5V |
| 7 | GND | OV |
|  | table 1 |  |



Fig. 2.3. Underside view of the main printed circuit board


Fig. 2.2. Case drilling details. Front panel (top), rear panel (bottom)


Fig. 2.5. Component overlay for the main printed circuit board


## INSTALLATION OF MAIN P.C.B. AND TESTING

Mount the processor p.c.b. back into the case with the three self-tapping screws and connect the power supply outputs to the board. Connect the unit to the mains, switch on and monitor the voltages on the main p.c.b. If they are correct the switches can be wired to the board. Always remember to switch off at the mains before inserting i.c.s or adding wires to the board.

Wire switches S1, S2 and S3, wire the bus connected to R34 to the +5 V rail. Insert IC1, switch on, and check that the 1 MHz clock appears at IC1 pin 6 . Insert IC 4 and check that when the run load switch is in the loąd position IC4 pin 8 is low and when the switch is in the run position pin 8 goes high. Fit IC2 and 3 and check that when S1 is in the run position pulses appear at IC2 pins 9 and 5 and IC3 pins

Address 377 octal should be loaded into the address latches IC13 and 14 and IC11 pin 2 should go low. Put S4 to 'O' and press S2, IC11 pin 2 should go high. Put S4 to '1' and press S2. IC11 should go low again. Put S5 to 'O' and press S2, IC11 should again go high. Rèpeat the above test for all eight bottom switches, only when they are all at ' 1 ' should IC11 pin 2 go low.

If everything is alright, wire the cathodes of I.e.d.s D1 to D16 to p.c.b. D1 goes to IC19 pin 1. D16 to IC22 pin 8 (Fig. 1.3). Also wire up DIN sockets 1 and 2 as shown in Table 1. Insert IC15, 16, 17, 18 and 23, check that S1 is in the load position and S4 to S19 are In their centre off position and switch the unit on. Check all the p.s.u. voltages again to check that they are alright under full load.

The processor can be tested by the short program shown below.


Fig. 2.7. P.c.b. design for the p.s.u

9 and 5. Fit IC5 and 6 and check that pulses appear at IC5 pins 6 and 8 when S1 is in the run position, at IC6 pins 4 and 6, and also at TR1 and TR2 emitters. When S1 is in the load position IC4 pin 3 should be low and when S1 is in the run position IC4 pin 3 should go high. Leave S1 in the load position.

Insert IC7. Check that IC7 pin 8 is high and goes low when S2 is pressed. Check that IC7 pin 6 is high and goes low when S3 is pressed. Insert IC8, 9, 10, 11, 13 and 14. IC13 pin 9 should go low and then high when S2 is pressed and released. IC11 pin 4 should be high and stiould go low when S3 is pressed. IC10 pin 11 should be high and should go low when S3 is pressed. Insert IC12, 19, 20, 21 and 22. Wire the centre connection of switches S4-S19 to the Veropins at the end of resistors R18 to R33, set all 16 switches to their centre off position and switch on the unit. Monitor IC19 pin 2 and check that it is floating (i.e. an open circuit TTL input). Check that when S4 is switched down (' $O$ ') the pin is switched to ground and when S4 is switched up (' $O$ ') the pin goes to +5 V . Carry out this test on pins 2, 3, 6 and 7 of IC19, 20, 21 and 22 with switches S 4 to S19, each switch control only one pin. Monitor IC1 1 pin 2, set the bottom eight switches S4 to S11 up at '1' and press S2.


50100
Fig. 2.8. Component overlay for the p.s.u

## ENTERING A SIMPLE PROGRAM

Consider the following program to increment register 1 in the CPU and output its contents to the l.e.d.s:-

| Octal | Binary | Octal | Binary |  |
| :---: | :---: | :---: | :---: | :---: |
| Add | Add | Data | Data |  |
| 0 | 0000000000000000 | 711 | 0000000111001001 | Clear RI |
| 1 | 0000000000000001 | 11 | 0000000000001001 | Increment R1 |
| 2 | 0000000000000010 | 1101 | 0000001001000001 | ) Output R1 to |
| 3 | 0000000000000011 | 371 | 0000000011111111 | ) Addu 377 |
| 4 | 0000000000000100 | 4 | 0000000000000100 | Jump io |
| 5 | 0000000000000101 | 1400 | 0000001100000000 | Add 1 |
| 6 | 0000000000000110 | 1 | 0000000000000001 |  |
| 376 | 0000000011111110 | 0 | 0000000000000000 | Write stant add into location 376 |

1. Select Load/Run switch $S 1$ to Load.
2. Set switches S 4 to S 19 to octal $\emptyset$.
3. Push Enter Add S2 to enter Add $\emptyset$ into Add register.
4. Set switches S4 to S19 to octal 711.
5. Push Enter Data switch to enter 711 into $\emptyset$ in RAM.
6. Set switches S4 to S19 to octal 1.
7. Push Enter Add switch to enter Add 1 into Ádd register.
8. Continue until the octal number 1 is entered into location octal 6 in the RAM.
9. Now select octal 376 on switches S4 to S19.
10. Push Enter Add switch, enter 376 into Add register.
11. Write octal $\phi$ into this location using switches $S 4$ to S19 and the Enter Data switch. This writes the start Add into location octal 376 (any spare location can be used for the start address).
12. Put switches S4 to S19 to centre (off) position.
13. Select Load/Run switch to RUN. The program should now commence and the l.e.d.s should increment.
14. It is important that the last entry into the Add register should be the start Add of the program.
NEXT MONTH: programming.


## Training

Catch 'em while they're young seems to be the motto in France. I note with interest a press report that 10,000 microcomputers are being ordered for French schools and universities and that 150 teachers are to get a year's training on their use and another 1,500 teachers a shorter course. In parallel there is a big competition for ideas on how to use MPUs in which up to 1,000 young inventors are expected to submit entries. A simllar scheme is in preparation for the UK with a funding of $£ 12.5$ million over five years, but is still in the paper and committee stage.

In the UK there are encouraging signs that science and engineering are regaining favour in schools and colleges as proper, even exclting, future careers. Salary structures have certainly improved over the past three years and the prospect, as seems certain, of a critical shortage of engineers over the next decade or so will at least give employment security and fine opportunities to all those who take the profession seriously. The latest salary survey shows that IEE members were averaging $£ 7,240$ p.a. in January this year compared with £6,210 p.a. in January 1978, an improvement well in advance of the cost of living. In the public sector electrical and electronic englneers' salaries on average were higher than in private industry so one assumes that public sector employees will keep quiet about comparability while those in the private sector might well start raising hella reverse of the general trend.

## Results

The fickle wheel of fortune spares nobody. EMI's high-flyer, the EMI Scanner, so profitable in its early years is now almost a dirty word with losses this year estimated by City sources as $£ 15$ million or more, largely attributed to slumping sales in the USA and increasing competition in the market place. Budget-price brain scanners,
for example, are expected from two Japanese manufacturers this year, Toshiba and Hitachi. But EMI can take comfort in having sold two top-price whole-body scanners to China for $£ 500,000$ and is clearly making further efforts to diversify its market outside the USA.

Plessey, déspite increased business looks like having a standstill year as far as profits are concerned. In Plessey's case the problems are in the loss-making Liverpool plant making telephone exchanges, and Garrard making record players. Industrial disruption has also eaten into Plessey profits, one estimate being $£ 800,000$. So wonderful performance in some areas of Plessey activities are masked in the overall results by difficulties elsewhere, and by no means always the fault of the company.

Plessey, however, remains fundamentally strong with a record order book of over $£ 800$ million and with exports and overseas sales accounting for over half total turnover and a market in 131 countries.

GEC is forecast as pushing up profits this year to over $£ 370$ million and topping easily the $£ 400$ million mark in 1979/80. Profit growth will be led by GEC's electronic divisions, spearheaded by Marconi. The Formation of GEC-Fairchild in the UK has sparked off speculation that GEC has its sights on acquiring the whole of Fairchild Camera and Instruments in the United States.

The Racal Group, fastest growing in both turnover and profit in British electronics, is expected to have another good year. Recent tidying up operations include two name changes. Racal-Thermionic, the professional-quality tape-recording company in the Group has been re-named Racal Recorders Ltd and a base in the USA has been established as Racal Recorders Inc with sales and service initially from Rockville, Maryland, and Corena, near Los Angeles. These will be followed by other sales and service outlets later this year at Chicago and Houston. British Physical Laboratories, acquired by Racal in 1974 becomes Racal-BPL, the change coinclding with the entry of BPL into the digital panel meter field.

Among Racal's recent off-beat support activities are backing for Britain's first amateur scientific satellite being coordinated by the University of Surrey and the supply of radio equipment to the Transglobe Expedition which sets off from Greenwich next September on a three-year circumnavigation of the world by its Polar axis over land, sea and pack-ice. The Racal equipment is said to be worth over £ 130,000.

## Hire

Hiring rather than buying instruments often makes good sense, especially if you need them only to meet short-term needs such as a small production run or in fieldcommissioning of a new installation. The UK instrument hire business has hitherto been shared by two companies, Livingston Hire and Labhire. Now there is a third entrant in the field, Leasemetrix. Not much
of a threat, you might imagine, against two experienced and well-established companies. That is, until you look at the Leasemetrix management. At the top is David Rennie who set up Livingston Hire in the 1960s and was top man there until parting company in 1975. Rennie is joined by sales manager R. J. Mundy and laboratory manager Ray Keogan who both held the same positions at Livingston.

An intriguing situation for industry watchers. Will Leasemetrix come up from behind and scoop the pool? Will there be a price war? Rennie says there will be no price cutting. As for coming up from behind, Rennie thinks there will be a big expansion in instrument hire and there is room for a third company. Financial backing for Leasemetrix comes from the Small Business Capital Fund which is a venture capital operation of the Co-operative Insurance Society.

## Show-Biz

Telecom 79, the giant telecommunications show to be held at Geneva in September will see Britain's biggest ever promotional exercise. Centre-piece will be a huge BPO/Industry joint stand covering 9,000 sq metres occupied by the Post Office, GEC, Plessey, STC, Marconi and Pye TMC. The commercial companies will also have their own individual stands as will 24 other British companies in the business. The operation is supported by the British Overseas Trade Board and the Electronic Engineering Association. Public Relations for the promotion on a day-to-day basis is headed by Peter Wymer, well-known in the industry for many years as publicity manager at Mullard and later as a publicity chief in the Post Office before becoming a private consultant.

## Mini-LP

The shape of things to come in the hi-fi market could be the $4 \frac{1}{2}$ in diameter LP record recently demonstrated in experimental form by Philips. A laser beam is used as a "pick-up" thereby eliminating record wear and giving a ten-year life with normal useage. Philips would clearly like its audio disc to become a world standard, following the path of the Philips tape cassette. The Japanese, however, are all working towards audio systems which will be compatible with videotapes. It could be the start of another damaging war in consumer electronics ending in another muddle of incompatible equipment. Philips say they will adopt a liberal licensing policy towards other manufacturers. If it goes ahead the Philips system should be commercially available by the mid-1980s. A big pluspoint for the $4 \frac{1}{2}$ in hi-fi disc is that it can be readily used in cars.

## Automated Oven

In an attempt to capture a larger share of the microwave oven market, Toshiba has introduced an oven controlled by inserting a magnetic card which is encoded with the cooking instructions.


Rapid click-free selection of four switched tones, each of subjectively balanced loudness level. Designed for the practising musician.


SAVE E21. A Chinaglia Dolomiti USI for £32.95 including postage and VAT. Features 39 ranges, $20 \mathrm{k} \Omega / \mathrm{N}$ up to 5 A and 1.5 kV with electromechanical overload protection, signal injector, antiparallax mirror, case and 12 months guarantee.


PAACTICAL


OUR JULY ISSUE WILL BE ON SALE FRIDAY, 8 JUNE, 1979

M.PPAMI 5



EOT11
Fig. 1. Circuit diagram of the Transmitter
and mains power is then available to the appliance plugged into the $13 A$ socket. The wattage of the mains power which can be controlled depends largely on the choice of relay 1750 W is the maximum for the one specified in the components list). Switches are provided for bistable (on/off), monostable (temporary on) and mains/battery operation. The battery supply can easily be dispensed with for it merely provides an on/off action via another pair of the mains relay contacts available at two sockets. The range of the control system for domestic use depends greatly on the type of furnishings in a room, but you should expect from 5 to 8 metres.

## TRANSMITTER

The circuit diagram of the transmitter is shown in Fig. 1. A CMOS 4011 i.c. is used as a square wave oscillator to drive the ultrasonic transducer at a frequency of 40 kHz . The oscillator is inactive until S3 is closed taking the voltage at pin 1 high. The 4011 contains four NAND gates, two of which are wired as an oscillator using R12, VR3, R14 and C8. The other two NAND gates are used as buffers to switch power to the transducer. The outputs, pins 10 and 11 , of the second two NAND gates go high and low in a complementary way at a frequency of 40 kHz . The variable resistor VR3 enables the frequency of the oscillator to be adjusted to the resonant frequency of the transducer.

## RECEIVER

A combination of bipolar devices and CMOS integrated circuits is used in the ultrasonic sensitive receiver shown in Fig. 2. The function of the circuit may be understood by dividing it into its basic elements. Remember that the overall function of the circuit is to close the relay contacts RLA1
when the ultrasonic transducer receives the 40 kHz sound waves.

Firstly the circuit operates from a nominal 10 V d.c. supply derived from the mains via transformer $T 1$, rectifier diodes D1 and D2, smoothing capacitor C1, and the series stabiliser components, TR1, R1 and Zener diode D4. For portable


Internal view of the Transmitter
operation of the circuit, S1 enables a 9 V battery to be switched into the circuit.

Upon detecting an ultrasonic beam pulsed at around 40 kHz , the ultrasonic receiver transducer generates a small sinusoidal voltage which the preamplifier, based on transistors TR2 and TR3, amplifies. The amplified signals at the collector of TR3 are rectified by D3 and appear as negative going pulses at pin 2 of IC1 which compares these voltage peaks with the voltage on pin 3. The latter voltage is set by R8 and the variable resistor VR1 which acts as a sensitivity control. Thus, when no signals appear at the collector of TR3, pin 2 is held high by R7 and the output voltage from the op amp is zero. When voltage pulses arrive at pin. 2 and drive this voltage below that of pin 3, the output voltage suddenly switches positive, a condition which is made clear when D5 lights. Capacitor C4 provides a certain amount of integration of the pulses and avoids spurious operation of the op amp.

The signal from the op amp passes to pin 4 of IC2, a dual monostable CMOS i.c. The rising voltage on pin 4 triggers the first of these monostables, IC2a, producing a pulse from pin 6 the duration of which is fixed by the product of the R9 and C5 values. This monostable acts as a pulse shaper to provide reliable operation of IC2b and the CMOS flip-flop IC3.


Fig. 2. Circuit diagram of the Receiver

The second monostable is triggered via pin 12 and produces a positive output voltage at pin 10 for a duration determined by the product of the VR2 and C6 values. The same trigger pulse from pin 6 of IC2a switches the flip-flop IC3 via pin 3. Each pulse operates the flip-flop so that its voltage at pin 2 goes alternately high and low. Switch S2 selects in position 'a' the flip-flop output and in position 'b' the monostable output. Either pulse passes to the Darlington pair current buffer transistors TR4 and TR5 to drive the relay RLA. Thus the circuit provides a stable (on/off) or monostable (temporary on) control of the relay. The monostable period is set by VR2. The mains relay contact RLA1 determines the mains current which can be switched and hence the power of the mains appliance which can be controlled.

## CONSTRUCTION

The transmitter components are assembled on a small piece of Veroboard as shown in Fig. 3. One mounting hole on the Veroboard firmly fixes the circuit by means of a 6BA


E6i0e
Fig. 3. Veroboard layout for the Transmitter
nut and screw to the inside of the plastic box shown in the photograph, in which the battery, transducer and switch are also fitted. Before screwing down the box lid, the variable resistor VR3 will need to be adjusted as explained under "setting up".

The receiver circuit is similarly assembled on the Veroboard as shown in Fig. 4. Note that this board also carries the rectifiers for the d.c. supply fed from the low voltage transformer windings. Decide if you want the battery facility. If not, S1a and S1b connections are not required. Double check all the spots where the Veroboard track needs to be cut away.

Note that diode D6 is wired directly across the relay contacts. One pair of the relay contacts (normally open) may be taken to a pair of 4 mm sockets for control applications if the unit is to be operated from the internal battery.

## SETTING UP

If a battery has been included in the design, switch to the battery mode using S 1 a position. Turn the sensitivity control VR1 fully clockwise and the red l.e.d. should light up. Turn back VR1 until the l.e.d. just goes out. As you do this, the relay may be heard to operate. Jangle some keys in front of the receiver transducer and the l.e.d. should momentarily light and the relay be heard to operate. This shows that the circuit is sensitive to ultrasonic noise from the keys. A similar procedure should be followed to check the circuit's operation from the mains.

Now check the function of the transmitter by pointing it towards the receiver from a distance of about one metre. Keep the button pressed and the l.e.d. on the receiver should light up. If it doesn"t, use a small screwdriver to adjust VR3. The setting of $V R 3$ is critical so take your time. The idea is to tune the transmitter to produce the maximum signal from the receiver transducer. If you have a multimeter, connect it across pin 6 to ground of the op amp and adjust VR3 in the transmitter until the maximum voltage is detected. Or an oscilloscope can be used connected across the receiver transducer to detect the maximum signal voltage swing. Plug in a table lamp to the receiver's 13A socket. Switch the


Fig. 4. Veroboard layout for the Receiver

## COMPONENTS

| Resistors <br> R1, R10 | 2 k 2 (2 off) |
| :--- | :--- |
| R2 | 22 k |
| R3 | 10 k |
| R4 | 100 k |
| R5 | 680 |
| R6 | 1 k 2 |
| R7 | 47 k |
| R8 | 220 k |
| R9, R11 | $1 \mathrm{M}(2$ off) |
| R12 | 470 k |
| R13 | 2 M 7 |
| R14 | 4 k 7 |

All resistors $\frac{1}{2}$ W 5\% carbon.

| C6 | $100 \mu 16 \mathrm{~V}$ elect |
| :--- | :--- |
| C7 | $470 \mu 25 \mathrm{~V}$ elect |
| C8 | 680 p polystyrene |

## Semiconductors

| D1, D2, D3, D6 | 1N4001 (4 off) |
| :--- | :--- |
| D4 | 11V BZY88 Zener |
| D5 | TIL 209 |
| TR1 | BC182L |
| TR2, TR3, TR4 | BC109 (3 off) |
| TR5 | $2 N 3053$ |
| IC1 | 741 |
| IC2 | 4528 |
| IC3 | 4027 |
| IC4 | 4011 B |

Switches
S1, S2 s.p.d.t. min. toggle (2 off)
S3 push to make p.b. switch

## Miscellaneous

$\times 1$ Receiver 40 kHz
X2 Transmitter 40 kHz
B1 PP6 battery
B2 PP3 battery
RLA 12 V 2 pole changeover 3 A contacts
Battery clips
Mains 13A socket
Case type $301(70 \times 50 \times 25 \mathrm{~mm})$ Maplin.
Case type $103(188 \times 110 \times 60 \mathrm{~mm})$ Maplin.
mode switch S2 to the bistable mode and press the transmitter button pointing the transmitter at the receiver from a distance of about 2 metres. Remember that the ultrasonic beam is quite narrow (about $10^{\circ}$ ). One operation of the push switch (not too brief) will bring on the l.e.d. and the relay will be heard to operate and the lamp will go on or off. Practice the operation until you can reliably control the lamp, one push for on and the next push for off. You should get control over a distance of at least 5 metres.

Now check the operation of the monostable by switching S2 to this mode. The lamp will normally come on and remain on for a period of time determined by the setting of VR2. Wait for the lamp to go off and then use the transmitter to bring it on. Set VR2 to give the delay required-up to about 50 seconds with the values of VR2 and C6 given. The duration can be increased by increasing the value of C6. This


Internal view of the Receiver

monostable facility is provided if you want to turn on appliances such as a porch light for a limited period of time. In all these adjustments take great care when the receiver is being operated from the mains not to inadvertantly touch any bare mains connections within the unit. Note also that the maximum mains power which can be switched by the relay is determined by the current rating of the relay provided it is a 250 V type, so check the wattage of the appliances.

If the receiver sensitivity control is set too high, the receiver might be triggered by noise which has an ultrasonic component. So beware of rustling papers, jangling keys, the click of a central heating thermostat and bats! You may have to turn down the sensitivity control to avoid unwanted triggering of the receiver circuit.

Finally, you might like to know that the ultrasonic beam from the transmitter can be reflected from objects in a room: the ceiling or the wall opposite the receiver, from a chair leg-or you.

# R.FI.SUPPRESSION for Power Supply Units B.E.TAYLOR* 

SCORES of very comprehensive articles have been written on the subject of suppressing (and filtering) conducted radio-frequency interference generated by switch-mode power supplies. Very few of these articles have been written with the non-specialist engineer in mind, and even fewer have been written that cover r.f.i. generated by linear regulators. This article attempts to show that the design of power supplies with good interference suppression can easily be achieved, provided that a few relatively simple rules are followed.

## SOURCES OF RFI

The first, and most important rule, is to design the power supply with r.f.i. suppression as an integral part of the design. It is futile simply to try to filter the noise at the line input and/or output terminals after the noise has been generated. The interference should be reduced at source. To this end, one must first recognise these sources of interference, and all supplieseven linear ones-have one or more of these sources.

How can linear regulators be classed as generators of r.f.i.? It is only when the linear regulator is closely scrutinised that one recognises one of the first sources of r.f.i. for all supplies: that of large, linepulse currents and their rates of change. One of the reasons for these large pulse currents is the line-frequency rectifier and the use of capacitor input filters. The reservoir capacitor forces the rectifier to conduct over very narrow conduction angles, and the sudden start and/or cessation of the resulting large current pulse that flows in line and neutral conductors-containing unwanted inductance (or the primary referred leakage inductance of the line transformer in a linear supply)-generates noise voltages that appear at the line and neutral terminals.
The effect of this rate of change of current is one of the major sources of
interference over the 10 kHz to 100 kHz band, and is also a factor up to a few megahertz. One of the remedies for this source of interference is to connect as large a value of capacitor (as large as the relevant safety specifications will allow) as near as possible to the a.c. terminals of the rectifier bridge.

Another source of r.f.i. (this time appearing at the output terminals) can be attributed directly to sudden changes in current flow, and is generated in the output rectifiers in switch-mode supplieswhether they be Schottky-barrier devices or plain fast-recovery rectifiers. The usual solution for these devices is to connect RC snubbers across the rectifiers or across the transformer secondary.

Yet another source of interference from currents and their rates of change is the emission of magnetic fields around conductors, chokes and transformers.

Not all r.f.i. is due to current. Many sources also arise from the capacitive coupling of rapid changes in voltage. Unfortunately, the 'dabbing' of the odd capacitor or resistor will have little or no effect. To eliminate these generators of interference, one must revert to the principles of good electrical and mechanical design.

## IDENTIFICATION AND ELIMINATION

At this point, it is best to identify some of these sources, to tabulate them and to look at some of the available means of elimination that may be employed:
(1) The first source of noise due to sharp edges in voltage waveforms is the capacitance between the cases of switching transistors and their heatsinks. Since the case of the transistor is usually tied to the collector (the isolated-case TO-3 transistor is not yet readily available, so its use cannot be freely advocated), and the heatsink is usually at ground potential, it is easy to see the capacitor so created as forming
a potential divider with the capacitors to ground in the line input filter. Because the capacitors to ground in the input filter are limited to a maximum value (to minimise earth leakage currents) it is just not feasible to keep increasing their capacitances ad infinitum. The capacitance of transistor case to ground should be eliminated or at least drastically reduced. Reducing this capacitance is fairly easy; the, use of aluminium oxide insulating washers in preference to mica or Melinex ones to increase dielectric constant is one method. The elimination of this capacitance, on the other hand, involves the use of one or other of the two most common ploys: the guard screen (the subject of a Gould patent) or the use of a heatsink electrically isolated from ground. The heatsink should be connected to the negative line rectified rail.
(2) One of the most easily recognised sources of noise is due to transformer inter-winding capacitance. The use of electrostatic screens should prove more than adequate in eliminating this source. On no account, though, should the screens be connected to ground, but to d.c. rails-one to each side of the transformer. The exception to this rule is the safety screen. Therefore, between primary and secondary, the screens should be in the following order:
(i) primary; (ii) guard screen connected to the positive line rectified d.c. rail; (iii) safety screen to ground; (iv) guard screen connected to the common output terminal; and (v) secondary.
(3) A third source associated with the transformer in switch-mode supplies, but one that is less easily recognised, is the capacitance between windings and the transformer core. The winding of greatest concern is the primary, which is usually found to be in close prox-
imity to the core (whether it is the innermost or outermost winding is largely immaterial). Once again, a guard screen between the winding and core may be used. On the other hand, it is easier simply to isolate the core electrically from ground. Whichever technique is used, care must be taken to ensure to connect either the guard screen or core electrically to the primary d.c. rail.
(4) A problem very similar to the transformer-winding/core capacitance is the capacitance between the winding and core of the output filter choke, especially if the 'hot' end of the winding happens to be nearest the core. A simple remedy is merely to transpose the connections if the current-carrying conductors are not made of copper strip. If transposition of connections is not possible, the measures described in the preceding paragraph are necessary.

## THYRISTOR INTERFERENCE

One of the most insidious of sources of r.f.i. and one which covers both current and voltage generation, is the thyristor or triac used in many soft-start circuits to limit inrush current. Claims are made that zero-crossing triggering of thyristors virtually eliminates them as generators of interference. This most certainly is not the case; thyristors always generate interference. It is only the magnitude of the noise that varies. The lesson to be learned, therefore, is that, if they must be used, the devices must be inserted in one of the rectified lines and triggered by d.c.

## ELECTROMAGNETIC FIELDS

Finally, it is worth looking, if only briefly, at r.f.i. generated by the emission of electric and magnetic fields. These fields emanate from devices and conductors handling pulse voltages and currents. The only viable approach to solving the problem is good layout. A few rules, if
applied diligently, will be repaid handsomely; these may be stated as:
(a) Do keep cable lengths as short as possible.
(b) Do lump together all puisecarrying conductors.
(c) Do not allow these conductors to come into close proximity to supply conductors.
(d) Do not route cables carrying supplies or outputs near to transformers, chokes and switch transistors.

## CONCLUSION

If, in the final analysis, all the foregoing rules are applied, a clean supply is almost certain to be the outcome. The application of a relatively simple filter should then be all that is necessary to make virtually any supply comply with most existing specifications.

[^2]

## heavy current electricity in the united KINGDOM

## By Lord Hinton of Bankside, Pergamon Press

$\mathbf{7 9}$ pages, Price $\mathbf{5} \mathbf{5}$ Hard Cover; $\mathbf{£ 2}$ Soft Cover

WHILST many of the readers of this magazine are probably aware of the salient milestones in the history and development of electronics throughout its various ramifications, beyond the power socket however, such knowledge is invariably lacking. This fascinating monograph will rectify any such deficiency as it provides a history of heavy current technology from its earliest development up to the time when the industry was nationalised in 1947.

The utilisation of electric power dates from Faraday's paper on electromagnetic induction in 1831 which precipitated the great change in world life style. Sadly, for this country, we lagged behind other industrial countries, both in the structure of an emerging electrical power industry and in the use of electricity.

We read of the stunting restrictions of central government that prevented a prosperous electrical manufacturing industry to be built up and which, no doubt, contributed to Edison's monopolistic incandescent light patent which funded a fortune for the inventor and spawned the giant General Electric corporation.

It was not until the 1926 Act had been passed that it became possible for Britain to have a forward looking electricity supply industry. That Act set up the Central Electricity Generating Board which had the job of creating and operating the national grid, the construction of which was not completed until 1933. However, although the C.E.B. had control over the grid retailing was still done by municipalities and by private companies. Nationalisation in 1947 established a new framework under the British Electricity Authority with ownership of all generating stations and subsequent rationalisation.

A pithy read that doesn't tax the mind. Highly recommended.
G.G.

April Fool? The 10-4 Newsletter is officially published quarterly but tends to appear every four months. So, the $£ 1.50$ subscription to C.B.A. entitles four issues. Apologies to all. ${ }^{-}$
A.T.


## FREQUENCY BY TIME

HIGH resolution, fully autoranging frequency meter, is available from Orbit Controls of Cheltenham, which uses the reciprocal of time period to compute frequency.

Using a central processor, the 75C 501 TIC meter can measure down to 0.0001 Hz , and faster than is possible by the more conventional approach. It can display the digital reading in any engineering units required.

## TEACHERS TO STUDY

AONE week course organised by the Department of Electrical Engineering at the University of Salford, is aimed at providing teachers who have some basic knowledge of electronics, with the opportunity to extend that knowledge. Running from July 16-20, 1979, the material covered will be adequate for the electronics option of the JMB "A" level physics syllabus, or other syllabi of comparable standard. It will cover operational amplifiers and integrated logic circuit applications, with approximately half the time being spent on experimental work. Details: The Administrative Assistant (Short Courses), Room 110, Registrar's Dept., University of Salford M5 4WT.

## CUT PRICE WATCHES

WE HAVE received from Timetron, a revised price list giving details of some very significant recommended retail price reductions in Casio watches. An example is the 46CS-27B Alarm/Chronograph which in the April/May ' 79 price list has come down in RRP from $£ 89.95$ to $£ 49.95$. Timetron are selling this watch for $£ 39.95$.

## OBITUARY

T is with regret that we report the death of David Cohen of RT-vC. A well known character in the electronic retail business. David was the sort of straight talking business man with whom you always knew where you stood-we liked him a lot!

RT-VC will continue under the guidance of his son. We offer our sympathy to his family and friends.

# WORKSHOP P.S.U ${ }_{\text {Shoare }}$ 

0NE of the most useful pieces of test equipment for the workshop is a variable output supply for use in testing and setting up new circuits. The design featured in this article was developed to meet the author's requirements. Firstly the power supply should cover a wide range, 0 to 50 or 60 V , and to avoid the need for a built-in voltmeter it should be possible to dial up any required voltage. Secondly, the output would have switchable current limiting, and a high maximum current-1A. The current limiting should protect the circuit the power supply is feeding-in this case the power supply switches to standby after excess current. Thirdly, it should be as difficult as possible to get an excess voltage on the power supply output. The fourth requirement is that the power supply should be unharmed by misuse and protected against overheating.

## PANELINDICATORS

The front panel layout is shown in the photograph. Upon switching on the circuit comes on to standby. After setting output conditions the "Output Connect" switch is turned on, and the reset button pressed. If all is well the output comes on and stays on. If not, the output current limits at the value selected, and the supply.reverts to standby when the button is released. When changing voltage or working on the equipment being tested the "Output Connect" switch is turned off, completely isolating the output and switching the power supply to standby. If the output voltage is increased with the output connected, the supply again reverts to standby. If the power supply should overheat the warning light comes on and the power supply cannot be reset until the temperature has dropped.

The idea is to make the system "fail-safe", as this is the only way to avoid expensive accidents when setting up new equipment. In practice this arrangement has proved very convenient to use for testing and fault finding.

## CIRCUIT ACTION

With the increased availability of cheap i.c.s it seemed obvious that the circuit should be built around one, and the 723 voltage regulator was chosen. The normal way of connecting this is shown in simplified form in Fig. 1. A part of the output voltage is compared with the reference voltage. If the ouput tends to be low the error amplifier output goes up so the output is maintained. However the 723 will only stand 40 V input, giving a maximum of about 36 V out.

One way to get higher voltage outputs is shown in Fig. 2. There is a little fiddling to keep the error amplifier working in the correct range, but the only real complication is a separate power supply for the i.c. This, however can be very simple. In each case, the output can be reduced to zero by connecting the frequency compensation terminal to the i.c. negative line-this feature will be used for current limiting and overheating protection. Also, in each case the output voltage is proportional to the "Set Output" resistance, but in Fig. 1 the minimum output is the same as the reference voltage, whereas in Fig. 2 the minimum output is zero.

The $\mathrm{V}_{2}$. output on the i.c. is only provided on the 14 d.i.p. version of the 723, but a metal can 723 could be used with an external 8 V Zener diode.

The complete circuit is shown in Fig. 4. Frequency compensation is provided by C 1 , and the emitter follower transistor (Figs. 1-2) becomes two transistors, TR4 and 5 to provide the required current gain.

As the reference voltage can vary between about 6.8 and 7.5 V , preset potentiometers VR2 and 3 have to be provided.

Current limiting is provided by sensing the voltage drop across R12 and the resistor selected by the current limit switch, S3. To avoid wasting power, and to avoid an unnecessarily large heat sink on the output transistor a range switch, S8 is used to switch the input to the regulator to about 60 or 35 V .


## COMPONENTS . . .

| Resistors |  |
| :---: | :---: |
| R1 | 1 k |
| R2 | 4 k 7 |
| R3 | 1k |
| R4 | 3 k 3 |
| R5 | 3k |
| $R 6$ | 270 |
| R7 | 5 k 6 |
| R8 | 5 k 6 |
| R9 | 1k |
| R10 | 1k |
| R11 | 100 |
| R12 | 47 |
| R13 | 47 |
| R14 | 15 |
| R15 | 5.6 |
| R16 | 4.7 |
| R17 | 4.7 |
| R18 | 12 W |
| R19 | 220k |
| R20 | 47k |
| R21 | 2k2 2 W |
| R22 | 22k |
| R23 | 100k |
| R24 | 100k |
| R25-R33 | 1k 2\% |
| R34-R38 | 10k 2\% |
| Unless oth high stability | wise specified all resistors $\frac{1}{4}$ or $\frac{1}{3}$ watt |
| Switches |  |
| S 1 | Push-button push to break |
| S2 | 2 decade thumbwheel edge switch (see text) |
| S3 | 2 pole 6 way rotary |
| S4 | Single pole double throw togyle (EV Type S7101) |
| S5 | Double pole double throw toggle (EVType S7201) |
| S7 | Double pole double throw toggle (EV Type S7201) |
| S8 | Single pole double throw toggle (EV Type S7101) |

## Potentiometers

VR1 1k horizontal preset (Type PR15)
VR2 1 k horizontal preset (Type PR15)
VR3 1k horizontal preset (Type PR15)
VR4 1 k linear wirewound (see text)

Capacitors
C1 1 n disc ceramic
C2 $1 \mu 63 \mathrm{~V}$ electrolytic
C3 $47 \mu 63 \mathrm{~V}$ electrolytic
C4 $2,200 \mu \mathrm{~F} 100 \mathrm{~V}$ electrolytic
C5 $470 \mu \mathrm{~F} 25 \mathrm{~V}$ electrolytic
C6 220p ceramic plate
Semiconductors
IC1 723, d.i.I, voltage regulator
TR1 OC81
TR2 BC184L
TR3 BC184L
TR4 2N5192
TR5 2N3055
TR6 BC184L
TR7 BC184L
D1 1N4148
D2 TIL209 Green
D3 TIL209 Red
D4 1N4002
D5 1N4002
D6 1N4002
D7 1 N4148
1N4148
D9 TIL209 Green
REC1 Diode bridge 2A 100 V r.m.s.
REC2 Diode bridge 0.5 A 40 V r.m.s.
D10 33V 400 mW Zener
D11 1N4148
D12 1N4148
D13 1 N4002
R25 VA1100

## Transformers

## T1 0-25-40-50V 1A (EV Type GP501) \} Mains T2 6-0-6V 100mA (EV Type 606/1)

## Heatsinks

(For TR4) $17^{\circ} \mathrm{CN}$ (EV Type TV4)
(For TR5) $\quad 2^{\circ} \mathrm{C} / \mathrm{W}$ (EV Type $10 \mathrm{DN400}$
(For TR1) Clip type (EV Type A1031)
Fuses
FS1 500 mA 20 mm
FS2 1.5 A 20 mm
'EV Type' references refer to components available from Electrovalue Lid., 28 St. Judes Rd., Englefield Green, Egham, Surrey, TW20 OHB.
The cabinet is a 'Norman', Type WB4, size
$280 \times 150 \times 75 \mathrm{~mm}$ approx.

Normally the difference between the input and output is less than about 33 V , so TR7 is not drawing current, and the voltage at the base/emitter junction of TR6 plus D8 has to reach about 1.2 V before TR6 draws any current, and makes the output fall. If the input is much more than 33 V higher than the output, because the supply is set to high range and is current limiting (or incorrectly set to high range when a low voltage is selected) there is a risk of excessive power being dissipated in TR5. To avoid this TR7 switches on and shorts out D8, so that current limiting occurs when FR6 base is +0.6 V , and the maximum current is halved.

## CONTACT RESISTANCE

R18 is shown as 1 ohm rather than 1.2 because the wiring and switch resistance of the prototype was about 0.2 ohms. As the contact resistance of the current limit switch could be a problem in time it is a good idea to wire two
switch sections in parallel on the 1 A range as shown. (This is "fail-safe" as higher resistance will reduce the maximum current.)

Reference to the 723 published data will show that there is already a current limit transistor equivalent to TR6 provided to do the same job. However, the rating of this transistor is inadequate in this configuration so an external device is used.

## STANDBY

In Figs. 1 and 4 the voltage at point " $x$ " is 0 when the output is stabilised. This point is sensed in the circuit by TR3. If the output voltage drops, for example, due to the onset of current limiting the voltage at the base of TR3 goes positive. When it reaches 0.6 V TR3 conducts and pulls down the output of the stabiliser, and D2 ("Standby") comes on.


Fig. 1. Operation of the 723 voltage regulator. Area within dotted lines indicates i.c.

Once this has happened the output will stay low, and can only be reset by opening S 1, the "Reset" button. In practice, R6 is included to make the normal voltage at " $x$ " about 0.3 V , so that the detection of lack of stabilisation is more sensitive. When S2 is operated it will often go open circuit as it switches. In this situation the voltage at " $x$ " goes positive, and the stabiliser goes to "Standby", whereas in a conventional circuit the output would start to rise to full voltage. If the new setting is to a higher voltage " $x$ " goes positive and the stabiliser goes to "Standby".

## OVERHEAT

The "Overheat" circuit has a temperature sensor mounted on the heat sink of TR5. The author is not aware of any cheap universally available thermistor designed for this job, so a germanium output transistor, TR1, is used (OC81 in prototype).

The leakage current of a germanium transistor is enormously temperature sensitive, and in this case a vice can be turned into a virtue! Although transistors are obviously not designed for this job, general purpose germanium types should work in this circuit.

If in a given case the leakage is too low, the base can be


Fig. 2. High voltage operation of the 723 regulator
left open circuit. The output of the overheat circuit is connected to pin 13 of the i.c. after the "Reset" switch, so that in the event of overheating the output cannot be brought on.

## NORMALINDICATION

The "Normal" diode D9 is connected to indicate actual output voltage, so D5 and 6 are needed to make sure that it comes on at 1 V . Although the l.e.d. is not very bright below about 5 V this was thought to be a useful indication of correct operation, and the extra complication of constant current circuits does not seem worth while. Incidentally D5 and 6 also protect the power supply against reverse voltages.

With the component values shown the output is 1 V for every kilohm selected by S2. As shown this is a two decade thumb wheel edge switch, plus a 1 kilohm linear wirewound potentiometer for fine control. However, any desired alternative may be used.

Possibilities that come to mind are a 10 turn potentiometer, or a 12 -way switch giving 3 V steps, and $\mathrm{a}+30 \mathrm{~V}$ switch, which could be the same, switch as the range switch. However, a reasonably good quality switch should be used, as should it go permanently open circuit the stabiliser output could be reset to maximum. (An intermittent open circuit, which is much more likely, would merely put the output to standby).


Fig. 3. Graphs showing workshop power supply unit's voltage regulation

The power dissipated by TR5 is the difference between input and output voltage, multiplied by the maximum current, and could be up to about 40 W . To cope with this a heat sink with a thermal dissipation of about $2^{\circ} \mathrm{C} / \mathrm{W}$ is indicated.

The maximum dissipation from the heat sink will only be achieved if the fins are mounted vertically in free air-that is, on the outside of the back panel.

It helps a little if the heat sink is painted matt black, and a smear of silicorıe grease must be used on both sides of the insulating washer for TR5. In practice the back panel of the case will dissipate some heat, and the prototype using the specified heat sink cut down from 4 to 3 inches runs quite cool, but it is not recommended to reduce the heat sink much more. TR4 runs at the power of TR5 divided by the $h_{\text {FE }}$ of TR5-a maximum of about 2 W . This power is comfortably dissipated by a small heat sink mounted on the circuit board.

The whole circuit depends on TR5 dissipating the necessary power with low leakage. The 2N3055 is not expensive, and to use a lesser transistor, or one of dubious origins, is asking for trouble.


Fig. 4. P.s.u. circuit

## RECTIFIER CIRCUIT

The stabiliser circuit is fed from the rectifier circuit of Fig. 5. Ironically, the compromises in the circuit have been made here, for the sake of specifying components that are universally available at a reasonable price. As the range switch is before the reservoir capacitor a thermistor is included to reduce the switching surge to reasonable proportions. However, this must increase the amount by which the power supply sags at high current.

The highest voltage rating of large electrolytic capacitors at a reasonable price is 63 V , which is just above the 60 V obtained at low loads using the 40 V tapping of the specified transformer. However, at a continuous load of 100 mA this drops to 56 V , and at 1 A to 45 V . The maximum output voltage is about 3 V less. As equipment to be tested will rarely demand continuous high current as well as high voltage, this compromise seems reasonable. However, for those who refuse to compromise, the alternative is to use a 2 N3442 for TR5. Then the high range can use the 50 V output of T1 which should be rated at 2 A . In this case the full
size heat sink should be used.
As transformers are expensive some constructors may wish to use alternatives, which is quite in order as long as the actual measured voltages prove suitable. T2 can be replaced by any available transformer giving a rectified output between 14 and 18 V .

## SAFETY

The cabinet and all exposed metalwork must be connected to mains earth (TR5 should have a cover). The output is earthed by S 4 connecting mains earth to the positive or negative output. It is not really safe to allow the output to float, as it is possible that a transformer could develop a primary/secondary short.

If both transformers are replaced by types having a screen between primary and secondary a floating output becomes permissible,

The fuses are not made accessible from the outside as their failure would indicate a fault within the power supply.


Fig. 5. Bridge supplies for main circuit

## STABILITY

The inherent stability of the 723 regulator is very high (a fraction of $1 \%$ in most circumstances), so the accuracy achieved depends largely on the components used. The absolute value of R4-R8 is not important as long as they do not vary. Thus $5 \%$ high stability resistors are good enough for this application. The resistors R25-R33 for the 1 V steps are critical, but a $5 \%$ error represents only 50 mV , which is close enough for most purposes.

When a nine is dialled up, however, nine of these resistors are in series, giving a possible error of 450 mV . Statistically the error in this case should be much less than $5 \%$, but the extra expense of $2 \%$ resistors is probably worthwhile. The resistors R34-R38 for 10 volt steps should however normally be $2 \%$.

The preset potentiometers VR2 and 3 are a potential weak link, but they only make up a small part of the total resistance. If preferred they could be replaced by multiturn preset potentiometers.

For the minimum thermal drift in the i.c. the resistances seen by the inverting and non-inverting inputs should be equal. This is achieved at about 15 V , but the unbalance at other voltages is not great enough to cause difficulties, as the i.c. does not dissipate enough heat to run at all hot.

## CONSTRUCTION

The general construction of the prototype can be seen from the photographs. The circuitry is mounted mostly on two pieces of Veroboard corresponding to Fig. 6 and Fig. 7. The layout for the rectifier circuit is given in Fig. 7. Layout for the stabiliser, Fig. 4, is given in Fig. 6. This uses rather a large number of links, and could probably be improved upon, but the extra expense of a printed circuit board does not seem warranted. It is recommended that Veropins should be used for external connections, as this makes assembly easier and reduces the likelihood of short circuits between tracks. The construction must be of a high standard if expensive disasters are to be avoided. In particular beware of whiskers of solder or removed copper getting between tracks.

The i.c. should be mounted in a holder.



Fig. 6. Stabiliser circuit


Fig. 7. Supply board T1 and C4 are elsewhere chassis mounted (see photograph)


## SETTING UP

If the unit has been built properly there should be no snags, but it is worthwhile testing methodically just in case something is wrong.

It is advisable to build and test the rectifier circuit before connecting the stabiliser circuit. Before connecting TR5 check that there is no short circuit to the heat sink.

Switch to 20V, 25 mA , low range, "Connect" turned on, and connect a voltmeter to the output terminals. Switch on, and the standby light should come on. Press the reset button, and the "Standby" light should go off, "Normal" light on and the voltmeter should read about 20V. Adjust VR2 to correctly set 1 V and VR 3 to set 20 V -repeat the process as
necessary as the two controls interact. Check for overheating (at this point the overheat circuit is not set up, so if necessary adjust VR1 so that the "Overheat" light is off).

To check the current limit select $10 \mathrm{~V}, 25 \mathrm{~mA}$, and connect a meter set to 100 mA in series with a 220 ohm resistor. Press the "Reset" button-the "Standby" light should stay on, and the meter should read 25 mA approx., as long as the "Reset" button is pressed, dropping to zero when the "Reset" button is released. If the range switch is set to $30-60 \mathrm{~V}$ and the test repeated the current should be halved.

To set up the overheat circuit remove the resistor and meter. Remove TR1 from the heat sink and put it in a cup of

## VIEWS INSIDE ...


(a)

(b)

Fig. 9 (a) Stabiliser Veroboard (b) Supply Veroboard (c) Showing mounting of OC81 sensor below TR5's heatsink
almost boiling water (just dip the can of the transistor in the water, not the leads). Switch on and press the "Reset"•button. Increase VR1 from minimum resistance until the "Overheat" light and the "Standby" light both come on. Check that the "Reset" button is inoperative. Switch off and reconnect TR1 to the heat sink.

If all is well the unit can be tested at higher current. The hardest test is low output voltage at maximum current. If all is still well set to high range, where the hardest test is about 30 V at maximum current. Also check that TR4 and the i.c. are not overheating.

Now test for leakage in TR5, which is shown by a voltage on the output, and thus a slight glow in the "Normal" light when the circuit should be in "Standby", especially on the high range. If all is well get TR5 hot by running at low output voltage, high current for some time until it is on the verge of overheating. Turn off the "Connect" switch, and check that the "Normal" light is completely off.

Give the power supply a good soak test, and if necessary slightly adjust VR2 to correct the output at 1 V and VR3 to correct the output at 50V. Check that the output is correct at other voltages, and check the current limit on all ranges.

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A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

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THIS circuit was designed as a cheap thermometer utilising an external $3 \frac{1}{2}$ digit DVM providing readings of 20 $\mathrm{mV} /{ }^{\circ} \mathrm{C}$. Whilst making no total claim for originality, I was impressed by the stability of a 741 used as a d.c. amplifier of fairly high sensitivity. The nearest commercial equivalent would have cost over $£ 120$, and would not have been very suitable (for an enzyme kinetics experiment).

On the basis of the success of this circuit, I might try extending the use of 741 s
in laboratory applications where, commercially, chopper amplifiers are used. It would not be too difficult to make a chopper-stabilised amplifier with 741s and analogue gates. DI (any small silicon diode) was minimally insulated with epoxy resin, for good thermal conductivity. Once the 741 offset had been nulled at the appropriate ambient temperature (R3 and R4 maintain approximate offset current balance), it drifted by $\langle 1 \mathrm{mV}$ over several months.

The temperature was rezeroed in
ice/water every few hours (drift $< \pm 3 \mathrm{mV}$ / 5 hr ). Response was very linear and repeatable over the operating range of $0-50^{\circ} \mathrm{C}$, and accuracy was probably limited by the calibration (accurate to 0.1 deg C). DI dissipates $300-400 \mu \mathrm{~W}$, and self-heating may not always be negligible. The +9 V supply is a minimum safe value for the 723 for a stable $V_{\text {ref }}$
B. J. Fowler,

Dept of Biochemistry, Liverpool Polytechnic.

## GUITAR FUZZ/SUSTAIN UNIT

COMMERCIAL guitar effects are usually very highly priced and therefore a circuit which offers two useful effects cheaply must be very appealing to most guitarists. The circuit shown is cheap to build and gives the ever-popular fuzz effect as well as sustain.
This design can be built either as a separate unit or is small enough to be inserted directly inside the guitar body.

A standard pre-amp consisting of TR1 enables even very low output guitars to be used successfully. The amplified signal is then fed to an op-amp IC1 via C1. Because of the high gain the waveform will be severely clipped. With S1 in the "fuzz" position the signal will be attenuated by R10 and taken to the output.
With S1 in the "sustain" position the output from IC1 will be attenuated and modified by the two stage RC network R8 C4 and R9 C5. This will remove most of the high order harmonics formed by the clipping to leave a sound more like the original. Sustain will be present because during the time clipping occurs the output level will remain constant independent of the input.


Should any residual fuzz sound at output be a nuisance then adjustment of the amplifier tone controls for maximum bass and minimum treble will eliminate it.

Battery drain is below 1 mA so a PP3 battery would make a compact power source. The high gain of the circuit may cause several problems: the circuit should be laid out in a logical manner to avoid instability and positive feedback.

The input lead should be screened to prevent hum and radio pickup. Owing to the increased sensitivity the guitarist should use his hand to damp the strings not being played. Guitar volume and tone controls should, of course, be left at maximum.
A. Niemiro,

Welwyn Garden City,
Herts.

## LAMP

## DIMMER

 ANY lamp dimmer circuits suffer from several drawbacks:(1) Power consumption is too high for operation in a sealed box.
(2) High levels of r.f.i.
(3) Failure of a component may have disastrous results.
(4) Lamp flicker at very low light levels and hence the inability to turn the lamp off without a switch.
The circuit shown consumes a maximum of 0.25 W and drives the lamp with a.c. using a thyristor and a bridge rectifier.


The lamp is wired in series with the circuit which enables r.f.i, to be reduced using only one capacitor across the mains input giving an effective RC filter with the lamp. Failure of any one component will at worst only turn the lamp fully on or off.

TR1 and TR2 form a switch which operates when the voltage across Cl exceeds that set by R1 and R2. The energy in C1 fires the thyristor after a delay set by VR1.

The 12 V Zener provides a working voltage to the circuit which falls to zero at
the end of each half cycle reducing the base voltage of TRI towards zero. The timing capacitor is thus discharged each half cycle which avoids lamp flicker due to charge being accumulated on Cl when VR1 is set for a delay longer than a half cycle. Triggering early in the following. cycle is thus avoided enabling trouble free control at near zero light levels.
M. A. McCabe,

Bradford,
W. Yorks.

# STEREO PEAK DETECTING PPM 

0NE disadvantage of peak program meters (PPMs), apart from their usual cost and complexity, is their ability of hiding serious overloads. This is because the logarithmic response makes a large overload look like an insignificant one. It is for this reason that this simple inexpensive circuit for a PPM was designed to include a peak detection circuit which can detect a $+1 d B$ overload.
The circuit shows for one channel. The figures in brackets indicate the ICI pin connections for the other channel. IC1 is a quad current differencing amplifier (CDA). The incoming signal is half wave rectified by ICla , the gain of which is controlled
as a function of the output voltage to give a logarithmic response. This response is tailored by switching in negative feedback as the output voltage increases. Four discrete linear slopes are combined to approximate the logarithmic response. Diodes D1, D2 and D3 are used as the switches. Diode D5 provides some temperature compensation. The output peaks charge C2 to give an attack time of about 1 ms . The decay time is about 160 ms . IC Ib forms a comparator/monostable which will drive the l.e.d. overload indicator for 40 ms periods if $\mathrm{a}+1 \mathrm{~dB}$ signal persists for 1 ms . A continuous overload will cause the l.e.d. to flash on and off.

The meter scale is linear and calibrated -35 dB to +5 dB . The meter zero is calibrated -35 dB , although it is, of course, infinity, but this makes little difference.

Setting up is very simple, since the overload indicator provides a means of calibration. First, a signal is applied to the input which is just sufficient in amplitude to light the overload l.e.d. VR2 is then adjusted so that the meter reads +1 dB . VR1 can then be adjusted so that a OdB input deflects the meter to 0 dB . This can be any where between 60 mV and 2.8 V r.m.s.
P. R. Williams,

Stevenage,
Herts.

## BOAT SPEED CONTROLLER

AFRIEND wanted to control the speed of a model boat without wasting valuable power across a power rheostat. This design evolved.
The on time ( $\mathrm{o} / \mathrm{p}$ high) is when C 1 is be ing charged via R1 and D1.

The off time ( $0 / \mathrm{p}$ low) is when C 1 is being discharged through VR1 and R2 to pin 7. This off time can be varied by ranging VR1 but the on time is not altered due to D1 bypassing R2 and VR1 on charge.

When the o/p at pin 3 is high TR1 is switched on. The o/p pulses are smoothed by C 2 which also eliminates damaging back e.m.f.

As the o/p transistor is a TIP 3055 and is saturated, no heatsink should be required.

C3 is to remove spikes from the supply line.

A 100 kilohm potentiometer was used as the servo which moves this had only limited movement. The variation of the resistance required to be about 10 kilohms. A resistor can obviously be put in parallel with VR1 if the movement is too great.
The speed can be varied from full to very slow, smoothly.
S. H. Allsop, South Anston, Sheffield.


PIN 3 OIP


MINIMUM VRI SETTING

## [區记

PIN 3 OIP

## MICRO-EUS

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#### Abstract

Appearing every two months, Micro-Bus presents ideas, applications, and programs for the most popular microprocessors; ones that you are unlikely to find in the manufacturers' data books. The most original ideas often come from readers working on their own systems, and payment will be made for any contribution featured.


THis month's Micro-Bus looks at two ac tivities that are normally considered extremely difficult for computers. The first is speaking, and a board which will provide speech output for a microcomputer is reviewed here; a program for generating random limericks used to evaluate the system is given in full, together with details for using it without a speech system to print limericks. The second activity, on a less serious note, is self-replication, and self-replicating programs in BASIC and M6800 assembler are given here.

## MICRO TALKS

The speech channel is, for humans, the most fundamental means of communication, but it is still largely neglected as a means of communicating with computers, and HAL in "2001 A Space Odyssey" remains a creation of science fiction. However electronically synthesised speech was generated as long ago as 1951 by Walter Lawrence's PAT, or Parametric Artificial Talker, and although it is still difficult to produce very high-quality speech there are now simplified systems available for use with microcomputers.

One such system is the USA-produced Computalker board, designed for the S-100 bus and sold here for around $£ 310$ with software. Another product, the Britishdesigned Microspeech board, has recently become available and Micro-Bus obtained one to review.

## MICROSPEECH REVIEWED

The Microspeech board, shown opposite, is designed for use with the SS-50 bus as used in the South-West Technical Products MP-68 microcomputer which is based on an M6800 micro. At £295 including software it may seem rather expensive, but the circuitry is fairly complex containing some 38 i.c.s. The hardware and software were designed by Tim Orr and Richard Monkhouse respectively, both of whom formerly worked at Electronic Music Studios.

## SYNTHESIS BY RULE

The Microspeech system uses a method of generating speech known as "synthesis by rule". The speech is stored in the most economical form of phonetic text, in which
unique symbols are used to represent the different sounds from which the words are composed. Typically a minute of speech coded in this way can be stored in each IK of memory.
Each speech sound is represented by an unambiguous one- or two-letter code; for example, "O" is the vowel sound in "hot" and "got", and "OO" is the vowel sound in "boot" or "true". The phrase "stupid computers" is rendered as "STYOOPIXD/KOMPYOOTETS. The "/" is used to separate words; a space gives a pause in the flow of speech. It is also possible to exert some control over the pitch of the voice, and this may be used to add intonation to make the speech sound more natural; for example "+" and "-" increase and decrease respectively the pitch of the voice by one unit, and these may be inserted between the phonetic codes at any point.
Two pieces of software provided with the system both convert the phonetic text into a series of parameter values which control the electronics of the synthesiser board. One, a stand-alone program, enables one to enter a piece of phonetic text and play it. The other
program is a patch to SWTP 8 K BASIC which makes it possible for a BASIC program to produce speech output. The phonetic text is printed to port 3 ; the speech software buffers it until an up-arrow is received, whereupon the text is spoken.

## BLOCK DIAGRAM

Microspeech's electronic vocal tract is shown in block form in Fig. 1. The nine parameters on the diagram which control the different sections of the model are:

## Frequencies:

FI-formant I
F2-formant 2
F3-formant 3
FF-fricative formant
FV-voice oscillator
To specify these parameters, nine 8 -bit numbers are output to the Microspeech board by the controlling software. Each number is converted to an analogue voltage by a $\mathrm{D} / \mathrm{A}$ converter and then steered to one of nine

The Microspeech board which will provide speech output from a microprocessor system.



Fig．1．Block diagram of the speech synthesiser electronics；the nine control voltages determine the speech sound produced．
sample－and－hold units by a multiplexer．The nine parameters are updated by the software at a rate of 50 times a second．The manipula－ tion of these parameters enables the human speech sounds to be simulated as follows：

Vowels are generated by producing waveforms with maximum energies at certain specific frequencies known as the formants． The first three formant frequencies turn out to be the most important in determining the per－ ceived vowel；for example，the＂ O ＂in＂hot＂ has formants at $730 \mathrm{~Hz}, 1 \mathrm{kHz}$ ，and 2.4 kHz ， whereas the＂OO＂in＂boot＂has formants at $300 \mathrm{~Hz}, 370 \mathrm{~Hz}$ ，and $2 \cdot 2 \mathrm{kHz}$ ．

Dipthongs，such as＂AY＂in＂pay＂and ＂OY＂in＂boy＂，are produced by gliding from one vowel sound to another．The vowel sounds are created electronically by passing the sawtooth waveform output from the VCO （Voltage Controlled Oscillator）through a line of three voltage－controlled bandpass filters， one for each formant（see Fig．I）．

A separate formant filter is used for nasals such as＂$M$＂in＂man＂and＂NG＂in＂sing＂． Nasals，dipthongs，and vowels are all voiced sounds so that there is a pitch associated with them，and this pitch varies with the intonation of the voice．The unvoiced sounds，such as the fricatives＂$F$＂in＂fat＂and＂SH＂in＂ship＂，are generated instead by passing the output of a noise generator through a bandpass filter．

The centre－frequency of the filter charac－ teristic determines the speech sound heard；a frequency of about 4 kHz will give an＂$F$＂ sound whereas one of 2.5 kHz will give an ＂SH＂sound．If the sound includes some voic－ ing these become the＂$V$＂in＂vow＂and the ＂ZH＂in＂azure＂respectively．

Finally，stop consonants such as＂D＂in ＂dog＂and＂T＂in＂top＂are generated by a period of silence followed by a gliding of the formants to the values determined by the vowel following the consonant．The difference between＂ D ＂and＂ T ＂is produced by the addi－ tion of a short burst of noise at the start of the latter sound．

## RANDOM LIMERICKS

In order to try out the Microspeech system with the BASIC software a short BASIC
program was written to make the micro com－ pose its own random limericks，and read them out！The program is given in Fig．2．A typical limerick produced by the program might be：

A vicious bland grocer from Spain
Once demolished some cakes on a train
He demolished so slow
That he demolished some dough
This vicious bland grocer from Spain．

Fig．2．BASIC program which com－ poses random limericks and speaks them or，with modification，prints them．

OOOL．REM SPEAKING LINERICKS OUIN GГITG 1000
ПNP唯 IIATA SAIJRDIHI，GRAISFUHL
ПOP5 DATA WIYLIH，VIXSHFTS
On30 DATA SPAHKLIHNG
0035 DATA GREFN，YUNG
ח1040 DATA VIYL ，RLAAND
OПAS DATA IJWLD
0050 UATA UUCHEHS，GRDWSER
0055 DATA GLUTEN，FLADTIHST
OnG UATA LADNDREHS
חП7n LATA WEHNRLIH，SPAYN
$0 \cap 75$ DATA CHAAD，SPEEK
ПП8n DATA KIXNGS
OH85 UATA KAWNTEHD，FIJLLIOWD
0090 DATA NOWTIXSD，DEHPAJLIXSHU
0095 DATA KITLEHKTEHD
0100 DATA SUM：STAAMPS
0105 DATA ET／ST门WT；ET／NYU：JD
0110 DATA SUN KAYKS，ET／FRUG
0115 0ATA AAND／FEHLT／TREHMBLIH
Пl？0 DATA TN／ET TRAYN
$01 ? 5$ DATA AAND／WE．HNT／MAAD
ПI3n UATA TWIYS F．T W巨巨K
ПI 35 DATA AAND／GRПП／WIXNGZ
ПI 40 DATA SHEE，HEF，SHEE
0145 IIATA HEE，SHEE．
П150［DATA KWIXK，SLITW，F YUII
OI55 DATA HAHRII，LAIT
OIGI DATA ET RRJXK，SUM DOW்
0165 DATA ET SKRJJ
0170 DATA SUM／LAHRD
O175 DATA ET PLAYT

Not up to Edward Lear perhaps，but with a cunning choice of words some of the limericks can turn out to be quite amusing．

The program of Fig． 2 works by choosing from among five alternatives at a particular point in the limerick；for example，the first ad－ jective is chosen from ：sordid，graceful，wily， vicious，and sparkling．These alternatives are specified，in phonetic form，in the DATA statements in lines 20 to 175 in the program． The subroutine at 7005 reads five strings from the DATA statements，chooses the one specified by the value of $C$（ 1 to 5 ），and prints it to the speech buffer．

If the subroutine is entered at 7000 the value of C is chosen at random，giving a ran－ dom choice of one of the five strings．The sub－ routine at 6000 prints an up－arrow to the speech buffer，causing the strings to be vocalized．Rhyming is ensured by making the choice of the phrase at the end of the second line depend on the choice of word at the end of the first line；the same method ensures a rhyme between lines 3 and 4 ．The program would eventually be expanded with a greater number of alternative words at each point，but even with just five alternatives the results can be unexpected．

The program of Fig． 2 can just as well be made to produce printed limericks；the strings in the DATA statements should be changed to the written equivalents of the phonetic forms shown，and the PRINT statements should be altered to give printing at the terminal．

INOO DEF FNA $(x)=\operatorname{INT}(R N D(0) * x)+1$
1010 RESTJRE
1020 PRINT 3．＂＇A1＂＇；
1030 GOSUB 7000：$x=C$
104 G G＇TSUH 7000：Y＝C
1750 GITSUR $7000: Z=C$
1060 Glosue 800 ก
1100 GIJSUE 7000：0＝C
1110 GITSUB 6000
1120 PRINT 3，＂WUNS＂
1130 GUSUE 7000
$1.131 \quad G \$=C \$$
1140 GПSUR 7900
$1150 \mathrm{C}=\mathrm{Q}$ ：GlISUR 7005
1161 GISUB 6000
$1180 \mathrm{C}=\mathrm{Z}$ ：GOSUR 7005
1190 PRINT \＃3，G\＄；＂S17W＂：
1191 GS＝C5＋G\＄
1200 GOSUR $71000: A=C$
1210 PRINT \＃3，＂DHAAT＂；GS：
$12.30 \mathrm{C}=\mathrm{A}:$ GПSUR $7 \Pi 05$
$124 \cap$ GISUB 6000
1250 RESTIJRE
1270 PRINT 3 ，＂DHIXS＂；
$1280 \mathrm{C}=\mathrm{X}:$ GIJSUB 7005
$1290 \quad C=Y: G O S U B$ 7nns
$1.300 \quad C=Z$ ：GilSUR 7005
1310 GIISUB 8000
132 ก $C=6:$ GUSUB 7005
1339 filj 4 6000
1340 G13TO 1000
6000 PRINT $43, \cdots, \cdots 3:$ RETURN
7000 C＝FNA（5）
7005 FiOR $\quad 1=1$ TJ 5：READ．AS
7020 IF $M=C$ THEN C $\$=A \$$
7030 NEXT N：PRINT＊3，C\＄：
7050 RE．TURN
RONO PRINT＊3，＂FRIM＂：：RETURN

|  |  |  | NAM | COPY |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0000 | 8D 00 | HERE | BSR | * +2 | PC ON STACK |
| 0002 | 30 |  | TSX |  |  |
| 0003 | 31 |  | INS |  |  |
| 0004 | 31 |  | INS |  |  |
| 0005 | EE 00 |  | LDX | $0, \mathrm{x}$ | $\mathrm{X}=\mathrm{HERE}+2$ |
| 0007 | 09 |  | DEX |  |  |
| 0008 | 09 |  | DEX |  |  |
| 0009 | C6 14 |  | LDA | B ELAST- | HERE+1 |
| 000B | A6 00 | move | LDA | A $0, \mathrm{x}$ |  |
| OOOD | BD EOCA |  | JSR | OUT2HS |  |
| 0010 | 5A |  | DEC | B |  |
| 0011 | 26 F8 |  | BNE | MOVE |  |
| 0013 | 39 | LAST | RTS |  |  |
| EOCA |  | OUT2HS | EQU | \$EOCA | IN MIKBUG |
|  |  |  | END |  |  |

```
1000 FOR N=1000 TO 1040 STEP 10
1005 DATA FOR N=1000 TO 1040 STEP 10
1010 READ A$
1015 DATA READ A$
1020 PRINT N;A$
1025 DATA PRINT N;A$
1030 PRINT N+5; "DATA ";A$
1035 DATA PRINT N+5;"DATA ";A$
1O4O NEXT N
```

Fig. 4. Self-replicating BASIC program which lists itself.

Fig. 3. (left) Self-replicating program for the M6800 micro will print a copy of itself at the terminal.

## INTELLIGIBILITY

Just how good is the speech produced by the Microspeech board? To evaluate the system a piece of phonetic text was typed in, and on replaying it we were pleasantly surprised at the result. However we were in for a shock; we played the same speech to an unsuspecting volunteer, and they did not even identify it as speech, let alone understand it.

It seems fair to say that the speech produced is perfectly intelligible provided that you know what it is saying! Perhaps the best way of describing it is to say that it is like listening to someone with a very unfamiliar accent; after about half-an-hour's practice the speech is almost perfectly understood, especially if the sense is fairly predictable. It seems that human speech is a very difficult thing to simulate electronically; using the most sophisticated synthesis-by-rule systems people typically identify only 50 per cent of the words correctly unless very careful attention is paid to producing perfect timing and intonation.

In conclusion, the Microspeech board is great fun for use in non-serious applications where perfect intelligibility is not important; for example, for producing spoken output
from games programs. It also has serious applications where experience with the speech it produces would overcome the problem of intelligibility; for example, dictation of the output from programs over the telephone, production of auditory alarms and warnings, and as a computer interface for the handicapped.

## SELF-REPLICATING PROGRAMS

It is a fascinating, and by no means trivial, problem to write programs which will create an identical copy of themselves. Two examples are given here, but readers who find better solutions are urged to communicate these to Micro-Bus immediately. In assembler language for a particular micro the problem is fairly simple, even with the restriction that the program must work wherever it is placed in memory.

An example for the M6800 micro is shown in Fig. 3; when executed it prints a copy of itself to the terminal in hex. It assumes the existence of a subroutine, OUT2HS, which will print the byte pointed to by the $\mathbf{X}$ register as two hex characters, and then increment the $X$ register. Alternatively the program can be
made to put a copy of itself into memory by changing the instruction at $\$ 000 \mathrm{D}$ to $\mathrm{A} 7,14$, 08.

A feature of this program deserving mention is the dummy subroutine call at $\$ 0000$ which puts the program counter onto the stack so that it can be loaded into the X register. It is probably possible to write similar self-replicating programs for other micros; one for SC/MP appears in the Mk 14 programming manual.

Writing self-replicating programs in a highlevel language such as BASIC poses a far trickier problem. One stumbling-block with BASIC is the difficulty of printing a quote character. Fortunately in SWTP BASIC, in which this problem was attempted, character strings in DATA statements may contain quotes provided that they do not contain commas or colons. The program in Fig. 4 when run, prints a copy of itself to the terminal To put it more graphically, the effect of typing RUN is the same as the effect of typing LIST! It is probably not possible to write selfreplicating programs in all dialects of BASIC, and successful attempts or impossibility proofs are welcomed.

## News Briels

## MORE FOR YOUR PET

To quote Commodore: "The PET family is here!" The 8 K PET (2001-8) computer is now cheaper, at $£ 594$ inclusive. An upgradeable 4 K version ( $2001-4$ ) is also available at $£ 497$.

Two new bigger memory PETs are scheduled for release in May, the 2001-6N and 2001-32N, each with beefier typewriter style keyboards as opposed to PETs earlier calculator type. For 16 K of RAM you pay £729, and for $\mathbf{3 2 K}$ RAM you will pay $£ 858$, but remember, in these versions there is no cassette deck! This is to make room for the larger keyboard.

Two printers expected to be available from April, one of which is the 2023 which supercedes the previously announced 2020 printer. Capable of producing all PET graphics including reverse field and lower case, this 80 columns, $7 \times 6$ needle matrix impact printer can hammer along at an average of $93 \mathrm{chars} / \mathrm{sec}$. It will also print double width capitals for document headings etc. For $£ 594$ you will be purchasing a software formattable microprocessor controlled hard-copy peripheral suitable for small business and engineering applications.


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May 1979 will also see the release of Commodore's 2040 Dual Drive Floppy giving a total of 360 K bytes on two standard $5 \frac{1}{4}$ inch disks. This peripheral uses two microprocessors of its own, plus fifteen memory i.c.s. Commodore claim to have eliminated all problems of double-tracking or double-density. The floppy disk operating system uses none of PETs user memory. Suitable for all models of PET and priced at $£ 799.20$ inclusive. Commodore Systems Division, 360 Euston Road. London NW 1.

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## 22p

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Wavechange switches.
1P12W, 2P6W, 3P4W or 4P3W all 43p ea.
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| BC109 | $8 p$ |
| BC147 | $7 p$ |
| BC148 | $7 p$ |
| BC149 | $8 p$ |
| BC148 | $9 p$ |
| BC177 | $14 p$ |
| BC178 | $14 p$ |
| BC179 | $14 p$ |
| BC182 | $10 p$ |
| BC182L | $10 p$ |
| BC184 | $10 p$ |
| BC184L | $10 p$ |
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| LWEAR |  | $\begin{aligned} & \text { CA3140 } \\ & \text { LM301AN } \end{aligned}$ | $\begin{array}{r} 38 p \\ N 26 p \end{array}$ | NE555 NE556 | 21p |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | LM318N | 85p | NE565 | 85p |
| THIS IS ONLY A SELECTIONI |  | LM324 | 45p | NE567 | 1700 |
|  |  | LM339 | 45p | SN76003 | 200\% |
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| 741 | 16p | LM382 | 120p | SN76023 | 140p |
| 747 | 40 p | LM1830 | 150p | SN76033 | 200p |
| 748 | 30p | LM3900 | 50p | SN76477 | 220p |
| CA3046 | 55p | LM3909 | 650 | TBA800 | 70p |
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|  |  | socket | socket |
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| 3 pin | 11p | 9p | 14p |
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| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | LS74 | $25 p$ | LS157 | 48p |
|  |  | LS75 | $30 p$ | LS164 | 65p |
| LS00 | 13p | LS76 | 25p | LS174 | 48p |
| LS01 | 13p | LS78 | 35p | LS175 | 48p |
| LSO2 | 13p | LS83 | $35 p$ | LS190 | 62p |
| LS03 | 13p | LS85 | 70p | LS192 | 60p |
| LS04 | 13p | LS86 | $30 p$ | LS193 | 60p |
| LS08 | 15p | LS90 | 36p | LS196 | 60p |
| LS10 | 13p | LS93 | $38 p$ | LS251 | 50p |
| LS13 | 28p | LS95 | 45p | LS257 | 50p |
| LS14 | 45p | LS123 | 70p | LS258 | 50p |
| LS20 | 13p | LS 125 | $38 p$ | LS266 | 30p |
| LS30 | 13p | LS126 | 38p | LS283 | 60p |
| LS32 | 16p | LS 132 | 60p | LS290 | 60p |
| LS37 | 24p | LS136 | 28p | LS365 | 40p |
| LS40 | 17p | LS138 | 50p | LS366 | 40p |
| LS42 | 40p | LS139 | 50p | LS367 | 40p |
| LS47 | 90p | LS151 | $50 p$ | LS368 | 40p |
| LS48 | 70p | LS153 | 50 p | LS386 | 35p |
| LS54 | 15p | LS155 | 55p | LS670 | 40p |
| TTL |  | 7454 | 12p | 74132 | 45p |
|  |  | 7473 | 20p | 74141 | 55p |
|  |  | 7474 | 22p | 74148 | 90p |
|  |  | 7475 | 25p | 74150 | 55p |
| 7400 | 10p | 7476 | 20p | 74151 | 40p |
| 7401 | 10p | 7485 | 55p | 74156 | 40p |
| 7402 | 10p | 7489 | 135p | 74157 | 40p |
| 7404 | 12p | 7490 | 25p | 74164 | 55p |
| 7408 | 12p | 7492 | 30p | 74165 | 55p |
| 7410 | 10p | 7493 | 25p | 74170 | 100p |
| 7413 | 22p | 7494 | $45 p$ | 74174 | 50p |
| 7414 | 39p | 7495 | 35p | 74177 | 50p |
| 7420 | 10p | 7496 | 45p | 74190 | $50 p$ |
| 7427 | 20p | 74121 | $25 p$ | 74191 | 50p |
| 7430 | $10 p$ | 74122 | 38p | 74192 | 50p |
| 7442 | 38p | 74123 | 38p | 74193 | 50p |
| 7447 | 45p | 74125 | 35p | 74196 | 50p |
| 7448 | 50 p | 74126 | $35 p$ | 74197 | 50p |
| CHOS |  | 4018 | 55p | $\begin{aligned} & 4050 \\ & 4066 \end{aligned}$ | 25p |
|  |  | 4023 | 12p | $4066$ | 35p |
|  |  | 4024 | 40p | 4068 | 18p |
| 4001 | 12p | 4026 | 90p | 4069 | 12p |
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