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| DPDT standard slide | 1974 |  | ¢0.14* |
| Toggle switch SPST |  |  |  |
| amp 250 V a.c. | 1975 |  | ¢0.33* |
| Toggle switch DPDT |  |  |  |
| 1 amp 250 V a.c. Rotary on-off mains switch | 1976 |  | f0.50* |
| Rotary on-off mains switch Push switch - Push to make | 197 B |  | ¢0.13* |
| Push switch - Push to break | 1979 |  | co. $18{ }^{*}$ |
| ROCKER SWITCH | Colour | No. | Price |
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| switches SPST - moulded | BLACK | 1981 | ¢0.30* |
| in high insulation. | WHITE | 1982 | ¢0.30* |
| Material available in a | BLUE | 1983 | £0.30* |
| choice of colours ideal | YELLOW | 1984 | ¢0.30* |
| for small apparatus. | LUMINOUS | 1985 | c0.30* |
| Description | No. |  | Price |
| Miniature SPST toggle, 2 amp 250 V a.c. | 195B |  | ¢050* |
| Miniature SPST toggle. 2 amp |  |  |  |
| $250 \mathrm{Va.c}$. | 1959 |  | £0.55* |
| Miniature DPDT toggle. 2 amp 250 V | 1960 |  | £0.70* |
| Miniature DPDT toggle. centre |  |  |  |
| off, 2 amp 250 V a.c. | 1961 |  | £0.85* |
| Push bution SPST. 2 amp | 1962 |  | ¢0.78* |
| Push button SPST, 2 amp |  |  |  |
| 250 V a.c. | 1963 |  | £0.83. |
| Push button DPDT. 2 amp | 1964 |  | ¢0.98. |
| MIDGET WAFER SWITCHE |  |  |  |
| Single-bank wafer type - sui | table for swi | itching at | OV a.c. |
| 100 mA or 150 V d.c. in non-r contacts. These switches have | eactiver loads <br> a spindle | s make-bef 0.25 in dia. | $\begin{aligned} & \text { e break } \\ & \text { nd } 30^{\circ} \end{aligned}$ |
| indexing. |  |  |  |
| Description |  | Order No. | Price |
| 1 pole 12 way |  | 1965 | ¢0.48** |
| 2 pole 6 way |  | 1966 | ¢0.48* |
| 3 pole 4 way |  | 1967 | f0.48*** |
| 4 pole 3 way |  | 1968 | ¢048* |
| MICRO SWITCHES |  | Order No | Price |
| Plastic button gives simple 1 p | le change ov | er action |  |
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|  | 1 in | 6 \% | 3 in | E2.12 |
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| 164 | 3 in | 2 m | 1 in | 44p |
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## FAST MOVING?

RECENT news of the development of further advanced passenger trains capable of 150 m.p.h. has triggered interest from overseas governments. The Japanese are known to have a particular interest in the systems used to tilt the trains by up to nine degrees on curves; this is an electro-hydraulic system, the design of which interests the Japanese Government. Let us hope that we can sell these systems, or at least the technology, to them rather than once again allow others to capitalise on our inventiveness.

## INFORMATION

We have also noted one or two other improvements-the installation of "travel computers" at main line stations being one of particular interest.

A simple unit that quickly provides all train times and other general information at the push of a few buttons is a great boon to many travellers. The unfortunate part is that one must be at the station to use the computer. With the now inadequate telephone information system, one can spend a whole day trying to obtain
train times, etc. as anyone who has tried recently in the London area will know.

With the widespread use of Viewdata still a good few years away, it is a problem which would appear to require an interim solution-perhaps the installation of a Viewdata terminal at all stations would assist!

## ROBOTS

These are just two minor areas where we are seeing the immediate application of "the new electronics" and it is being widely reported that the advance of technology will change the whole face of industry and society. With approximately 70,000 industrial "robots" already in use in Japan is it any wonder that they are able to produce many items cheaper than Western countries. Although we in Britain are employing robots in increasing numbers, we are generally rather behind other industrial countries in this area, for a variety of reasons.

The knowledge that a revolution is about to commence is said to be partly behind the NEB plan to set up a microelectronics company in partnership with a group of engineers. This controversial scheme has rocked the
established companies in this country-few of which have made the necessary capital investment to enable them to compete with i.c. manufacturers in America and Japan.

The business of chip production was recently referred to as "silicon printing" by Dr. Ed. Sack of General Instrument Microelectronics, one of the few companies producing chips in Britain. It is sad to note that GIM was set up by a. group of British engineers with American backing about 10 years ago to design and sell chips for the communications industry. However, many of its products are now selling well abroad but not in Britain because the technology employed is in advance of the British systems.

GIM see the next boom as being in games-particularly hand held onesand toys. They have developed a remote control system for toy cars which employs two m.p.u.'s. GIM are also active in programmable TV games and have joined forces with EMI who will be selling software in cassette form. Perhaps their recent Queen's Award for export achievement will be augmented by a rapidly expanding home market!

Mike Kenward

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## Letters

Queries regarding articles published in PE should be addressed to the Editor, at the Editorial Offices, and a stamped, addressed envelope enclosed. We cannot undertake to answer questions regarding other items nor to answer technical queries over the telephone.

THIS touch switch can be used to directly replace an ordinary household light switch without the need for either additional wiring or fixing. The unit is capable of handling a mains load of up to 300 Watts and if the quadrac CSR1 (a diac and triac combined) is mounted on a suitable heatsink this figure can be increased to 750 Watts.

## CIRCUIT DESCRIPTION

The complete circuit diagram of the Touch Switch is shown in Fig. 1. Gates " $a$ " and " $b$ " of IC1 are connected as a bistable multivibrator with gates " $c$ " and " $d$ " used as a buffer stage.

When the switch is off the output from the buffer stage (pins 10 and 11) is high and the transistor TR1 is on. Because TR1 is connected across the diode bridge D1 to D4, when it is on the capacitor C 4 is prevented from charging. As the quadrac is switched on by a rising voltage across C4, when the transistor TR1 is on the quadrac and consequently the lamp are both off.

If when the lamp is off a finger is placed against the "ON" plate this capacitively grounds the plate due to the high input impedance of the stage and a voltage is developed across R1 and R2. This voltage takes the input of gate " $b$ " high which results in the output of the bistable being switched from the low to the high state. This high state output from the bistable is inverted by the buffer stage to a low state and TR1 is turned off.

With the transistor off, capacitor C4 is no longer prevented from charging via R7 and the voltage across the capacitor increases. When this charging voltage reaches approximately 33 V the quadrac switches the lamp on.

When the switch is off the "ON" touch plate is illuminated to enable the switch to be easily located in the dark.

The capacitors C1 and C2 filter any voltage spikes that may occur from motor starting surges elsewhere in the house, such as when a fridge or freezer is turning on. These spikes could cause false triggering of the switch.


## CONSTRUCTION

The p.c.b. design for the Touch Switch is shown in Fig. 2 with Fig. 3 showing the layout of the components. Note that where R1 is joined to R2 and R3 is joined to R4 the components are not soldered to the board.

As the plain side of the printed circuit board is used as the frontplate for the switch (with all the components being mounted on the copper track at the rear of the board) only four drilled holes are necessary in the p.c.b.; two for fixing the switch to the wall and one in the middle of each touch plate.

After the holes have been drilled the p.c.b. can be sprayed the desired colour and then dry cellotape should be placed on the front panel over the two touch plate holes. If the board is then turned over clear epoxy resin can be used to fill the "ON" plate hole while the "OFF" plate hole should be filled with black epoxy resin. When both resins have set the cellotape can be removed.

The components should then be soldered onto the board keeping the leads as short as possible.

## TESTING AND MODIFICATIONS

The "lamp" wire from the touch switch should be connected via the lamp to the neutral lead. If the power is turned on and a finger placed over each touchplate in turn the light should go on and off. Care should be taken to ensure the live and lamp wires are connected correctly otherwise the switch will not operate satisfactorily.

If the switch is to be used to drive a fluorescent light the circuit should be modified as shown in Figs. 4 and 5.

By incorporating the CR Network shown in Fig. 6 the light will remain on for a preset time period determined by the value of the $C R$ network, i.e. if $C=22 \mu F$ and $R=10 M \Omega$ the delay is $22 \mu \mathrm{~F} \times 10 \mathrm{M} \Omega=2 \mathrm{mins}$.

## COMPONENTS . . .

## Resistors

| R1, R3 | $10 \mathrm{M} \Omega$ (2 off) |
| :--- | :--- |
| R2, R4 | $4.7 \mathrm{M} \Omega$ (2 off) |
| R5, R7 | $100 \mathrm{k} \Omega \frac{1}{2} \mathrm{~W}$ (2 off) |
| R6 | $100 \mathrm{k} \Omega$ |
| R8 | $220 \mathrm{k} \Omega$ |

All $5 \% \frac{1}{4} \mathrm{~W}$ carbon unless otherwise stated
Capacitors

| C1 | 150 pF 50 V Ceramic |
| :--- | :--- |
| C2 | 47 pF 50 V Ceramic |
| C3 | $22 \mu \mathrm{~F} 16 \mathrm{~V}$ Tantalum |
| C4 | 10 nF 100 V Polyester |

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TR1 ZTX304
IC1 CD4001
CSR1 400V 6.5A Quadrac

## Miscellaneous

Miniature wire ended neon RS type 586-015

## CONSTRUCTOR'S NOTE

All the components including the p.c.b. can be obtained from G.J.D. Electronics; 105 Harper Fold Road, Radcliffe, Manchester.


Fig. 1. Complete circuit diagram of the Touch 8witch


Fig. 2. Printed circuit board design


Fig. 4. Modification necessary for switching a fluorescont light


Fig. 3. Component layout for p.c.b.


Fig. 5. Modification required to p.c.b. design for switching a fluorescent light


Fig. 6. Modification for obtaining a preset "ON" time period


## CONSTRUCTION

Mount all the switches and the potentiometer on the front panel and wire them up as shown in Fig. 2.1, using ribbon cable but leaving the p.c.b. end of the ribbon unconnected.

Insert the i.c.s lastly when assembling the p.c.b., removing them from the conductive foam packing only at the moment necessary. The games chips themselves (IC1 and IC2) should be inserted last of all. Some earthed tin foil laid out on the worktop surface will help to reduce the possibility of damage due to static electricity. It would be advisable to mount the modules early on so that the remaining component space is more apparent. On the UM1263 (sound modulator) it is necessary to remove the lid and solder a 100 pF capacitor (C5) between the coaxial socket centre-pin and the p.c.b. The capacitor lead can be threaded to the inside of the modulator box through the socket centre hole, and then soldered at the point where it emerges.

board powered from external power pack

## minimal <br> setting-up <br> procedure



Fig. 2.1. Switch panel wiring diagram. This matrix wiring system was referred to in Part 1, Fig. 1 (wrongly as Fig. 10)

Fig. 2.2. Component board layout. Note that CI 7 couples the Sound Modulator output socket to the p.c.b.



Fig. 2.3. Printed circuit layout (actual size)

Fig. 2.4. Component side p.c.b. (actual size)



The hand-held units (shown here with lid removed), contain dual-axis potentiometers for movement of the bat both up and down, and left and right across the television


The serve buttons supplied by Teleplay are mounted on the outside surface of the box, with only the connection tags passing through the lid

## CORRECTIONS TO PART ONE

In part one, capacitors C10 and C12 were marked in the components list as $0.8 \mu \mathrm{~F}$. This should read $0.18 \mu \mathrm{~F}$.

TR1-TR3 are BC337 transistors, and the tunable choke should be $40 \mu \mathrm{H}$ R17 is 100 תand not $1 \mathrm{~K} \Omega$.

It is also necessary to add the following resistors to the components list:

| R38, R39 | $8.2 \mathrm{~K} \Omega$ |
| :--- | :--- |
| R53 | $5.6 \mathrm{~K} \Omega$ |
| R54 | $2.2 \mathrm{~K} \Omega$ |
| R55 | $222 \mathrm{~K} \Omega$ |
| R60, R61 | $6.8 \mathrm{~K} \Omega$ |
| R62 | $100 \Omega$ |
| C17 | 100 pF silvered mica |

The main box should be cut for cable entry holes, and for the miniature jack socket (d.c. supply input). The hand-held units require a square hole of $15 \times 15 \mathrm{~mm}$ to accommodate the dual axis controls, in addition to further drillings for the push buttons and cable entry.

Screened 5 -way cable is necessary to connect the hand-held units to the main box, and the wiring of these can be seen in the main circuit diagram.

Once all the switches and potentiometers are mounted and wired up, the p.c.b. can be fixed into the main box using self-tap screws, and then wired to the various controls. Low loss coaxial lead should be used for the aerial input signal to the television set, the other end of which is connected to the UM1163E36 vision modulator coaxial output socket.

The system is now ready to check out, but first look over the p.c.b. and wiring for possible dry joints, and ensure that all diodes and electrolytic capacitors are connected in the right polarity.

## SETTING-UP

All modules supplied by Teleplay should be pre-tuned, but it may be necessary to adjust the oscillator frequency using L 1 , to lock the picture onto the screen. This should be done with the unit powered up and the television tuned to Channel 36. Fine tuning of the PAL colour encoder crystal is achieved by trimming XL1 ( $4 \cdot 4336 \mathrm{MHz}$ ).

The unit is powered by a mains adaptor unit which plugs straight into a 13 A socket, and jacks into the Game Centre via a low tension lead (jack tip + Ve).

## I7 EI. II

## DIEITAI THERMOMFIER

M. PLANT

N order to provide a BCD to seven segment display of an analogue quantity, analogue-to-digital circuitry is required. Formerly this meant the use of a large number of discrete and integrated circuit devices which posed problems for the constructor of electronic instruments making use of these digital techniques. The availability of the Siliconix LD130 CMOS i.c. now enables the analogue to digital conversion to be performed with the one device plus a handful of additional components. Sophisticated instrumentation functions can now be obtained with a few components: the LD130 lends itself to the measurement of temperature, voltage, current, resistance, sound intensity, rotational speed, for example, indeed any physical quantity which lends itself to conversion into a voltage change which can then be processed digitally.

In this article the construction of a digital thermometer is described which has the following specifications:

## Range <br> Transducer <br> Display <br> Resolution <br> Over-and-under range indication <br> Minus sign <br> Power supply <br> $-99.9^{\circ} \mathrm{C}$ to $+99.9^{\circ} \mathrm{C}$ <br> pn junction diode <br> 3 digit 7 segment <br> $0.1^{\circ} \mathrm{C}$ <br> flashing display <br> illuminated I.e.d. <br> $6 \mathrm{~V}(4 \times 1.5 \mathrm{~V}-\mathrm{HP} 7)$

Initially the circuit was assembled on Veroboard and gave satisfactory results but a printed circuit board assembly produced improved performance.

## THE LD130

Details of the internal operation of the LD130 will not be described here but are covered in the relevant Siliconix application note. As Fig. 1 shows the LD130 is an 18 pin dual-in-line package which features the low power
consumption of all CMOS devices: 25 mW at $\pm 5 \mathrm{~V}$ power supply. The lower limit of the supply voltages is $\pm 4.3 \mathrm{~V}$ and the upper limit is $\pm 8 \mathrm{~V}$ ( 60 mW consumption). The twin supply voltages must be matched to within 1.5 V for correct operation of the device.

Internal counting is achieved through an oscillator which becomes functional when a single capacitor C4 is connected between pin 14 and ground. This capacitor and C1 together determine the sampling rate of the circuit. In use the i.c. samples the analogue voltage and displays its value in digital form at a sampling rate of $f_{\text {osd }} / 6144$.


Fig. 1. Pin identity for the LD130


Fig. 2. Graph of frequency related to capacitor choice


Fig. 3. Thermometer circuit

The oscillator frequency as a functon of C4 is shown in Fig. 2 for various values of the positive supply voltage V 1 . Thus at $\mathrm{V} 1=5 \mathrm{~V}$ and $\mathrm{C} 4=0.001 \mu \mathrm{~F}, \mathrm{f}_{\mathrm{osc}}$ is about 30 kHz . Therefore the sampling rate is about 5 Hz .

The value of the integrator capacitor C 1 must be chosen according to the equation $\mathrm{C} 1=\left(10^{3} / \mathrm{f}_{\text {osc }}\right) \mu \mathrm{F}$ approximately, that is, C 1 should be of the order of $0.02 \mu \mathrm{~F}$. The value of the auto zero capacitor should be $0.1 \mu \mathrm{~F}$.

The sign and under-and-over range signal appears at pin 5. The analogue input is applied to pin 17 and a reference voltage $\left(\mathrm{V}_{\text {ref }}\right)$ is required at pin 2. The multiplexed outputs from pins 10 to 13 and the digit scanning signals from pins 7,8 and 9 are connected as shown in Fig. 3.

## CIRCUIT ACTION

The complete digital thermometer circuit omitting the power supply circuitry is shown in Fig. 3. The reference voltage $\mathrm{V}_{\text {ref }}$ for the LD130 is provided by the current regulator diode D2 in series with the potentiometer VR1, the wiper of which is connected to pin 2 . This particular current regulator has a zero temperature coefficient and therefore VR1 is able to provide a stable voltage- $V_{\text {ref }}$.

The digits of the seven segment display are scanned by the LD130 pins 7,8 and 9 via the npn Darlington current buffer transistors TR1, TR2 and TR3.

High gain Darlingtons were used ( $\beta=50,000$ ) although the constructor may use discrete pairs of npm transistors to obtain the same function. The l.e.d. D1, indicating the negative sign of the temperature, is driven by transistor TR4.

The digit segments of the common cathode displays $\times 1-3$ are driven from IC2 through the current limiting resistors of IC3. For increased brightness these resistors may be reduced to 100 ohms and, of course, individual 0.25 W resistors may be used.

## TRANSDUCER

Provided the forward current is much greater than the reverse saturation current, the voltage across a pn junction diode biased with a constant current varies by about $-2.2 \mathrm{mV} /{ }^{\circ} \mathrm{C}$ and thus provides a linear temperature-tovoltage transducer-D5. A proportion of the temperature dependent voltage across this sensor is selected by R3, VR2, and R4 voltage divider, one end of which is connected to the negative supply voltage in order that VR2 may be able to set the zero of the temperature scale.

For correct and stable operation of the thermometer, the LD130 must be provided with a short term stable positive supply voltage. As you will see from Fig. 2, any variation in the supply voltage produces a marked change in the oscillator frequency $f_{\text {osc }}$ which in turn leads to jitter of the display reading. This variation in positive supply voltage as seen by the LD130 is liable to be caused by the variable and high currents drawn by the display. These currents can vary from 4 mA , when a digit is blanked, to about 100 mA assuming say 12 mA per segment for a display of 8 .

These currents, flowing through the battery impedance and the copper tracks on the circuit board, cause the voltage seen at pin 15 of the LD1 30 to vary. Some a.c. decoupling at this pin is therefore provided by means of the diode D4 and the capacitor C3.

Note that this provides a positive supply voltage for the LD130 of 0.6 V less than the battery e.m.f. The second circuit design feature which ensures display stability is the return of the analogue ground pin 1 of the LD130 direct to the ground connection of the power supply, i.e. the battery negative.

Rather than use a separate battery to supply the negative voltage with respect to analogue ground required by the LD130, an inverter is used to provide a -5 V supply relative


Fig. 4. Main board component layout


Fig. 5. Etching detail of main board


Fig. 6. Inverter p.c. detail


Fig. 7. Component layout
to the zero volts terminal of the battery. An inverter circuit suitable for this is shown in Fig. 8, the output voltage of which is D8's Zener voltage minus TR8's 0.6 V be drop, that is 5.6 V .

## ASSEMBLY

The circuit boards were designed to fit into a $120 \times 65 \times$ 40 mm box. Four 1.5 V calculator batteries power the circuit and display and these just fit across the width of the base of the box. A four pack HP7 type battery holder was broken apart to obtain the terminals. These were fitted into two pieces of SRBP glued into place along the inside edge of the box. This provided a tight fit for the batteries.

The main board is stood off with screws and spaces. Track layout and component assembly for this are given in Figs. $4 / 5$. The base of the box also carries the inverter p.c.b. the track layout and component overlay for this is shown in Figs. $6 / 7$. This board also contains three electrolytic capacitors to soak up any voltage variation on the wires to the LD130 caused by large display current variations as explained above.

A twin power supply leaves the inverter board for the $A / D$ p.c.b.; the negative lead going to pin 3 of the LD130, and OV to the analogue ground connection pin 1 with the battery positive to pin 15 of the LD130 via diode D4.

It is recommended that a d.i.l. holder be used for the i.c.s so that thay may be reused unless the constructor has the facilities for desoldering i.c.s in the event of failure. IC1 and IC2 are CMOS devices and need the usual care when handling them, in particular do not solder components in place when power is supplied to the board and do not insert or remove these i.c.s when power is supplied.

## SENSOR

The $p n$ junction temperature sensor is the base-emitter junction of any general purpose silicon bipolar transistor. Both the BC107 series metal can TO18 style and the ZTX300E line types were found suitable.

Using the ZTX 300 , the collector lead was cut off close to the encapsulation leaving only the base and emitter leads. Single core screened cable, core to base and screen to emitter was soldered to these transistor leads. One connection must be sleeved to avoid shorting.

Quick setting Araldite mixture was smeared over the sleeved and bared connections and the screened cable then threaded through a piece of nylon tubing or similar until the transistor sits tight against one end of the tube. The excess


Araldite was then wiped off so that when the remaining was set it formed a water tight seal against the effects of moisture when the probe is in use.

The other end of the screened cable is terminated in a 2.5 mm plug so that it can be plugged into a socket in the box at the side of the push switch. However to save expense the plug and socket can be dispensed with and the lead taken directly to the circuit board.


Fig. 8. Inverter circuit

## COMPONENTS . . .

\author{

Resistors <br> \begin{tabular}{ll}
R1 \& $82 \Omega$ <br>
R2 \& $4.7 \mathrm{k} \Omega$ <br>
R3 \& $20 \mathrm{k} \Omega$ <br>
R4 \& $270 \mathrm{k} \Omega$ <br>
R5 \& $47 \mathrm{k} \Omega$ (2 off) <br>
R6-R7 \& $4.7 \mathrm{k} \Omega$ <br>
All 1 W carbon

 <br> \section*{Capacitors} <br> 

C1 \& $0.022 \mu \mathrm{~F}$ polyester <br>
C2 \& $0.1 \mu \mathrm{~F}$ polyester <br>
C3 \& $100 \mu \mathrm{~F}$ elect. 25 V <br>
C4 \& $0.001 \mu \mathrm{~F}$ polystyrene <br>
C5-C6 \& $10 \mu \mathrm{~F}$ elect. $25 \mathrm{~V}(2$ off $)$ <br>
C7 \& 470 pf <br>
C8-C10 \& $660 \mu \mathrm{~F}$ elect. (3 off)
\end{tabular}

}

## Potentiometers

VR1 $2 k \Omega$ cermet 20 turn variable
VR2 $20 \mathrm{k} \Omega$ cermet 20 turn variable

## Inductor

$\begin{array}{ll}\text { L1 } & 1.5 \mathrm{mH}\end{array}$

Semiconductors<br>TR1-TR3 MPSA12 Darlingtons (3 off)<br>TR4 BC109<br>TR5 BC477<br>TR6 BC108<br>D1 TIL209<br>D2-D3 CRO33 current regulator diode (Siliconix)<br>D4 $\quad$ N4148<br>D5 Sensor diode; ZTX300 (base/emitter only)<br>D6-D7 1 N4148 (2 off)<br>D8 BZY88 6.2V<br>X1-X3 MAN3640 (Monsanto) (3 off)<br>IC1 LD130 (Siliconix)<br>IC2 CD4511<br>IC3 $7 \times 150 \Omega$ thick film resistor array (Stock No. 140-013-R.S.)

## EXTRACTING THE DIGIT

Constructors should note that if resolution is not required to the nominal accuracy of $0.1^{\circ} \mathrm{C}, \times 3$ in Fig. 3 can be omitted and the associated driver transistor TR3. Resolution is then to $1^{\circ} \mathrm{C}$ giving a steadier display which is more restful on the eye and slightly easier on the pocket!


## CALIBRATION

When the boards have been assembled in the box, and all connections checked, the push switch may be pressed whereupon the display will do one of the following:
(a) Does not light: Check the operation of the switch and all circuit connections.
(b) Lights and all digits flash on and off rapidly. Adjust VR1 (Range) so that the voltage at pin 2 of the LD130, relative to ground, to 0.35 V . This may bring the reading to a steady value, excepting rapid updating of the third digit. If not, adjust VR1 until the display is steady. Place the sensor body in some water at a steady temperature of say $25^{\circ} \mathrm{C}$ making sure base/emitter leads are not short ciruit. Adjust VR1 until the display reads this value.
Note that due to the operation of the LD130 the display will not stabilise at a precise tenth of a degree reading but will range by as much as $0.3^{\circ} \mathrm{C}$ around the true reading. With experience it will be possible to estimate the temperature to the nearest tenth. Heat some water in another container to say $75^{\circ} \mathrm{C}$ and place the sensor in it applying the same precautions as before and adjust the range potentiometer until the display reads $75^{\circ} \mathrm{C}$. Return the probe to $25^{\circ} \mathrm{C}$ and readjust VR1 until the display reads this value. Repeat the procedure until the correct temperatures are displayed.

Finally check that the l.e.d. indicating negative temperature lights up when the sensor is in the fridge.

## COMPONENTS

All components are readily available except for the i.c.s and current regulators. Both the LD130 and the CRO33s can be obtained from Semiconductor Specialists (UK) Ltd., Premier House, Fairfield Road, Yiewsley, West Drayton, Middlesex. The Darlington pair MPSA12 digit driver transistors can be obtained from Technomatic Ltd. 54 Sandhurst Road, London NW9.


FRANK W. HYDE

## OUT OF THE ECLIPTIC

All spacecraft till now have operated close to the plane of the ecliptic, the plane in which the planets of the Solar System revolve around the Sun. In order to study the higher latitudes of the heliosphere, that is, the environment around the Sun, it is necessary to leave the thin disc of this environment occupied by the planets.

Most of the planets have orbits quite close to the plane of the ecliptic. These are at different angles ranging from approximately $7^{\circ}$ for Mercury to $0^{\circ}$ for the earth. The exception is Pluto which has an inclination of just over $17^{\circ}$. Thus with this one planet as an exception, the planets are contained in the narrow space at right angles to the place of the ecliptic of $14^{\circ}$.

Since the Sun is in an oblate spheroid, it follows that much of what can be directly viewed is subject to angular distortion, when the observing point is confined to a narrow angle. It is of paramount importance, in an attempt to understand the astrophysics of our own star, that a spacecraft is put in an orbit at right angles, or nearly so, to the ecliptic plane.

To this end the European Space Agency's first deep space mission will be set up, for a launch in 1983, of two spacecraft simultaneously. The launch will be a joint operation with NASA. Designated Out of Ecliptic (OOE) by the European Space A gency and Solar/Polar by NASA, these two spacecraft weighing some 300 kg each will be directed toward Jupiter. It is necessary to use the "sling shot" technique to achieve the orbits required. As the two craft near Jupiter, the trajectories will diverge.

One spacecraft will go round Jupiter from north to south and the other from south to north and then toward the Sun, one toward the southern hemisphere and the other to the northern hemisphere. They will be in the vast unknown volume of space round the Sun.

Under certain conditions, the Sun, in addition to the solar wind, emits particles of
very high energy. Streams of such particles are capable of producing "blast" waves in the solar plasma. Lethal gamma-ray bursts could be produced in this manner. It will be possible, it is hoped, to measure the amount of protection that the solar wind provides in inhibiting the intrusion of particles and radiation from galactic sources. From the data acquired by the vehicles it will be possible to discover the precise values for the loss of mass and energy of the Sun. The changes in the acceleration of the Solar Wind can be recorded and related to the other scientific measurements.

The effects of Novae and super Novae are already known in relation to the environment. Recently Hoyle and Wickramsinghe have written on the subject of debris and cometary matter bringing hostile organisms into the Earth's environment. The protection afforded by the high activity of the Sun is a direct effect. An examination of the effects on both Man and the environment can be correlated with the weather and health. This alone would justify the mission of the two spacecraft.

A total of eight experiments will be put aboard each of the spacecraft. One of these is an entirely British package. Dr. Peter Hedgecock of Imperial College is the principal investigator of the project involving a sensitive magnetometer. It is designed to measure the interplanetary magnetic field drawn out from the Sun by the plasma flow. The measurements will be used to provide a map of the Sun's magnetic field in the heliosphere. This will also help to show how lethal radiation is deflected from Earth by the solar magnetic field.

## THE SHIVERS OF THE SUN

In April this year there was a European Conference on Solar Physics at Toulouse. It is remarkable that these conferences suddenly serve as a reminder that, matters obvious, are frequently shrouded in mystery.

The mystery is usually provided by the assessment of the mechanism involved. Yet by the very nature of the body, many photographs and films of the surface show violent and continual activity. Perhaps this is an elaborate way of saying that seeing is not necessarily believing. So it is implied that corroboration is a necessity.

In the matter of the "Shivering Sun" it seems obvious that the internal activities at the temperatures of the elements involved, would lead to a disturbed state. To the three groups of astronomers, who independently came to similar conclusions in this matter some time ago, there must at least have been some considerable satisfaction that their position was unscathed.

It must be said that one of the observers only, saw physical and direct changes. It is certain that more will be heard of this. To be more serious, the phase measurements made by Hill at various frequencies do at least give some clue as to the "ringing" of the Sun.

## URANUS

Another star occulation by the rings of Uranus has disclosed that there are in fact, 8 ring systems around this remarkable planet. This observation also disclosed that one of the rings varies in width. At the point where the
ring is nearest to the planet it is narrower than at the point farthest from Uranus. This ring also appears to precess. These observations were made with the 2.5 metre du Pont telescope at Campas Observatory in Chile by Eric Persson of Hale Observatories. The rings are very tenuous for until a star is occulted by them it is not possible to see them with any certainty.

## LANDSAT 3

The first pictures from Landsat 3 show considerable improvement over those of the first two Landsats. The type of imaging on Landsat 3 is different from that on the previous vehicles. The Return Beam Vidicon (RBV) system has now been accepted as a useful tool for the purpose required. Disappointment with Intelstats 1 and 2 was partly due to the system used and also to a degraded camera system. Landsat 3 was launched in March this year and has vindicated the Intelstat project. The RBV system does have a higher order of resolution that the MSS (Multispectral Scanning) system previously used.

The pictures from Intelstat 3 show that objects of 50 metres in length can be resolved and areas of 0.25 hectare. This higher resolution is also accomplished by two cameras instead of four. The two cameras image two adjacent areas of 99 kilometres square. The spectral band width is $0.5-0.75 \mu \mathrm{~m}$. Further improvements in resolution were obtained by doubling the focal length of the lens systems on the cameras. Such items as roads, railways and areas like lakes and reservoirs are clearly seen and provide useful information for both Earth resources scientists and resources surveyors.

There seem to be one flaw in the whole matter and that is the objections from the armed services. They seem to fear that too much information is being revealed by way of maps and other means, which may reach unauthorised persons. It is a sad reflection on the integrity of technology when equipment is deliberately degraded in the interest of the services. It seems quite illogical, since what one nation can do another can do also, especially in the matter of observation where the altitudes involved are around 900 kilometres.

## HALLEY'S COMET

In previous SPACEWATCH reports Halley's comet has been the object of assessment for a special mission. After the fiscal set-back which seemed likely to kill it years ago there are now second thoughts. NASA has now accepted that it would be a valuable mission. Because of the heavy commitments to EXOSAT and the Large Space Telescope, there may be some hard bargaining.

The new mission is for a two in one project designed to widen the scope of the experiments. A flight past Halley's Comet would be followed by a swing by the Earth and a show flyby of another comet Tempel-2, in 1988. This project would be the first to use solar-electric propulsion, to attain the high velocity which can match the speed of the comets. It is thought that there is a likelihood of the mission now getting under way.

## HEAT IN THE PIPELINE

0NE could only guess what it would be called, but it had to come, and RCA brought it onto the electronics scene. It's called the "transcalent" device, and it's a new class of power semiconductor which uses heat pipes to cool off hot junctions.

These lightweight devices feature integral heat pipes bonded directly to large silicon wafers capable of handling currents of hundreds of amperes. The first devices available include rectifiers, thyristors and transistors.

The integral heat pipes in these devices minimise thermal resistance and increase radiator fin efficiency, thereby permitting RCA to produce semiconductors significantly smaller and lighter than conventional semiconductor/heatsink assemblies of similar power ratings. Typical size reduction is by a factor of four, and weight reduction by a factor of seven.


Other advantages of RCA's heat-pipe/integral-fin construction technique include improved resistance against overloads and highcurrent surges, and the opportunity either to reduce the silicon-junction temperature for enhanced reliability, or to operate at full ratings at high ambient temperatures.

Installation is also simplified by the elimination of separate heatsinks.
The first RCA transcalent devices available are the P95000EB Series of $250 \mathrm{~A}, 500 \mathrm{~W}$ rectifiers with blocking voltages up to $1,200 \mathrm{~V}$, the P95400EB Series of 400A, 500W thyristors with blocking voltages up to $1,200 \mathrm{~V}$, and the P95200EE4 100A, 500 W n.p.n transistor.

The rectifiers and thyristors use compact radiator structures resulting in a weight of only 340 g , and a volume of less than 230 cubic centimetres. The transistor uses a different radiator structure with a dissipation capability of 500 W , yet the device weighs less than 1 kg and occupies a volume of less than 1,100 cubic centimetres.

Any of these devices can be supplied with radiator structures to accommodate either air or liquid cooling. Thermal resistances are of the order of 0.1 to $0.2^{\circ} \mathrm{C} / \mathrm{W}$, and operating ambient temperatures at full ratings range up to $50^{\circ} \mathrm{C}$.

The transcalent device is appropriate for a wide range of commercial and military applications involving fixed, mobile or aerospace equipment. Typical uses are in welders, electro-chemical platers, power conversion and distributing systems, motor-speed controls, militaryvehicle drives, radar power supplies and aircraft power systems.

## EAGLE'S LAUNCH

0N May 21 Eagle International unveiled over 132 new editions to its range of consumer and industrial electronics products, including new hi-fi, public address, security, business and educational equipment. The biggest ever launch in Eagle's history.

Many of these new products are being displayed at the 1978 Radio Show, where the Eagle International stand is in the Piccadilly Hotel, Piccadilly.

The centre piece of the range of consumer products is 20 new hi-fi separates including five loudspeakers, four amplifiers, three receivers, two tuners, two turntables and a front-loading cassette deck. Eagle are also introducing a number of audio products and accessories, such as headphones, cartridges, table radios, and ribbon aerials. Important additions to the public address range with new disco and group PA systems have also been made.

Among the new products in the security field are domestic burglar and fire alarm equipment, including one single-zone combined fire/intruder panel, suitable for private homes and smaller offices or industrial premises.

Eagle is also introducing five test equipment products including multimeters; three emergency lighting products, two headsets specifically designed for educational purposes, and eight intercoms.

## NEW MICROPROCESSORS

ANEW 16-bit microcomputer, the Intel 8086, has been formally introduced, and is Intel's highest performer yet, being seven to twelve times faster than the 8080 at program execution. It joins the 8085A and 8048 industry standard micro families, and employs the same software development tools as MCS-80 and MCS-85. To protect the customers' software investment, the 8086 is source code compatible with the 8080 A and 8085 via a translator package.

The 8086 provides all the previous features with 16 -bit arithmetic, signed 8 - and 16 -bit arithmetic dynamically relocatable programs; design features multi-processor configuration support, and it can address a megabyte of memory. The standard clock rate is 5 MHz ( 8 MHz optional).

The Intel HMOS process has put 29,000 transistors on a chip only $225 \mathrm{~mm}^{2}$ which is better than the average home constructor can manage!

Another new device from Intel is the 8022, and this will be of much more interest to the amateur because it is the first micro with an onboard analogue to digital converter! This most useful addition to the microprocessor is accompanied by an input port which can be directly connected to a "touch-panel" keyboard.

The 8022 has twice the program memory ( 2 kilo bytes) as that of the 8021. With a dual input A to D converter, variable threshold 8 -bit input port of 100 mV sensitivity, zero-crossing capability, and an extra test pin for conditional jumps, this microprocessor has to be a device we shall hear a lot more of.

The A to D converter is of the successive approximation type accepting inputs up to 5 V , and requires an external voltage reference source.

Machine code instructions select which input is to be used, whilst the converter runs continuously, updating a results register every $40 \mu \mathrm{~s}$. This speed of conversion allows multiplexing of the two analogue inputs, to provide many analogue channels without complex handshaking, or converter stop/start instructions.

Test pin 1 of the 8022 will detect sinewave zero crossing, so that when used in conjunction with mains control, the system can switch power via triacs, etc. at the optimum time for minimum generation of electrical noise. This "symmetrical" zero-crossover switching is also preferred by the Electricity Generating Board.

The 8022 is housed in a 40 -pin package, and has an internal clock generator which requires one external component-a crystal or resistor. There are also two high current drive outputs.

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## News Rriafs

## CHAREED AND ARRESTED

AN ingenious system for printing has been employed to produce a unique device for depositing alphanumeric characters on moving chart recorder paper, by Gould Instruments Division.

Primarily intended as an add on unit for their standard "continuous stream" ink chart recorders, Gould have developed this improved system (pictured below) whereby ink is virtually "shocked" into a set of minute uniform particles, and charged to a high potential so that it can be electrostatically deflected along one axis. The position on the paper at which a particular ink particle is arrested will depend upon the potential of the deflector plates at that time.
The characters are described as "highly readable" and are formed on a $5 \times 7$-dot matrix. They appear along the edge of the oscillograph paper to allow annotations concerning relevant parameters whilst the recording is being made. In some cases this can allow event triggered recording, to save chart paper, with the nature of each trace and event details in juxtaposition. The "ink jet" device is instructed with ASC11 words which can originate from a manually operated keyboard or perhaps a minicomputer system for fully automatic monitoring.


The mechanism works like this: the basic impulse jet consists simply of a glass capillary tube with an orifice at one end, a coaxial piezoelectric driver, and a plastics supply line leading to an unpressurised ink reservoir. The piezoelectric driver is pulsed, and the shock waves squirt a metered amount of ink through the jet, just like a tiny microscopic valve-less pump. This art of obtaining ink particles of consistently equal masses and velocities is important for the next step; which is to charge the particles and electrostatically deflect them with a staircase waveform, and know for certain where they are going to be headed for afterwards.
As the paper advances under the head, the deflection staircase waveform builds up each character in raster fashion (5 rows of 7 dots), the pump "spitting" out (or missing out) ink at the appropriate times, controlled by the electronics.

Some of the statistics are: character width held constant over a $1,000: 1$ chart speed range (from $0.05 \mathrm{~mm} / \mathrm{s}$ to $50 \mathrm{~mm} / \mathrm{s}$ ), achieved through paper/character generator speed synchronisation. The mechanism is silent, and could print on almost anything without damage. A single $10^{3} \mathrm{~cm}$ cartridge will print over two million characters.

A real-time clock module is also available for time scale print-out along the oscillograms.


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# MIEROPROEESSR Kit Reuien. <br> By P. A. Birnie 



Contente of the CON-COS box as supplied in the kit.

The appearance in the market-place of a bewildering number of microprocessors has sparked off the home computer industry-first in the U.S.A. and later in the U.K.and has resulted in an equally bewildering number of "systems" for the amateur. These range from the simplest "keys and lamps" systems, which are more useful as teaching aids than as home computers, through the hex. keypad/hex. display systems to the keyboard and v.d.u. systems running in BASIC and capable of performing the most sophisticated of computing tasks.

Recent months have seen the emergence of several "keys and lamps" systems from such firms as Ritro and Newbear Computing, and these are designed primarily as a means of "getting started" in home computing while at the same time offering the facility of later expansion to virtually any level of system complexity. The system reviewed here is the Ritro AMICOS system, which is 6800 based and when operational allows the user to run programs entered by means of keys in hex. machine code, and read out the "answers" on two l.e.d. arrays, i.e. it is intended primarily as a cheap means of getting into microprocessor computing.

As can be seen in the photograph, an array of 16 l.e.d.s and 16 switches allows the address bus to be monitored on l.e.d.s and have addresses entered onto itself by keys. A similar array of 8 l.e.d.s and 8 switches performs the same functions on the data bus. Other switches allow single step or clocked operation, automatic address increment when inputing data, access to the 6800 register, interrupts and other functions. In other words, access is given to all parts of the system to allow the basic principles of microprocessor operation to be rapidly learned and later applied to the solution of problems by relatively simple programs.


MAIN-COS components as supplied in the kit.

## THE HARDWARE

The kit of parts consists of two plastic boxes containing MAIN-COS and CON-COS and a large thick instruction manual-MAIN-COS being the name given to the circuit module containing the 6800 microprocessor and peripheral circuitry, CON-COS, on the other hand, is a circuit module which interconnects the operation panel and MAIN-COS. Both modules are impressively packed in the appropriate conducting foam or polystyrene packaging and each plastic box is slipped into a sleeve which outlines the contents and overall function of that module, and it is at this point that a problem, which was to recur, came to light. The MAIN-COS sleeve is printed in Dutch and English, but the CON-COS sleeve is printed only in Dutch, which proves no problem with such words as "processor", but rather more tricky when one is confronted with "drukknoppen" (pushbuttons?).

The manual consists of an A. 4 sized loose-leaf book about 25 mm thick with sections on construction of the MAIN-COS and AMI-COS modules, theory of the 6800 , sample program derivation and general background of the devices used in the system. Unfortunately, the manual contains only about 50 pages of actual constructional information, about one half of its total being devoted to a reprint of device data sheets-very useful background, but few constructors will need or want to plough through this material. A section devoted to a very clear explanation of the 6800 instruction set is very well produced and the author found the information was more easily understood than that given in the Motorola Programing Manual.

A further section deals with program writing and associated flow-diagram production; the explanations are very clear and two programs are written from flow diagrams through to machine code listing. These programs, an 8 bit binary to three
word BCD convertor and a "running light" program, are subsequently handy as an easy way of checking on correct function of the system when completed. The shortest section of the manual is entitled "General Mounting Directions", and is intended to give guidance on constructional techniques. The advice given is at a very basic level and is obviously translated from some other language, which has caused some amusing "directions" to be given-mentioning a "soldering rod", and requesting that a joint should be kept still "during the hardening process" (please see the footnote concerning the construction section of the manual).


Presentation of the kit including the complete manual.

## CONSTRUCTING MAIN-COS

The circuit board used for MAIN-COS is a simple eurocard size double-sided fibre glass board without plated through holes and thus the first task is to make a number of through board connections using the 0.8 mm wire supplied. The manual says to cut 149 pieces and link as shown in a photograph of the board. Unfortunately, due to the poor quality of this photograph, it requires considerable care to link all the required holes and not link holes subsequently to be occupied by i.c. legs or component leads, particularly as the figure of 149 seems to be excessive137 links being used in this prototype. While completing this task, ample opportunity is given to examine the circuit board, which is to a very high quality with a gold-plated double-sided 64 way edge connector and relatively high packing density of i.c.s and a few discrete passive components.

The next activity involves inserting a 7404 inverter package and a "test l.e.d." and probe. This assembly is extensively used during construction, in conjunction with "forcing wires" or earth-connected wires, to de-bug the system as it is assembled. This allows the experimenter, without even a multimeter, to confidently construct the kit-a very helpful idea. Assembly of the board is performed in several stages, each stage being followed by a comprehensive test routine using tables which instruct one to monitor with the test l.e.d. and "force" with forcing wires at specified points, thus checking each i.c. and its interconnections step by step.

The construction of the clock generator in microprocessor systems is frequently a cause for concern, but the AMI-COS module simply uses two back-to-back monostables (74123) with fixed resistors and capacitors, resulting in the prototype constructed by the author, in a clock frequency of 750 kHz and a mark/space ratio of about 1:1. This frequency and mark/space ratio can, of course, only be checked using an oscilloscope, so, if the constructor requires a given clock ratio to allow real-timing in software, then he will need to have access to such an instrument.

The final stage in construction involves inserting the 6800 microprocessor and two ROM chips and applying a +5 volt supply, at which point the processor board tests itself out using a diagnostic program contained in the ROM's. A set of eight small l.e.d.s flashes-through a defined sequence, testing, among other things, data and address lines, RAM locations and the presence of other modules. Failure of any test results in the appropriate 1.e.d. being lit, allowing relatively easy debugging of the MAINCOS module-a very neat solution to the problem of testing microprocessor systems in general, and all the more remarkable for its appearance in a basic system such as this. Assuming the manual has been followed, then the MAIN-COS board should function correctly, and may be set aside pending the construction of CON-COS; Fig. I shows a block diagram of MAIN-COS.

## CONSTRUCTING CON-COS

CON-COS occupies a second eurocard which again has to have a number of through holes linked, the number given in the manual-92-being correct this time. Again, the picture


Fig. 1. MAIN-COS block diagram.


Construction of the kit in the Desko case. Veroboard was used for switch and I.e.d. wiring.
showing the positions of the holes is of very poor quality and a diagram would have been much more useful. Construction continues in stages, a test schedule allowing a thorough check to be made of all devices and interconnections. At any rate, this is the intention, but this part of the constructional information is bad, having many errors, causing much cross-reference to the circuit diagrams (see footnote). If the constructor has sufficient ability to overcome these problems, then construction and testing of the board is relatively straightforward and results in a module which controls the interface between MAIN-COS and a front panel-Fig. 2 shows the block diagram of CON-COS.

The final part of construction consists of mounting the switches and l.e.d.s in the CON-COS kit of parts on a panel, or, as was done by the author, on the front plate of a Desko, sloping front case (TEK 364 from West Hyde Developments).


Fig. 2. Block diagram of CON-COS.

The switches and l.e.d.s can either be mounted on a sheet of Veroboard or a printed circuit board which can be obtained as an optional extra from Ritro. The front panel must be connected to CON-COS using insulated wires which are soldered direct on to the board rather than connecting via the edge connector, effectively tying the panel and CON-COS together by a harness of 61 wires. A simple power supply giving 2 amps at +5 volts must be provided by the constructor to drive the MAIN-COS and CON-COS modules.

## TESTING AND PROGRAMMING

Testing of the completed system is carried out in two parts. Firstly, the test program in the two ROMs in MAIN-COS is used in conjunction with the RESET key to check out the system using the eight test l.e.d.s on MAIN-COS. This test program also clears the front panel displays and prepares the system for the second part of the test. This final test is probably best performed using the "running lamp" program-an 18 hex. byte program which causes all l.e.d.s to light except for one, which "runs" along the data and address displays at a rate which can be varied by making small changes to the program.

The program can be stepped through one instruction at a time or at full system clock rate to allow an easy check of the system performance. The author's attempts were rewarded in success, after several "hiccups" caused by errors in the front panel circuit


MAIN-COS and power supply construction.
diagram, which effectively caused some switches to be "upside down" on the panel, so that, for instance, the switch which selects either program counter or index register was inverted when connected "as diagram". In practise, this prevents one easily entering a program as the RAM address of the program bytes is not displayed on the front panel and apparently indicates that the auto-increment of the program counter is not working-all very confusing and the source of several hours of head scratching!

Having sorted out these problems, the system proved very easy to use and seemed reliable, although this aspect of the system is probably more influenced by the quality of construction than by the content of the kit itself.

## VERDICT

The purpose of any "keys and lamps" microprocessor system must be primarily to introduce the newcomer to microprocessors to the intricacies of what is a complex subject. In this respect, AMI-COS, as presented to us, cannot be said to be a complete success. The circuit boards and hardware are all top quality and the concept of step by step checking, using a built-in test l.e.d., is excellent. The use of the microprocessor to check itself and the system out with the aid of test programs in ROM is similarly a very well thought-out concept, and yet all this good engineering
is spoiled by a poor set of assembly instructions.
The system, once constructed, is excellent as a "tutor" in microprocessors, when used in conjunction with the relevant sections of the manual, but, at a price of $£ 166 \cdot 50$ plus VAT, seems expensive when, for only small additional outlay, a system running on keyboard and TV set-v.d.u. could be obtained.

For more information contact Ritro Electronics (UK) Ltd., Grenfell Place, Maidenhead, Berkshire.

## FOOTNOTE

As a result of our kit construction and findings concerning the manual, Ritro and Mr. P. Birnie have got together and the construction section of the manual has now been completely rewritten.

The completed unit in the Desko case. Approximately $£ 20$ 's worth of extra components (including the case) were used in the construction of this unit.


## P.C.B. ASSEMBLY JIGS <br> W. ENGLISH

ONE of the main problems involved in soldering p.c.b.s and stripboard is being able to hold the boards in an accessible position to enable a satisfactory joint to be made.
The two assembly jigs described here show how this can be accomplished with both large and small boards.

## CONSTRUCTION (TYPE 1)

A very simple but effective jig for holding small pieces of board is shown in Fig. 1. It consists of a piece of 12 mm plywood $130 \times 130 \mathrm{~mm}$ onto which is glued a wedge shaped piece of 15 mm softwood cut to an angle of 30 degrees.

To complete the device, a spring clothes peg with the jaws modified as shown below is glued to the slope of the wedge.


## CONSTRUCTION (TYPE 2)

The assembly jig shown in Fig. 2 is capable of accepting boards up to $300 \times 140 \mathrm{~mm}$. If larger sizes are to be used, then the relevant dimensions can be scaled up.

The board to be soldered is gripped in slots cut into two bars, one of which is movable to take boards as narrow as 20 mm . A self-tapping screw through the metal band at each end of the movable bar enables it to be clamped so that the board is held firmly. A detail of this is given in Fig. 3.



# A. R. Damper 

MANY readers of P.E. are newcomers to the construction of electronic circuits, and will most probably construct simple projects on tagstrips or Veroboard. It will not have escaped their notice that most large published projects give details of a printed circuit layout. The reason is simply that printed circuit boards (p.c.b.s) are simple to make (once you know how!), and provide a much neater and professional looking endproduct. Smaller published circuits often give a Veroboard layout or no constructional details at all. This article is intended to be a guide for those wishing to make p.c.b.s from circuit diagrams for the first time. Single-sided boards only will be considered. Double-sided boards are mostly used in more complex projects, and when published in magazines, can usually be obtained ready made.
There are two main types of board material to choose from, SRBP and glassfibre. In general applications either may be used, the SRBP being cheaper but not as strong as glass-fibre. In higher quality applications where maximum isolation between tracks is essential, glass-fibre boards are preferable. For small general projects SRBP is quite adequate.

## AN EXAMPLE

The object of the exercise is to produce a conductor network on the board by covering, or masking the areas where copper is to remain, and then dissolving away the remaining areas. However, before the tracks can be marked onto the board the layout must be determined from the circuit diagram. A simple circuit (one stage amplifier), provides an example. See Fig. 1.

The circuit is a simple one with no interconnections to other parts of a complex design, enabling the layout on the board to follow the layout of the diagram. Now see how this circuit can be
transformed into a finished p.c.b. The points marked with and " $O$ " represent the need for a hole in the board to take a component lead. At point " $A$ " there are four holes necessary; one for a capacitor lead, two for resistors, and one for the transistor base lead. If the components were now to mystically vanish, Fig. 2 results, and the Os indicate where the holes are to be drilled. This is the general layout and only gives a guide to the track layout.

The resistors are probably low-wattage and can therefore be mounted vertically; consequently the distances between the two holes for Rs need only be about 6 mm . However, C2 is an electrolytic capacitor, and for an amplifier having a low pass characteristic, may be of large value, hence large size. The holes for C2 may need to be 50 mm apart. So from Fig. 2, Fig. 3 is produced taking into account the component sizes. It is therefore very important that the components are obtained prior to building the board if their size is unknown. Figure 3 still shows the track from the component side of the board and must be reversed to give the layout as it will be etched. To ensure that the input and output are isolated, it is always best to fill in large otherwise empty spaces with conductor, and earth it making sure there is one continuous path to earth. This gives the final full size layout as in Fig. 4, the shaded area being the "filled-in" portion carried out after the main track is applied.

## METHOD

There are quite a few different types of etch resists on the market. In the author's opinion the best board is produced by a combination. Until recently the resist used was painted onto the board (enamel paint and nail-varnish being common), but these did not lend themselves to small scale work such as integrated circuit con-
nections. Then etch-resist pens appeared which enabled the circuit to be drawn directly onto the board, and although they sound fine, the author has found that they often "run" to produce very thick lines unsuitable for fine work. Without running these pens down, the dry transfer system seems the cleanest and simplest method. It is also fairly cheap, and if a mistake is made, the offending line can be scraped off. So, the author recommends that for the main track, dry transfers are used whilst the larger filled-in areas are covered with nail varnish, paint or etchresist ink.

## MAKING THE BOARD

Having worked out the final layout as in Fig. 4 the board can be prepared. Firstly cut from a larger sheet, a piece of copper-clad board of the required size, using a hack-saw with a fine blade and then file the edge smooth. The surface of the copper must now be thoroughly cleaned to remove all grease, dirt and finger-marks. A good method is to rub the surface gently with a cloth after sprinkling a little "Ajax" or similar cleanser onto the board. This is very effective and no trouble whatever has ever been experienced with the fine scratches produced. The board is then rinsed in fresh water and dried.

Dry transfer systems are sold by many P.E. advertisers and complete packs consists of ten sheets, each carrying a different symbol such as lines, drilling points, transistor and i.c. sockets, etc. whilst extra individual sheets are usually available also. It is advisable to buy extra sheets of lines, as these are most frequently used. The author once used Letraset sheets of symbols and line rules, before this system became available, and in certain circumstances such as when fine lines are needed, Letraset is still handy. However, these sheets are more

expensive and therefore only used when necessary. Letraset is useful for applying lettering to the board to identify component positions. If Letraset is included in the masking the etchant should be reduced to around half the normal concentration, as this type of transfer may start to loosen. The p.c.b. transfer system is of course unaffected by the full strength solution.

Working from the layout, the transfers should be applied by placing the required symbol face down on the board and rubbing the backing sheet gently with a bluntish pencil. When transference is complete, the backing sheet is carefully peeled away and the remaining transfer symbol gently pressed onto the copper to secure it. Clean hands are necessary for this operation. Should something go wrong, the transfer can be removed by scraping it off with a finger-nail or blunt instrument.

When all the lines (as in Fig. 4) have been applied, the larger shaded area can
be filled in with etch-resist ink or paint. The board should then be left to dry.

## ETCHING

The etching solution used is ferric chloride, which is also obtainable from advertisers in this magazine. The solution is made up as follows. For each pound weight of ferric chloride, one pint of cold water should be added, and placed in a plastics container (a plastics orange juice bottle with the top sawn off is ideal). The ferric chloride should then be added slowly, stirring all the time with a piece of wood or plastics rod. Don't add too much at a time because the solution gets hot, and it becomes necessary to stop and let it cool. Never add water to the crystals, because the heat generated is enormous and the steam produced might shower you with hot ferric chloride, or melt the plastics container. If any ferric chloride gets on your skin, wash the area thoroughly with water. Store the solution in a plastics container with a secure
stopper.
To etch the board, pour some of the etchant into a shallow plastics or glass tray and lay the board face down on the solution. This allows any by-product from the etching process to fall away from the surface of the board. If the board sinks, or you would rather see what's going on, lay the board face-up in the bottom of the container, but keep the solution moving by gently rocking the container. When etching is complete, remove the board with non-metallic tweezers and rinse in cold water. After drying, the resist can be removed by rubbing the surface with medium or fine grade "wet and dry" paper obtainable at d.i.y. shops. The holes can then be drilled with a 0.8 mm drill. Some components will have larger diameter leads and the appropriate drill should be at hand. To give the drill a start, it is useful to mark the position with a centre-punch, but be very gentle otherwise the board may crack.


## GETTING STARTED

Delta Distribution is a very new company initially concentrating on power supply transformers, regulators and common i.c.s. Good, fast service is their intention and they are at present setting up mail order and telephone credit services with Access and Barclaycard.

The product range will be expanded as soon as possible and the current price list is available from Delta Distribution Co., 75 Willowtree Ave., Gilesgate Moor, Durham, DH1 1DZ (0385 62760).

## SOUND ON VISION

An all British invention, the Videograph, has recently been announced. It converts sound from a record, tape, organ or synthesiser into a display on a domestic colour or black and white TV screen.

Two inputs allow direct display of stereo sound as independent left and right channels, or alternatively a mono signal may be used to feed, for example, low frequencies to one channel and high frequencies to the other.

When used with a colour TV the waves are orange and blue, while the background performs an eight stage colour cycle.

Connection to the TV is directly via the aerial socket.

The Generator Kit costs $£ 12$ and the UHF Colour Modulator Kit is $£ 6.95$. Cabinet and controls cost $£ 6.95+£ 1 \mathrm{p} \& \mathrm{p}$.

Ready built units are available at $£ 39.95$. All prices include VAT.

William Stuart Systems Ltd., Dower House, Herongate, Brentwood, Essex.

## AMERICAN COMPUTER BITS

Another supplier of computer bits and pieces has arrived on the scene; Interam Computer Systems Ltd, who are geared up to service both the hobbyist and small businesses.

As we move further into the "software age" more of these suppliers are sure to pop up, with all sorts of novel odds and ends. Among the literature sent for our perusal by Interam were details of such things as the Cybernetic Systems 1000 Speech Synthesiser. This p.c.b. can be programmed to talk, using standard English and American phonemes, callable using ASCII characters. It is described as a real conversation piece!

Then there is the Newtech Model 6 Music Board which is $\mathrm{S}-100$ bus compatible, and with built-in audio amp and loudspeaker can be programmed to produce melodies, rhythms, sound effects, morse code, touchtone synthesis, and much more.

And yet another American product available from Interam, is the George Risk 753 ASCII Keyboard module with 53 keys on the popular ASR-33 format and 3 user definable keys.

What is PROROM? This is an 8 K EPROM memory board with on-board programming circuitry intended to make high programming charges a thing of the past, and made by Mountain Hardware (USA).

Much much more is available from Interam; disk systems, optical tape readers, a Z80 c.p.u. system, software, and many American books and magazines.

More details are obtainable from Interam Computer Systems Ltd, 59 Moreton Street, Victoria, London SW1.

## ONE-CHIP TV SOUND SYSTEM

The TDA 1190 is a 12 -pin silicon monolithic i.c., mounted in a "batwing" plastic power package. The device which performs all the functions needed for a TV sound system has an audio output power of typically 4.2 watts into a $16 \Omega$ load with a supply voltage of +24 V . The TDA 1190 is particularly suited to use with 5.5 MHz (PAL) sound systems.

The package provides a FM IF amplifier and detector of high sensitivity which only requires the addition of few extra components and a single tuned coil. AM rejection is excellent; figures of 55 dB being typical. An active low pass filter is used to attenuate IF harmonics. Other features include an on-chip audio power output stage and a d.c. volume control.

Although primarily intended for use in TV sets the device can be used in inexpensive FM radios and other FM receiver applications at IF frequencies up to 10.7 MHz .

For further information contact Fairchild Camera and Instruments (UK) Ltd, 230 High Street, Potters Bar, Herts.

## SOLDERING TOOLS

For many years the name Adcola has been synonymous with soldering irons. "Adcola ' 78 " is a very well produced booklet about the other products manufactured by "The soldering equipment people".

Perhaps initially written for volume production solderers this booklet makes useful reading for any hobbyist.

The advantages of thermostatically controlled "soldering stations" are well documented. Their main improvement over the commonly used "thermal balance" iron is a more rapid rise to operating temperature and an ability to maintain a set temperature during repetitive working.

Among the numerous sizes and profiles of plain copper and iron plated bits is an $80^{\circ}$ p.c.b. tip. Iron plated bits oxidise much less quickly than plain copper and are claimed to have a ten times greater working life.

Of particular interest to project builders is a desoldering bit for 14 or 16 pin i.c.s although it does require to be used in a 27-30 watt iron. Adcola also supply an i.c. puller. This is a simple spring loaded extractor used to withdraw i.c.s. from a board after a desoldering bit has melted the surrounding pin solder.

Other desoldering aids are a heated tip with suction bulb, a desoldering gun and various sizes of desoldering braid.

For quick access to both sides of a rigidly held p.c.b. Adcola's Variclamp deserves consideration. This is a lightweight portable holding device which can hold items in any preset position.

The right tools make the job easier and help towards a good finish. The range of cutters, nippers, pliers, shears and strippers gives the craftsman constructor the means of achieving professional quality boards.

Copies of "Adcola '78" and a full price list may be obtained from Adcola Products Ltd, Adcola House, Gauden Road, London SW4.

## TRISTATE L.E.D.

A light-emitting diode which produces red or green emission according to the polarity of the applied voltage is now available from Distronic. Known as the Xciton XC5491, the device uses a back-to-back double-diode configuration which produces red or green light of equal intensity for both colours.
The forward bias is typically 2.2 V at a current of 10 mA with a dynamic resistance of $25 \Omega$. The maximum continuous forward current is 25 mA for both colours and the operating temperature is $-55^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$.

The XC5491 is supplied in a T-1-3/4 package measuring 0.2 in diameter $\times 0.34$ in height, and is supplied with wire-wrappable leads.

For further information contact Distronic Limited, 50-51 Burnt Mill, Elizabeth Way, Harlow, Essex.

## CHUCKLESS P.C.B. DRILLING MACHINE

A p.c.b. drilling machine operating on a new design system allows inexperienced operators to drill perfect p.c.b.s immediately.

The unit has no chuck and does not require guide bushes for turbo shank tungsten carbide drills.

The drill bits run in vee-blocks at the end of the drill arm allowing extremely accurate running at 15,000 r.p.m. Belt idler pulleys run on ballraces and the mains motor is silent running.


An integral vacuum attachment for efficient swarf removal will optionally accept standard Hoover vacuum cleaner attachments if required.

The ultra lightweight drill arm can be easily raised and lowered to allow modifications to p.c.b.s where components are already in place. The machine will accept drills from 0.35 mm to 2 mm diameter.

Standard fitments include back stop and side gauge-fully adjustable in both X and Y axis, and a jig to ensure accurate location when fitting drill bits.

The unit cost $£ 140$ with accessories and is available from P.B. Electronics (Scotland) Ltd, 9 Radwinter Road, Saffron Waiden, Essex.

## STORAGE SCOPE

The new oscilloscope available from Gould Advance, is a general purpose two channel digital storage oscilloscope and waveform digitizer which features several new facilities, including an $\mathrm{X}-\mathrm{Y}$ display and a sensitivity of $100 \mu \mathrm{~V} / \mathrm{cm}$ combined with isolation from earth to avoid hum and ground loop problems.

It is based on an 8-bit analogue/digital converter operating at a 1 MHz sampling rate into a 1 k bit store to provide a -3 dB bandwidth up to 600 kHz .

In addition to facilities for displaying the sum or difference of the input signals, the instrument has a comprehensive range of signal conditioning and trigger facilities, including a "trigger-window" feature which is unique in oscilloscopes. In this mode, trigger occurs whenever the input goes outside either of two levels, symmetrically spaced above and below the original trigger level.

Internal trigger sensitivity is equivalent to 2 mV of signal at frequencies from d.c. to 1 MHz , and external sensitivity is better than 100 mV up to 1 MHz . Trigger coupling allows selection of d.c. or a.c. with high-frequency and/or low-frequency rejection if required.

An important feature of the digital storage system is a signal delay switch, which inserts a quarter scan length delay into the digital signal path, so that events happening prior to the trigger event may be viewed on the screen.

An 18-position rotary switch varies the speed rate over a very wide range, $100 \mu \mathrm{~s} / \mathrm{cm}$ to $50 \mathrm{~s} / \mathrm{cm}$, while an X-expand control offers the facility of expanding the display horizontally, even after storage has taken place.

In addition to the normal dual-channel form of display against a time axis, channel 1 can

be displayed against channel 2 in the $X-Y$ mode.

A split-trace facility is incorporated; in the single-trace mode, alternate samples of the input signal are frozen, so that an image of the signal may be stored and superimposed on the display of the current input waveform.

The digital storage system incorporated in the Gould Advance OS4100 offers considerable advantages over conventional tubestorage oscilloscopes incorporating expensive limited-life storage tubes. The X - Y facility means that the instrument is ideally suited for looking at transients where more than one variable is involved.

For further information including price contact Gould Instruments Division, Roebuck Road, Hainault, Essex.

## RADIOCODE CLOCK

A clock which has been specifically designed to receive the time and date information which is transmitted by the National Physical Laboratory from Rugby on MSF 60 kHz is available from Circuit Services.

By decoding the Rugby signals the clock can set itself automatically within two minutes of being switched on and displays either the time or the date. No subsequent adjustments are necessary because the clock is capable of accounting for both leap years and BST.

Its estimated range is around 1,000 miles and for continental use the clock has an add-on-hour facility or alternatively, a modified version can be supplied which receives a similar signal from the DCF77 transmitter at Mainflingen, West Germany. This allows the clock to be used in Eastern Europe where the MSF transmission may be weak.

The unit can be supplied with an alarm/timer module which enables the clock to control other equipment at certain times for precise periods. A record/replay unit can also be supplied which allows the time or date to be recorded on one track of a conventional tape recorder while the other tracks monitor instrument outputs, transients, etc. On replay the recorded time is shown on the Radiocode Clock display. In this mode the clock operates

exactly as if it was receiving a normal transmitted signal. Once the tape has been started on replay, the clock circuitry requires around two minutes to synchronise with the code. After synchronisation has taken place any events recorded on other tracks of the same tape will be timed to within approximately 10 mS of their occurrence.

Another advantage of this system is that if the Rugby transmitter should happen to break down during an important recording period, the back up crystal clock will take over and provide an accurate time reference.

For further information contact Circuit Services, 6 Elmbridge Drive, Ruislip, Middx.

## HONEY AT 2.507

The Casio F-100 digital chronograph can display time, calendar, or a stopwatch function. The $1 / 100$ th second stopwatch measures net and lap times and can record 1st and 2nd place times.

Timekeeping is $\pm 15$ seconds per month and gains or losses of up to 30 seconds are easily adjusted by pressing two buttons and zeroing in on a time signal. A complete setting of hours, minutes and seconds takes a little longer.
The calendar display is similarly easy to set, and looks after itself with only the 29th February to contend with.
Although water resistant the watch should not be subjected to swimming, bathing or even car washing. The manufacturers say the display may disappear if kept under constant sunlight but will reappear after normal temperature resumes. Battery life is approximately 18 months.
It is not understood why the function buttons on the watch are coloured grey, yellow, brown and green whereas the user's guide refers to them as B.L.C. and A.
If you want to brush up on your watch knowledge in German, French, Spanish or Italian then Casio's user's guide serves as a miniature Hugo's. Soon, perhaps, the average music centre will have Stoppuhrfunktion: Travail buttons; oxido de plata batteries; and interfaced Luce shows.

The English language and the traditional clock face are disappearing into an oriental

sunset. The last two lines of The Old Vicarage, Grantchester by Rupert Brooke survived the attack by Peter Sellers' cafe waitress but what hope have they against the digital time display.

Stands the church clock at ten to three?
And is there honey still for tea?
F-100, £19.95, Tempus, 19-21 Fitzroy St., Cambridge, CB1 IEH.

## DAWN CHORUS DEVICE

Aptly named the Wren, this new quartz movement miniature alarm clock by Smith's Industries is a mere 59 mm high.

The hands and hour positions are luminous and the moulded case is available in white with a black or gilt dial, or brown with a gilt dial.

The recommended retail price is $£ 12.90$.

## AMPLIFONE

This telephone amplifier fits nearly under a standard instrument and not having any wiring to the telephone can be installed instantly.


Dialling and ringing tones and voices are amplified enabling the caler to hear incoming sound "hands off". The flight deck is simplified to one combined On/Off switch and volume control.
Posted and packed price is $£ 10$.
Southern Developments, 120 High Street, Billingshurst, Sussex, RH14 9QS.


## SHAFT SHEAR

Manufactured from hardened Swedish tool steel, this guillotine-type tool will cut copper, aluminium, mild and stainless steel, and plastics rod of the following diameters: $\frac{1}{16}, \frac{1}{8}$ and $\frac{1}{4} \mathrm{in}$, and $3,4,5$, and 6 mm .


Shock and vibration have caused problems when the spindles of sensitive components have been sewn or cut on a lathe; but the Telpro Shaft Shear may be used to cut the spindles of even delicate helical potentiometers, since it does not transmit shock or vibration.

A further advantage is that it gives such a clean, smooth cut that no filing or other finishing operation is needed before control knobs are fitted.

Manufacturers are Tele-Production Tools Lid., Stiron House, Electric Avenue, Westcliff-on-Sea, Essex SS0 9Nw.

## Mini vice

This, Design Centre selected vice, needs only a $\frac{5}{16}$ inch clearance hole in a work bench to give an infinitely positionable third hand. It is an ideal addition to the Home Radio Electronic Workshop (below) and is also supplied by them, at $£ 5 \cdot 50+8$ per cent VAT and 85 p postage, but post free if supplied with an Electronic Workshop.


## TRAVEL

If you have ever been evicted from the kitchen table just as you were getting your project into a "critical" mode you will understand the marketing theory behind The Electronic Workshop. At the first sign of an approaching ketchup bottle the self contained unit can up and away to the safety of the cupboard under the stairs.
The working surface is two feet wide and 20 inches deep. Rubber mat covers the centre area and formica protects the side portions. Pieces of pine $1 \frac{1}{2}$ inches high form the outer edges and also double as tool holders.

At each end of the back section is a flush 13A shuttered mains socket. The right hand
compartment contains a power pack, giving $0-20 \mathrm{~V}$ at 1 A . The left hand compartment houses a small $35 \Omega$ speaker and volume control. The centre compartment acts as a tool box and dispenser for reels of solder, plain wire and tinned copper which feed out of three holes into the work area.

The price of the workshop complete, but for wiring up, is $£ 45$. Ready wired, $£ 54$. VAT and carriage, $£ 4.56$.

Home Radio (Components) Ltd. 240 London Road, Mitcham, Surrey CR 4 3HD.

Please note that the photograph shows a mark one version which had AC and DC controls on the power supply. The present workshop has a single control.


## DIGITAL THERMOMETER REVIEW

There are many different types of digital thermometer now available which cover a very wide temperature range.

The hand-held types use cmos technology to ensure low power consumption with either l.e.d. or l.c.d. indication.

The shape and type of sensor depends on the particular application for which the unit is required, i.e. medical, surface, liquid, powder or gas measurements.

## 800 SERIES

This series incorporates twelve different models with a wide variety of thermocouples to cover the temperature range from $-150^{\circ} \mathrm{C}$ to $1,750^{\circ} \mathrm{C}$.

To ensure maximum life is obtained from the internal batteries a three position switch: momentary-off-on is used. This facility enables the instrument to be either switched on only for the instant at which it is required, or maintained continuously on. HP7 batteries are included with the unit and rechargeable cells with a charger are available as an optional extra.


The $\mathbf{8 0 0 0}$ series vary in price from $\mathbf{£ 9 4 . 0 0}$ to $£ 125.00$ subject to the temperature range and resolution required.

Further details can be obtained from Channel Electronics Ltd., PO Box 58, Seaford, Sussex.

## THERMISTOR PROBES

The Technoterm 1500 uses precision thermistors with negative temperature coefficients in their probes. This enables response times of between 6 and 10 secs to be achieved over a temperature range of $-40^{\circ} \mathrm{C}$ to $140^{\circ} \mathrm{C}$, depending on the type of probe chosen.

A wide range of probes is available to suit most applications. Accessories include nickel cadmium batteries, recharger and a handy dispenser for silicone paste which reduces the response times on rough surfaces.


The Technoterm 1500 is priced at $£ 169.00$ and is available from Thermographics Ltd., 16 Canal Street, Congleton, Cheshire.

## CLINICAL TYPE

The 4755 has been designed specifically for medical and environmental measurements and is available in two ranges, $33^{\circ} \mathrm{C}$ to $42.9^{\circ} \mathrm{C}$ (clinical) and $1^{\circ} \mathrm{C}$ to $48^{\circ} \mathrm{C}$ (environmental).
The medical unit is capable of retaining the highest temperature detected within a patient and to prevent cross infection both the instrument and the sensor can be sterilised. To avoid the need for sterilisation after each test disposable sheaths for the sensor are supplied with the unit. The response time of the thermometer is 15 secs with rechargeable cells as an optional extra.


The Digitron 4755 is available priced £92.00 from Digitron Instrumentation Ltd., Mead Lane Industrial Estate, Hertford, Herts.

## TEMPSTIK

The Tempstik thermometer has been designed to meet the low cost instrument need and is capable of measuring a temperature range of $-100^{\circ} \mathrm{C}$ to $200^{\circ} \mathrm{C}$ with reasonable accuracy.

The instrument has a liquid crystal display and is powered from a PP3 battery which is not included.


The Tempstik which is priced at $£ 73.00$ is available from Process Measurement Ltd., 8 High Street, Littlehampton, West Sussex.

## BENCH TYPE

The 7000 series was designed for bench or rack mounted applications and is mains operated. The temperature is indicated on three or four 14 mm Sperry gas discharge displays. Thirteen models are available for the temperature range $-100^{\circ} \mathrm{C}$ to $1,800^{\circ} \mathrm{C}$.

All the models in this series can be fitted with an alarm circuit which consists of two totally independent alarm points each having its own relay and front panel l.e.d. The relay contacts which are rated at 240 V 2 A provide normally open, common and normally closed outputs at the rear of the unit.


The price of the 7000 series is between $£ 109.00$ and $£ 149.00$ depending on the temperature range chosen.

For further information contact Jenway Ltd., 26 Broomhills Industrial Estate, Rayne Road, Braintree, Essex.


THIS circuit was developed to control the speed of a small model vehicle driven by a permanent magnet electric motor, and provides the following features:

1. A wide range of continuous speed control down to about 200 r.p.m. motor speed.
2. Substantially constant speed despite changes in mechanical resistance due to inclines, cornering, or surface variation-carpet to lino, for example.
3. Substantially constant speed despite changes in electrical resistance-varying track length in a model railway, for example.
4. Economical low-speed operation, unlike some opamp based controllers.

## PRINCIPLE OF OPERATION

The controller makes use of the fact that when the permanent magnet motor is temporarily disconnected from the drive battery, it acts as a generator driven by the wheels of the free-wheeling vehicle, with the generated voltage representing the actual speed of the vehicle.

The controller compares this generated voltage with a voltage selected to represent the desired speed, and when the vehicle speed is less than the desired speed the controller momentarily reconnects the battery to the motor.

This process is repeated continuously, resulting in the motor being fed with a stream of fixed magnitude pulses, with a repetition rate governed by the external conditions, as shown in the waveform diagram.

## CIRCUIT DESCRIPTION

A voltage representing the desired speed is fed to terminal A. This voltage may be derived in various ways, the potential divider VR1 being shown as an example. Transistor TR1 compares this voltage with the generated voltage from the motor via filter components R9, C3, and switches on TR2 when the actual speed is less than the desired speed. Monostable M1 then produces a fixed magnitude pulse at

## COMPONENTS

## Resistors

| R1 | $10 \mathrm{k} \Omega$ |
| :--- | :--- |
| R2 | $10 \mathrm{k} \Omega$ |
| R3 | $39 \mathrm{k} \Omega$ |
| R4 | $39 \mathrm{k} \Omega$ |
| R5 | $470 \Omega$ |
| R6 | $1 \mathrm{k} \Omega$ |
| R7 | $10 \Omega 2 \mathrm{~W}$ |
| R8 | $1 \mathrm{k} \Omega$ |
| R9 | $39 \Omega$ |
| R10 | $1 \mathrm{k} \Omega$ |

( t W carbon except where otherwise stated)

## Capacitors

| C1 | $0.022 \mu \mathrm{~F}$ plastic |
| :--- | :--- |
| C2 | $0.1 \mu \mathrm{~F}$ plastic |
| C3 | $0.047 \mu \mathrm{~F}$ plastic |
| C4 | $0.01 \mu \mathrm{~F}$ ceramic |
| C5 | $0.01 \mu \mathrm{~F}$ ceramic |
| C6 | $0.01 \mu \mathrm{~F}$ ceramic |

## Semiconductors

| TR1 | BC107 | TR4 | AD142 |
| :--- | :--- | :--- | :--- |
| TR2 | BC157 | D1 | AAY12 (see text) |
| TR3 | BFY50 | IC1 | SN74123 |



Fig. 1 (above). Circuit diagram; Fig. 2 (below) waveforms

pin 13 of the i.c. and applies power to the motor via TR4, which is an AD142, chosen for its low saturation voltage at high current levels.

Diode D1 is provided to prevent the monostable output terminal from being held low by the base-emitter junction of TR3, and must be a gold bonded germanium component to ensure that TR3 turns off adequately when required.

When the power is removed from the motor inductive effects cause a negative voltage spike at the motor terminal $D$, as shown in the waveform diagram, so monostable $M 2$ is provided to prevent re-operation of M1 until this back-e.m.f. has decayed and the voltage across the motor truly represents the vehicle's speed.

Separate Nicad batteries were used to avoid interference problems caused by the large surges of motor current and the sensitivity of the TTL monostables.

## MONOSTABLES

The 74123 is a dual retriggerable, resettable monostable multivibrator providing an output pulse whose duration and accuracy is a function of external timing components.

There are two inputs per function, one active low and one active high. This permits triggering on either the positive going leading edge or negative going trailing edge of a pulse. Triggering is independent of input transition time or pulse width.

Since the switching voltage requirement for silicon is higher than the germanium with no pulse output from M1, D1 conducts and TR3/TR4 remain off.

With a pulse output, D1 is blocked and TR3 conducts so that TR4 provides current to the motor.

To prevent the pulse period being extended with the appearance of the inductive spike, Q output is routed back


Fig. 3. Board layout


Fig. 4. Component connections to board
to M 2 producing a high output at pin 5 which inhibits the Q output of M1.
As can be seen from the waveform diagrams the pulse widths at $M 1$ and $M 2$ outputs have been preset by choice of R4-C2 and R3-C1 respectively, the pulse period being a function of the product of these.

## CONSTRUCTION

All components except TR4, R7, R8, C5 and C6 are mounted on a piece of $0.1 \mathrm{in} \times 0.1 \mathrm{in}$ Veroboard as shown. $\star$

## ..: a selection from our postbag <br> Readers requiring a reply to any letter must include a stamped addressed envelope.

Opinions expressed in Readout are not necessarily endorsed by the publishers of Practical Electronics.

## Audio compass

Sir-A number of compasses have been, and are being, adapted to provide an audible signal for use by blind yachtsmen. However, none of these provide all the facilities that $I$, as a blind helmsman, would like to have incorporated into one device.

The device that I want would comprise the following: a unit which will generate an electronic signal indicating the boat's direction, detected by the strength and/or direction of the compass's magnetic field; a calculator-type keyboard to be used to enter desired courses and alterations to them; an audio output stage; and, at the heart of it all, a microprocessor to control and coordinate everything.

I have a good knowledge of programming and a basic knowledge of electronics. But I need help to design and construct this device. If you would like to help me with this, please contact me, preferably by telephone, 01-591 5491 (home) or 01-432 6277.

Kenneth C. Knowles, 3 Sheringham Drive,

Barking, Essex
IG119AL.

## CB v TV continued

Sir-I read with interest your article "CB Radio" under Industry Notebook in the May edition of Practical Electronics.
While one is delighted at the content of the article with regard to the recognition of the TV receiver front end problem and the development of better systems, it is a pity that your last paragraph, re interference, is typical of the uninformed and hysterical comment in the electronic/radio press concerning CB Radio. Over the last year comments like this have caused concern to laymen and authority alike.

Of course the great mass of population would not take kindly to TV interference caused by CB Radio and rightly so. But before you spread alarm and despondency a little investigation on your part would have revealed that interference (or prevention of it) stood high on the list of priorities produced by the CB pressure groups. Indeed their call was for VHF/UHF spectrum with low power (2W) FM. Not the AM/SSB kilowatts on 27 MHz
which has been responsible for the interference problem in other countries.
A. J. Cawthorne,
T.ENG(CEI). FSERT. G3TDJ. Bude, Cornwall.

Nexus comments: "As a licenced amateur Mr Cawthorne will be aware of the problems of TVI and of controlling abuses of radio usage however well the regulations are framed and policed. We all start off with good intentions. If, in fact, CB users with kilowatt outputs do exist this proves the point. There is no reason to suppose that British CB enthusiasts, once they catch the bug, will be content with 2 Watts or even 20 Watts. Mr Cawthorne will also be aware that his fellow fully-licenced amateurs, supposedly responsible, are often tempted to abuse their conditions of licence. We cannot assume that $C B$ enthusiasts with on-demand permits, no RAE, and no signed log-books, will be better behaved."


## IC 555 PROJECTS

By E. A. Parr<br>Published by Bernard Babani Ltd<br>141 pages $180 \times 115 \mathrm{~mm}$. Price £1.45

0NE of the most versatile i.c.s available to constructors in recent years has been the 555 timer. This book explains the operation of the timer in detail and then describes the basic circuits that can be built around it.

Specific applications for the device are a collection of designs which have appeared in P.E. and other magazines and are given under four headings; model car, model railway, alarm and general circuits.

Variations of the 555 , namely the 556,558 and 559 which are the dual and two quad versions available are also briefly covered. Practical notes are given on how to construct the circuits with tables to determine the values of the timing capacitors. Many readers will find this book useful both for constructing the circuits and as a handy pocket reference book on the 555 timer.
D.J.S.

## OP-AMPS; THEIR PRINCIPLES AND APPLICATIONS

By J. B. Dance Published by Newnes Technical Books 88 pages, $135 \times \mathbf{2 1 5 m m}$. Price £2.25.

SO MANY books published have been along the same lines as this, and indeed on exactly the same subject, that it is perhaps better to stick to the bare facts.

The book has been written for the non-specialist engineer and home constructor. A non-mathematical approach has been adopted, and internal workings of devices area not discussed in any depth; only applications.

The initial three chapters are devoted to the 741 i.c., followed in Chapter 4 by external frequency compensation. Chapter 5 covers f.e.t. input op-amps, and the last two chapters go into i.c.s for audio applications. At the end of the book there is a glossary of op-amp terminology.

Although the original concept of operational amplifiers was that of performing mathematical operations, no devices for performing mathematical functions are included, as part of the author's aim of keeping the book at a simple practical level. There are clear and well printed working circuit diagrams throughout, each with component values given, and anyone seeking to understand operational amplifiers could do worse than read this material, which is an updated version of the Electronics Australia series "Op-Amps Without Tears."


Copies of Patents can be obtained from: the Patent Office Sales, St. Mary Cray, Orpington, Kent

Price 95p each

## SICNAL CLIP•胃O NITCATI

The Sony Corporation has already patented (BP 1439 241) protective circuit for shutting down an audio amplifier when the impedance value at its output falls below a preset value, for instance due to shortcircuiting of the driven loudspeaker. Now, in BP 1458 857, Sony describes a circuit which will indicate clipping of the output signal as, for instance, may follow from over driving the amplifier into distortion.

The circuit shows a power amplifier with three differential amplifiers (Diff. Amp. 1, 2, 3) and a push-pull output. The signal path
from the output to the base of TR2 forms an in-phase negative feedback loop. Detecting terminals SK1 and SK2 are con'nected to the collectors of TR4, TR5 respectively of differential amplifier 2.

In normal operation, similar signals of a few volts are delivered to detecting terminals SK1 and SK2. Transistor TR12 of the detecting circuit is conductive, and hence TR13 is non-conductive. This causes TR14 of the monostable multivibrator to be conductive, and transistor TR15 nonconductive. As a result, the light emitting
diode D3 is energised to emit green light which indicates that the power amplifier is operating normally.

When the amplifier is subjected to excess load or the load is short-circuited or the output signal is clipped, the voltages at terminals SK1, SK2 approach that of the voltage source $-V_{C C 1}$ for the differential amplifiers. As a result, D1 or D2 is biassed to $-V_{\text {cc1 }}$, TR12 goes non-conductive and TR13 conductive. D3 extinguishes and the red l.e.d. D4 illuminates, to indicate overload or clipping.



A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.
Why not submit your idea? Any idea published will be awarded payment according to its merits.
Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.
Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere


THE circuit shown above is for an attack/release envelope shaper with automatic triggering.

IC1 is a 555 tiner connected in the astable mode with VR1 controlling the duration and VR2 the width of a pulse. No series resistors are used to enable maximum span and flexibility of operation. Automatic triggering is obtained by S2 with S1 used for manual triggering.

The pulse from the output of 1 C 1 (pin 3) is fed via $\mathbf{S} 2$ through D3 and VR4 (which controls the attack time) and charges C3 which in turn switches on TRI.

When the pulse is finished C3 is
discharged through VR3 and D2 to DI and R1. The purpose of D1 and RI is to form a discharge path for C3 and to indicate when a pulse is applied to C3. The discharge time of C3 depends upon the setting of VR3. As C3 gradually discharges TR1 is turned off.

The cycle of the envelope shaper is monitored by D4 which in turn is controlled by TR2.

To operate, the circuit requires 100 to 200 mA and can be driven either by a 9 V PP3 battery or a separate power supply unit.

Stephen Trouse,
Australia.

With only a few components, a modern pH meter (acid/alkali level) can be built using the relatively new RCA 3140T operational amplifier.

Stripboard is not suitable because of the close proximity of the tracks, and so the TOS style op amp should be used with its leads spread out and terminated on a good quality p.c.b. with lead 3 of the i.c. soldered to a teflon insulated terminal. From there, a stiff bare copper wire should connect to the input socket.
The meter ME1 can be a cheap $\pm 100 \mu \mathrm{~A}$ movement and it should be easy to re-mark the scale from 2 to 12 pH , with 7 pH at the centre (being neutral), or from 0 to 14 pH . The unit can be powered from two PP3 9V batteries.

With the electrode in a pH 7 solution (distilled water will do) adjust VR1 for a reading of 7 . Now place the electrode in a buffer solution of pH 4 (acidic solution for calibrating) and adjust for a reading of 4. An alkaline buffer solution (above pH 7 ) could be used instead. The instrument is now calibrated. See P.E. March and April 1977 for pH theory.

## Walter Hediger

Switzerland


## TOUCH SENSITIVE KEYBOARD

THHE circuit described here was designed to provide a synthesiser keyboard with an additional control voltage output, whose magnitude is proportional to the velocity at which a key is struck. This voltage can, for example, be used to control the maximum aplitude which a note attains during the attack time to give the keyboard a piano-like feel, in that it is sensitive to how "strongly" the music is played.
The circuit, shown below, requires an extra set of changeover contacts on the keyboard. ICI detects when
any contact spring is touching neither of its contacts. Thus the output of IC1 will switch negative for a duration which is inversely proportional to the key velocity. During this time, capacitor C3 is discharged through VR2 to a voltage proportional to the key velocity. TR2, in switching on, charges C4 to a voltage governed by the charge on C3. The output is prevented from going positive at this time by D4 and D5. When the contacts remake, TR2 shuts off preventing any further charge on C4, whose voltage is now buffered by IC2 and passed to the out-
put. C3 now re-charges ready for the next cycle. This sequence of events is prevented from re-occuring when the key is released by the keyboard trigger signal, which inhibits IC 1 during this time. VRI sets up the key detection circuit, and should be adjusted so that the output of ICl gives a negative pulse when a key is pressed slowly. VR2 sets up the velocity/voltage relationship, and is adjusted to give the required output voltage range.
P. R. Williams,

Stevenage,
Herts.


## CALCULATOR-DECADE COUNTER



Aconventionally-built decade counter can be rather costly and complicated compared to a calculator with the same digit capability. However, by using the adding function, a calculator can be converted into a decade counter by entering the key sequence $1+, 1+, 1+, 1+, \ldots$ etc. electronically. The corresponding display will then be $1,2,3,4, \ldots$ etc. The given circuit is designed for adding on to the CBM SR 7919D calculator, with part of the calculator circuit board shown, showing the three take-off points required (Fig. 2).

The circuit essentially consists of a monostable multivibrator driving two switching circuits. This circuit operates on an independent supply and it is advised not to decouple the supply with an electrolytic capacitor. When the monostable receives an input, it generates an output pulse of a given duration. The leading edge of this pulse triggers the "I" input while the trailing edge triggers the "+" input; forming the " $1+$ " sequence for a single input.
The time delay of the monostable is made variable by VRI so that it can be set just above the minimum duration at which the calculator counter will operate. Even then the time delay (time between the "l" and "+" input) shows to be quite high which means that the maximum frequency input is low. In fact in my set-up, the maximum counting rate is only about 4 per second!

This counter can be used in applications where low and extremely low counting rate suffices when the time out signal feature of the calculator can be utilised.
For example, by feeding a crystal derived 1 Hz input, the counter can be used as a precision seconds timer. Similarly it can be used as a counter for registering the number of people entering a building, etc. by adding the resistor and switch as shown in dotted lines in the diagram.


Fig. 2
Furthermore the calculator-counter may be connected to a conventional counter to increase its counting capacity. In this way the inherent low counting rate of the calculator-counter can be compensated. For example, the unit digit of the calculator-counter will become the thousand digit if appropriately connected to a conventional 3 digit counter.

Pek Yaw Kee,
Sarawak,
East Malaysia.

## CMOS DOORBELL

THis i.c. doorbell is built around a CD4011 nand gate package, using only a handful of readily available components, and forms two oscillators.

The basic tone for the bell is produced by the high frequency oscillator (G3, G4, R2 and C2), and is interrupted by the low frequency oscillator (G1, G2, R1 and C1). These component values can of course be varied to produce different interrupt rates or basic tone.

The output is fed to a BC108 tran-
sistor which drives an $8 \Omega$ loudspeaker, and a PP3 battery can be used to power the circuit. The prototype was
housed in a plastics case measuring $116 \times 80 \times 35 \mathrm{~mm}$.
A. W. Cunningham, Glasgow.


## TRACE MULTIPLIER



Fig. 2

THis circuit, while using only three common cmos i.c.s allows four separate signals to be displayed on a single beam oscilloscope with independent vertical position and amplitude controls. Instead of using an internal free-running oscillator, this circuit employs the sweep output of the oscilloscope to trigger the switches and therefore displays the first trace on the whole of the screen before displaying
the second in the same way, the switch being triggered by the negative going pulse of the sawtooth wave from the sweep output as it moves back to the other side of the screen.

As the sweep output of most oscilloscopes often exceeds 50 V , a series capacitor, series resistor and potentiometer are used to modify the wave and trigger flip-flop. Four sequential outputs are obtained by using the $Q$
output of $F / F_{1}$, to trigger $F / F_{2}$, thus having $F / F_{2}$ run at half the frequency of $F / F_{1}$. Then the $Q$ and $\bar{Q}$ outputs of both flip-flops are connected to the inputs of four NOR gates as shown, whose output oily goes high when both inputs are low. The outputs of the four gates therefore go high in sequence and trigger the electronic switches which allow the four inputs into the oscilloscope one by one.

Each input, before reaching the switches must be attenuated to a useful amplitude relative to the others, and also must be given a different height on the screen to the other traces. This is achieved by the circuit in Fig. 2, which must be repeated for each input. For this reason, it is best to use preset potentiometers as eight normal potentiometers would cost more than the смоs circuitry itself.

It may be necessary to use a different capacitor on the input from the oscilloscope's sweep output, but this depends on the voltage range of the output. We have found that a $0.02 \mu \mathrm{~F} 200 \mathrm{~V}$ capacitor works admirably where the sweep output is 100 V peak-to-peak.
A. Hetherington

Nevilles Cross Durham.

## LED FLASHER



Flashing l.e.d. indicators are most often used to indicate a hazard. But there are times when a rather less insistant winking indication, would be appreciated, indicating to proceed at caution.

This circuit which uses an LM 3909N l.e.d. Flasher/Oscillator i.c. has a pulse frequency that can be varied by VR1, from 10-60 pulses per minute.

With the decay control VR2 set to zero, the time of each pulse of light from the l.e.d. corresponds to the discharge time of Cl , this discharge time can be lengthened, by increasing the resistance by means of VR2, between the positive of Cl and pin 2 of the i.c. So that each pulse of light fades away instead of switching abruptly "off".

Decay time of up to 1 second can be obtained with the components shown. It should be noted that a long decay is not possible with a fast frequency.
M. Miller,

Reading,
Berks.

# 12v CONVERTER for Weller TCP 1\&2 Irons D. J. BRADBURY 

WHEN working on an automobile electrical system it can be very useful to have a soldering iron that will operate from the vehicle's battery. This relieves both the inconvenience and possible hazards involved in arranging external supplies such as a main extension lead.
The unit described in this article is a 12 V to 24 V d.c. converter designed to supply Welier TCP1 and TCP2 temperature controlled soldering irons from a standard car battery. The converter is housed in a small die-cast box which will also serve as a stand for the iron when it is not in use.

## CIRCUIT DESCRIPTION

A 24 V Weller iron consists of a 45 to 48 W heating element in series with a magnetic "Curie" effect temperature switch. Because of this switch the iron only takes current when the soldering bit falls below its correct operating temperature. Thus to work efficiently the converter circuit must be able to supply 50W to the soldering iron and yet take minimal quiescent current when its load is open circuit.

The circuit shown in Fig. 1 achieves both these aims, operating at an efficiency of over 80 per cent whilst the iron is warming up and taking only micro-amps when the iron reaches its correct temperature.

When the circuit is connected to a battery and the soldering iron plugged in, bias current is fed to the base of TR1 via the transformer T1, diodes D1, D2 and the iron. This starts to turn TR1 on, causing its collector voltage to fall, enabling the transformer to increase the voltage applied to the load. The load thus biases TR1 on harder until the transistor finally saturates, giving a peak voltage of approximately 28 V across the soldering iron.

While the transformer has a voltage across its windings, a magnetising current slowly builds up until the ferrite transformer core is saturated; it then rises rapidly as the core loses its high relative permeability. When the collector current of TR1 reaches about 10A, the transistor no longer receives sufficient base drive to remain saturated and so its collector voltage begins to rise. This causes the load voltage to fall, reducing the base drive of TR1 which further reduces the load voltage and by this feedback action the transistor is turned off.
When TR1 is turned off, its collector voltage rises rapidly above the supply due to the magnetising current flowing in the transformer. Also the output voltage of the transformer goes negative, reverse biasing diodes D1 and D2, so isolating the transformer from the load. The magnetising current causes the unloaded transformer to 'ring' for half a cycle at a frequency mainly determined by its inductance and the value of C 1 . The high voltage half cycle 'ring' reverses the flow of the magnetising current and as a result defines the length of time for which TR1 does not conduct.

This period must be kept short if the converter is to be efficient. However, the shorter the time the higher the voltage required across $T 1$ to reverse the magnetising current. Since part of this voltage appears across TR1 a compromise was chosen for the converter that gives the

## COMPONENTS

## Resistors

R1

## Capacitors

C1
C2
Semiconductors
D1, D2
D3
ZS271 (2 off)
TR1
BD243B

## Transformers

Length of 22 s.w.g. enamel covered copper wire Bobbin (type DT2205, ITT Part No 52352E)
Ferrite pot core Itype FX 2241, ITT Part No 52707BI

## Miscellansous

* 4 mm banana sockets (2 off)
*3 pin 1.5A Bulgin P430 socket
Diecast box $120 \times 95 \times 30 \mathrm{~mm}$
Support spring (Weller SK 185, ITT Part No 66304X) Funnel (Weller SK 1527, ITT Part No 18668E) FS1 Fuseholder Bulgin F55 with 5A fuse
*See text



Fig. 1. Circuit diagram of the Converter

Fig. 2. Printed circuit board design
transistor a peak collector-emitter voltage of 55 V during its off period which lasts for about 25 per cent of the full oscillation cycle.

When the magnetising current flowing in T 1 is finally reversed, the output voltage of the transformer swings positive and turns TR1 on again thus starting another cycle. The converter continues to oscillate in this manner, feeding 24 V r.m.s. to the soldering iron until its working temperature is reached and the magnetic switch opens.

As the bias for TR1 is supplied through the load, when the temperature switch opens the converter ceases to oscillate. In this condition the only current taken by the circuit is the leakage in TR1, C2 and D3, the sum of which is very small.

When the iron cools, the magnetic switch will close, and so the converter will reheat the bit. The unit will thus operate intermittently, maintaining the soldering iron at its correct working temperature.

The diode D3 has been included in, the circuit to protect the converter against reverse polarity connection to the battery. Normally the diode is reverse biased and so it draws little current from the circuit. However, if the supply connections are accidentally reversed the diode will take a heavy current and blow the 5A fuse before the converter circuitry is damaged. Although a series diode would have the advantage of saving the protection fuse if the battery polarity is wrong, it was not used in this case as it would have to pass a current of 5A under normal operating conditions and so for a silicon device would nearly double the power losses of the converter.

## CONSTRUCTION

The prototype was constructed using the printed circuit board layout in Fig. 3 with the component overlay shown in


Fig. 3. Component layout and wiring diagram

Fig. 4. If the printed circuit board is to be traced, constructors should remember that the copper tracks will carry high currents and so it is recommended that the diagram is followed closely, keeping the tracks as wide as possible.

Once the board has been etched, cleaned and drilled, the components and lead out wires may be soldered. The supply input leads should be fitted with large crocodile clips for making connections to the battery. Although the power dissipation in TR1 is not very high, it is necessary to mount the transistor on the converter box to keep the device within its temperature ratings. To facilitate this, the transistor is soldered to the back of the printed circuit board with its leads left long so that they can span the gap between the box and the circuit.

The transformer for the converter consists of a 39 turn coil wound with 0.71 mm dia. ( $22 \mathrm{~s} . \mathrm{w} . \mathrm{g}$.) enamel covered copper wire mounted inside a 30 mm dia. ferrite pot core. The coil should be neatly wound in layers on the plastic bobbin with a tapping taken 15 turns from the start of the winding. As the converter oscillates at a frequency of about 4 kHz , the transformer can be the source of an annoying audible whistle. To minimise this, when assembling the pot core halves smear a thin layer of grease on the mating surfaces to dampen core vibrations.

The transformer is fastened to the converter box using a 1 in . long countersunk 2BA bolt with nut and spring washer. The spring washer should be placed between the box and the transformer as in this position it acts as a vibration insulator. Do not overtighten the retaining nut as no advantage is gained by doing so and it could result in breaking the brittle ferrite cores.

The $120 \times 95 \times 30 \mathrm{~mm}$ die-cast alloy box used to house


Fig. 4. Case drilling and mounting details

the converter was drilled to the dimensions shown in Fig. 4. Since the two Weller irons have different connector arrangements the diagram includes the holes for both types of sockets.

The position of the hole to mount TR1 is not given' in the diagram as it will depend on how the transistor was soldered to the printed circuit. The correct location can be found by placing the component board in position inside the box and marking the hole through the transistor tab after having checked that the device is lying flat. When the transistor is mounted it should be electrically insulated from the box by using a mica washer and plastic bush.

## TESTING

Having completed the converter and checked its wiring, plug in the soldering iron and connect the unit to a 12 V battery, being careful to observe the correct polarity. The converter should emit a soft whistling sound and the following voltages may be measured from the negative end of C2 using a moving coil voltmeter. TR 1 collector +12 V , TR1 base +0.8 V , soldering iron output +20 V .

If there are any serious discrepancies in the voltage readings, disconnect the battery and recheck the converter for faulty components, dry joints and wiring errors. In cases where the converter cannot be heard to oscillate and all the voltage readings are zero, check the fuse and the battery polarity. Never use fuses rated at more than 5A or those with "slow blow" characteristics as they may result in damage to D3 or TR 1 under fault conditions.

If the converter is operating correctly it should oscillate for between 1 and $1 \frac{1}{2}$ minutes from switch-on, after which time the soldering iron will have reached its working temperature and the soft whistle will cease. The converter may be occasionally heard as it reheats the bit.

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# Semiconductor UPDATHzom FEATURING: mo4301 AD3500 

## ALARMING DEVELOPMENT

One of the problems of using modern large scale integrated circuits is that we, the users, have become obliged to surrender a lot of our design independence to the semiconductor manufacturers.

To ensure volume sales of their chips, the manufacturers tend to build devices which are aimed at the widest possible market, and we, as willing customers, have had to forego some of the "oddball" applications that we could indulge in when using discrete transistors and individual amplifiers or logic gates.

Of course, a ray of hope is visible in the shape of the microprocessor, but unless you happen to be a confirmed "Micro-nut" there are still many fearsome obstacles to deflect you from that path, not least the cost of it all.

When you think about it, the bastions of the LSI industry are calculators, clocks, microprocessors, voltmeters, and TV games, and precious little else. Not exactly a fertile ground for the amateur who wants the benefits of LSI but who can cope with only so many variations on the digital clock theme before trading the whole lot in for a pair of beam-tetrodes and a 50-Watt soldering ir on!

The point I am trying to make is that any original large scale integrated circuits that do turn up deserve a rousing welcome from our beleaguered band, and this month I have managed to turn up a beauty!

I refer to the MD4301, from the Canadian firm of Mitel, (no-don't groan, you can get them from Rastra Electronics Limited!), which is unassumingly labelled a "CMOS Alarm Circuit." Inside its tiny fourteen pin plastic package, the MD4301 has an amazing array of odds and ends which enables it to act as, among other things, a complete fire alarm system with all the practical bits and pieces you would design in yourself, given the chance.

There is a comparator circuit, which will trigger an alarm condition for a 150 mV voltage differential between its two inputs.

There is an audio oscillator which is turned on by an alarm condition, a 300 mA switch output to drive a horn or siren, and a
separate pulsed l.e.d. driver for local poweron indication.

There is also a "low-battery" detector which gives an unobtrusive but hard to ignore output of 50 milliseconds each minute on all alarm outputs when the battery voltage gets too low for comfort, (which shouldn't be too often, because the standby battery drain is a miserly 25 microamps!)

There is an alarm trigger "in" and "out" which allows any number of MD4301 circuits to be cascaded so that any one active unit can trigger all the others for a full scale fire alert, and other variations and options too numerous to mention.

To use the MD4301 in a self contained fire alarm system, you need only hook it up to an ion chamber smoke detector cell, a nine volt battery, a horn or siren and a few discrete components. A much more exciting and useful prospect than building yet another digital clock!

## $3 \frac{1}{2}$ IS TWICE AS GOOD AS 3

Some time ago 1 featured a 3 digit single chip voltmeter device in this column, and suggested that the traditional moving coil meters with their inherent fragility and accuracy limitations would soon be losing ground to the newcomer. A new chip from National brings the demise of springs and jewelled bearing even closer. They have managed to cram a complete $3 \frac{1}{2}$ digit voltmeter system on to a single LSI chip, a system which up to now had taken at least two chips, and usually many more, to implement.

A $3 \frac{1}{2}$ digit display is actually twice as
good as a 3 digit display because you get a full scale output of 1999 instead of 999, double the dynamic range and precision of earlier single chip systems.

The new chip is coded AD3500 and it contains not only the digital conversion circuitry but also the l.e.d. segment drivers, the system clock and the analogue "front end".

The chip uses low consumption CMOS technology and runs from a single plus five volt supply. so building a digital multimeter with the AD3500 should be child's play!

To build a complete voltmeter all you need to add lapart from some kind of power source) is an l.e.d. display, a low cost 75492 digit driver, a voltage reference, and a handful of discrete resistors and capacitors-the whole thing could be fitted on to a circuit board about 8 cm square.

The addition of a range switch and a few extra resistors would allow a full complement of voltage and current ranges, and with a constant current source added, resistance ranges would be possible.

If you wanted to be really cunning, you could use the over-range outputs provided by the AD3500 to control an auto-ranging system which did the range changing for you using reed relays or solid state analogue switches, very useful for handsfree trouble shooting!

In terms of accuracy, the new device beats the moving coil meter hands down with a $\pm 0.05$ per cent specification. You will probably degrade that figure with your external components of course, but the traditional meter is hard pressed to deliver even 2 per cent accuracy!



## The Pay-Off

Further to my recent reports on overseas investment, especially in the United States, it is worthy of note that Racal has now formed a Data Communications Group headed up by D. Leighton Davies. Leighton, a B.Sc.(Hons) graduate from the University of Wales, was ten years with Solartron and in senior positions with GEC Computers and Automation, and STC, before becoming managing director of Racal-Milgo Ltd in 1969, then a tiny UK operation owned 50/50 by Racal in the UK and Milgo Inc in the USA. It was virtually a marketing company set up to exploit Milgo products outside the Americas.

First-year turnover was in the tens of thousands of pounds. Now, less than ten years later, Leighton heads up a wholly British-controlled data communications group of world standing with a predicted £75 million profitable turnover in the current financial year and operating from manufacturing, systems engineering and marketing bases in Reading, Frankfurt, Tokyo, Miami and Sunnyvale, and through an associated company in Rio de Janeiro and Brazilia. The present huge boost in turnover came from Racal's recent acquisition of Milgo Inc and Vadic Inc in the USA and further acquisitions are being sought.

Just one example explains what overseas investment is all about. Racal-Milgo Inc, Miami, Florida, has just signed a contract with Eastern Airlines for a System 200 Management Centre to streamline Eastern's data network covering 300 locations in North America and the Caribbean. As well as the Management Centre (which monitors and controls a network of up to 16,256 communications devices and modems on as many as 64
channels) the Eastern Airlines contract includes orders for hundreds of high-speed modems. No British company confined to a UK base would have been considered for such a contract, let alone win it. The Eastern Aidines contract plus many others in the US is a pay-off for Britain.

It works in reverse, too. North Thames Gas would not have bought $£ 120,000$ of equipment to improve its data links from a company based solely in Miami, Florida. But North Thames Gas were very happy to contract with Reading-based Racal-Milgo with its full local engineering support, even though a percentage of the equipment is built by the now British-owned Racal-Milgo in the USA. For this type of business you need to be on the spot.

## Outlook

Public opinion polls are not invariably reliable but a recent poll of electronics goods manufacturers has shown a downturn in optimism (as distinct from downright pessimism) on future prospects, especially in exports in the capital goods sector where a number of respondents are clearly suffering the effects of stronger foreign competition. Paradoxically, except for the hard-pressed consumer sector, the great bulk of returns showed increased business and general buoyancy. Best sector of all is automatic test equipment (ATE) with a boom in the home market, perhaps because of the shortage of good test engineers as well as their cost, and of course it is much easier to introduce laboursaving equipment into the progressive electronics industry than into moribund industries where job protection is a worker obsession, perpetuating out-moded processes.

Apropos my remarks in earlier paragraphs on overseas investment, there is still plenty of movement at home too. Racal, already mentioned, is spending $£ 1.5$ million on microelectronics development, Pye has just completed a $£ 5$ million complex housing 1,200 workers for mobile radio, Decca Communications has acquired the whole of the h.f. radio business of Granger Associates of California, and English Electric Valve has added 100,000 sq. ft. of manufacturing space through purchase of the former Marconi-Elliott i.c. plant at Witham, Essex.

## Poland Buys British

The controversial Polish order for British shipyards, heavily subsidised by the taxpayer, has turned up trumps for Redifon who landed the contract for all the electronics packages on the 22 ships involved. Redifon has undertaken to plan and commission all the equipment, together worth many millions. For starters Redifon will supply 44 of their new R1000 Series communications receivers worth,
possibly, $£ 5,000$ or more each, and this is just the fringe.

Decca Marine has also done well in the Polish market recently by contracting £ 330,000 worth of Decca Navigator receivers and plotters to the Polish fishing fleet operating in the Baltic. There are now some 500 Polish ships fitted with the Navigator. The Japanese have also recently contracted to expand their Decca Navigator coverage with new chain stations so that all four major Japanese ports will now have Decca coverage.

## Employment

I note that the IEE Annual Report comments on the scale of recruitment advertising in the past year, so high that special recruitment supplements had to be issued with the Institution journals. Anticipated revenues from situations vacant adverts were handsomely exceeded and a total of $£ 1,000$ a day was received in the year from this source.

The shortage of engineers is tending to push up salaries and I notice far more firms offering posts in the $£ 5,000-£ 7,000$ bracket than formerly. But is this enough when one reads that a London bus driver can make $£ 6,000$ a year plus perks like free travel? Glancing through the ads the cash plums are still overseas provided you can put up with the climate and general conditions. Up to $£ 21,000$ in Libya, $£ 20,000$ in Iran, $£ 18,000$ (tax free) in Saudi Arabia.

Such salaries are only for senior people with long experience on major products but even scaled down to average engineer level it seems overseas salaries in the Middle East are still more than twice that which can be earned at home and nearly all carry tax concessions.

More of the gentle sex are entering engineering but it is still a mere trickle. Of 190,000 chartered engineers registered in the UK there are only 200 women, less than half of whom work in industry. Of the women I note that the three senior ladies on the secretariat of the Electronic Engineering Association are all designated in the Annual Report as Ms whereas none of the men, not even the Director, is dignified by Mr.

## More on Mats

The French company Thomson-CSF is also in the field of mobile automatic telephones (MATS) and claims to have 8,000 subscribers world-wide using their equipment linked to public or private telephone exchanges. Their new microprocessor-controlled car telephone has a capacity of 16 pre-programmable numbers of up to 24 digits each. There is clearly going to be keen competition for this fast-growing business.



#### Abstract

Appearing every two months, Micro-Bus will present ideas, applications, and programs for the most popular microprocessors; ones that you are uniikely to find in the manufacturers' data books. The most original ideas will probably come from readers working on their own microcomputer systems, and payment will be made for any contribution featured here. This is also the place to air your views, in general, on this new technology, so let's be hearing from you!


## SCOPE FOR GRAPHICS

0SCILLOSCOPES were used for troubleshooting with the earliest computers, and so it is hardly surprising that they soon became permanently connected as a convenient way of displaying drawings produced under computer control. Since then the CRT has remained the most widely used peripheral for high-speed graphical output.

An oscilloscope can only display a single point at a time, so to present a line or a picture it is necessary to refresh the scope by repeating the same sequence of movements of the spot over and over again; at least 30 times a second if the picture is to be seen without flicker. There are two main ways of controlling the spot from a computer, and both will be covered here. One is to store the coordinates of every point to be displayed in memory, and use these to drive the display. The other method is to generate the individual point co-ordinates as they are to be displayed from a "display file" which contains shorthand commands for drawing lines, circles, characters, etc. A program illustrating


Fig. 1. Block diagram showing the circuitry needed for a microprocessor-controlled graphics display using an oscilloscope.
this approach will show how lines can be generated given the co-ordinates of their endpoints.

## SCOPE INTERFACE

The configuration needed to control a CRT from a computer is shown in Fig. 1. The two D/A converters determine the number of possible point positions; with two 8 -bit D/A converters one can display any point on a 256 $\times 256$ grid. The outputs of the D/A converters drive the $X$ and $Y$ inputs of the scope, and these should be d.c. coupled. Since there is no need for timebase circuitry or for sensitive input amplifiers it should be possible to construct a suitable display quite simply. The prototype used two Ferranti ZN425E 8-bit D/As, and these were interfaced to the microprocessor by two 8 -bit latches and some addressing circuitry; alternatively a MC6820 PIA could have been used.

The program in Fig. 2, which is for the M6800 micro, illustrates the method of generating a display from the co-ordinates of


Fig. 2. Program for the M6800 which displays co-ordinate pairs from memory.
every spot position. BUFFER is the address of the list of co-ordinates, and with the list shown the program will display a short diagonal line from $(0,0)$ to $(4,4)$. In this and the next program the D/As are assumed to be located at addresses $\$ 8100$ and $\$ 8101$. If a

PIA is used to interface them with the micro a few instructions will have to be added at the start of the programs to configure it correctly.

## INTERPOLATION PROGRAM

This method of storing the co-ordinates of every spot position really eats up memory; to draw a square border around the screen would require about 2 k bytes of memory ( $1,022 \mathrm{co}$ ordinate pairs). It has the advantage that any arbitrary drawing can be displayed; however most graphs and diagrams can be broken down into a number of straight line segments, so a far more economical approach is to store only the co-ordinates of the endpoints of the lines in memory, and generate the points in between as they are needed. Lines other than diagonals or horizontals and verticals cannot be exactly represented on a square lattice, so the program must generate the best approximation.

The program of Fig. 3 does just this-given the two endpoints of a line it generates the intermediate points. It has the feature that it needs no division or multiplication operations, so it can be used to generate fairly complex displays in real time. With the co-ordinate pairs shown at FIRST the program will draw a triangle between the points $(0,0),(7,3)$, and $(0,6)$. The first side of this triangle is shown in detail in Fig. 4: the approximated line is composed of three diagonal moves, $(1,1)$, and four horizontal moves, $(1,0)$.

## PROGRAM FLOWCHART

The flowchart of Fig. 5 shows how the program works, and will help those unfamiliar with the M6800 to implement the algorithm on another micro. First of all the two unit vectors DIAGAB and SINGAB are calculated; some combination of these will make up the required approximation to the line. Each point of the line is generated from the previous point by adding either the diagonal move vector, DIAGAB, or the horizontal/vertical move vector, SINGAB. The choice of which to add is determined by the test variable TEST.

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|  | 8101 | Yaxis | Equ |  | \＄8101 | PORTS | 0045 | 97 | 07 |  | STA | A | TEST＊1 |  |  |
|  |  |  |  |  |  |  | 0050 |  | 00 |  | 5.36 | － | 10 |  |  |
| 0000 | 0002 | NEWPR | RMB |  | 2 | POINTS TO COORDINATE PAIR | 0052 | D7 | 06 |  | 5 TA |  | TEST |  |  |
| 0002 | 0002 | DIAGAB | RMB |  | 2 | DIAGONAL MOVE | 0054 | A6 | 00 |  | LDA | A | 0， x |  |  |
| 0004 | 0002 | SINGAB | RMB |  | 2 | SINGLE MOVE | 0056 | E6 | $0:$ |  | LDA | B | 1，$\times$ |  |  |
| 0006 | 0002 | test | RMb |  | 2 |  | 0058 | 7D | 0006 | TESTM | TST |  | TEST | UHAT MO | OVE？ |
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| 0009 | 0001 | DOUBLE | RMB |  | 1 | no．of double moves | 005 F |  | 05 | SInglm | ADO | A | SINGAB |  |  |
| ODOA | 0001 | SINGLE | RMB |  | 1 | NO．OF SINGLE MOUES | 0061 |  | S |  | ADD | B | SINGAB +1 |  |  |
|  |  | ＊ |  |  |  |  | 0062 | 36 96 |  |  | PSt | A |  |  |  |
| 0008 | CE 0098 | Entry | LDX |  | EFIRST |  | 0064 | 95 | 0 |  | LDA | A | TEST＋1 |  |  |
| OODE | DF 00 | NEULIN | STX |  | NEUPR |  | 0066 | 97 | 07 |  | ADD | A | DOUBLE |  |  |
| 0010 | CE 0101 |  | LDX |  | Cs 0101 |  | 0068 |  | 06 |  | LDA | A | TEST＋1 |  |  |
| 0013 | DF 02 |  | STX |  | diagab |  | 006 A | 89 | 00 |  | ADC | A | \＆ 0 |  |  |
| 0015 | CE 0000 |  | LDX |  | 530 |  | $006 C$ | 97 | 06 |  | STA | A | TEST |  |  |
| 0018 | DF 04 |  | STX |  | SINGAB |  | 006 E | 32 |  |  | PUL | A |  |  |  |
| $001{ }^{\text {d }}$ | DE 00 |  | LDX |  | NEWPR |  | 006 F |  | 12 |  | Bra |  | OUT |  |  |
| 001 C | A6 02 |  | LDA | A | 2．x | NEXT A，B PAIR | 0071 | 98 | 02 | DOUBLM | ADD | A | diagab |  |  |
| 001 E | E6 03 |  | LDA | 日 | 3．x | Of COORDINATES | 0073 | D8 | 03 |  | ADD | B | diagab＋1 |  |  |
| 0020 | A0 00 |  | SUB | A | 0，x |  | 0075 | 36 |  |  | PSH | A |  |  |  |
| 0022 | 2404 |  | BCC |  | APOS |  | 0076 |  | 07 |  | LDA | A | TEST＋1 |  |  |
| 0024 | 00 70 0 |  | NEG | A |  |  | 0078 |  | OA |  | Sue | A | SINGLE |  |  |
| 0025 | $\begin{array}{ll}70 \\ \text { E0 } & 01\end{array}$ |  | NEG |  | Diagab |  | 0078 |  | 07 |  | STA | A | TEST＋1 |  |  |
| 0028 | $\begin{array}{ll}\text { E } & 01 \\ 24 & 04\end{array}$ | APO5 | SUB | 8 | lex |  | 007 C | 96 | 06 |  | LDA | A | TEST |  |  |
| 002 C | 50 |  | NEG | 8 |  |  | ODTE | 82 | 00 |  | 5 SC | A | $\Sigma 0$ |  |  |
| 002 D | 700003 |  | NEG |  | DIAGAB ${ }^{1}$ |  | 0080 | 37 | 06 |  | STA | A | TES T |  |  |
| 0030 | 1！ | BPOS | CBA |  |  |  | 0083 | 87 | 8100 |  | STA | A |  |  |  |
| 0031 | 2409 |  | BCC |  | AGEB |  | 0086 | F7 | 8101 | Out | STA | ${ }_{\text {A }}$ | yaxis | display | Y POINT |
| 0033 | 36 |  | PSH | A |  |  | 0089 |  | 0008 |  | STA | B | YAXIS |  |  |
| 0034 | 9603 |  | LDA | A | D1AGAB＋1 |  | 008 C |  | CA |  | BNE |  | TOTAL |  |  |
| 0036 | 9705 |  | STA | A | SINGAB＋1 |  | OOBE | 08 |  |  | INX |  | TESTM |  |  |
| 0038 | 17 |  | TBA |  |  |  | 0085 | 0 B |  |  | INX |  |  |  |  |
| 0039 | $\begin{array}{lll}33 & \\ 20 & 06\end{array}$ |  | PUL | B |  |  | 0090 | 8 C | 00A3 |  | CPX |  | Elast |  |  |
| 003 A | 20 36 |  | 明A |  | CONT |  | 0093 | 27 | 03 |  | aEd |  | BACK |  |  |
| 0030 | 9602 | AGES | PDA | A | diagab |  | 0095 | 7 E | O00E |  | JMP |  | NEVL IN |  |  |
| 003 F | 9704 |  | STA | A | Singab |  | 0098 | 7 F | 0008 | BACK | JMP |  | Entry |  |  |
| 0041 | 32 |  | PUL | A |  |  |  |  |  |  |  |  |  |  |  |
| 0042 | 9708 | CONT | STA | A | total |  | 0098 |  |  | FIRST | A ${ }^{\text {a }}$ | TR1 | ANGLE＊ |  |  |
| 0044 | D7 09 |  | STA | 8 | DOUBLE |  | 0098 | OOA |  | last | EQU |  | 0，0．7．3， | ，0，0．0 |  |
| 0046 | 10 |  | SBA |  |  |  |  |  |  | ${ }_{*}$ LAS | Eav |  |  |  |  |
| 0047 | 97 0A |  | STA | A | SINGLE |  |  |  |  |  |  |  |  |  |  |
| 0049 | 18 |  | ABA |  |  |  |  |  |  |  | END |  |  |  |  |

Fig．3．Program for the M6800 to display lines from the co－ordinates of their endpoints．

The photograph of Fig． 6 shows a pen－ tagram drawn by specifying the five corner co－ ordinates twice each in the correct order，and illustrates the maximum complexity that can be achieved before flicker becomes intolerable． It is also possible to produce moving displays by inserting instructions at BACK to alter the co－ordinates in the＂display file＂between each sweep of the drawing．With slight modification the program could be used to calculate the path of a moving object，such as a tennis ball in a microprocessor－controlled game．

## PROGRAMMING SWITCHES

Sometimes within a program one wants to jump to one of several addresses depending on the value of a variable．For example，a program might perform a different function depending on a number typed in or a key pressed；the＂switch＂routine would then cause a jump to the relevant section of code． In a high－level language like Basic or Fortran this function is achieved by a statement like：

GO TO N OF 50，20，45，77， 12


Fig．4．Points generated by the in－ terpolation program of Fig． 3 for the line joining $(0,0)$ to $(7,3)$ ．The initial values of the variables used by the program are shown．
where $50,20,45$ ，etc．are labels in the program which will be jumped to if $\mathrm{N}=1,2,3$ ， etc．This can be implemented in machine code on a micro by means of a＂jump table＂con－ taining the addresses of the various different routines．

The great variety of branch－on－condition instructions available in the M6800 micro makes possible the unusually compact ＂switch＂shown in Fig．7．LABEL 0 to


Fig．5．Flowchart showing the operation of the main section of the interpolation program in Fig． 3.


Fig．6．Pentagram dieplay generated using the interpolation program described in this article．

|  |  |  |  | NAM |  | SW1TCh |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0000 | 86 | 0100 | Switch | LDA | A | N |  |
| 0003 | 06 |  |  | tap |  |  | put a in status |
| 0004 | 25 | 08 |  | 日Cs |  | xXI |  |
| 0006 | 2 E | OE | xx0 | BGT |  | Labelo |  |
| 0008 | 26 | 2 C |  | BNE |  | Labela |  |
| D00A | 28 | 4A |  | Buc |  | Labela |  |
| 000 C | 20 | 68 |  | Bra |  | LABEL6 |  |
| O00E | 2 E | 16 | xxı | EGT |  | labeli |  |
| 0010 | 26 | 34 |  | BNE |  | LABEL3 |  |
| 0012 | 2 B | 52 |  | BvC |  | labels |  |
| 0014 | 20 | 70 |  | BRA |  | Label 7 |  |

Fig．7．Eight－way switch for the M6800，achieved by putting the control variable into the status register and testing its effect on the condition codes．
LABEL 7 are the eight addresses which are branched to for values of N from 0 to 7 respectively，giving an 8 －way switch．In this example the addresses are assumed to be $\$ 10$ bytes apart at $\$ 0016, \$ 0026, \$ 0036$ ，etc．

# Coustaut Display FREQUENGY METER 



# J. BECKER 

AN ELEMENTARY
PIECE OF WORKSHOP OR LABORATORY EQUIPMENT

[T is often undesirable to engage an oscilloscope or other versatile apparatus to merely monitor frequency.-and to this end the "Constant Display Frequency Counter" specification has been kept simple. The unit is not crystal controlled. but uses an clementary low frequency oscillator which necessitates far less dividing down circuitry, the whole unit thus comprising just a few readily available components. This would be an encouraging project for the beginner

## CLOCK GENERATOR

The 1 Hz clock signal is produced by IC Ia (see Fig. 1), whose rate depends upon C1, VR1 and R1. The pulse width generated is about 45 ns. and this enables the system to "read" the count almost instantancously.

The monosta力le IC ib is repctitively triggered by the output from the multivibrator to produce pulses of about 70 ns . controlled by C2 and R2, and which in fact nccurs fractionally afterwards due to propogation delay in the i.c. of about 20 ns . This second pulse is used to reset the counter at the end of each cycle.

## DECADE COUNTERS AND LATCHES

The input pulses to be counted are amplified and shaped by TR1 and fed to a series of decade counters (IC2-6), each of which successively divides the signal by 10 . Each group of four outputs from each of the decade counters is taken. BCD fashion. to the Data inputs of the quad-latch i.c.s. IC7-11.

When the clock inputs of the latch i.c.s are held at a logic zero, the outputs remain in their previous-logical states. but at the clock input positive excursion to logic I. the ouputs follow the logic states of the Data inputs. Consequently the outputs

From IC 2-6 will effectively be ignored by IC7-11 until they receive the "read" pulse from IC la, at which time, and for the curation of the pulse, the latch outputs will follow the logical states of the decade counters. Upon cessation of the "read" fulse the gate outputs will hold their new logical states and ignore further changes at their Data inputs-until they receive t.ee next "read" pulse!

## DECIMAL DECODERS AND DISPLAYS

Following the latches. IC 12-16 are decoders providing rutputs suitable for the numeric displays X1-5. The outputs from the decoders logically follow the outputs of IC7-11 and will only change state when these change state. thus each indicator will continue to display a particular number until the latches alter outputs.

The decade counters are reset at the start of each cycle by the arrival of the pulse from IC Ib approximately 20 ns after the "read" pulse from IC Ia. The result is that if the same number of pulses is counted by a particular decade stage in two or more cmsecutive sampling cycles, then the relevant display will not c lange its number, and will only do so if a consecutive count is d.ficrent from the previous one. Thus if the frequency stays the same the display will appear to be static even though the sampling is still continuing once a second.

## POWER SUPPLY

The a.c. voltage from transformer T 1 is rectified by D1-4. and the d.c. ripple is reduced by C4. IC 17 reduces and stabilises the main power rail 10 a nominal 5 V . d.c. It is internally pontected against thermal or current overloading, and if oycrloaded will automatically shut itself off until the cause is


Fig. 1. Full circuit diagram of Frequency Counter

removed, whereupon it will revert to normal operation. In this particular unit it is essential that it is bolted to the metal case in order to provide an adequate heat sink, as it is being used close to its specified limits. The displays have an acceptable voltage range of 2.2 V to 3.4 V , and are driven from the regulated supply via three IN 4001 diodes, each of which reduce the voltage by about 0.7 V to provide a supply line of about 3 V . Because of the high currents flowing (nearly 1A), the power line connections between the p.c.b., IC17 and C4 must be made using fairly heavy duty wire, and only in the manner shown. Failure to do so could result in unreliable performance of the counter, and "jittery" triggering of the clock generator.

## CONSTRUCTION

The original front panel is re-mounted internally on two blocks of wood fixed to the side panels, and with the p.c.b. mounted on it. This assembly is set back just enough for the displays to level with the front panel mounting-flange of the box. A clear perspex sheet is used as the new front panel, and should be painted on the inside with emulsion (white on the prototype), after masking off the viewing window and lightly sandpapering the remaining surface area.

The mains switch, panel mounting fuse holder, transformer and reservoir capacitor are mounted in the rear section of the box. It is also advantageous to fix a tag strip on the back panel

Fig. 2. Printed circuit board full size


The original front panel is re-mounted internally in the box to provide a base on which to mount the p.c.b. A new front panel is them made of clear perspex and painted, leaving a display window.



Fig. 3. Component layout (not full size)

along with switch and fuseholder, to facilitate the necessary low tension wiring junctions.

The p.c.b. is shown in Fig. 2, and the component layout of this is shown in Fig. 3.

## SETTING-UP

Following the usual checks for short circuits between tracks, preferably with a magnifying glass, and for possible assembly errors, the unit should be switched on and allowed to warm up for a few minutes. Then a known frequency can be fed in, and VR1 adjusted so that the display read-out is correct for that frequency. A hole is provided in the case immediately below VR1 to provide screwdriver access. In the absence of a good signal generator, the 440 Hz of "Concert A" of a musical instrument, or the note of a mechanical tuning fork of the same frequency, can be fed into the counter via a microphone and suitable preamplifier. Alternatively the residual 100 Hz ripple on C 4 can be fed in, and a switch is included to enable this, but the

## COMPONENTS . . .

## Resistor 3

| Resistor |  |
| :--- | :--- |
| R1 | $39 \mathrm{k} \Omega$ |
| R2 | $5.6 \mathrm{k} \Omega$ |
| R3 | $100 \mathrm{k} \Omega$ |
| R4-RG | $1 \mathrm{k} \Omega \quad$ (3 off) |

All resistcrs $\frac{1}{4} \mathrm{~W} 5 \%$ carbon film


| Semiconductors |  |
| :---: | :---: |
| IC1 | 74123 |
| IC2-IC6 | 7490 ( 5 off) |
| 1C7-IC11 | 7475 (5 off) |
| IC12-IC16 | 7447 (5 off) |
| IC17 | 78055 V 1 A regulator |
| TR1 | BC108 or BC109 |
| X $1-\times 5$ | EP27 7-seg display (5 off) |
| D1-D4 | 50 V 1 A rectifier stack |
| D5-D7 | IN4001 |
| Miscellaneaus |  |
| S2 | S.P.D.T. toggle switch |
| S 1 | D.P.S.T. toggle switch (mains) |
| F1 | 1 amp fuse and holder |
| SK1-2 | 2 mm sockets (red and green) |
| T1 0-4.5V 0-4.5V 20VA transformer (Type207-122) |  |
|  |  |
| Printed circuit board, capacitor clip for C4, tag strip, probe |  |
| leads, metal case (RS 509888) |  |
| All components | including p.c.b. are available |
| Phonosonics |  |

S2
S1
SK 1-2
207-1221

Printed circuit board, capacitor clip for C4, tag strip, probe All components including p.c.b. are available from Phonosonics


Fig. 4. Wiring diagram. Use either double wires or heavy gauge wire when connecting up C4
higher the calibration frequency the greater the potential accuracy. However, beware that many oscillators can be far less stable than the counter; indeed with the prototype the most stable oscillator tested was the Tone Generator of P.E. Joanna, but even that was found to drift when checked against a mechanical tuning fork.

## ALTERNATIVE SAMPLING RATES

If a sampling rate other than 1 Hz is required, Cl may be increased or decreased without limit, but the resistance total of R1 and VR1 must be kept within the limiting range of $5 \mathrm{k} \Omega$ to $50 \mathrm{k} \Omega$ unless additional circuitry is used. Also if an external potentiometer is contemplated in place of VR 1, the fixed resistor (of at least $5 \mathrm{k} \Omega$ ) must also be kept physically close to IC 1.

The clock rate frequency to be expected from changes to C 1 may be approximated by assuming that doubling the capacitance will double the frequency count, and halving it will halve the count. However, in practise the actual count time is
likely to vary from that calculated due to component tolerances and capacitor leakage currents, especially with electrolytic capacitors. In respect of tolerances, the actual values obtained from a nominal value can vary by as much as $\pm 5 \%$ for R 1 , $\pm 20 \%$ for VR1, and $-10 \%$ to $+50 \%$ for C1. Capacitor leakage currents will depend, of course, upon the specific types used.

## STABILITY

The unit was checked by the author against the frequencies generated in the P.E. Joanna, which itself was kept in tune against a mechanical tuning fork, and after the initial warm-up period the counter was found to be stable within about 1 pulse per thousand, although the clock rate of IC 1a was found to be slightly dependent upon ambient temperature. However, although a more accurate clock pulse might be achieved by making use of the 100 Hz ripple available from C 4 , it was felt that the extra components required did not justify the marginal improvement.

## PoInts ailsine

## LINEAR CAPACITANCE METER (June 1978)

Note that in Fig. 2, resistor R9 in series with S3 (run switch) should be marked R19.

In Fig. 4, the underside link (dotted) should go to IC1 pin 7 and not pin 6. An extra wire link is necessary between IC7 pins 12 and 5 .

Do not miss out the track cut on Fig. 4, which lies to the right of the junction of R1 and R2, and which has not shown up very clearly in print.

The common line between B1 and B2 should be wired to OV on the component board (notshown in Fig. 4). Also link pin 5 to pin 12 on IC7. A track break is necessary to isolate S2 a wiper from IC5 pin 4.

See Market Place, July 1978 for 1 per cent components.

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| 74LSAON | 0.29 | 74 COBN | 0.24 | 7417 | 0.36 | 74141 N | 0.86 |
| 74LS42N | 1.07 | 74 Cl 10 N | 0.24 | 7420 N | 0.22 | 74148 N | 35 |
| 74LS47N | 1.09 | 74 Cl 14 N | 1.41 | 7423 N | 0.32 | 74145 N | 0.86 |
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| 74LS49N | 1.09 | $74 \mathrm{C3ON}$ | 0.24 | 7427 | 0.32 | 7415 N | 76 |
| 74LS51N | 0.26 | $74 \mathrm{C32N}$ | 0.24 | 7430 N | 0.22 | 74153 N | 76 |
| 74LS54N | 0.26 | 74 C 42 N | 0.92 | 7432 | 0.30 | 74154 N | 20 |
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| 741576 N | 0.42 | $74 \mathrm{C73N}$ | 0.54 | 7438 N | 0.32 | 74157 | 0.78 |
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| 7401 | 140 | 7427 | 25p | 747 | 30 p | 74122 | 40p | 74153 | 3p | 7418 | 100 |
| 7402 | 14 p | 7428 | 40p | 7475 | 30p | 74123 | 80 p | 74154 | 120p | 74181 | 200 |
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| 1400 |  | 74116 | 200p | 74368 150p | $9302 \quad 175 p$ | 4014 840 | －AY1－0212 | 600p | MC1495 | 400p | AC127／8 ${ }_{\text {a }}$ | ${ }_{\text {BFF56 }}$ | ${ }_{33 p}^{22 p}$ | ${ }_{\text {TIP4 }}$ | ${ }^{70 \mathrm{p}}$ | ${ }_{2}^{2 N 3823}$ | $\begin{aligned} & 70 p \\ & 90 p \end{aligned}$ | DIODES | bRIOGE |
| 7401 | 148 | 74118 | 130 D | $74390 \quad 2000$ | 9308 3160 | 4015 84p | －AYY－1313 | ${ }^{668 p}$ | －MC1496 | 100． | A0149 70 p | BFY90 | $9{ }_{90}$ | T1P42C | ${ }_{32 \mathrm{p}}$ | －2N3903／4 | 18 p | －0447 9p |  |
| 7402 | 14p | 74119 | ${ }^{210 p}$ | $14393 \quad 2000$ | $9310 \quad 275 p$ | 4016 45p | －Ayl－s050 | 212p | －MC3340 | 160 p | ADE61／2 45 | ${ }_{81 Y 83}$ | 700p | IIP2955 | ${ }_{78 p}{ }^{\text {Pp }}$ | －2N3905／6 | ${ }_{20}$ |  | －1a ${ }^{\text {a }}$ 500V 210 |
| 7403 | 14 p | 74120 | 110p | 74430 225p | 9311 275p | 4017 B0p | －Ars－1315 | ${ }^{600} \mathrm{p}$ | －MC3360 | ${ }^{120 p}$ | $8 \mathrm{BC} 107 / 8 \quad 11 p$ | ${ }^{\text {日RY } 39}$ | 45p | IIP3055 | 700 | － 2 N4036 | $65 p$ |  | － 14.400 V 320 |
| 7404 | 17 p | 74121 | ${ }^{28}$ |  | 9312 160p | 4018 89p | AY5－1317 | ${ }^{6369}$ |  | 120 p | 8C109 11p | BSX19／20 | 20 p | －Tis43 | 34 p | －2N4058／8 | 120 | －0A90 ${ }^{\text {ap }}$ | － 2 A － 50 V 300 |
| 7405 | ${ }^{18} \mathrm{p}$ | 7412 | 48 p | 74IS SERIES | 9314165 | 4019 45p | －AY5－1320 | 320 p | MK50398 | ${ }^{150}$ | －BC147／8 9p | － 81105 | 190 | －Tis93 | 30 p | － 2 N 4660 | 120 | －0A91 9p | －2A 100 ${ }^{\text {a }}$ 35p |
| 7406 | 32 p | ${ }^{74123}$ | 55p | 741500 22p | $9316 \quad 2250$ | $4020 \quad 1000$ | －ca3019 | ${ }^{80} \mathrm{p}^{\text {p }}$ | －Ne531 | ${ }^{130}{ }_{p}$ | ${ }^{-8 C 149} 100$ | －bul 08 | 250p | ： $2 \times 1008$ | 12 p | －2N4061／2 | 180 | ＊0a95 9 9p | － 2 A 400V 45p |
| 7467 | 33 p | 74125 | $55 p$ | 74LS02 22 p | 9322 150 p <br> 9368  <br> 150  | 4021 110p | －СаЗ3046 | ${ }^{70 p}$ | ＊NE540 | ${ }^{2000}$ |  | －Bul205 | 220 p | －21x300 | 13 p | － $2 \mathrm{~N} 41231 / 4$ | 220 | －OA200 ${ }^{\text {gp }}$ | －34 200 V 80p |
| 7408 | $19 p$ | 74126 | 60 p | 741504 22 p | $9368 \quad 200 \mathrm{p}$ | 4022 100p | ${ }^{\text {－}}$ ca3048 ${ }^{\text {ca3080t }}$ | 2250 720 | NE543K | $225 p$ | －BCL59 11p | － Bu 208 | 2009 | － $21 \times 500$ | 15 p | －2N4125／6 | 22p | －0a202 10p | －34 800V 12p |
| 7410 | $19 p$ | ${ }^{74128}$ | 750 | ${ }^{741508} 822$ | ${ }_{9374}^{9370} 2000$ | 4023 22p | －Ca，${ }^{\text {cas }}$ | 225p | NE555 | 30 p | －${ }_{-8 C 169 C}{ }^{12 p}$ | － Cu 4068 | 145 p | － $21 \times 502$ | ${ }^{18 p}$ | － 2 N 4289 | 20 p | －1N914 4p | －4A 100V 95p |
| 7411 | 24 p | 74136 | ${ }_{75} 5$ | ${ }_{741513}{ }^{245}$ | 9601 100 p | （4025 408 | ${ }^{\text {Ca }}$ C 309090 ab | ${ }_{375}$ | NE581日 | ${ }_{425 p}$ |  | M M 4991 | 1750 | 2 N 457 A | 250p |  | $27 p$ $90 p$ |  |  |
| 7412 | 20 p | 74141 | 70p | 741514 1000 | 98002175 | $4026 \quad 1300$ | CA3130S | 100p | NE5628 | 425 | ${ }^{\text {BCL }} 179$ 18p | M 22501 | 225 | 2N696 | 35p | － 2 N4871 | 600 | $1 \mathrm{~N} 4001 / 2 \mathrm{Sp}$ | ${ }_{6 A}^{64} 100 \% 1908$ |
| 7413 | ${ }^{30 \mathrm{p}}$ | 74142 | ${ }^{200}$ | 741520 220 | 9603 60p | 4027 50p | CA3140E | 700 | Nf565 | ${ }^{130}$ | $\because \mathrm{BC182/3} 10 \mathrm{pap}$ | MJ2955 | 100 p | ${ }^{2} \mathbf{N 6 9 7}$ | 250 | －2N5087 | ${ }^{27 p}$ | 1N4003／4 ${ }_{\text {6p }}$ | 6A 400\％120p |
| 7414 | ${ }^{60} \mathrm{O}_{\mathrm{p}}$ | 74145 | ${ }^{908}$ | 741522 ${ }^{28}$ |  | 4028 84p | CA3160E | 1000 p | NE566 | 155 | －${ }^{\text {EC184 }} 101 \mathrm{p}$ | M M 3001 | 225p | 2N698 | 45 p | － 2 25099 | ${ }^{27 p}$ | iN4005 6p | 104400 V 200 p |
| 77416 | 27 p | ${ }^{74147}$ | $190{ }^{190}$ | ${ }^{741527} 38{ }^{315}$ | interface | 4029 100p | ${ }_{\text {Ex }} \times 209$ | 7500 9250 | NE567 | 155p | ${ }^{\text {BCLI87 }}$ 309 | －MJE330 | 65 | 2N706A | 20 p | － 2 NS172 | ${ }^{27} 7^{2}$ | 1 N 400677 7p | 254400 V 400 p |
| 7417 7420 | ${ }^{270}$ | 74148 74150 | ${ }^{150}{ }^{50}$ | 741530 22p |  | $4030 \quad 550$ | ICL7106 | 9250 3400 |  | 4000 | ： $\mathrm{BC212/3} 11 \mathrm{p}$ | MJE2955 | 100p | 2 N 701 AA | 20p | 2 2N5179 | 27. | $1 \mathrm{~N} 5401 / 3 \quad 14 \mathrm{p}$ | triacs |
| 1421 | 40 p | 74151 | ${ }^{70}$ | 74\S55 30 | MC1489 100p |  | IM3014 | ${ }_{36 \mathrm{p}}$ | －SN76013N | 140 p |  | －MPF102 | 70 p |  | ${ }_{18 p}^{45 p}$ | 2NS191 | ${ }^{8} 8$ | $1 \mathrm{~N} 5404 / 7$ 19p | PLASTIC |
| 7422 | 22p | 74153 | 70 p | ${ }^{74} 4$ LS 73 50p | 75107 ：609 | ${ }_{4034} \quad 200 \mathrm{p}$ | （M311 | $190 p$ | －SN76013N0 | 120 p | ${ }_{8 C 47 / 8}{ }^{\text {c }}$ | －MPF103／4 | ${ }_{40}$ | 2N1131／2 | ${ }_{20 p}$ | －2N5245 | 40 p | ZEMERS | 3A 400V 600 y |
| 7423 | $34 p$ | 74154 | 100p | 741574 40p | $75182 \quad 230 \mathrm{p}$ | 4035 110p | 1 M318 | 200 p | －SN16023N | 140p | －BC516／7 50p | －MPF 105／6 | $4{ }^{\text {p }}$ | 2 N1613 | 25p | －2N5296 | 550 | 27v33v | 6A 40JV 70p |
| 7425 | $3{ }^{30 \mathrm{p}}$ | 74155 | 90 p | ${ }^{741575} 55^{50}$ | $75450 \quad 120 \mathrm{p}$ | $4040{ }^{100 p}$ | LM324 | 70p | －SN76023N0 | 12 p | －BC5478 16p | －MPSAO6 | 30 p | 2N1711 | 25 p | －2N5401 | ${ }^{50} \mathrm{p}$ | $400 \mathrm{mw} \quad 3 \mathrm{p}$ | 68500 V 88p |
| 7426 7427 | ${ }_{340}^{40}$ | 74156 74157 | 70 p |  | 75451／2 72 | ${ }^{4041} 4048$ | LT339 | 90p | －SN76033N | 1750 | ${ }^{-8 C 549 C 5}$ | －MPSA12 | ${ }^{50 \mathrm{p}}$ | 2N2102 | ${ }^{60} \mathrm{p}^{1}$ | －2N5457／8 | ${ }_{4}^{40 p}$ | IW 15p | ${ }^{84} 400 \mathrm{~V}$ 75p |
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| 7433 7437 | ${ }^{40 p}$ | 74162 74163 | ${ }_{1000}^{100}$ | 7415107 <br> $74 . S 112$ <br> 100 <br> 100 |  |  | －im339N | $140 p$ 360 | －t¢a820 | 990p | BD131／2 BOY56 | ${ }_{-820088}$ | 130 200 200 | 2N2484 | ${ }^{30 \mathrm{p}}$ | 2N6 247 2N8254 | ${ }^{190}{ }^{190}$ | 41 | 168500 V 130 p |
| 7438 | 35p | 74164 | $120{ }^{\circ}$ | 74LS123 75p | $14 \mathrm{CO日}$ 27p | 4049 40 | LM710 | 50 p | －toal022 | ${ }_{6000}$ | ${ }_{8 F} 200{ }^{\text {a }}$ | －${ }^{\text {20，}}$ | ${ }_{200}$ | ${ }_{2}{ }^{\text {N2 } 2904 / 5}$ | $25 p$ | ${ }_{2 N 6290}$ | ${ }_{65 p}$ |  | ThYRISTORS |
| 7440 | 17 p | 74165 | $130_{p}$ | 74LS124 180p | $74 \mathrm{C10}$ 27p | 4050 49p | （m733 | ${ }^{100}{ }^{\text {p }}$ | XR2206 | 400p | －8F2448 35p | －TIP29a | 40 p | 2N2906A | 24p | 2N6292 | 65p |  | ${ }_{14}{ }^{\text {14 }} 400 \mathrm{~V}$ |
| 7441 | 70p | 74166 | 140， | 7415132 120p | $14 \mathrm{Cl} 4{ }^{20} 9$ | 4051 | ［M74 | $22 p$ | －${ }^{\mathrm{X} 22207}$ | 400 p | ${ }^{-852568} \quad 70 \mathrm{p}$ | －Tip290 | 55 p | $2 \mathrm{2N2907A}$ | 30 p | 3N128 | 1200 | $100+555$ | 1A 600 V 700 |
| 7442 A | ${ }^{60 p}$ | 74167 | ${ }^{200}{ }^{\text {p }}$ | $74151333^{60 p}$ | $74.20{ }^{78}$ | $4052 \quad 80{ }^{\text {a }}$ | LM747 | 70 p | －$\times$ R2218 | 6750 | BF257／8 32p | －TIP30a | 48 p | －2N2928 | 9 p | 3 N 140 | ${ }^{100 p}$ |  | 3 A 400 V 90. |
| 7443 744 | $112 p$ $112 p$ | 74170 74172 | ${ }_{720 \mathrm{p}}^{240 \mathrm{p}}$ | 7415138 <br> 7415139 <br> 60 p <br> 1 | $74 C 30$ 279 <br> 74.32  <br> 360  | $\begin{array}{rr}4053 & 80{ }_{0} \\ 4055 & 1250\end{array}$ | LM748 | ${ }_{70}^{350}$ | －XR2240 | ${ }^{4000}$ |  | ＊TIP30¢ | ${ }_{60} 0^{0}$ | 2N3053 | 20 p | 3 N 201 | $110 p$ |  | 8A 600V 140p |
| 7445 | $112 p$ $100 p$ | ${ }_{74173} 74$ | 20p 1200 | 7415139 $741511000_{0}$ | $\begin{array}{rr}74 \mathrm{C42} & \\ 786 \\ 110 \mathrm{p}\end{array}$ | $\begin{array}{ll}4055 & 125 \\ 4056 & 135 p\end{array}$ | （M3911 | ${ }^{1300}$ | ${ }_{\text {2N4 } 4245}$ | ＋935 |  | TIP3IA | $58 p$ $62 p$ | －${ }_{\text {2N3054 }}$ | $65 p$ $48 p$ | $3 N 204$ 40290 | 250p | N3055 | 12a 400 V 160p |
| 7446A | 93 p | 74174 | 93 p | 7415153 60p | $744^{88}$ 250p | $4059 \quad 6000$ | －M4136 | ${ }^{120}{ }^{\text {p }}$ | 2N4258 | 400p | ＊BFR4 4 30p | TIP32A | 689 | 2 N 3442 | 1400 | 40360 | $4{ }^{\text {p }}$ |  | 16A 400 180p |
| 7447A | 70p | 74175 | $85 p$ | 7415157 60p | $74 C 73 \quad 75 p$ | $4050 \quad 115$ | ＊MC1310P | 150p | 2N1034E | 2000 | $\because 8 F A 79$ 30p | TIP32C | 82 p | 2N3553 | 240p | $40361 / 2$ | 45p |  | 16a 600V 220p |
| 7448 | 80 p | 74176 | 90 p | 741S158 120p | 74C74 70p | 4063120 p | MC1458 | 55p | 95H90 | 8009 | ＊BFR80 30，30p | TiP334 | 90p | － 2 N3565 | $3{ }^{\text {p }}$ | 40364 | 120 p |  | ${ }_{97105} 110 \mathrm{p}$ |
| 7450 | $17 p$ | 74171 | 90p | 7415160 130p | 74C85 200p | 4066 55p | voitage re | egulator | R |  |  | ［17P33C | 114p | －2N3643／4 | ${ }^{48 p}$ | 40408 | 70p |  | c1060 45p |
| 7451 | $17 p$ | 74178 | ${ }^{160} \mathrm{p}$ | $7415161100 p^{\text {p }}$ | 74 7886 65 | $4068 \quad 22 \mathrm{p}$ | Fixed Plastic T | 10－220 |  |  | ${ }^{88 \times 29}$ | TiP 340 | 1150 | －2N3702／3 | $12 p$ | 40409 | ${ }_{65}^{65}$ |  | MCR101 36p |
| 7453 7454 | $17 p$ | 74180 | 93p | 74LST62 140p | ${ }^{744690} \quad 95$ | $4069 \quad 200$ | $14 .+\mathrm{ye}$ |  | －ve |  |  | TIP34C | ${ }^{1600}$ | － 2 N3704／5 | ${ }^{12 p}$ | 40410 | ${ }^{65 p}$ |  | 2N3525 120p |
| 7460 | 17 p | 74182 | ${ }^{200 p}$ | 7415184 120 p |  | 4070 4230 | 5V7805 | ${ }_{90}^{90}$ | 7905 | ${ }_{120 \mathrm{p}}^{120}$ |  | TIP35C | 22900 | － $2 \mathrm{~N} 370 \mathrm{~B} / 9$ | 12 p | ${ }_{40594}$ | ${ }^{300 p}$ |  | 2N5060 2N5064 |
| 7470 | 36 p | 74984 | 150p | 7415185 80p | 74C150 250p | $4072 \quad 22 \mathrm{p}$ | 1287812 1547815 | ${ }_{90 p}^{90}$ | 7912 7915 | 120 p | 8Fx88 30p | TIP36A | 2700 | －2N3773 | 300p | 40595 | 105p |  |  |
| 7472 7473 | ${ }^{30 \mathrm{p}}$ | 74185 | 150p | 7415173 110a | 74C151 260 p | 4073 22p | 18 c 7818 | 90. | 7918 | 120 p | 8FW10 |  | 340p | － 2 N 3819 | 25p | 40603 | ${ }^{\text {bp }}$ |  |  |
| 7474 747 | $3{ }_{30} 3$ | 741 | 700p | 741S174 110p | ${ }^{74 C 157}$ 250p | $4075 \quad 229$ | 2407824 | ${ }^{90 p}$ | 792 | 120p | BFY50 22p | TIP418 | 65p | ${ }^{2} \mathrm{~N}$ | 50p | 40673 | 7p |  | Sat |
| 7475 | 36 p | 74191 | 100 p | 7415181 320p | 74C181 155p | 4081 $\begin{array}{ll}\text { 42p }\end{array}$ | 100 ma to－9 |  |  |  | MEMDRIES | AY5－1013 |  | OTHER |  | $40871 / 2$ | $\begin{aligned} & 90 p \\ & 900 p \end{aligned}$ |  |  |
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| 7481 | $100{ }^{1}$ | 74194 | 100p | 74LS192 140p | 74C164 120p | 4098 107p |  |  |  |  | $21078 \quad 600{ }_{p}$ | SN745262 | 1450p | 4209 4289 | ${ }^{390} 0^{p}$ |  | 11p | 18 pin $\quad 25 p$ | ${ }_{20}^{24} \mathrm{pman} \quad 33 \mathrm{p}$ |
| 7482 | ${ }^{84}$ | ${ }_{7}^{74195}$ | 95p | ${ }_{7}^{7415193140}$ |  | $\begin{array}{ll}4502 & 1200 \\ 4503\end{array}$ | LM309k | ${ }_{135 p}$ | rBA625 | 120p | $\begin{array}{ll}2111.2 & 325 p \\ 2112.2 & 3000\end{array}$ | TMS6011 | 500p | 4889 4801 | 900p |  | $13^{19}$ | 22 gin | $\begin{array}{ll}28 \mathrm{pin} & \text { 420 } \\ 40 \text { pin }\end{array}$ |
| 7484 | 1009 | 74197 | ${ }_{80}{ }^{\text {P }}$ | 7415198 120p | ${ }_{74 C 175}{ }^{219}$ | ${ }_{4507}^{4509}$ | LM317T | 200p | r1430 | 65p | $\begin{array}{ll}2112.2 & 3000 \\ 2114 & 1500\end{array}$ | EPROM |  | 6820 | 600p |  |  |  |  |
| 7485 | 110 p | 74198 | ${ }^{150}$ | 7415221 100p | ${ }^{74 C 192}$ 150p | 4510 99p | LM323K | ${ }^{625 p}$ | 78 HO 5 KC | ${ }^{\text {575p }}$ | 51015 | ${ }^{17024}$ | 500 p | 6850 | 700 p | ${ }_{\text {\％}}$ | ${ }_{30}$ |  | 40 |
| 86 | $3{ }^{34}$ | 74199 | ${ }^{150}$ | 74152402450 | $74 \mathrm{Cl} 33{ }^{1500}$ | $4511 \quad 150 \mathrm{p}$ | （M723 | ${ }_{\text {370 }}^{\text {37 }}$ | 78MGT2C | 135p | 6810 400p | 2708 | 900 p 3000 p | 8205 8212 | 320 p 2250 | 14 pin | $\begin{array}{ll} 40 p \\ 40 p \end{array}$ | $24 \text { pin } \quad 350$ | 40 p |
| ${ }_{7}^{74999}$ | ${ }^{210 p}$ | 74221 | ${ }^{160}{ }_{\text {p }}$ | 74152412459 | $74 \mathrm{Cl194} 220 \mathrm{p}$ | $4514 \quad 250 p$ | 2N5777 | ThONICS |  |  |  | 4702 |  |  |  |  |  |  |  |
| 7491 | B0p | 74259 | $250{ }^{2}$ | 7415243 2459 | 74C221 ${ }^{\text {7 }}$ | $\begin{array}{ll}4516 & 110 p \\ 4518 & 100 p\end{array}$ | OCP71 130 | 15p ORPP | 12 90 p ORP61 <br> 00   <br> 90p   <br> TIl78   | 90 p 70 p | ROM／PROM ${ }_{2}$ |  |  | ${ }_{8} 8224$ | 2250 4000 |  |  | ORS |  |
| 7492A | 45 p | 74265 | 90 p | $7415245300 p$ |  | 4520 100p | $0.125^{\prime \prime}$ | LEDs 0. | ．${ }^{\circ}$ |  | 7481888 | $\mathrm{CPO}_{4}$ |  | 8228 | 5750 | $2 \times 10$ Way | ${ }^{8} 5$ |  |  |
| 7493 A | 33p | 14278 | ${ }^{290} \mathrm{p}$ | 74.5251200 p | 4000 SERIES | $4528 \quad 100 \mathrm{p}$ | TII32 ，R． | ${ }^{75}$ | TH220 Red | 16p | 745287 745387 | ${ }_{6502}$ | ${ }_{1200 p}^{670 p}$ | 8251 | ${ }^{7000}$ | $2 \times 15$ way | 100 p | $\begin{aligned} & \frac{2}{2} \times 18 \text { Way } 1200 \\ & 2 \times 22 \text { Way } 135 p \end{aligned}$ | 25 Way 160p |
| 74948 | ${ }^{84}{ }^{\text {p }}$ | 74279 | ${ }^{1400}$ | 7415257 120 | $400015 p$ | 4543 180p | T1209 Red | 13 p | T1L222 ${ }^{\text {a }}$ | ${ }^{18 p}$ | 93436 | 6502 | ${ }_{1000}^{1200 p_{0}}$ | 8255 9905 |  |  |  |  |  |
| ${ }_{7} 74968$ | ${ }_{65 p}$ | ${ }^{74283}$ | ${ }^{1900}$ | 7415259175 | $4001 \quad 17 p$ | 4553 $450 p$ <br> 4560  <br> 2500  | ${ }_{\text {Hil } 212 \mathrm{Y}}$ | 20p |  | 1220p | 93446 650p |  | 600p |  | ${ }_{275}{ }^{2}$ |  |  | $12 \frac{1}{2} \%$ |  |
| 747 | 180 p | 14285 | ${ }_{400}$ | 7415373 2000 | ${ }_{4006}^{4002}$ | 4583 ${ }^{4959}$ | Til216 Red | 180 | Cligs | 3 p |  |  |  |  |  |  |  |  |  |
| 74100 | ${ }^{1300}$ | 74290 | ${ }^{150} \mathrm{p}$ | 74L5374 195p | 4007 18p | 4584.90 p | DISPLAYS |  | fn0357 | 120p | dd 25 | \＆VAT |  |  |  |  |  |  |  |
|  | ${ }^{650}$ | 74293 | ${ }^{150}{ }^{\text {p }}$ | $81 / 5951600$ | ${ }_{4008}^{4008} 80$ | 40014 900 | 3015 F | ${ }^{200} \mathrm{p}$ | FNO500 | 120 p |  |  |  |  |  |  |  |  |  |
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