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Our July issue will be on sale Friday, 9 June 1978 (for details of contents see page 765)

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Practical Electronics June 1978





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TECHNOLOGY:

Double sided epoxy printed circuit board with plated through holes. Dual gate effect transistors; Silvered coils.

FI 2846 IF AMP AND DECODER



TECHNICAL CHARACTERISTICS: Intermediate frequency – 10.7MHz. IF Bandwidth – 280kHz; Signal to noise ratio – 70dB with 1mV input; Distortion – mono 0.1%, stereo 0.3%; Sensitivity - 30uV up to the 3dB limit; Channel separation – 40dB at 1kHz; Pass band – 20 to 15,000Hz; Rejection at 38kHz greater than 55dB; Am rejection – 45dB; De-emphasis – 50 to 75 μ s; Pilot capture at 19kHz + 4%; Channel matching within less than 0.3dB; Output impedance – 100 Ohms; Output voltage – 500mV; Phase locked loop stereo decoder; Output for LED VU-meter; Null indicator; Outputs for AGC AFC and interstation muting; Consumption - 55mA LEDs extinguished; 100mA LEDs illuminated; Power supply – 15V; Dimensions 195 × 76mm.

CIRCUIT TECHNOLOGY Epoxy printed circuit board; Monolithic integrated circuits; ceramic filter.

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EASY BUILD SPEAKER DIY KITS Specially designed by R1-VC for cost conscious h-fi enthusiasts, these kits incorporate two teak-simulate enclosures, two fill 3' × 8' (approx.) woofers, two tweeters and a pair of matching crossovers. Supplied complete with an easy-to-follow circuit diagram, and crossover components. StepEo PAIR Input 15 waits rms. 30 waits peak, each unit. # p & p 5:50 Cabinet size 20" × 11" × 93" (approx.) SPEAKERS AVAILABLE WITHOUT CABINETS. It's the units which we supply with the enclosures illustrated Size 13" × 8" (approx.) woofer (EMIL 2] app reweeter, and matching crossover components. Stereo pair Power handling 15 waits rms. 30 waits peak. + p & p £3.40	20 x 20 WATT STEREO AMPÉIFIER Superb Viscount IV unit in teak-finished cabinet. Superb Viscount	45 WATT MOND DISCO AMP 63509 A 9 F2 50 Size approx. 133" × 53" × 63 45 watts ims 90 watts peak output. Big leatures include two disc inputs, both for ceramic cattridges, tape input and microphone input. Level mixing controls fitted with integral jush pull switches. Independent hass and treble controls and master volume. 70 & 100 WATT MOND OISCO AMP Size approx. 14" × 4" × 104" Brushed aluminum lascia and rotary controls. Five vertical slide cootrols master volume.
	COMPACT 12" Bass woolers with cropped sides, 14,000 Gauss magnet, 30 watts R.M.S. handling + 34" £4,900 + £4,000 p & p NOW AVAILABLE fully built and tested. Output 30 + 30 watts rms. 60 + 60 peak. p & p f2,50 AD-ON STERED CASSETTE TAPE DECK KIT Designed for the experienced 0.1.Y. man. This kit comprises of a tape transport mechanism. ready built and tested record replay electronics with twin V.U. meters and level control for mating with mechanism. Specifications: Sensitivity - Mic. 0.85 mV 20K OHMS: Oun, 40mV # 40K DHMS: Output - 300mV RMS per channel # 1KHz from 2K OHMS source: Cross Talk - 30dbit Tape Counter - 3 Digit. Resettable: frequency Response - 40Hz - 8KHz± 6dbit Deck Motor 9 Volt DC with electronic speed regulations: Key Functions - Record, Rewind. Fast Forward Play, Stop & Eject. p & o f2, 50 PL extras: Mains titansformer to suite f2, 50 + f1 p & 5. Det witas: Mains titansformer	Tape rever, mc: level, deck rever, PLOS in IEN DECK FADE for perfect graduated change from record deck No. 1 to No. 2, or vice versa. Pre fade level control (PFL) lets YDU hear next disc before fading fut m. VU meter monitors output level Dutput 100 watts RMS 200 watts peak. CHASSIS RECORD PLAYER OECKS BSR MP60 TYPE Single 12" x 8]" BSR MP60 type, complete with magnetic cartridge. 12" x 8]" BSR automatic record player deck cueing device and stereo ceramic head. p & p f2.55 f 995 BSR MP 60 type, complete with magnetic cartridge. fammed stylus, and de luxe plinth and cover. Went & Tack cartridge player. This unit will match Home & Track cartridge Player. This unit will match famed stylus, and de luxe plinth and cover. famed stylus, and de luxe plinth and cover. famed stylus, and de luxe plinth and cover. famed stylus (Mid Stylus) famed stylus (Mid Stylus) famed stylus (Mid Stylus) famed stylus, and de luxe plinth and cover. famed stylus (Mid Stylus) famed stylus (Mid Stylus) famed stylus (Mid Stylus) famed stylus (Mid Stylus)
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$\begin{array}{ccccc} \textbf{TANTALUM} & \textbf{BEAD} \\ \textbf{35V:} & 0 \ 1\mu F & 0 \ 22 & 0. \\ 1 \ 0 & 2 \ 2\mu F & 3 \ 3 & 47 & 6 \\ \textbf{20V:} & 1.5 & \textbf{16V:} & 10 \\ \textbf{47, 100} \ \textbf{40p, 10V:} \ \textbf{22} \\ \textbf{47, 68, 100, 3V:} \ 68, 1 \end{array}$	САРАС 33 047 8 25V: 1 0µF 13р 2µF, 33, 4 00µF, 20	1TORS P 068 0 5 10 5 each 5 47 6V: 5 peach 5	OTENTIO arbon Track, 20Ω, 1K, & KΩ 2ΜΩ sin KΩ 2ΜΩ sin KΩ 2ΜΩ di	WETERS (W Log & 2K (lin only) igle gang gle gang D/ ial gang ste	AB or EGEN) W Linear values Single 26p 26p P switch 55p reo 70p	OPTO ELECTRONICS* LEDs clip TIL209 Red 13 TIL211 Grn 24 TIL212 Yellow 27 T1110 20	7454 7460 7470 7472 7473 7474 7475	17 17 28 25 32 27 38	7416 7416 7417 7417 7417 7417 7417 7417	6 141 7 20 0 230 2 625 3 170 4 87 5 87	4030 4031 4038 4039 4040 4040 4041	325 100 108 320 105 80 2 75	4415 4415 4419 4422 4433 4435 4440	F 795 V 795 280 545 1225 825 1275	LM30 LM30 LM30 LM31 LM32 LM33 LM34	01H 11A 8 4 9 8	170 30 110 195 98 55 120	TAAG TAA7 TAA9 TBA1 TBA5 TBA5 TBA5	61A 00 20S 40Q 50Q 41BX11	155 353 300 70 220 330 250
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DIN 2 PIN Loudspeaker	PLUGS	SOCKETS	In Line	SWIT	CHES * Miniature	DPDT 6 Tag 85r Non-Locking Push to Break 25p	AD14 AD14 AD16	0 9 1 *	69 BC 60 BC 42 BC	328 338 441	13 12 30	BFX86* BFX87* BFX88	28 23 24	0C171 0C201 0C202	★ 4 ★ 12 ★ 14	0 2N9 5 2N9 0 2N9	20★ 30★ 61★	51 18 61	2N3903 2N3904 2N3905	18 18 24
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PHONO assorted colours Metal Screened	9p 12p	5p single Sp double 10p 3-way	15p	ROCK Lights ROTA	ER: Illuminated (whit when on JA 240V RY: IADJUSTABLI	te) 52p E STOP) 1 pole / 2-12	AF11 AF11 AF12	7★ 8★ 1★	22 BC 55 BC 48 BC	549 557 558	11 13 12	BFY71 BSX20 BSX201	20 18 18	TIP290 TIP30 TIP30	6 5 4	0 2N1 2 2N1 7 2N1	304★ 305★ 306★	50 28 35	2N4064 2N4236 2N4289	120 145 20
BANANA 4mm 2mm	10p 10p	12p 10p	12	ROTA DIL S	RY: Mains 250V AC	C 4 Amp 42p file – Texas}	AF12 AF12 AF12	4★ 5★ 7★	55 BC 35 BC 35 BC	Y30★ Y34★ Y39★	57 75 78	BSY65 BSY95 BSY95A	30 25 # 18	TIP30 TIP30 TIP31	B 6 C 6 4	64 2N1 55 2N1 10 2N1	307★ 308★ 613★	50 46 20	2N4859 2N5135 2N5136	65 42 42
WANDER 3mm	8p 9p	8p 9p	-	8 pin 20 pin	10p; 14 pin 12p; 1 26p; 24 pin 30p; 28	16 pin 13p; 18 pin 20p; pin 42p; 40 pin 58p.	AF13 AF17 AF18 AF18	9★ 8★ 0★ 6★	35 BC 70 BC 70 BC 50 BC	2Y40 2Y43 2Y58 2Y59	75 22 22	BU105 BU2051 BU2081 E5567	130 190 225	TIP31 TIP31 TIP31 TIP32	A 4 B 4 C★ 6	0 2N1 0 2N1 6 2N1	671★1 671B★1 893★ 986	90 195 28 60	2N51791 2N51801 2N51911	r 60 r 60 r 65
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IC AY-3-871 Basic Kit (just a	0 £9.7 dd contr	8★ ols)	Comp	ete Kit	price incl. VA	Plastic + Ve 1A: 5V, 12V, 15V, 18V, 24V 99p each	BC14 BC14 BC14	3 7 8	7 B	F115# F154# F156#	22 25 29	MPSU0 MPSU0 MPSU0	34 2 ★ 58 5 48	TIS60	4	47 2N2 45 2N2	2926R 2926R 2926Y	8824	40595 40603 40636	90 65 125
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Complete Kit incl. c Price: Only	ases & co 24.30	ontrols	availa tities	ble in at £65 .	limited quar 00 plus P & I	P. LM320-12 145p LM320-15 155p	BC18 BC18 BC16	8 9 0	11 B 11 B 27 B	F178 F179 F180	25 30 20	MPU13 0C25# 0C26#	1* 39 120 150	ZTX 10 ZTX 10 ZTX 10)7 1)8 1)9 1	11 2N 11 2N 11 2N	310B 3121 3133	39 40 43	Pair 10p ex	ctra.
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KITS FOR SYNTHESISERS, SOUND EFFECTS



P.E. MINISONIC Mk. 2 SYNTHESISER

P.E. MINISONIC MK. 2 SYN INESISEH A portable mains-operated Ministure Sound Synthesiser, with keyboard circuits Although having slightly fewer facilities than the large P.E. Synthesiser the functions offered by this design give it great scope and versatility Consists of 2 log VCOs. VCF. 2 envelope shapers, 2 voltage controlled amps, keyboard hold and control circuits. HF oscillator and detector, ring modulator, noise generator,

inker, porter suppry.	
Set of basic component kits	from £62-23
Set of printed circuit boards	£9-71

P.E. SYNTHESISER (P.E. Feb. 73 to Feb. 74)

The well aclaimed and highly versatile large-scale mains-operated Sound Synthesiser complete with keyboard circuits. Other circuits in our lists may be used with the Synthesiser to good advantage.

The Main Synthesiser: PSU, 2 linear VCOs, 2 ramp generators, 2 input amps, sample hold, noise generator, reverb amp, ring modulator, peak level circuit, envelope shaper, voltage controlled amp. Set of basic component kits Set of busic component kits £13-20 Set of printed circuit boards The Synthesiser Keyboard Circuits (can be used without the Main Synthesiser to make an independent musical instrumenti: 2 logarithmic VCOs, divider, 2 hold circuits, 2 modulation amps, mixer, 2 envelope shapers and PSU. Set of basic component kits Set of printed circuit boards £48-18 £7.66 GUITAR EFFECTS PEDAL (P.E. July 75) Modulates the attack, decay and filter characteristics of an audio signal not only from a guitar but from any audio source, producing 8 different switchable effects that can be further modified by manual controls. Possibly the most interesting of all the low-priced sound effects units in our range. Circuit does not duplicate effects from the Guitar Overdrive lutt. range Circuit Overdrive Unit £7.59 Component set with special foot operated switches Alternative component set with panel switches Printed circuit board £4-98 £1.43

SOUND BENDER (P.E. May 74)

A multi-purpose sound controller, the functions of which include envelope shaper, tremolo, voice-operated fader.	
automatic fader and frequency-doubler. Component set for above functions (excl SWs) £7:84 Printed circuit board £1:81 Optional extra—additional Audio Modulator, the use of which, in conjunction with the above component set, can produce jungle-drum rhythms Component set (incl PCB) £2:88	
PHASING UNIT (P.E. Sept. 73) A simple but effective manually controlled unit for introducing the "phasing" sound into live or recorded music	
Component set (incl. PCB) £2-87	
PHASING CONTROL UNIT (P.E. Oct. 74) For use with the above Phasing Unit to automatically control the rate of phasing	
Component set (incl. PCB) £4-48	ł
SOPHISTICATED PHASING AND VIBRATO UNIT A slightly modified version of the circuit published in Elektor'. December 1976, and includes manual and automatic control over the rate of phasing and vibrato Component set Printed circuit board £2:33)
WAH-WAH UNIT (P.E. Apr. 76)	

The Wah-Wah effect produced by this unit can be	controlled
manually or by the integral automatic controller	
Component set (incl. PCB)	£3-55
AUTOWAH (INIT /D.E. Mor. 77)	

Automatically produces Wah-pedal and Swell-peda	Isounds
 each time a new note is played Component set, PCB, special foot switches Component set and PCB, with panel switches 	£7·27 £4·83

POST AND HANDI ING

I

COMPONENTS SETS include all necessary resistors. capacitors, semiconductors, potentiometers and transformers. Hardware such as cases, sockets, knobs, keyboards, etc. are not included but most of these may be bought separately. Fuller details of kits, PCBs and parts are shown in our lists.

CIRCUIT AND LAYOUT DIAGRAMS are supplied free with all PCBs unless "as published". PHOTOCOPIES of all P.E. texts for most of the kits

are available-prices in our lists.

P.E. JOANNA PLUS ORGAN VOICING

The basic five octave electronic piano (P.E. May/Sept 75 and Sound Design) has switchable alternative voicings for Honky-Sound Design has switchable alternative volcings for Honsy-Tonk, ordinary plano, and Harpsichout or a mixture of any of these three, together with facilities including fast and slow tremolo, loud and soft pedal switching, and sustain pedal switching. The modification retains all the circuitry associated with the piano but in addition provides an organ-voice envelope facility with 5 switchable pitches, variable attack and sustain, phasing and vibrato.

Set of components (excl switches) for PSU, Frequency generator, Pitch and Note Divider, Envelope Shapers, Voicings, and Control circuitries. (Order as KIT 71-5) £109-75 Set of PCBs (Order as PCB SET 71-6) £29

SYNTHESISER TUNING INDICATOR (P.E. July 77) A simple 4-octave frequency comparator for use with synthesisers and other instruments where the full versatility of the P.E. Tuning Fork is not required.

Component and PCB (but excl sw.) £7-45

GUITAR FREQUENCY DOUBLER (P.E. Aug. 77) A modified and extended version of the circuit publishe

Component set and PC8 £4-22

GUITAR SUSTAIN (P.E. Oct 77)	
Maintains the natural attack whilst extending	note
Component set, PCB and foot switches	
Component set, PCB and panel switches	

WIND AND RAIN UNIT

A manually controlled unit for p	ballening the abore names
Component set (Incl. PCB)	£3-72
GUITAR OVERDRIVE UNIT (P E. Sophisticated, versatile Fuzz un switchable controls affecting the l	Aug. 76) it. including variable and luzziquality whilst retaining
the attack and decay and also p	oviding filtering. Does not

duplicate the effects from the Guitar Effects Pedal and be used with it and with other electronic instruments Component set using dual slider pot Component set using dual rotary pot Printed circuit board £	can 8-86 8-20 1-62
FUZZ UNIT Simple Fuzz unit based upon P.E. "Sound Design" cir	cuit.

TREMOLO UNIT

Based upon P E	Sound Design	circuit.	
Component set	(incl. PCB)		£3-6

TREBLE BOOST UNIT (P.E. Apr. 76) Gives a much shriller quality to audio signals fed through it The depth of boost is manually adjustable. Component set (incl. PCB) £2:40 £2-40

P.E. TUNING FORK (P.E. Nov. 75)

Produces 84 switch-selected frequency-accurate tones. A LED monitor clearly displays all beat note adjustments. Ideal for tuning acoustic or electronic musical instruments. £15-59 Main component set (incl. PCB) Power supply set (incl. PCB) £7.03

SEE OTHER PAGE FOR KEYBOARDS, AND OUR LISTS FOR OTHER COMPONENTS AND ACCESSORIES STOCKED

TI rrent rate if changed) to full ost and handling. (Does not rders).

Phonoson

ORDER SUPPLIERS PRINTED CIRCUIT BOARDS, KITS AND COMPONENTS то WORLD-WIDE A MARKET.

0 F	SYNCHRONOME	PE	Mar	76)
- F.E.	STRUTTURE		Widi	(0)

An accented-beat electronic metronome, providing	duple.
riple and quadruple times with full control over t	he beat
ate Can also be used as a simple drum-beat	rhythm
generator includes power supply	
Component set (incl. loudspeaker)	£11.62
Printed circuit board	£2-04

TAPE NOISE LIMITER

/ery effective circuit for reducing the hiss found in mos	t tape
ecordings. All kits include PCBs Standard tolerance set of components Superior tolerance set of components Regulated power supply (will drive 2 sets)	£2·96 £3·76 £4·69

ENVELOPE SHAPER WITHOUT VCA (P.E. Oct. 75)

Provides full manual control over attack, decay, sustain and release functions, and is for use with an existing voltage controlled amplifier Component set (incl. PCB) £4-66

ENVELOPE SHAPER WITH VCA (P.E. Apr. 76)

This unit	t has its o	own vo	oltage co	ontrolled	amplifier	and	has full
manual	control	over	attack.	decay.	sustain	and	release
function	S						
Comp	onent se	t (incl	PCB)				£6-68

TRANSIENT GENERATOR (P E. Apr. 77) An envelope shaper, without VCA, having the usual attack, decay, sustain and release functions, and in addition it also provides a Repeat Effect enabling a synthesiser to be programmed to imitate such instruments as a mandolin or banjo. C

omponent set	£4-52
rinted circuit board	£1-82

WAVEFORM CONVERTER

duration

£4-90 £3-48

> Slightly modified from a circuit published in "Elektor". Converts a saw-tooth waveform into four different waveforms: sine-wave, mark-space saw-tooth, regular triangle form, and squarewave with an externally variable mark-space ratio. Component set (incl. PCB but excl. sw/s) £8-19 VOLTAGE CONTROLLED FILTER (P.E. Dec. 74) Part of the P E. Minisonic now released as an independent kit for use with other synthesisers Component set (incl. PCB) (Order as Kit 65-1) £8:22

RING MODULATOR (P.E. Jan. 75) Part of the P.E. Minisonic now released as an independent kit for use with other synthesisers. Component set (incl. PCB) (Order as Kit 59-1) £5-50

NOISE GENERATOR (P.E. Jan. 75) Part of the P.E. Minisonic now released as an independent kit for use with other synthesisers. Component set (incl. PCB) (Order as Kit 60-1) £3-35

SOPHISTICATED POWER SUPPLIES

A wide range of highly stabilised low noise power supply kits is available-details in our lists.

MICROPHONE PRE-AMP (P.E Component set (incl. PCB)	Apr	77)	£3·78

VOICE OPERATED FADER (P.E. Dec. 73) For automatically reducing music volume during talk-over —particularly useful for Disco work or for home-movie shows. Component set (incl PCB) £3:97

DYNAMIC RANGE LIMITER (P.E Apr. 77) Automatically controls sound output to within a preset level. Component set (incl. PCB) £4-58

EXPORT ORDERS are welcome, though we advise that a current copy of our list should be obtained before ordering as it also shows Export postage rates. All payments must be cash-with-order, in Sterling and preferably by International Money Order or through an English Bank. To obtain list send 50p.

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AND OTHER PROJECTS

PHOTOGRAPHS in this advertisement show two of our units containing some of the P.E. projects built from our kits and PCBs. The cases were built by ourselves and are not for sale, though a small selection of other cases is available

LIST—Send stamped addressed envelope with all U.K. requests for free list giving fuller details of PCBs, kits and other components.

OVERSEAS enquiries for list Europe send 20p: other countries----send 50p

KIMBER-ALLEN

KEYBOARDS AND CONTACTS Kimber-Allen Keyboards as required for many published circuits. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C, the keys are these are the finest moulded plastic keyboards available. All octaves are to to the plastic spring-loaded, fitted with actuators, and mounted on a robust aluminium frame. $\pounds 25.50$ 3 Octave (37 notes)

4 Octave (49 notes)	£32-25
5 Octave (61 notes)	£39-75
Contact Assemblies (gold-clad wire) for use with the above keyboar	ds (1 required for each

note): Type GJ: Single-pole change-over Type GJ: Single-pole change-over Type GB: 2 pairs of contacts, each pair normally open Type GC: 3 pairs of contacts, each pair normally open Type GH: 5 pairs of contacts, each pair normally open Type GH: 5 pairs of contacts, each pair normally open Type 4PS: 3 pairs of contacts plus single-pole changeover each 24p each 27p each 36p each 45p each 57p each 53p

Printed Circuit Boards for use with GJ, GB and 4PS contacts (thus eliminating much interwiring) are available. Details in our lists

	TRANSIST
RHYTHM GENERATOR	AC128
15-Rhythm Tempo, Timing and Logic control unit (excl. sw's	AC176
but incl. PCB) £12-90	BC108
10-Instrument Effects circuits £13-56	BC109
PCB for Effects circuits £4-25	BC109C
Power Supply incl. PCB £12.00	BC177
128-NOTE THINE PROCRAMMARIE SECUENCED	BC187
TECHOIL TONE-FROUNAMMADLE SEQUENCEN	8C204
(P.E. Nov/Dec 77)	BC209C
Enables a voltage controlled synthesiser to automatically play	BC213
pre-programmed tunes of up to 32 pitches and 128 notes long.	BC262
Programs are keyboard initiated and note length and rhythmic	BC415
pattern are externally variable. (Please use order codes quoted in	BC478
brackets.)	BD132
Main Circuit (Nov) excl. sw's (K1176-1) £20-60	BF244A
Power Supply (KI1 76-3) £6-05	BF245A
ED Coupter (KIT 76-2) £1-35	MD8001
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Containing a range of first quality miniature ceramic capa- citors. 16160 - 24 - 3 of each value:- 22pf, 27pf 33pf 47pf 68pt	Type	Price	Type	Price	8RAND N	EW — F	ULLY GUAF	Price) Type P	rice Ty	pe	Price	SWIILHES 1965 - 1 pole 12 way 48p* 1966 - 2 pole 6 way 48p* 1967 - 3 pole 4 way 48p*
82pt 16161 – 24 – 3 of each value: – 100pt 120pt, 150pt, 180pt, 220pt, 270pt 330pt, 390pt 18162 – 24 – 3 of each value: – 100pt 19162 – 3 of each value: – 100pt 19162 –	AC127 AC128 AC128K AC128K AC132	£0-18 £0-18 £0-26 £0-20	BC147 BC148 BC149 BC157	*£0.08 *£0.08 *£0.08 *£0.08	BC556 BC557 BC558 BC558 BC559	*£0.14 *£0.13 *£0.12 *£0.14	BIP19 BIP20 BIP19/ 20MP	£0-16 £0-38 £0-38 £0-80	TIP3055 £0 TIS43 £0 TIS90 •£0 UT46 •£0 ZTX107 •£0	-50 2N -24 -22 2N -22 2N -22 2N	13708A 13709 13710 13711	*£0.07 *£0.07 *£0.07 *£0.07	IC PAKS Manufacturers "Fall-outs" which
10102 - 24 - 301 each Value - 47001, 5600f, 6800f, 8200f, 10000f, 15000f, 2200pf, 2200pf, 3300pf 600 ^e 16163 - 21 - 3 of each value - 4700pf, 6800pf, -01uf, -015uf, -022uf, -033uf,	AC134 AC137 AC141 AC141K AC142	£0.20 £0.22 £0.30 £0.20	BC158 8C159 BC167 8C168 BC169	*£0 10 *£0 10 *£0 12 *£0 12 *£0 12	BD115 BD116 BD121 BD124 BD131	£0.50 £0.80 £0.65 £0.70 £0.38	BRY39 BU105 BU105/0 BU204 BU205	£0-45 £1-70 2 £1-95 £1-70 £1-40	ZTX108 £0 ZTX109 *£0 ZTX300 *£0 ZTX500 *£0 2N1613 £0	010 2N 010 2N 012 2N 014 2N 020 2N	3819 3820 3821 3823 43823	£0-20 £0-35 £0-60 £0-60 *£0-12	include functional and part functional Units. These are classed as 'out-of-spec' from the maker's very rigid specifications, but are ideal for learning about 1.C's and
ELECTROLYTIC	AC176 AC176K AC178 AC179 AC180	£0.18 £0.26 £0.25 £0.25 £0.20	BC169C BC170 BC171 BC172 BC173	*£0 12 *£0 10 *£0 10 *£0 10 *£0 12	BD132 BD131/ 132MI BD133 BD135	£0-40 £0-85 £0-40 £0-35	BU208/0 E1222 MJE2955 MJE3055 MJE3440	2 £2.95 £0.38 £0.98 £0.60 £0.52	2N1711 £0 2N1889 £0 2N1890 £0 2N1893 £0 2N2147 £0	20 2N 45 2N 45 2N 30 2N 575 2N	4059 4060 4061 4062 4284	*£0-14 *£0-14 *£0-12 *£0-12 *£0-18	experimental work. 16224 – 100 Gates assorted 7400-01- 04-10 50-60 etc. 16226 – 30 MXI Assorted types 7441- 16226 – 30 MXI Assorted types 7441-
PAKS A range of paks each containing 18 first quality, mixed value miniature electrolytics.	AC180K AC181 AC181K AC187 AC187K	£0-28 £0-20 £0-28 £0-18 £0-20	BC177 BC178 8C179 BC180 BC181	£0 16 £0 16 £0 25 £0 25	BD136 BD137 BD138 BD139 BD140	£0.35 £0.35 £0.40 £0.36 £0.36	MP8113 MPF102 MPF104 MPF105 MPSA05	£0.52 £0.35 £0.38 £0.38 £0.38	2N2148 £0 2N2160 £1 2N2192 £0 2N2193 £0 2N2194 £0	00 2N 38 2N 38 2N 38 2N	4285 4286 4287 4288 4288	*£0.18 *£0.18 *£0.18 *£0.18 *£0.18	47-30-154 etc £120 16227 - 30 Assorted Linear Types 709-741-747-748-710-588 etc 16228 - 8 Assorted types SL403 76013
16201 - values from .47mFD - 10mFD - 60pe 16202 - values from 10mFD - 100mFD - 60pe 16203 - values from 100mFD -	AC188 AC188K AD140 AD142 AD143	£0 18 £0 20 £0 60 £0 85 £0 75	BC182L BC183 8C183L BC184L 8C207	*£0.10 *£0.10 *£0.60 £0.60 £0.11	BD139/ 140MI BD155 8D175 BD176	£0-80 £0-80 £0-60 £0-60	MPSA06 MPSA55 MPSA56 OC22 OC23	*£0.30 *£0.28 *£0.28 £1.50 £1.50	2N2217 £0 2N2218 £0 2N2218A £0 2N2218A £0 2N2219 £0 2N2219A £0	22 2N 22 2N 20 2N 20 2N 20 2N	4290 4291 4292 4293 4293	*£0.18 *£0.18 *£0.18 *£0.18 *£0.55	16229 – 5 I.C's 76110 Еду. to MC1310P-MA767 £1.50*
680mFD 60p* CARBON RESISTOR	AD149 AD161 AD162 AD161/ 161MP	£0 60 £0 42 £0 42 £0 35	BC208 BC209 BC212 8C212L BC213	*£0.11 *£0.12 *£0.11 *£0.11	BD177 BD178 BD179 BD201/	£0.68 £0.68 £0.75	0C24 0C25 0C26 0C28	£1.35 £1.00 £1.00 £0.80	2N2904 £0 2N2904A £0 2N2905 £0 2N2905A £0	18 2N 21 2N 18 2N 20 2N	4923 5135 5136 5138	*£0.65 *£0.10 *£0.10 *£0.10	SEMI CONDUCTOR
PAKS These paks contain a range of Carbon Resistors assorted into the	AF114 AF115 AF116 AF117 AF117	£0 21 £0 21 £0 21 £0 21 £0 21	BC213L BC214 BC214L BC237	*£0-11 *£0-12 *£0-12 *£0-16	BD203 BD204 BD203/ 204MI	£0-80 £0-80	0C25 0C35 0C36 0C70 0C71	£0.90 £0.90 £0.24 £0.15	2N2906A £0 2N2906A £0 2N2907 £0 2N2907A £0 2N296G •0	19 2N 20 2N 22 2N 09 2N	5245 5294 5296 5457	£0 30 £0 34 £0 56 £0 32	I.C's and Zeners. ALL NEW & CODED. Approx 100 pieces - Offering the amateur a lantastic bargain PAK and an enormous saving £2.25
Tonowing groups. 16213 - 60 mixed ∦w 100ohms - 820 ohms 60p* 16214 - 60 mixed ∦w 1K ohms - 8·2K ohms 60p*	AF124 AF125 AF126 AF127	£0.30 £0.30 £0.30 £0.32	BC251 BC251A BC301 BC302	*£0.16 *£0.16 £0.28 £0.28	BDY20 BDX77 BF457 8F458 BF459	£0.80 £0.90 £0.37 £0.37 £0.38	TIC44 TIC45 TIP29A TIP29B TIP29C	*£0.35 £0.40 £0.52 £0.50	2N2926Y*£0 2N29260*£0 2N2926R*£0 2N2926B*£0 2N3053 £0	08 2N 08 2N 08 2N 08 2N 08 2N 16 2N	5458 5459 5551 6027 6121	£0.32 £0.35 £0.36 £0.39 £0.70	MAMMOTH I.C. PAK
16215 - 60 mixed ¹ / ₃ w 10K ohms - 83K ohms 600° 16216 - 60 mixed ¹ / ₃ w 100K ohms - 820K ohms 600° 16217 - 40 mixed ¹ / ₃ w 100 ohms -	AF139 AF180 AF181 AF186 AF239	£0 58 £0 60 £0 58 £0 35	BC303 BC304 BC327 BC328 BC337	£0.28 £0.38 *£0.16 *£0.15 *£0.15	8F594 BF596 BFR39 BFR40 BFR79	*£0 30 *£0 28 £0 24 *£0 25 *£0 28	TIP30A TIP30B TIP30C TIP31A TIP31B	£0.50 £0.60 £0.45 £0.47	2N3054 £0 2N3055 £0 2N3414 *£0 2N3415 *£0 2N3416 *£0	40 2N 40 40 16 40 16 40 29 40	6122 311 313 316 317	£0-70 £0-38 £0-95 £0-95 £0-40	16223 - Approx 200 pieces assorted fall-out integrated circuits, including: Logic, 74 series, Linear, Audio, and D.T.L. Mandy coded devices, but
820 ohms 60pe 16218 - 40 mixed 1 K ohms 8-2K ohms 60pe 16219 - 40 mixed 1 w 16219 - 40 mixed 1 w 10217 - 40 mixed 1 w 10218 - 40 mixed 1 w	AU102 AU103 AU104 AU110 AU113	£0.38 £1.20 £1.18 £1.00 £1.00	BC338 BC440 BC441 BC460 BC461	*£0.15 £0.30 £0.30 £0.38 £0.38	BFR80 BFX29 8FX30 BFX84 BFX85	*£0.28 £0.22 £0.30 £0.22 £0.24	TIP31C TIP32A TIP32B TIP32C TIP41A	£0.49 £0.51 £0.53 £0.49	2N3417 •£0 2N3614 £1 2N3615 £1 2N3616 £1 2N3646 •£0	29 40 00 40 00 40 00 40 00 40	326 327 346 347 348	£0.40 £0.45 £0.45 £0.65 £0.80	UNTESTED SEMI-
16220 − 40 mixed ½w 100K ohms − 820K ohms − 80 mixed ½w 100K ohms − 16230 − 60 mixed ½w 1 Meg − 10 Meg ohms 60pe	8C107A BC1078 BC107C BC108A BC108B	£0-08 £0-08 £0-08 £0-08 £0-08 £0-08	BC477 BC478 BC479 BC547 BC548	£0-20 £0-20 £0-20 *£0-12	BFX86 BFX87 BFX88 BFX90 BFX90	£0.25 £0.22 £0.22 *£0.55 £0.16	TIP41B TIP41C TIP42A TIP42B TIP42B	£0.51 £0.53 £0.53 £0.55 £0.57	2N3702 ±0 2N3703 ±0 2N3704 0 2N3705 ±0 2N3705 ±0	08 40 08 40 07 40 07 40 07 40	360 361 362 406 407	£0.36 £0.36 £0.38 £0.45 £0.35	CONDUCTOR PAKS 16130 - 100 Germ. gold bonded OA47 diodes 16131 - 150 Germ. point contact
10 Meg ohms COMPONENT	BC109C BC109B	£0.08 £0.08	BC549	•£0.12	BFY51	£0.16	TIP2955	£0.65	2N3707 •£0 2N3708 •£0	08 40 07 40	409 409	£0 52 £0 75	100mA OA70/81 diode 60p 16132 - 100 Silicon diodes 200mA OA200 60p 16133 - 150 Silicon fast switch diode
PAKS 16164 - 200 Resistor mixed value approx (Count by weight) 60p*				14	- SEI	STULLO	2 1 1	LIU	3				16134 50 Silicon rectifiers top
16165 - 150 Capacitors mixed value					FULL SPE	CIFICATI	ON GUARA	RICE. NTEED.					hat 750mA 60p 16135 - 20 Silicon rectifiers stud type 3 amp 60p
approx (Count by weight) 60p° 16.166 50 Precision resistors Mixed values 60p 16.167 two resistors mixed values	Type 7400 7401	Price 0-14 0-14	Туре 7409 7410	Price 0.15 0.14	Type 7441 7442	Price 0.64 0.64	Type 7482 7483	RICE. NTEED. 0.85 0.85	Tγpe Pi 7493 0 7494 0	rice Typ 040 74 088 74	pe 1 2 2 123	Price 0.50 0.70	hat 750mA 60p 16135 - 20 Silicon rectifiers stud type 3 amp 60p 16136 - 50 400mW zeners D07 case 60p 16137 - 30 NPN transistors BC107/8 plastic 60p ⁴ 16138 - 30 PNP transistors BC177/178
approx (Count by weight) 600+ 16166 50 Precision resistors Mixed values 600 16167 - w resistors mixed values 80 600+ 16168 5 pieces assorted ferrite rods 600+ 16169 - 2 Tuning gangs MWW 600+	Type 7400 7401 7402 7403 7404 7405 7406 7406	Price 0-14 0-14 0-15 0-15 0-15 0-15 0-30	Type 7409 7410 7411 7412 7413 7414 7416	Price 0.15 0.14 0.23 0.23 0.27 0.58 0.28	BI-PAK FULL SPE 7441 7442 7445 7445 7446 7447 7448 7425	Price 0 64 0 64 0 90 0 90 0 78 0 80 0 48	Type 7482 7483 7484 7485 7486 7489 7490	RICE. NTEED. Price 0.85 0.85 0.98 1.00 0.30 2.50 0.42	Type Pr 7493 0 7494 0 7495 0 7496 0 74100 1 74110 0 74118 0	rice Typ 040 74 088 74 080 74 00 74 00 74 00 74 00 74 00 74 090 74	pe 122 123 141 154 180 181 190	Price 0.50 0.70 1.20 1.00 2.00 1.40	hat 750mA 60p 16135 - 20 Silicon rectifiers stud ype 3 amp 60p 16136 - 50 400mW zeners 007 case 60p 16137 - 30 NPN transistors BC177/178 plastic 60p 16138 - 30 PNP transistors BC177/178 plastic 800 PNP transistors BC177/178 plastic 60p 16130 - 25 PNP T039 2N2905 Silicon - 25 PNP T039 2N2905 600
approx (Count by weight) 60p* 16166 50 Precision resistors Mixed values 60p 16167 we resistors mixed values 80 60p* 16168 5 pieces assorted ferrite rods 60p* 16169 - 2 Tuning gangs MW/LW VHF 60p* 16170 - 1 Pack wire 50 meters assorted colours single strand 60p 16171 - 10 Reed switches 60p* 16172 - 3 Micro switches 60p*	Type 7400 7401 7402 7403 7405 7405 7406 7406 7407 7408	Price 0-14 0-14 0-15 0-15 0-15 0-15 0-30 0-30 0-30 0-15	Type 7409 7410 7411 7412 7413 7414 7416 7416 7440	Price 0.15 0.14 0.23 0.27 0.58 0.28 0.28 0.15	BI-PAK FULL SPE 7441 7442 7445 7446 7446 7446 7447 7448 7475 7480 7481	Price 0 64 0 64 0 90 0 90 0 78 0 80 0 48 0 50 0 95	Type 7482 7483 7484 7485 7486 7486 7489 7490 7490 7490 7491 7492	RICE. NTEED. Price 0.85 0.85 0.98 1.00 0.30 2.50 0.42 0.75 0.45	Type Pr 7493 0 7495 0 7495 0 7496 0 74100 1 74110 0 74118 0 74119 1 74121 0	rice Typ 140 74 88 74 80 74 80 74 50 74	pe 122 123 141 154 180 181 190 198 199	Price 0.50 0.70 1.20 1.00 2.00 1.40 1.85 1.85	hat 750mA 60p 16135 - 20 Silicon rectifiers stud type 3 amp 60p 16136 - 50 400mW zeners D07 case 60p 16137 - 30 NPN transistors BC1778 plastic 60p ⁴ 16138 - 30 PNP transistors BC177178 plastic 60p 16143 - 25 NPN T039 2N697/2N1711 silicon 60p 16141 - 30 NPN T018 2N706 silicon switching 60p 16143 - 30 NPN plastic 2N3906 16143 - 30 NPN plastic 2N3906
approx (Count by weight) 600° 16166 50 Precision resistors Mixed values 600° 16167 w resistors mixed values 80° w resistors mixed values 16168 5 pieces assorted ferrite rods 600° 16170 - 1 Pack wire 50 meters assorted colours single strand 600° 16171 - 10 Reed switches 600° 16171 - 10 Aread switches 600° 16172 - 3 Micro switches 600° 16174 - 5 Metal jack sockets 3 x 3-5 mm 2 x standard switch types 600°	Туре 7400 7401 7402 7403 7404 7405 7406 7406 7406 7407 7408	Price 0.14 0.14 0.15 0.15 0.15 0.30 0.30 0.30 0.15	Туре 7409 7410 7411 7412 7413 7414 7416 7416 7417 7440	Price 0.15 0.14 0.23 0.27 0.58 0.28 0.28 0.28 0.15 Price 0.20	FULL 3PE FULL 3PE 7441 7441 7442 7445 7446 7446 7446 7447 7448 7480 7480 7481	Price 0 64 0.64 0.90 0.90 0.78 0.80 0.48 0.50 0.95 VIOS	Type 7482 7483 7484 7485 7486 7486 7489 7490 7491 7492 SIC'	RICE. NTEED. 9 Price 0.85 0.98 0.98 0.98 0.98 0.98 0.45 0.45 0.45 0.45 0.45 0.45	Type P1 7493 0 7494 0 7495 0 7496 0 74100 1 74110 0 74118 0 74119 1 74121 0	rice Tyr 940 74 988 74 970 74 980 74 980 74 990 74 195 74 930 74 195 74 930 74 195	pe 122 123 141 154 180 181 198 199 199	Price 0.50 0.70 1.25 1.00 2.00 1.85 1.85 Price 0.23	hat 750mA 600 16135 - 20 Silicon rectifiers stud ype 3 amp 600 16136 - 50 400mW zeners D07 case 600 16137 - 30 NPN transistors BC17/8 plastic 600 16138 - 30 PNP transistors BC17/7178 plastic 600 16140 - 25 NPN T039 2N697/2N1711 silicon 600 16141 - 30 NPN To18 2N706 silicon. switching 600 16143 - 30 NPN plastic 2N3906 16144 - 30 PNP plastic 2N3905 16144 - 30 Germ. 0C71 PNP 600 16145 - 30 Germ. 0C71 PNP 600 16146 - 15 plastic power 2N3056 NPN
approx (Count by weight) 600° 16166 50 Precision resistors Mixed values 600° 16167 - w resistors mixed values 80° - w resistors mixed values 80° - 10° - 10° 16168 5 pieces assorted ferrite rods 60° 16170 - 10° - 10° 16170 - 10° 10° 10° 10° 10° 10° 10° 10°	Type 7400 7401 7402 7403 7405 7406 7405 7406 7407 7406 7407 7408	Price 0.14 0.15 0.15 0.15 0.30 0.30 0.30 0.30 0.315 0.15 0.15 0.15 0.18 0.98 0.98	Туре 7409 7410 7411 7412 7413 7414 7417 7416 7417 7416 7417 7440 СD4012 CD4012 CD4013 CD4015 CD4015 CD4015 CD4015	Price 0.15 0.23 0.23 0.27 0.58 0.28 0.28 0.15 Price 0.20 0.52 0.98 0.50 0.98 0.50	BI-PAK FULL 3PE FULL 3PE 7441 7445 7445 7445 7446 7447 7446 7447 7448 7475 7480 7481 7481 7481 7481 7480 7481 7481 7481 7480 7481 7480 7481 7480 7481 7480 7481 7481 7485 7480 7481 7485 7480 7481 7485 7480 7481 7485 7480 7481 7485 7485 7480 7481 7485 7485 7485 7485 7485 7485 7485 7485	Price 0 64 0 64 0 90 0 780 0 780 0 780 0 90 0 780 0 90 0 90 0 90 0 90 0 90 0 90 0 90 0	Wes I INP 0 ON GUARA 7483 7483 7484 7485 7486 7486 7489 7490 7491 7491 7492 S ICC Type CD 4031 CD4035 CD4037 CD40401 CD40404	RICE. NTEED. Price 0.85 0.98 1.00 0.30 2.50 0.42 0.75 0.45 0.45 0.45 0.45 0.95 0.95 0.95 0.95 0.95	Type Pr 7493 0 7494 0 7495 0 7496 0 74100 1 74118 0 74118 0 74118 0 74119 1 74121 0 74121 0 74121 0 74121 0 74121 0 74121 0 74124 0 74140 0 741400 0 741400 0 741400 0 741400 0 74140000000000000000000000000000000000	rice Typ 340 74 388 74 380 74 380 74 300 74 300 74 300 74 300 74 300 74 300 74 300 CD 10 CD 55 CD 10 CD 10 CD	De 122 123 141 154 180 199 199 De 4071 4072 4082 4082 40511	Price 0.50 0.70 1.00 1.00 1.85 1.85 1.85 0.23 0.23 0.23 0.23 0.23 1.30	hat 750mA 600 16135 - 20 Silicon rectifiers stud ype 3 amp 600 16136 - 50 400mW zeners D07 case 600 plastic 600 16137 - 30 NPN transistors BC17/8 plastic 600 16138 - 30 PNP transistors BC17/7178 plastic 600 16138 - 25 NPN T039 2N697/2N1711 silicon - 25 PNP T039 2N2905 16141 - 30 NPN T018 2N706 silicon 16142 25 NPN 8FY50/51 600 16144 - 30 PNP plastic 2N3905 silicon - 600 16144 - 30 PNP plastic 2N3905 silicon 600 16145 - 30 Germ 0C71 PNP 600 16145 - 30 Germ 0C71 PNP 600 16145 - 30 Germ 0C71 PNP 600 16146 - 15 plastic power 2N3055 NPN N0220 Case 1:20 16147 - 10 T03 metal 2N3055 NPN 1:20 16148 - 20 Unijunction transistors 1649 - 20 Unijunction transistors
approx (Count by weight) 600° 16166 50 Precision resistors Mixed values 600° 16167 - w resistors mixed values 80° - 16170 - 1 Pack wire 50° 16172 - 3 Micro switches 60° 16174 - 5 Metal jack sockets 3 x 3.5 mm 2 x standard switch types 16175 - 30° Paper condensers - mixed 16176 - 20° Electrolytics trans. 190° 16176 - 20° Electrolytics trans. 190° 16178 - 5 Mains slide switches 80° 16179 - 20° Assorted tag strips and 16179 - 30° Assorted tag strips and 16180 - 15 Assorted control knobs 60° 16180 - 15 Assorted control kn	Type 7400 7401 7402 7403 7405 7406 7406 7406 7408 Type CD4000 CD4001 CD4001 CD4001 CD4008 CD4008 CD4008 CD4008 CD4010 CD4011	Price 0.14 0.15 0.15 0.15 0.30 0.30 0.30 0.15 0.18 0.18 0.18 0.18 0.18 0.18 0.18 0.18	Туре 7409 7411 7412 7413 7414 7416 7417 7440 СD4012 СD4013 СD4015 СD4015 СD4015 СD4015 СD4015 СD4019 СD4019 СD4021	Price 0.15 0.14 0.23 0.28 0.28 0.28 0.15 Price 0.20 0.52 0.988 0.50 0.50 0.50 0.51 0.98	BI-PAK FULL 3PE 7441 7442 7445 7446 7447 7446 7447 7448 7480 7480 7480 7481 CL	Price 0 64 0.64 0.90 0.90 0.90 0.90 0.95 0.95 Price 0.90 0.20 0.80 0.20 0.20 0.20 0.80 0.55 0.55	Wes Inv P on GUARA 7483 7484 7485 7486 7486 7490 7491 7492 S IC' Type CD403 1 CD4035 CD4043 CD4044 CD4044 CD4044 CD4045	RICE. NTEED. Price 0.85 0.85 0.85 0.85 0.85 0.85 0.35 0.250 0.250 0.42 0.75 0.45 Price 2.20 1.30 0.95 0.95 0.85 0.85 0.85 0.42 0.42 0.42 0.45 0.45 0.45 0.45 0.45 0.42 0.45 0.45 0.45 0.45 0.42 0.45	Type Pr 7493 C 7494 C 7495 C 7496 C 7496 C 74100 1 74110 C 74112 C 74121 C 741	rice Tyr 140 74 188 74 170 74 180 74 180 74 100 74 185 74 100 74 185 74 100 74 100 CD 100	De 122 123 141 154 180 198 199 199 199 14071 4081 4081 4082 44081 4516 4518 4520	Price 0.70 0.70 1.20 1.40 1.85 1.85 0.23 0.23 0.23 0.23 0.23 1.30 0.23 1.60 1.40 1.25 1.25	hat 750mA 600 16135 - 20 Silicon rectifiers study upe 3 amp 600 16136 - 50 400mW zeners 007 case 600 16137 - 30 NPN transistors BC1778 plastic 600° 16138 - 30 PNP transistors BC177178 plastic 600° 16138 - 30 PNP transistors BC177178 plastic 600° 16140 - 25 PNP T039 2N897/2N1711 silicon 600° 16141 - 30 NPN T018 2N706 silicon. switching 600 16144 - 30 PNP plastic 2N3906 silicon 600° 16144 - 30 Germ. 0C71 PNP 600 16144 - 30 Germ. 0C71 PNP 600° 16144 - 10 T03 metal 2N3055 NPN T0220 case f1.20 16146 - 20 Uniunction transistors 16146 - 10 1 amp SCR T039 f1.20 16150 - 8 3 amp SCR T066 case f1.20
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FROM





MONEY

PROBABLY one thing that is of universal interest is cash. As electronics enthusiasts we have particular interests in money. Will the chancellor reduce the tax rate? Surely that interests all earners? Will he change the V.A.T. rates? More about that later. Will the government impose further wage restraints?

We now know the answers to most of these points—this is being written a few days after the budget. Although we doubt if many M.P.s are constructors or, even if they have the interest, have time to read this, however a couple of points are worth making ...

CAREERS

At the time of writing it is still not obvious if we will be returning to "free collective bargaining". For the last two years many in the electronics industry have found that their qualifications and experience no longer give them any monetary advantage. For some time we have seen the "electronics workers" on the move; more applicants for jobs than ever before simply because it is one way to get ahead financially.

It may be argued that this movement is healthy for the industry although we

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Mike Kenward Gordon Godbold ASSISTANT EDITOR Mike Abbott TECHNICAL EDITOR Alan Turpin PRODUCTION EDITOR David Shortland TECH. SUB EDITOR

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doubt it. What it does mean is that people are finding a way of beating the system and that they are finding it necessary to do this.

It has now been suggested that four year degree courses should be introduced. Great, we are in favour. The rate technology is moving we are going to need some of the best, most highly educated people we can find, and make no mistake the British educational system can produce the best. It may mean fewer degrees but we will need the elite; there is already a danger of too many chiefs and too few indians. But will we find these people, can we continue to encourage them to take a degree course, and a longer one than at present-perhaps not if the rewards are not there at the end of the rainbow!

V.A.T.

No doubt many readers, in particular those in the component supply business, will remember all the fuss a while back over the introduction of two rates of V.A.T. to electronic components. It may be necessary to remind some that this system is still in force.

It means that one component—say a loudspeaker—can be subject to different rates if sold for different applications. It would probably be interesting to carry out a survey now that manufacturers, distributors and retailers have been living with this system for some time. For instance, it may be that proportionately fewer speakers are now made and sold for radio applications—an application that is subject to the higher tax rate although just as many will find their way into radios. This is no doubt more prevalant in the amateur world than anywhere else. ٩.

We can only hope that at some stage someone will be able to make enough fuss about the component situation to get the whole thing reviewed and then maybe, just maybe, we could get back to the lower rate for all components. This would reduce the workload of all concerned in their supply and, most important, reduce the price of some of them to us.

One point that we are thankful for is that this product (P.E.) is not subject to V.A.T. We are sure that all readers will stand behind us on that one, even if their views do not concur with those expressed above.

Mike Kenward

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★ PINPOINTS ILLUSIVE WIDE TOLERANCE ELECTROLYTICS
★ ENABLES PRECISION MATCHING OF CAPACITORS
EASY TO USE, EASY TO READ
THIS PIECE OF EQUIPMENT IS JUST WHAT YOU NEED!

THE need to measure capacitance accurately and quickly arises fairly often in electronics. The construction of Wien bridge oscillators, precision timers, active filters and tuned circuits all demand component selection if best results are to be obtained. In the past the most common methods of measurement have been the use of a capacitance bridge or the measurement of reactance using a multimeter and a.c. source. Both methods are cumbersome and lead to instruments with non linear scales, and in the form usually constructed by amateurs have limited accuracy and range. The instrument described here overcomes most of these problems offering direct measurement of capacitance over a range of eight decades on a linear scale.

PRINCIPLE OF OPERATION

The operation is based on the integrator shown in Fig. 1a. The behaviour of this circuit is described by the mathematical expression:

$$Vout = -\frac{1}{RC} \int V_{IN} dt$$
 (1)

Solving this equation gives

$$V_{OUT} = -\frac{V_{IN}}{RC} \Delta t$$
 (2)

By making VOUT = -VIN and rearranging equation 2 as an expression for C, we have

$$C = \frac{\Delta t}{R}$$
(3)

It follows that we can measure the capacitor in the feedback loop of the integrator by measuring the time taken for the output voltage to fall to $-V_{IN}$. The measurement of time interval is the most accurate measurement available and it follows that this need not limit the accuracy and precision of the measurement. In the meter to be described the limitations of accuracy are imposed by the meter movement, range resistors, and timer capacitor, but the accuracy could be greatly improved if required, by measuring time digitally.

CIRCUIT DESCRIPTION AND OPERATION

The operation of the instrument is indicated in the block diagram Fig. 1b. The integrator is fed from a reference voltage generator and the integrator output is compared with this voltage in an inverting comparator. An analogue timer, based on a second integrator, measures the time taken for the output of the integrator to fall to -VREF, and the output is shown on a panel meter. In use the unknown capacitor is connected across the input terminals and the RESET/START button depressed. At the end of the measurement period a green l.e.d. indicates that measurement is complete. A second push of the RESET/START button discharges the unknown capacitor and resets the circuit for a further measurement. The reset state is indicated by a red l.e.d. and is automatically entered on initial application of power.

The complete circuit is given in Fig. 2 and its operation is best understood with the aid of the timing diagrams in Fig. 3. The instrument consumes little power and may be battery operated. In Fig. 2, can be seen the supply arrangement and the VREF generator, which is a simple zener diode source buffered by an operational amplifier connected as a voltage follower. The temperature stability of this circuit is not particularly good but is maximised by ensuring a constant Zener current of about 1mA. This is insignificant compared to the performance limitations of the analogue meter movement. If a digital version was constructed it would be worthwhile using a more expensive reference diode The reference voltage is adjusted to 1V by VR1. It here should be noted that this adjustment is only needed in an analogue instrument because VREF is also used in the time measurement circuit.

The reference voltage is supplied to the first integrator IC2 via the transmission gate IC7a and one of the range resistors. The unknown capacitor is connected as the feedback element of this integrator in parallel with the transmission gate IC7b. The negative going integrator output is compared with VREF at the summing junction of IC3 which operates open loop as a comparator. The output voltage of the comparator is

SPECIFICATION

Ranges Accuracy*	8 ranges: 300pF-3000µF in decade steps Typically 2·5% on all ranges (dependent
	and capacitor)
Precision	1% on range 1 (300pF)
	0.25% all other ranges
Measurement time	3 seconds max from 3µF-3000µF
	300ms all other ranges
Power consumption	25mA max (each battery)
*Desite to define	d as the relative standard deviation for
repeated measureme from the equation:	a as the relative standard deviation for ents on the same capacitor, calculated
	15 14 512

Percentage Precision = $\frac{\sqrt{\frac{\Sigma (x-\bar{x})^2}{n}}}{x} \times 100$

where x is the mean of n measurements, and x is an individual measurement. The precision is a guide to the accuracy which can be obtained if measurements are referred to a known value capacitor.

monitored by the indicator circuit comprising R15, R16, TR1 and D3, and is also fed into the NOR gate IC4a. The comparator output is gated with the output of the RESET/START generator, and the resultant output of IC4a is the actual time taken for Cx to charge to the reference voltage (which relates to the value of Cx). In a digital instrument this time would be measured directly with a clock generator.

In the analogue instrument described here, time is measured by the second integrator comprising IC5, C3 and one of the range resistors R10-R14. The way this circuit measures time is apparent from equation 2. We see that in Fig. 1a the output voltage of the integrator is proportional to the time for which the input voltage VIN is connected, for constant R and C. In the meter unit the input voltage to the second integrator is VREF and it is connected through the transmission gate IC7c which is open only during the integration period of the unknown capacitor Cx. This time measuring integrator is reset by the transmission gate IC7d. It is this second integrator which requires VREF to be set accurately to 1V in order to measure time accurately. It is further important that the time measuring integrator capacitor is a precision component. Although 1% precision capacitors are expensive it is worth pointing out that this design requires only one such component in order to cover an eight decade range. This is because the input resistors of the two integrators can both be varied.

In the RESET AND RUN circuitry of Fig. 2 half of a dual JK FLIP FLOP is used, connected as a simple toggle circuit. The initial condition of reset is established by holding the set input of the CD4027 high during the period when power is first applied. As C5 charges through R22 the output of IC4d eventually goes low which then enables the CLOCK input. The CLOCK input is driven from the output of the monostable comprising IC4b, c, R21, C4 and S3. The input of IC4b at pin 1 is normally held low by R20, C4 is uncharged, and the input applied to IC4c is high. The output of IC4c is therefore low and this is applied to the second input of IC4b. As soon as pin 1 of IC4b receives a positive pulse from S3 via R19, the output goes low (being a NOR gate). This is transmitted via C4, which can only charge slowly via R21, to the input of IC4c whose output therefore goes high. This high is applied to the input of IC4b which therefore remains low even if the other input has now gone high. This condition remains until C4 has charged through



Fig. 1 (b) Block diagram of Linear Capacitance Meter

R21 when the circuit reverts to its initial state. The long time constant of C4, R21, was chosen by experience to prevent false output pulses. It is surprising how long human fingers dwell over pushbuttons!

INITIAL RESET

Fig. 3 shows the initial conditions as produced by the "power-up reset" function. The first integrator output VINT is held at zero because IC7b is open and IC7a is closed. The second integrator output is similarly zero. As soon as the push button is pressed the first and second integrators ramp negatively until the first integrator crosses -VREF. At this point the comparator IC3 changes state and disenables the transmission gate IC7c via the NOR gate IC4a. The timing capacitor C3 remains charged because the off resistance of the transmission gates IC7c and IC7d is extremely high and the input bias current of the CA3140 is extremely low. The output of IC2 continues to ramp negatively until the amplifier saturates. This is close to the negative rail. The circuit is now in a stable state with the meter displaying the measured capacitance. Capacitor C3 will of course be discharging slowly, but the rate is measured in only fractions of a millivolt per second. In the author's instrument no discernable movement of the pointer takes place in a one minute period after completion of a measurement. A second press of the push button initiates a reset and returns the circuit to its initial state ready for the next measurement.

CONSTRUCTION

The circuit was built on a piece of stripboard measuring 36 conductors by 43 holes (see Fig. 4.) and then mounted against the back of the meter, using the meter screws for fixing. The case size or shape is not too important, but the prototype used a plastics box of dimensions $172 \times 100 \times 55$ mm, and was fairly compact at this size. The battery holders were clamped under a stiff cardboard plate, and fastened by two 6BA nuts and screws.



Fig. 2. Circuit diagram. Cx is the capacitor under test. The MINT line is the measurement period integration signal



Fig. 3. Timing diagram

COMPONENT SELECTION

A number of components call for some comment, and a few are best selected during setting up of the meter. The use of the relatively new CA3140 operational amplifier is of prime importance in the design. This device is one of a new family of BIMOS amplifiers which incorporate the advantages of MOS and BIPOLAR technologies on a single chip. They seem destined to take over from the 741 in many areas because they can offer greatly improved performance, albeit currently at somewhat increased cost. Both integrators in the present circuit use the CA3140 to minimise errors caused by the integrator capacitor charging (or discharging) via the input bias current of the amplifier. The maximum input bias current for the device is 50pA (typical 10pA) at 25°C compared with $1\mu A$ for the standard 741. This current is small compared with the smallest charging current used in the circuit, which is 100nA, and therefore is not a significant source of error. The CA3140 also has a very creditable slew rate of $9V/\mu S$ making it some 15-18 times faster than the 741, and therefore quite useful as a comparator. In this circuit the positive and negative extremes of the comparator output are directly compatible with the CMOS logic circuits. The CA3140 does contain mos transistors in its input stage and is susceptible to the same problems of static as any смоs logic i.c.





Electrolytic capacitors may have tolerances as wide as -25 + 100%. In multiple time constant systems the meter can be used to ensure that, for example, "period A" really does outlive "period B" when near-value capacitors are used

Fig. 4. Component layout (actual size). Ensure that the fixing holes are spaced correctly for the meter being used

In general the accuracy of the instrument is dependent on the range resistors, the timer capacitor C3, and the reference voltage VREF. The latter can be set accurately if the constructor has access to a digital voltmeter, otherwise it can be set up with a multimeter to $\pm 2\frac{1}{2}$ %. The range resistors should be high stability 2% metal oxide types and could be selected with a digital multimeter to advantage. Note, however, that the range resistors are in series with the transmission gates IC7a and IC7c. They therefore define the charging currents only when they are large compared with the "on" resistance of the gates. The typical resistance of a CD4066 at room temperature can be as much as 550 Ω . Ideally this device should be selected for low "on" resistance but if this is impracticable the most serious error occurs on range I and this can easily be circumvented.

When range 1 is selected the input resistor of the second integrator is nominally $1k\Omega$. Thus with a typical CD4066 the actual input resistance defining the charging current would be about $1.12k\Omega$ with a worst case of $1.55k\Omega$. This would obviously give rise to errors of 12% and 55% respectively, with the indicated capacitance being too small. The simplest solution is to select this resistor by direct calibration with a known capacitor. This is quite simple on this particular range because silver mica capacitors of 100pF-200pF of 1% tolerance are cheaply and easily available.

The procedure for calibrating the instrument on this range is therefore as follows. Using $1k\Omega$ as the range resistor, press the RUN button and record the reading. Now reset the meter by pressing the button again and place a known 100pF (or similar value) capacitor across the input terminals and press the button. The difference between the second and first readings should be the value of the capacitor. If the reading is too low reduce the value of the range resistor and vice versa. Alternatively a $1k\Omega$ skeleton preset could be fitted. Note that the reason for making two measurements is that the circuit does not contain provision for eliminating stray wiring capacitance which will be of the order of 5–10pF. This only affects the lowest range and can be otherwise neglected.

The meter multiplier resistances R17, R18, have not been defined because the actual values will depend on the meter movement used. This is not critical since the output of the second integrator is of low impedance and can swing close to the negative rail. Thus any voltmeter with a sensitivity of 5V f.s.d. or better could be used directly without R17/R18, because the output is defined in terms of volts/µF according to Table 1. Alternatively if a current meter is available the multiplier resistors R17/R18 must be used. Two resistors have been specified because in general it is not possible to select a single resistor from the preferred values available to make a meter multiplier. Note that it is essential to know or to measure the internal resistance of a current meter in order to calculate the multiplier resistance. This should be borne in mind if a purchase is made since many of the cheapest meters available through retail outlets do not have this information supplied. The method of calculating R17/R18 is best illustrated by example. The author had available a $30\mu A$ movement of internal resistance 1400 Ω . This is converted to a 3V f.s.d. meter by a multiplier resistor fulfilling the equation:

$$Ef.s.d. = If.s.d. \times (Rmeter + Rmultiplier)$$

where Ef.s.d. is the required full scale deflection voltage, and If.s.d. is the meter sensitivity for full scale deflection. Thus in the present example

$$3.0 = 3.0 \times 10^{-5} (1400 + \text{Rmultiplier})$$

 $\therefore \text{ Rmultiplier} = 10^5 - 1400$
 $= 98600\Omega$

COMPONENTS

Resistors	
R 1	4·7kΩ
R2	100kΩ
R4, R8, R9, R12	10kΩ 2% m.o. (4 off)
R 5, R 11	100kΩ 2% m.o. (3 off)
R6, R10	1MΩ 2% m.o. (2 off)
R 7, R 14	10MΩ 2% m.o. (2 off)
R3, R13	1kΩ 2% m.o.
R15, R23	27kΩ (2 off)
R16	470Ω
R17, R18	see text
R19	1kΩ
R20	1MΩ
R21	10M Ω
R22	100kΩ
K24	68075
All resistors #W	5% unless otherwise stated.

All resistors $\frac{1}{4}$ W 5% unless otherwise stated. R7 and R14 may be difficult to obtain and so selection from 5% tolerance resistors may be necessary

Potentiometer

VR1 50kΩ min preset

Capacitors

C1, C2 C3 C4 C5	1,000µF/12V elect (2 off) 1µF 1% polycarbonate (Electrovalue) 220nF polyester
C5	100nF polyester

Transistors and diodes

D1	BZY88 C3V9
D2	i.e.d. red
D3	I.e.d. green
D4	1N4001
TR1, TR2	BC109

Integrated circuits

741
CA3140 (3 off)
CD4001
CD4027
CD4066

Switches

- S1 d.p.s.t. switch
- S2 2-pole 8-way (make before break)
- S3 push to make (momentary contact) switch

Miscellaneous

B1, B2 4 off HP7 each. Battery holder to suit 4mm terminals red, black 1mm sockets red, black Pointer knob Meter (see text) Clips for I.e.d.s Plastics box (prototype used 172 × 100 × 55mm) Stripboard 0-1in matrix





This can be made up from $91k\Omega$ and $7.5k\Omega$ to an acceptable accuracy from E24 resistors. It is worthwhile making the output voltage of the final integrator available externally if a digital voltmeter is available, because although the accuracy of the instrument is limited, the precision or reproducability is very high indeed. This makes it possible to measure capacitors to better than $\pm 0.2\%$ providing a known standard capacitor is available to correct the readings.



SKYLAB

In March this year space engineers of the USA successfully re-established contact with Skylab. The space station has been dormant for four years. It was necessary to do this as a first step towards the boosting of the vehicle to a higher Earth orbit.

In April a further manoeuvre was made. Another will be needed in or about October 1979. These actions are required because Skylab is losing height faster than was predicted when it was launched in May 1973.

At that time it was expected that Skylab would remain in stable orbit until the 1980s when the Shuttle would be available to assist.

The fear that made these decisions necessary was that Skylab might make an unscheduled and perhaps disastrous reentry and not burn up without mishap. The effect of the early demise of Cosmos 954 no doubt has had some effect on the decision about Skylab. It is perhaps relevant to point out that large units may not completely burn up and that some 5 to 10 per cent may fall to Earth. Should the second action on Skylab, in April, subsequently prove ineffective then the vehicle can be targeted to remote regions of the oceans.

The first corrective move commenced in March. A series of commands were sent to Skylab to activate the receivers and speaker systems. It was expected that the telemetry system would operate even after the interval of four years. The condition of the temperatures and pressures, the orientation of the vehicle in space and the gas that was still available for its thruster motors. The first command did in fact bring a response but this was lost after two minutes.

As the space station passed over the

Bermuda tracking station some 92 minutes later a second command was sent. There was a "hum" response but no data. The flight control then resorted to the back-up system for communication. This was entirely successful. Further commands were then sent to charge the station's batteries from the solar panels. Finally the computer was checked out on March 13th.

If the plans work out successfully then Skylab may have a second lease of life. Studies are already being undertaken as to how the station could be used in conjunction with the European Spacelab brought up in the Shuttle.

If the problems are solved then Skylab might well become the first space platform for the construction of large space structures to collect energy from the Sun or as a launch pad for other missions.

COSMOS 954

The post mortem on Cosmos 954 has so far yielded a few scraps and a little information. The parts that have been found have been radio active at about 20 roentgens/hour. One large piece found in a crater two metres in diameter showed a level of 100 roentgens/hour and was removed in a safety package for examination. New equipment was sent out to search for parts which might have been buried in the ice.

From the information available it has been estimated that the satellite was of 6,000 kilogrammes. Specialists have estimated that a satellite of such mass would have a residue of 5 to 10 per cent which was not vaporised. It would seem therefore that it was as the USSR said, the satellite was intended and indeed did burn up almost completely on re-entry.

The nuclear reactor on board was thought to be about 50 kilogrammes of enriched uranium. It is worth noting that a killer satellite could be used in emergencies of the Cosmos 954 type. It is perhaps a significant point that a few days after the scare the Soviet Union launched Cosmos 967, and in a circular orbit at 66 degrees from the equator. Eight days later Cosmos 970 was launched into a highly elliptical orbit inclined at 65 degrees to the equator. Before one whole revolution had been completed Cosmos 970 had been moved to a more circular orbit and at the same inclination as Cosmos 967, so that it passed close to Cosmos 967. On command from control on the ground Cosmos 970 blew up. It was considered that this was a satisfactory indication that Cosmos 967 would have been destroyed had control so wished when they were close together.

It should be noted that both the USSR and the USA are examining the use of high energy laser beams for use against spacecraft. Already a level of generation has reached hundreds of joules per pulse.

The USA has been attempting to close the gap in defence against satellite vulnerability. One of the most sensitive areas is that of solar "sails". Although they are transparent to most forms of radiation they are susceptible in the region of the wavelengths which absorb power. The positive step is to remove the solar arrays and use radio isotope generators. Already two US satellites have been fitted with such generators and a range of new applications were planned with generators from 10 to 100 kilowatts. However, President Carter has proposed a ban on reactors in orbit. This is ironic because the USSR has already adopted the thermal isotope generator. At least ten of the recent launchings have had them.

SHUTTLE BOOKINGS IN ADVANCE

The US Space Agency is now taking reservations for the years 1982 and 1983. This information comes from the manager of flight cargo schedules, Chester M. Lee.

Iran and West Germany have made partial payments for 1982/83 launches. Japan is considering a spaceflight in 1983. Canada, India and the European Space Agency (ESA) have also reserved Shuttle flights.

Mr Lee said: "We have all the cargo we can handle for 1980 and a few spaces left in 1981."

The Goddard Memorial Symposium on the international uses of the space shuttle and space laboratory drew 400 participants from the United Kingdom, Italy, Canada, Germany, France, India, Japan and the United States. The prime topic at the symposium was the European Spacelab, built by ESA to fly in the Shuttle's cargo bay. This multi-purpose facility will be used for experiments in Earth observations, astronomy, physics, solar and atmospheric chemistry, biology and space manufacturing.

NASA and ESA will equally share the first Spacelab flight, now scheduled for December 1980. Spacelab 1 will be truly of an international content. Spacelab 2, scheduled for April 1981 will carry the experiments of 47 USA scientists and 12 United Kingdom scientists. Among the principal investigators are Allen Gabriel of the Appleton Laboratory and Peter Willmore of the University of Birmingham. Spacelab 1 will be carried by Shuttle Orbiter 102. It is being built in California. The first of its six test flights will take place in June 1979.

JUPITER: PLANET OR STAR

A Soviet scientist has produced some evidence to add to the theory that Jupiter is a star rather than a planet. The suggestion is that Jupiter is either a star in formation or a dying one.

Professor N. Kozyrev of the Leningrad Pulkovo Observatory told a scientific meeting that the nucleus of Jupiter has a temperature of 196,000 degrees Centigrade. The professor constructed a mathematical model of Jupiter's nucleus and the results of using this model seem to agree with those obtained from astrophysical observations and also from details of the Jupiter heat stream recorded by Pioneer 10 and Pioneer 11 probes.

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HOW TO ENTER

The contest is for practical items incorporating electronics as a major part of the overall system. There is no restriction on the application, e.g. it could be a video tape recorder or a door bell. It may be for domestic use or for industry.

Entries must be written/drawn clearly on one side of plain paper with the entrant's full name and address at the top of every sheet. Each entry to comprise:

- (a) a brief summary of the design (about 50 words);
- (b) any such further descriptions, drawings, sketches, photographs and circuit diagrams, etc., you consider the judges may need to form the best appraisal of your design. DO NOT SEND ACTUAL MODELS.

Each entry must have a properly completed entry coupon cut from P.E. firmly affixed to the back of the summary. Entrants may be requested to supply prototypes for evaluation at later stages of the judging. All entries must be in English.

Readers may be assured by P.E. that ideas submitted will not be misused or transmitted to other parties by anyone concerned with the competition.

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NAME (Mr/Mrs/Miss)
ADDRESS
TELEPHONE NUMBER if any
I certify that * delete clause NOT applicable
 * (a) This entry is my own original idea and has not been copied from any other source; * (b) This entry has been made on behalf of the company/group members listed and is our own original idea not copied from any other source; (c) This idea has not been published or offered for publication elsewhere. I/We agree to abide by the rules and conditions.
SIGNED



SECOND CHANCE

Another free entry coupon will appear next month.

The closing date is August 31st 1978.

RULES AND CONDITIONS

There is no entry fee nor limit to the number of entries a reader may submit but each entry must be accompanied by a proper printed entry coupon, cut from PRACTICAL ELECTRONICS, and must bear the entrant's own full name and address. Entries will also be accepted from groups or companies, in which case the entry must be submitted and represented by an individual and the other involvements declared on a separate sheet of paper attached to the back of the summary with the coupon.

All accepted entries will be examined by a panel of expert judges including the Editor of Practical Electronics, and assessed on (a) originality cluding the Editor of Practical Electronics, and assessed on (a) originality of the idea, (b) technical merit, (c) practicability, (d) economic viability, (e) market potential, (not necessarily in that order). The prizes will be awarded for the best entries in order of merit. In the event of the same idea being submitted by two or more entrants, presentation of the entry (clarity, best expression, etc.) will decide such winner(s) or winning order.

In the event that the judges consider there are not enough entries of a sufficiently high standard, the Editor reserves the right not to award any prize(s) at his discretion.

Entries arriving after closing date will not be considered, nor will any received that are illegible, not wholly understandable, are not accompanied by a properly completed entry coupon or in any other way do not comply exactly with the instructions and rules.

No responsibility can be accepted for entries lost or delayed in the post or otherwise; proof of posting will not be accepted as proof of receipt. No entries can be returned.

No entries can be returned. The competition sponsors reserve the right to adapt or amend any entry—after judging has been completed—for purposes of publication and/ or commercial development. They also reserve the right to consider any other entries for commercial production. Where any idea is adapted or developed for commercial production payment and/or royalties and/or direct involvement in the company concerned will automatically be negotiated with the designer or person named on the entry coupon. Winning entries will not necessarily be developed commercially, but Practical Electronics will nav the usual reproduction fee for any entries

Practical Electronics will pay the usual reproduction fee for any entries published.

Ideas already covered by a patent owned by the representer, but not already in or on offer for commercial production, may be submitted but

this fact must be clearly stated together with the relevant patent nnmber. Decisions of the judges, and of the Editor in all other matters affecting the competition, will be final and legally binding. No correspondence will be entered into nor interviews granted.

Winners will be notified by post and brief details of winning entries published later in Practical Electronics.

The contest is open to all readers, but those outside the U.K. may be requested to provide a British address to which any prize may be sent. Development of any idea must take place within the U.K.

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HE LPU103 power supply unit reviewed here is one of a new range of kits available from Arrow Electronics. The company claim the kits are of "bug proof" design and because they are only supplied in kit form the minimum of components have been used whilst still retaining a high standard in both performance and reliability.

Nickel cadmium batteries can also be recharged using the p.s.u. with the charging voltages and currents being displayed on the meter of the instrument.

SPECIFICATION

The output voltage of the LPU103 is variable from 0 to 30V with current limiting available in two ranges: 0 to 100mA and 0 to 1A.

The unit can sustain short circuits at full current for considerable lengths of time but because of heat dissipation, when the short is removed the meter calibration will alter by 3-5 per cent until the case temperature is reduced.

allow adequate air circulation.

Therefore sufficient space must be left around the unit to



CIRCUIT DETAILS

The circuit of the LPU103 is designed around two 741 operational amplifiers connected as differential voltage comparators, one to control the current and the other the voltage. The outputs of these two i.c.s are fed to a triple Darlington of which one transistor, a 2N3055, is used as the series pass transistor and is mounted on a heatsink.

The problem of output transients when switching the unit on is eliminated by the inclusion of an output switch. This switch allows the unit to be on continuously with the output terminals only showing a voltage when it is on.

TOOLS

The only tools required to build the unit are, small wire cutters, a suitable earthed soldering iron with a 3/32 inch bit, fine nosed pliers, screwdriver, 4BA spanner and, to avoid



Circuit diagram of the LPU103

damaging the front panel when fitting the switches and potentiometers, the correct sized spanners.

The kit includes solder, plenty of wire and also rubber sleeving for the soldered joints.

ASSEMBLY INSTRUCTIONS

If any type of kit is to be constructed successfully clear instructions are of paramount importance. Therefore before starting to build the unit the instructions were read through carefully and using the component list provided each part was checked and identified. The p.c.b.s were also checked for any short or open circuit.

The instructions advised that all the components should be placed so that their values or type printed on the body can be read from above the board to enable them to be easily checked in case of incorrect positioning; with this noted the first of the two p.c.b.s could be soldered.

The assembly instructions are very comprehensive and any constructor who reads them carefully should have no difficulty in successfully building the kit.

CONSTRUCTION

Vero pins were supplied for connecting wires to and from the p.c.b.s and these were inserted using a layout drawing to ensure correct positioning and then soldered to give a good connection to the track.

The instructions gave a point by point soldering guide with a p.c.b. component layout for visual checking.



The only criticism of the p.c.b. assembly was the fitting of a 1000μ F 63V capacitor which according to the instructions should be soldered second. As this is such a large component it was felt that this would hamper the assembly of the rest of the board and so noting its position the board was soldered leaving this capacitor to be fitted last.

After both p.c.b.s had been soldered they were cleaned and checked for any shorts and then placed to one side and the front panel assembly started.

As the front panel of the unit is anodised, care was taken to avoid handling it with dirty hands or marking it when fitting the switches, potentiometers and sockets.

The heatsink and transformer were already mounted onto the chassis so only the power transistor and p.c.b.s had to be fitted. The main p.c.b. had to be filed on one corner to enable it to fit correctly. With this completed the unit was ready for wiring.

To reduce the noise to a minimum in the finished unit the position and length of the interconnecting wires is of prime importance and for this reason the length of every wire used is given.

With the wires soldered into position and sleeved where appropriate the unit was then ready for testing.

TESTING AND CALIBRATION

All the supply connections were rechecked and using a multimeter both phases of the mains lead and earth were tested for shorts. The interconnecting wires were also checked to ensure they were soldered correctly.

With the testing completed without problems the unit was connected to the supply and as the unit worked first time the voltage and current outputs were calibrated.

The final test to be carried out on the p.s.u. was to check the output noise level.

The reading obtained for the output noise was outside the manufacturer's stated limit. The instructions advised that this figure could be improved by adjusting the front panel wiring and after several attempts the best reading obtained was 0.15mV which was inside the stated limits.



FINISHED UNIT

The LPU103 is a rugged reasonably priced instrument capable of meeting the needs of most constructors. The specification matches many of the more expensive ready built units available. As each kit is housed in an attractive hammer finished case with a distinctive red stripe across its front panel, constructors can if they wish build up a complete range of matching test equipment.

The LPU103 kit can be obtained from Arrow Electronics Limited, Leader House, Coptfold Road, Brentwood, Essex. Price £29.99 (including VAT and Post and Packing).

FOOTNOTE

Peter Clarke of Arrow tells us that he had so many problems trying to get instructions for his kits printed that eventually, being faced with a further three weeks delay, he went out and bought himself a printing machine and the equipment to make the plates. He then taught himself how to print and printed the instructions, all within a week.

It's good to hear of a company that is prepared to take such steps to supply the goods.





POTTERY has become a very popular hobby with many amateur potters now possessing their own small electric kilns. Many such kilns are only supplied with an indicating pyrometer and have no automatic temperature control. The sight of a kiln which has been forgotten and gone above its firing temperature is heart-breaking because the clay melts (vitrifies) at high temperatures and can cause many pounds worth of damage to the inside of the kiln.

The unit described here does two things. Firstly, it enables a control temperature to be set, and holds the kiln at this temperature; secondly, it enables the potter to "soak" his glazes, thus improving the glaze quality. The controller can also be used with enamelling kilns.

THE PYROMETER

It is assumed that a thermocouple and indicator (indicating pyrometer) are already fitted to the kiln. Fig. 1 shows how the voltage across the indicating meter varies with temperature. This particular pyrometer is for a stoneware kiln capable of reaching 1300°C, so the thermocouple will be of the platinum-rhodium type.

CIRCUIT DESCRIPTION

The complete circuit diagram of the controller is shown in Fig. 2. The unit is connected directly across the pyrometer meter at points A and B using nickel-plated terminals and a short length of single-core screened cable. With the high input impedance of IC1 (when used as a voltage follower) the connection of the controller across the meter will not affect the meter reading. No cold-junction temperature compensation has been included since it is assumed that the controller



Fig. 1. Graph showing the millivolt/temperature curve of the platinum-rhodium thermocouple

will always be working above 1000°C, therefore compensation will have little effect.

The output of IC1 is amplified 100 times by IC2. The voltage variation at the output of IC2 is therefore 0-1/3 volts





Fig. 2. Circuit diagram of the controller



Fig. 3. Kiln up to temperature alarm (optional)



Fig. 5. Main circuit board



Fig. 4. Wiring diagram of the kiln contactor

for the thermocouple response shown in Fig. 1. IC3 acts as a voltage comparator with its reference voltage derived from a temperature-compensated, 6.2 volt, zener diode.

When the voltage of pin 2 of IC3 goes positive with respect to pin 3 (reference) then the output of IC3 will go negative, turning off TR1, de-energising RLA and switching off the kiln. On the author's controller no hysteresis was included as the relatively long thermal time constant of the thermocouple and kiln prevented erratic operation. However, hysteresis may be added by the inclusion of R9 if erratic operation occurs. VR2 should be a ten-turn wirewound potentiometer fitted with a turns counting dial.



Fig. 6. Wiring diagram of power supply unit



COMPONENTS . . .

Resistors

R1, R2, R5, R7, R8	10kΩ (5 off)
R3	1MΩ
R4	2·2 kΩ
R6	$3k\Omega$ (see text)
R9	$4.7M\Omega$ (see text
R10	100Ω
All 5% 4W carbon	

Potentiometers

VR1 10kΩ 20-turn cermet trimmer ³/₄in VR2 1kQ 10-turn wirewound

Capacitors

C1 0.1μ F ceramic C2 0.22µF 45V polycarbonate

Semiconductors

1N4148 D1 1N821 (6-2V Zener) D2

TR1 BFY51

Integrated Circuits

IC1, IC2, IC3 741 op amp (3 off)

Switches

S1 5A toggle

- 1A toggle D.P.S.T. **S**2
- S3 Single pole on/off toggle

Miscellaneous

Heavy duty relay RS type 348-920 with mounting plate and socket Chassis mounted fuse holder (2 off) 100mA fuse (2 off) Turns counting dial for VR2 Thermocouple Outlet plug and socket Diecast box 180 imes 120 imes 60mm Contactor (if req.)

Constructor's Note

A suitable outlet plug and socket can be obtained from Harrison Mayer, Craft and Education Division, Uttoxeter, Meir, Stoke-on-Trent.

The catalogue number is 365741.

CONSTRUCTION

In the prototype the components were soldered on to 0 lin. matrix stripboard using the layout shown in Fig. 5.

After soldering, the board was mounted into a $180 \times 120^{\circ}$ \times 60mm diecast box and secured in position using 6BA countersunk screws and spacers. The heavy duty relay was fitted into the case on a mounting plate and socket.

The mains power supply used in the prototype was a fully encapsulated commercial unit, but any regulated dual supply with an output current of 100mA could be used. The mains input to the power supply was protected with two 100mA fuses fitted into chassis mounted fuse holders.

If the kiln to be used is fitted with safety switches on the doors a mains contactor is normally fitted in the back compartment. If it is not, a suitably rated contactor should be fitted into the back compartment and can either be permanently wired to the controller using high temperature cable or by using an outlet plug and socket. If a plug and socket is to be used care should be taken to ensure that it is capable of handling the high temperatures involved. For this reason it is recommended that constructors use the type given in the component list.

A "kiln up to temperature" alarm circuit is shown in Fig. 3. This unit can be incorporated into the controller if required using the relay contact RLA2. The 4.5V battery, switch 3 and the buzzer were all enclosed in a separate case outside the unit.

SETTING UP

Those constructors with access to a high impedance d.c. millivoltmeter or a d.v.m. can produce a millivolt/temperature curve for their own thermocouple.

If a chrome-alumel thermocouple is used its output will be approximately four times that shown in Fig. 1 and the resistor R6 should be lowered so the reference voltage V can be varied from zero to Vt:

Where $Vt = mV \times 100$.

At the highest working temperature needed.

With R6 selected the input leads should be shorted together and VR1 adjusted to give zero volts at the output of IC3. The unit can either be calibrated by applying known millivolt levels to the input of the IC1 and adjusting VR2 until the relay de-energises or it can be directly calibrated in use from the pyrometer reading.

When in use the controller should be placed a short distance away from the kiln, otherwise the components will become overheated.

Finally a table (or graph) showing the setting of VR2 against temperature cut-out should be produced.

R.W.COLES B.CULLEN

PART TEN

an additional PROM because of course you can now do all the programming yourself! In addition, the generation of PROMPT II makes an excellent introduction to the use of the CHAMP-PROG system, and so we have treated the production of this new firmware as a worked example accordingly.

WUNBYTE II

If you examine the flowchart and listing of PROMPT published last month you will notice that the only part of the program directly involved with the programming hardware is the subroutine WUNBYTE which addresses the source and destination, and generates with software the accurately timed program-enable pulse. To generate the new PROMPT II firmware all that is required is the replacement of this subroutine by a new one which transfers data in the other direction. The new subroutine can be called WUNBYTE II and does not need to be as lengthy or as complex as the original because there is no longer a need for the program-enable timing counter. A listing of the new subroutine is shown in Fig. 10.1. and as you can see WUNBYTE II starts at the same address as WUNBYTE so that the JMS WUNBYTE call is still effective. The new subroutine is shorter than the original, but this is of no consequence because the BBL instruction will pop the stack as usual, and operations will recommence at the line immediately following the JMS.

Apart from the new subroutine and an area of blank space following it, the rest of PROMPT II is identical to the original PROMPT, and of course the new PROM is destined for the same socket on the CHAMP main board, where it can be used alternately with the original when required.

PROGRAMMING SEQUENCE

To create PROMPT II the following sequence must be followed.

(i) Connect up CHAMP-PROG to CHAMP and connect the mains supplies. Ensure that CHOMP and PROMPT are in their respective sockets on the CHAMP main board, and place an erased PROM in the CHAMP-PROG programming socket.

(ii) Switch on CHAMP and enter the 56 lines of WUN-BYTE II into CHAMP program RAM starting at, say, address 200H (you could of course start *anywhere* in the CHAMP RAM space).

(iii) Press RESET then TEST to enter PROMPT, turn on the CHAMP-PROG mains, and then enter Adr1, Adr2 and Adr3 as follows to copy WUNBYTE II into the new PROM at location 15AH.

- Adr1 = 200H (for example)
- Adr2 = 237H (200H + 56 decimal)
- Adr3 = 05AH (destination in PROM)

(Remember to turn the PROM POWER switch to the ON position immediately before pressing the ENTER DATA button after Adr3 has been keyed in).



CHOLED OF

THE combination of CHAMP and CHAMP-PROG produces' a microprocessor development system which can be used not only to experiment with software techniques, but also to produce other working microprocessor systems with dedicated PROM based programs and hardware interface circuitry which has been fully tested in advance on the CHAMP breadboard.

This month we conclude the CHAMP series with a deeper examination of the uses to which CHAMP-PROG can be put, and with the constructional details of the CHAMP-U.V. PROM erasing system.

USING CHAMP-PROG

You may remember that in part one of the CHAMP series we stated that CHAMP-PROG extended the CHAMP system not only in its role as a PROM programmer but also as a system for copying data *already* stored in a PROM *back* into the CHAMP program RAM. A facility for copying data into RAM makes the whole system even more flexible and allows the following:

(i) Programs can be "dumped" into a PROM to release the valuable CHAMP CMOS RAM area for more pressing jobs with the knowledge that the original programs can be easily reloaded into RAM when necessary.

(ii) PROMS can be modified by loading their contents into CHAMP RAM, making the necessary changes, and then reloading the erased PROM with the updated contents.

(iii) PROMS can be "insert/delete" edited using the relocating ability of the PROMPT program.

(iv) PROMS can be duplicated by first copying them into RAM and then using CHAMP-PROG in the usual way.

The PROMPT software published last month does not have the ability at present to load data back into RAM, because there simply was not room in the single 4702A chip to do this. The PROMPT program is, however, laid out in such a way that it is very easy to produce a new version, say PROMPT II, which will perform this useful function. Before you start to groan at the prospect of *another* financial outlay for software, remember that if you already have a CHAMP-PROG and PROMPT, then PROMPT II will only cost you the price of

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(iv) When the "done" message is displayed, turn the PROM POWER switch to the OFF position and then press RESET and TEST to re-enter PROMPT. Enter the following addresses to copy the part of PROMPT *before* WUNBYTE into the new PROM, and then initiate programming in the usual way.

- Adr1 = 100H (start of PROMPT)
- Adr2 = 159H

Adr3 = 000H (start of PROMPT II)

(v) When "done" is displayed once more, turn the PROM POWER switch to the OFF position, and then press RESET and TEST to re-enter PROMPT. The following addresses are then entered to program the remaining part of PROMPT into the new PROM in the usual way.

- Adr1 = 1A6H (start of MATCH)
- Adr2 = 1FFH (end of PROMPT)

Adr3 = 0A6H

(vi) When "done is again displayed turn off the PROM power and remove the newly programmed PROM. PROMPT II now lives!

USING PROMPT II

Apart from the subroutine WUNBYTE, the new PROMPT II is identical to the old one, and addresses are entered exactly as before with the same meanings:

Adr1 = start of source data

- Adr2 = end of source data
- Adr3 = start of destination area

The important difference is of course that the *source* addresses now refer to a PROM in the programming socket, and the *destination* address refers to a location in CHAMP program RAM. This also means that the most significant digit of addresses 1 and 2 is redundant and can be set to anything (usually zero to avoid confusion), and that the most significant digit of Adr3 now becomes important and is used to select the destination chip in CHAMP program RAM. Provided you think in terms of source and destination rather than RAM and PROM no confusion should arise when swopping

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	5	29			SRC	9	
	6	OB			SBI		SELET DEG. BANK 1
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	2	AL			LD	1	
	1	EO			WRM		STORE 1ST NIBBLE FOR MATCH.
	4	69			INC	9	NEXT RAM CHAR
	5	29			SRC	9	
	6	AO		1	LD	0	STORE 2 NONIBBLE FOR MATCH
	7	ΕO			WRM		
	8	28			EIM	ß	D
	9	00			0	0	ADDRESS CORRECT
	A	29			SRC	9	PROGRAM RAM
_	В	AE			LD	E	DESTINATION CHIP.
	C	EI			WMP		
_	D	2D			SRG	Þ	SELECT PROGRAM RAM
	8	AL			LD		BYTE
	F	E3			WPM		WRITE IST NIBBLE
-		_					i

MCS 4C PROGRAM SERET

TITLE			WUNBYTE II				DATE OF 01 78
							PAGE NO. 3 OF 3
HEX			BIN MNEMORIC				
PAGE	LINE	BOM	CODING	LABEL	OPERATION	OFERAND	COMUNICS
	8 .	AO			LD	0	
	1	E3			NPM		WRITE 2ND NIBBLE
	2	00			NOP		
	3	00			NOP		1
	4	OE			RPM		PEAD DATA BACK
	5	85			хсн	5	FOR HATCH CHECK
	6	OE			RPM		The case of the term
	1	84			XCH	4	<u> </u>
	8	28			EIM	9	1
	9	20			2	0	++
	Å	29			SPC	<u>a</u>	STORE MATCH DATA
	в	A5	<u> </u>		LD	5	IN DATA DAM
	c	EO			WOM		
		69	-		INC	9	
-	R	20			SPC	0	
		A4		·	10		+ <u>+</u>
-	a .	20		<u> </u>	WRM		++
	a ,	60			BBL	0	END OF NUNBYTE TT
<u> </u>	2					= 77	
						⊢�─	
						ø.	
	-					40	
	2					<i>x</i>	
	7				6%	T .	
	- 8				15		
	-				· X ·		
	9				14/10		
	A			6			
	8			\$	N		
		-		~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~			+
				_/. V			
	B						
	1 8			1			

Fig. 10.1. WUNBYTE II program listing

from one PROMPT version to the other! Data transfer is initiated as usual by the depression of ENTER DATA after keying in Adr3, although now the "done" message is displayed almost immediately because the transfer to RAM is very much faster. When using WUNBYTE II, there is no need to turn the PROGRAM POWER switch to ON of course, since all the necessary power is supplied by CHAMP itself.

OTHER POSSIBILITIES

No doubt many readers have spotted the fact that with extra PROM space available on CHAMP, the two versions of PROMPT could both be available simultaneously, and of course there would be no need to duplicate all of the original PROMPT, only the WUNBYTE II would be needed, with some means of selecting *which* subroutine is used by PROMPT when it is run. (This would require a reference to switches, and the ENTER DATA and ENTER ADDRESS buttons could easily be redefined for this purpose.)

Of course the addition of an extra PROM or PROMS is a major modification to CHAMP, but this could nevertheless be done without too much trouble.

If you *did* add this extra PROM it would of course be mostly empty, and then your thoughts could turn to what *other* goodies you could add to the system. How about a WUN-BYTE III which would not *transfer* data but simply check one area against another, thus providing a new VERIFY facility in fact. Or how about a WUNBYTE IV which would be used to move data around in CHAMP RAM and would not need the benefit of CHAMP-PROG at all. Insert/delete editing would be possible with WUNBYTE IV providing some spare locations were left at the start of a program (think about it). Flow charts for these other WUNBYTE variants are shown in Fig. 10.2 and these could be the source of a lot of fun for CHAMP users, with many other possibilities no doubt suggesting themselves as experience is gained.

PROM ERASER

An essential companion to a PROM programmer like CHAMP-PROG is of course a PROM eraser, and fortunately these units are not difficult to build. All that's really required is a lightproof box with a short wave ultraviolet (U.V.) lamp mounted inside it and a resting place for the PROMS being erased. We decided against the minimal "lash up" approach to the design of an eraser at a very early stage for two very important reasons:

(i) Short wave U.V. light at the correct wavelength of 2537 Angstroms can be harmful to living organisms, and this of course includes CHAMP programmers!

(ii) Over exposure of 4702A proms to the erase light can shorten their life.

In order to avoid the implications of both (i) and (ii), a good deal of thought was put in to make CHAMP-U.V. safe for both the programmer and PROM. Technically, there is no real problem to building a PROM eraser, short wave lamps are available from specialist companies such as Anderman who seemingly stock them primarily for medical purposes. The lamps resemble normal 8 watt fluorescent strip lights, and use exactly the same miniature 2-pin base, and the same ballast choke and starter. Lamp power is provided by the usual 240V a.c. mains supply. The required integrated light dose, which is defined as intensity \times exposure time, is stated by Intel to be six Watt-Seconds per square centimetre, and this can be provided quite quickly by the small 8W tubes when they are placed within lin of the PROM.

CHAMP U.V.

CHAMP-U.V. is built in a plywood case measuring $380 \times 140 \times 100$ mm and is completely self contained. Eye safety is ensured by the use of a microswitch which will not allow the lamp to light until the lid is closed and secured. PROM safety is ensured by the incorporation of a clockwork timeswitch which allows "set-and-forget" erasing to be undertaken.

Fig. 10.2. Flowcharts for WUNBYTES II, III & IV. Note: WUNBYTEIV makes a RAM block-move possible. Source and destination blocks can overlay each other, but with an incrementing address counter data can only be moved *down* in RAM, if over-writing unused source data is to be avoided. Modifying PROMPT to permit address decrementing would be possible, and would allow data to be moved up (i.e. to a higher start address). WUNBYTE IV allows program blocks to be moved aside to make room for single extra instructions when required



The circuit of CHAMP-U.V. is shown in Fig. 10.4, and as you can see, after the complexities of CHAMP and CHAMP-PROG, CHAMP-U.V. should come as a spot of light relief! The microswitch, which is operated by the box lid, is in series with the mains input, as is the clockwork timer. The neon mains indicator is wired to show that mains is applied correctly when the lid is closed, and not to show that the timeswitch is still on. The timeswitch itself makes a distinct sound rather like the ticking of a time-bomb when active, and so there seemed little need for additional facilities to announce the end of an erase cycle. The timeswitch can be set by means of a knob inside the case to provide erase cycles ranging from zero to thirty minutes, with periods of about twenty minutes being the norm for 4702A devices.

The lamp is mounted lengthways in the lid of the case and provides a full 200mm of active length for the erasure of PROMS. A total of twelve PROMS can be erased simultaneously when required, and these are mounted on a strip of conductive plastics foam which is located immediately under the tube when the lid is in the closed position.

CONSTRUCTION

The CHAMP-U.V. case is made from 8mm plywood and is pinned and glued together using simple butt joints. The best way to build the case is to start by assembling the body and the lid as one piece and then to saw the resulting box completely through to separate the lid. Plywood runners are




Fig. 10.4. Circuit diagram of CHAMP-U.V.



Fig. 10.5. Wiring layout of U.V. unit

It is most important that the lid operated microswitch feature of this design is incorporated, to immediately switch off the U.V. tube when the top is lifted. The optimum position for the microswitch is *near the front* of the box as shown in this photograph

COMPONENTS

Ultraviolet tube. 12 inch, 8 watt, 2537 Angstrom Choke. Smart & Brown 69386, 8 watt, 0.16A (or similar).

Starter. GEC 155/100 (or similar).

Timeswitch. 30 minute clockwork timer unit. Contacts *closed* during timing period, and of mains rating. (Available from many surplus suppliers).

Microswitch. 240V a.c., 2A, with leaf spring and actuating plunger.

Neon indicator lamp. 240V type.

Miscellaneous. 2-pin tube sockets. 3-way terminal block, 4 mm and 8 mm plywood, aluminium sheets, etc.

The U.V. tube is available from Anderman & Co. Ltd., Central Avenue, East Molesey, Surrey KT8 0QZ.



It is essential that these U.V. lamps are used correctly if health damage through U.V. exposure is to be avoided. Observe the manufacturers caution fully.



then pinned and glued inside the box to locate the timer panel and the PROM panel, which are both cut from aluminium sheet. Two aluminium brackets are also cut out and bent to form a PROM carrier which can be lined with conductive plastics foam when the rest of the case is completed (Fig. 10.3).

The lid is attached to the body of the case by means of a plastics piano hinge secured externally with small woodscrews, and this hinge, together with aluminium shuttering glued along the other three edges of the lid, ensures a lighttight seal for complete safety.

The U.V. lamp bases are fitted to the inside of the lid with the aid of small plywood blocks, and the starter, ballast and a terminal block are secured to the bottom of the case using small woodscrews and nuts and bolts. The microswitch is secured to the inside of the case with small woodscrews, and must of course be carefully positioned to ensure correct operation when the lid is closed. A small plywood fillet is mounted in the case lid to actuate the microswitch.

Wiring up should be carried out with mains quality flexible





FREE OFFER NOTICE

No further Intel Programming manuals are available, but many MCS Users Manuals remain. These will continue to be sent free of charge on receipt of an 8×10 inch envelope with 25p stamp (32p 1st class). wire in accordance with Fig. 10.5. Notice that the aluminium panels *must* be connected to the mains earth for safety reasons.

When wiring up is completed, the unit can be connected to the mains and tested for correct operation. A conventional fluorescent tube could be substituted for the U.V. version during the testing phase if it becomes necessary to operate the lamp with the lid open.

The prototype case was finished with aerosol paints and polyurethane varnish using the same techniques as before on CHAMP and CHAMP-PROG. Finally, a carrying handle should be screwed to the lid of the case, and an attache case latch screwed to the front to hold the lid closed for transportation purposes.

USING CHAMP-U.V.

Complete erasure of PROMS prior to reprogramming is absolutely essential for reliable operation. The erasure process is a linear one and does not occur suddenly, and so even when a cell location appears to be erased, further exposure may be required to reach a satisfactory level of gate discharge.

A CHAMP-U.V. system built as described will probably erase all 4702A type PROMS satisfactorily if they are given a 20 minute exposure, but for greater accuracy, system calibration can be an advantage. This is achieved by programming a PROM with all "ones" (FFH in every location) and then giving it short erase increments of say, 2 minutes; checking after each increment for proper erasure. When the chip appears to be completely erased, note the time required and in normal practice always use a cycle of *five times* that duration.

This calibration need only be carried out once, since **PROMS** are very consistent in their requirements, and a factor allowing for ageing in the lamp tubes has been incorporated.

PROM PREPARATION

Before erasure, always check the quartz window on the PROM for any dirt particles which may cause shadows on the chip, and also wipe them over with a swab soaked in methylated spirit to remove any films which may be opaque to U.V. light. This latter precaution is particularly necessary if gummed labels have been used to cover the PROM when in use.

CHAMP-U.V. is quite capable of erasing any U.V. sensitive PROM including the larger capacity 2704 and 2708 types. The 2704/2708 chips do, however, use a different technology and generally require exposure periods of up to one hour for correct erasure. If CHAMP-U.V. is to be used to erase these devices exclusively, a clockwork timer with a one hour endurance could easily be substituted instead of the thirty minute unit specified earlier.

CONCLUSION

This brings us to the end of the CHAMP series, and we feel sure that everyone who has successfully built all, or part, of the CHAMP system will share our enthusiasm in the results obtained. Our CHAMP is in constant use and has been used to develop several dedicated systems and some "just for fun" software. There are of course many possibilities for additions to the basic system, and we will be glad to hear from anyone who has specific needs which may be catered for in subsequent articles, and from those who wish to pass on programs or hardware designs to their fellow CHAMP enthusiasts.

Meanwhile, the CHAMP programming service for CHOMP, PROMPT, and user programs will continue to be available as long as it is required.

Good luck, and successful programming!



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Copies of Patents can be obtained from : the Patent Office Sales, St. Mary Cray, Orpington, Kent Patent

Price 95p each

SILO VOIGING BP 1 477 217

Several circuits for enabling a wouldbe singer to perform along with a solo voice on a pre-recorded disc or tape, the pre-recorded voice being routed to one loudspeaker of a stereo pair and the amateur voice to the other have recently been patented by Sony. Now, in BP 1 477 217, Sony patent what is the logical conclusion of this train of patenting activity—a circuit for eliminating a featured solo voice and enabling would-be singers to replace that voice with their own.

The circuit, Fig. 1, shows a right channel amplifier TR1 operating as an emitter-follower. The left channel amplifier TR2 is operable, under switch control (S1), either in similar manner as an emitter-follower or as an inverting amplifier, e.g. common-emitter configuration. The adders 1, 2 and 3 together combine the signals from the left and right amplifiers with that from a microphone X1 into which the would-be vocalist sings. The adder outputs are then fed to tape recorder amplifiers with playback through the loudspeakers.

A stereo source from conventional disc or tape, is applied to the inputs of the left and right amplifiers and the right stereo signal is applied to adders 1, 3. The left stereo signal is, in one position of the phase switch S1, inverted in phase. In the alternative switch position, the phase is non-inverted.

It is here important to note that the featured solo sound on a conventional recording (which solo sound the Sony circuit is required to eliminate and replace with an amateur sound) is recorded in phase and in equal amplitude in both channels. In the inverting state of the left channel amplifier, the adder 2 is supplied with amplified right stereo signal, including the original common solo signal components, along with the phase-inverted left stereo signal, which now includes the same solo signal components as the right but in opposite phase. Adder 2 thus subtracts right from left and in so doing cancels the common solo components.

Fresh solo sounds, produced by microphone X1, can now be added at 1, 2 to the remaining signal, which corresponds to instrumental accompaniments. The reproduced sound thus corresponds to the original accompaniment, plus fresh solo. For the purposes of comparison, a mono mix of the original solo and accompaniment can be reproduced by the left channel loudspeaker either simultaneously or alternatively with the replacement mix.



Market Place

Items mentioned are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press,



13 AMP SOCKET TESTER

Here is a tester which could have started out as an idea in Ingenuity Unlimited. It is a very compact 13 amp plug with two indicator lights to show up live, neutral or earth faults.

A positive must for any "sparks" tool box, contractors, installation engineers.

The retail price is $\pounds 3.95$ and quantity discounts are available from Galatrek Engineering, Scotland Street, Llanrwst, North Wales. (0492 640311).

Inventive minds that would relish the opportunity of materialising an electronic idea into a commercial product with financial backing should turn to page 728.

ACE CARD

The Casio Mini Card calculator is the same length and width as a playing card. It is as thin as two ice cream wafers. Being so thin it is fragile and in fact will flex without much force. Nevertheless, for the careful user it is an extremely compact shirt pocket masterpiece of miniaturisation.

The LCD display will show up the respective sign when any of the four basic mathematical functions are performed. There are memory plus and minus keys, memory recall, a per cent function and clear and all clear keys.

If an overflow of the eight digit display occurs during calculation, in an answer or in the memory, an E appears and further operations are halted until the all clear key is pressed.

A couple of G-10 silver oxide batteries give approximately 1,000 hours of continuous operation.

Designated the LC-78, it is available at £16.95 from Tempus, 19–21 Fitzroy Street, Cambridge, CB1 1EH.





KITCHEN QUARTZ

Smiths Industries have introduced a range of domestic quartz clocks under the brand name "Astral".

The model shown is the "Melanie Pendulum", with blue or brown pattern and battery powered decorative pendulum, at ± 18.95 .

For those not wishing to be pendulous the price is $\pounds 11.50$.



NON TIC TAC

This LCD alarm clock is the size of a packet of razor blades and has a quiet but effective waking tone, a pleasurable change from the old analogue fire bells.

Some digital alarms require a considerable amount of button pushing if an earlier alarm time is needed. This little fellow cleverly uses simultaneous button pressing to advance in 10 minute jumps or straight from a.m. to p.m. etc. Inadvertent resetting is prevented by the incorporation of a hold down setting lock.

Its logic element is MOS-LSI and for insomniacs there is a backlite(sic). The operating instruction booklet does read a little funny at times—"The reminder alarm will ring when the local time four minutes before it automatically cuts off."

Clock accuracy is the familiar ± 30 seconds per month.

The model, TAC-1, which gets its name from the abbreviation of Travel Alarm Clock, is available at £22.50 from Tempus, 19-21 Fitzroy Street, Cambridge.

Tempus also offer a battery change kit—watch case opening tool, battery specifications chart, replacement instructions and non-metallic tweezers—all for 35p. Watch batteries 65p each

SMOKE DETECTOR IC

A single chip smoke detector i.c. is announced by Siliconix of Swansea. The SM110 operates from a standard 9 volt alkaline battery with a current drain of less than 10 microamps in the standby mode. Its input impedance is very high making it suited to photoelectric sensors; the output needs only a simple drive circuit to power either a piezoelectric or electromechanical horn.

Other features include a very low input leakage current, adjustable sensitivity, noise input suppression, adjustable sourcing current and trouble signal timing as well as latching and non-latching operation.

The price of the SM110 is $\pounds 2$. A 12.6 volt version, the SM120 is also available, price soon to be announced.

Full data from Siliconix Ltd., Llanllienwen Close, Morriston, Swansea, SA6 6NE.

SEMINAR ON CASSETTE

Late last year Intel held a very successful two-day Memory Designers Seminar at Wembley, London. The proceedings of this seminar were recorded and are now available on C-120 cassettes complete with slide book and explanatory manual.

AVP811	CCD Memory Devices	£4
AVP812	Static and Dynamic PAM	£ A
1111012	Static and Dynamic RAMS	2.4
AVP813	EPROMS and 22-pin 4K	£4
	RAMS	
AVP814	Memory Design with 4K	£6
	and 16K RAMS	~~~
A VD914	Debugging a Manager	C 4
AVFOID	Debugging a Memory	£4
	System	
AVP817	The complete set	£15
Intel Cou	moration (LIK) Itd. A I	Zatwaan

Towns Road, Cowley, Oxford OX4 3NB.



MINI-REEL

The Burgess mini-reel extension cable is fitted with a twin 13 amp outlet and eight metres (26 feet) of three core cable.

A thermal and current overload cut-out permits normal usage of up to six amps.

Weighing less than a metric bag of sugar the mini-reel has a recommended price of \$8.35 plus VAT.

PRICE REDUCTION

Ampex has announced a price reduction, effective immediately, on the Ampex 220 demagnetizer and headcleaner.

The demagnetizer and headcleaner is a special cassette patented by Ampex in which a rotating ceramic magnet is used to degauss the head of a cassette recorder or player. Simultaneously, a strip of nonabrasive fabric is transported by the machine to clean the head and tape path. This enables the recorder/player to deliver its best performance.

The Ampex demagnetizer cassette works automatically without batteries or mains power and can be used hundreds of times without reduction of its effectiveness.

From its former recommended retail price of $\pounds 3.32p$, the Ampex 220 demagnetizer is now reduced to a recommended price of $\pounds 2.89$.

Ampex magnetic tape products are available throughout the United Kingdom, from leading hi-fi and record shops. Ampex International, 72 Berkeley Avenue, Reading, Berks.

BIG LED

A large (20mm) l.e.d. display has been placed on the market by Hewlett-Packard. The new HDSP-3400 series numeric device is the largest display in Hewlett-Packard's seven segment product line which starts at 2.6mm.

Readable in bright light at distances of up to ten metres the big l.e.d. is designed for use in electronic instruments, pointof-sale terminals, television sets, weighing scales, digital clocks and other applications requiring low power consumption in a large, easy to read display.

Packaging is standard 15mm d.i.l. Price £1.80.

Data, Hewlett-Packard Limited, King Street Lane, Winnersh, Wokingham, Berkshire, RG1 5AR.

IC MOTOR CONTROL

A range of hybrid i.c.s in TO3 8 pin packages for the control of motors up to 0.1 horsepower from low level inputs, has been announced by **Rapid Recall.**

The devices comprise of a 741 operational amplifier, a special driver chip and a complementary pair of power transistors together with a frequency compensating capacitor all enclosed in a TO3 package. The internal circuitry is electrically isolated from the outer casing thus allowing easy heat sinking.

There are three basic models which differ in output current capability. They are 8510 (1 amp), 8520 (2 amp) and 8530 (2.7 amp), all of which will provide maximum output current at output voltages up to 24 volts. Multiple devices can be connected in parallel to provide even higher currents or voltages. Each device is available in two temperature ranges: -55 to 125° C (suffix M) and -25 to 85° C (suffix I).

The chip has short circuit protection, the maximum current of which is set by a pair of external resistors chosen to suit the application. The 741 is powered by an internal regulator and will provide a gain of >100dB if required. Quiescent current of the unit is only 50mA max.

An interesting application of the device is in programmable power supplies. The input can be coupled to a digitalto-analogue converter which can, in turn, be controlled by thumbwheel switches,. This arrangement allows the output voltage to be set to within $\pm 0.1V$ d.c.

Further details obtainable from Rapid Recall Limited, 9 Betherton Street, Drury Lane, London WC2H 9BS.

RECHARGEABLE IRON WITH SPOTLIGHT

A new version of the Engel B50 rechargeable soldering iron is available.

The iron now incorporates a built-in spotlight to illuminate the working area and has long life rechargeable nickelcadmium batteries. Of compact design, the B50 will give up to 100 intermittent operations (350 continuous). Recharging can be performed overnight in about eight hours (overcharging is impossible).

The trigger switch is fitted with a safety catch to prevent accidental operation. The bit, for work up to 2.5 sq. mm, heats up to an operating temperature in the region of 350 degrees centigrade in about seven seconds. Other bits are available.



Designed for recharging from normal AC mains, the B50 comes complete with cleaning pad, protective cover, two lighting fittings and screwdriver. A particular advantage is that no stray eddy currents which might damage a sensitive i.c. are generated when the iron is being used.

Priced at £16.50 including carriage, packing and VAT it is available only from the UK agents, Kelgray Products Ltd., Kelgray House, Sandy Lane, Crawley Down, Sussex RH10 4HS.



H IGH quality stereo headphones have been available for many years now but they are usually considered as merely an adjunct to a full scale stereo system. This is a pity since even a relatively cheap pair are capable of the kind of reproduction usually associated with expensive speakers. There are also many situations which preclude the use of a speaker system at orchestral levels whereas a pair of headphones will deliver the same sound levels with an input of a few milliwatts, without disturbing the neighbours!

It was these thoughts that prompted the design described in this article. The main requirements of such an amplifier are the same as those for any hi-fi unit with the main problem being the minimisation of noise, especially hum.

The amplifier must also possess a fairly high input impedance in order to match most signal sources. In the development stage many different circuits were considered but most were found not to measure up to the requirements already outlined.



Fig. 1. Circuit diagram of one channel of the Headphone Amplifier

Consideration was given to the inclusion of Baxendall tone controls but this was rejected on the assumption that the amplifier would be used mainly as a monitor of high quality sources, many of which would already provide such control.

Sensitivity is 20mV for maximum output, and as this is very high it is recommended that a potential divider, or a log. volume control is used in front of the amplifier. This should have a value of at least $220k\Omega$ in order to avoid shunting the input impedance of the circuit.

CIRCUIT DESCRIPTION

The circuit diagram of one channel of the headphone amplifier is shown in Fig. 1. The transistor TR1 which is connected in the common emitter mode provides all the voltage amplification for the circuit.

IC1 is a 741 op. amp. which is used with 100 per cent negative feedback and operates as a voltage follower with unity gain. In this mode the bandwidth extends from d.c. to over



Fig. 2. Series regulator circuit which can be used if the unit is to be fitted into an existing piece of equipment.



Fig. 3. Circuit board layout for Headphone Amplifier

100kHz.

The non-inverting input of IC1 is taken from the collector of TR1 and the i.c. used as a non-inverting amplifier with signal feedback applied via R6 to the base of TR1.

The complete circuit acts as a virtual earth amplifier, the gain of which is determined by the ratio of R5 to R6. The output power available from this arrangement is limited to about 25mW, which proved sufficient to deafen most listeners!

The current consumption of the circuit is about 6mA and it can be operated either from a 9V battery or the series regulator circuit shown in Fig. 2.

COMPO	NENTS	
Resistors		
R1	R101	100k Ω (2 off)
R2	R102	560kΩ (2 off)
R3	R103	68kΩ (2 off)
R4, R5	R104, R105	27kΩ (4 off)
R6	R106	1MΩ (2 off)
*R7		1kΩ
	S	
C1	C101	25μF (2 off)
C2	C102	100µF (2 off)
C3	C103	100µF (2 off)
C4		1,000µF
All 25V	electrolytics	
Semicond	luctors	
*D1		BZY 88 10V
TR1	TR101	BC109 (2 off)
* 1 R2	10404	BFX 84
IC1	10101	741 op. amp. (2 off)
Miscellan	eous	
1 off 0.1	in matrix stripb	oard
2 off ster	reo jack plug ar	nd socket (see text)
1 off PP3	3 battery (see te	ext)
1 off bat	tery clip (if req.)
	uer for I.C. (if re	iq.)
1 011 3 .P	.o.i. toggie sw	iten (see text)
* See Ei	. O a set to set	

CONSTRUCTION

In the prototype both channels of the amplifier were mounted on 0.1 in Veroboard using the layout shown in Fig. 3.

Care should be taken to ensure that the appropriate breaks are made to the copper tracks and that all the electrolytic capacitors are correctly orientated before soldering them to the board. The two i.c.s can either be mounted in holders or soldered directly onto the Veroboard.

After soldering, the board was checked for any solder shorting the copper tracks and the excess flux was also removed.

The output connections from the circuit board to the headphones were made via a stereo jack socket and the two input leads to the amplifier were made using screened cable. The screened lead of each cable should be connected to the OV line.

HOUSING THE UNIT

The amplifier can either be housed in an existing piece of equipment or in a separate case.

If the unit is to be installed in an existing piece of equipment the series regulator circuit shown in Fig. 2 could be used with the inputs of both channels connected internally and the jack socket fitted to the control panel.

The prototype is used as a piece of test equipment fitted into a separate case and powered by a PP3 battery.

The two screened input leads were terminated using another stereo jack socket and an on/off switch was fitted to the positive lead of the battery.

USES

The headphone amplifier has many applications, the prototype being used mainly for checking f.m. tuners on the author's bench. \bigstar



Radio Circuits Explained By Gordon J King Published by Butterworth Co Ltd 145 pages, 160mm × 240mm. Price £5.50

THERE have been many advances made in the field of radio circuits and with the introduction of new devices many different design ideas are being incorporated into modern receivers. The problem for both the technician and hobbyist is keeping abreast of these advances.

This book examines the developments made and explains the principles involved in each section of the receiver with the aid of clear diagrams and graphs.

The opening chapters cover block flow and FIMS diagrams and progress through mixers, RF amplifiers and oscillators to detector circuits. Basic audio and power amplifiers are discussed and with the use of manufacturers' diagrams it is shown how they are employed in practical circuits. Also included are power supplies and regulators with the final chapter dealing with stereo coding and decoding.

Many people should find this book very helpful in developing a clearer understanding of the techniques used in modern radio circuits.

D.J.S.



THIS probe was designed to enable temperature measurements from -10 to 100° C to be made using the 0-100mV range of a digital multimeter. It is possible for an analogue meter with the same full scale deflection (0-100mV) to be used, but for readings below 0°C the meter connections would have to be reversed.

CIRCUIT DESCRIPTION

The circuit diagram of the probe is shown in Fig. 1. Its operation is dependent on the linear relationship between the changes in resistance and temperature of a p.n. junction.

The two f.e.t.s are used as constant current generators with the 741 amplifying the difference in voltage between the temperature probe (a) and the 0° C reference level (b) which are connected to pins 2 and 3 of the op. amp. The meter is connected across VR2 and measures the amplified

COMPONENTS
Resistors
R1 2·2kΩ 2% ±W
R2 680Ω 2% ±W
R3 3·3kΩ 2% ±W
R4, R5, R6, R7 100kΩ 1% ±W (4 off)
R8 1·2kΩ 5% ‡W carbon
All metal oxide types except where stated
Potentiometers
VR1 1kΩ · 10 turn cermet
VR2 10k Ω single turn cermet
Switches S1 D.P.S.T. pushbutton (latch type) Semiconductors
D1 1N4148
D2 Miniature I.e.d. (red) T.I.L. 209
1 R1, 1 R2 2N3819 n channel f.e.t. (2 off)
1C1 /41 op. amp.
Miscellaneous
1 off $$ Polystyrene case 100 $ imes$ 50 $ imes$ 40mm
2 off PP3 batteries
2 off Battery clips
1 off Tube of Araldite
Length of copper tube ≩in inside diameter High temperature wire R.S. type (357–110) 0·25in 2-pole jack plug and socket

output of the 741. This output is directly read as degrees Centigrade.

If the probe senses a temperature of 0° C, the difference between the two 741 inputs should be zero and the meter should indicate 0° C.

When the probe is exposed to a positive temperature change its resistance is reduced and the voltage applied to pin 2 of the 741 falls. The voltage on pin 3 remains constant and the 741 amplifies the voltage difference between these two pins. The amplifier output is then measured by the meter across VR2.

The l.e.d. is used as an on/off indicator with its current controlled by R8.

CONSTRUCTION

The prototype was housed in an R.S. polystyrene case with the on/off switch, l.e.d. and jack plug fitted into the side of the case and the two 4mm sockets mounted on the top.

The components are soldered onto a printed circuit board, the design of which is shown in Fig. 2 with the component overlay shown in Fig. 3. The two board contacts which are used for the output terminals should be drilled out to 4mm diameter and after the p.c.b. has been soldered and checked it can be mounted into the case (Fig. 4) using the two 4mm sockets to hold it in position.

PROBE

A cross section of the probe is shown in Fig. 5; it can be constructed using $\frac{3}{8}$ in inside diameter copper tubing and is connected to the unit via high temperature wires and a jack socket.

The diode must be well insulated to ensure that it does not short out to the case of the probe and after the high tempera-



Fig. 1. Circuit diagram of the Temperature Probe



Fig. 2. Printed circuit board design

CASE

COPPER TRACK PCB

SOCKET

741 OP. AMP.







Fig. 5. Probe assembly



Fig. 4. Mounting details for p.c.b.

11111

VR2

BATTERIES

The unit is powered by two batteries which are fitted to the bottom of the case and then covered with a piece of foam to protect the back of the p.c.b. from any damage.

CALIBRATION

'After the unit has been assembled and tested it is then ready

for calibration. As the characteristics of diodes vary from device to device the instrument should be recalibrated whenever the sensing diode/probe is replaced.

For calibration, boiling water is used as the 100°C standard and melting ice as the zero 0°C standard. The probe should first be immersed in a bowl of melting ice and water and the multiturn pot VR1 adjusted until the meter connected across the output terminals indicates zero. The probe should then be placed in simmering water and VR2 adjusted to obtain full scale deflection on the meter.

With the calibration completed the unit is now ready for use. \bigstar



A look at some of the more interesting items at the Birmingham Exhibition Centre (13-17 March)



W & T Avery Limited are pioneering the use of microprocessors in the weighing machine industry. A number of successful applications have already been made, one of which is at the National Coal Board's Grime-thorpe Colliery in Yorkshire, where a weighing-in-motion system has been installed as part of a new bulk loading scheme.

In the picture above a locomotive has shunted filled wagons through the loading bay and over the weighing system seen in the foreground. The system is designed to weigh two-axle wagons whilst travelling at

speeds of up to 8 km/h. The inset shows the instrumentation used. The printer produces individual axle weights for each wagon and the total weight for the entire

train excluding the locomotive.

An entirely new type of test instrument for the analysis and synthesis of digital waveforms has been introduced by Gould Instruments Division. Designated the Gould Advance DSA600, it consists of a memory which stores digital waveforms plus peripheral circuitry which permits the capture of signals from a circuit under test, the programming or modification of signals by the operator, the generation of the stored signals and the display of the memory contents.

Analyser applications include logic testing and simulation on both asynchronous and synchronous systems, such as avionics, radar, communications, data processing and industrial control, for research and development, test, service and production purposes. It can also be used to turn a basic oscilloscope into a form of storage oscilloscope and can be used in conjunction with the Gould Advance PG52 pulse generator to provide a programmable high-power data output.







The Moore & Wright "Micro 2000" electronic hand micrometer. The Micro 2000 uses a unique combination of specially developed solid state electronics and precision optical systems to measure to an accuracy of ± 2 microns and show the reading on a brightly lit integral digital display. Above is a 2-3 seater open sports car developed by Electraction Ltd., of Maldon, Essex. The glass fibre body is mounted on a galvanised steel tubular chassis arranged to absorb energy in the event of an accident through a safety bumper at the front and an energy absorption compartment at the rear. The batteries are mounted on sliding trays.

Up to 90km (56 miles) Range: Speed:

Up to 58km/h (36 m.p.h.) Thyristor controller made by Cable-Control: form (Pulsomatic Mark 10).



At the F.W.O. Bauch stand, visitors could enter realms of Startrek when trying out the new IE-10A Audio Spectrum Analyser from Ivie Electronics Inc. A slick hand held unit weighing only 15oz (430gms) can be aimed at a sound source to get an immediate breakdown of its frequency components. Powered by rechargeable Ni-Cad cells, the analyser/sound level meter will display graphically on a matrix of I.e.d.s sound pressure levels over a 45dB range.

pressure levels over a 45dB range. The 160 l.e.d. display matrix gives ten octave channels on the x axis (32Hz-16KHz), and the y axis is selectable for 1, 2, or 3dB resolution, calibrated from 45dB to 140dB S.P.L. (-116 to +9dBm) on A or C weighting. A range of inexpensive accessories enable

the analyser to measure amplifier power, voltage and harmonic distortion.

> 777 005

The new digital clamp meter KEW 777 from Eagle International is designed for safety and ease of operation. It has large, clear definition liquid crystal display and measures to 1,000 amps a.c. as well as 1,999 ohms and 1,000 volts a.c.



Also from Gould is the DMM9 a new 4½ digit multimeter with a 0.05 per cent measurement accuracy and true r.m.s. measuring facilities.

It features 28 a.c. and d.c. voltage, current and resistance measurement ranges, including a separate 10A current range, and is also available with optional probes for temperature, radio-frequency and high-voltage measurements.

The DMM9 has a maximum reading 19999, and maximum resolutions on the current, voltage and resistance ranges of 10μ V, 10nA and $100m\Omega$, respectively.

A combined a.c./d.c. facility is also available to measure a.c. waveforms with a d.c. content. Because true r.m.s. voltage measurement is the only accurate way of assessing the energy content of an a.c. waveform, the DMM9 is ideal for applications where power is the main parameter of interest, e.g. in the electricity supply industry or in applications involving thyristor control.



A railway of the future, controlled by micro computers, was the centrepiece of the CAF MicroSoft Ltd. stand. With 13 points, 25 yards MicroSoft Ltd. stand. With 13 points, 25 yards of HO gauge track, an eight stage speed con-troller, and two Fleischmann diesel-outline locomotives, the layout represents a versatile, flexible, low-cost peripheral to a microcomputerbased industrial control system.

Latest product news from Bryans Southern is of a compact "combination" instrument that they believe to be unique throughout the world. It introduces entirely new transient store elec-tronics developed by Bryans, and fitted within the mainframe of their well-proven 28000 series chart recorder. The resulting instrument offers a facility for processing transient signals and producing precision hard copy plots of them.





... a selection from our postbag

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

"Camm's Comic"

Sir—Whilst in no way disagreeing with M. Hughes' tribute to Mr Fred Bennett I must object to the disparaging reference to "Camm's Comic". In the immediate post-war years through to the dawn of the transistor age the so-called "comic" provided a fund of information for many embryo radio/electronic engineers where contemporary publications featured circuitry and components beyond the capabilities and finances of most students.

Personally I feel my introduction to the magic of this hobby (and latterly, my profession) owes much to the writings of the late F. J. Camm and I am surprised that no attempt has ever been made to commemorate him—perhaps by an annual competition for the best article from a newcomer?

As for the amateur forsaking the "junk-box", junk box does not equate with junk components nor apparatus. Rather the term junk may be applied to many of the modern gadgets seemingly devised to use as many components as possible instead of offering useful function.

> L. G. Rix, BSc, Melton Mowbray.

Electronics Club

Sir—I wish to inform you that our Club was set up in September 1977 at Shakespear House, Clapcot Way, Wallingford, Oxon.

The Club is a non-profit-making club for the pursuance of electronics as a hobby. The Club exists to provide mutual exchange of knowledge and experience in the fields of radio, electronics, electrical engineering and related sciences and technology. The club also provides education and encouragement to younger members who wish to make electronics their hobby and/or their career.

> J. Gilpin, Wallingford Electronics Club.

Future

Sir—I was prompted by your editorial in the March issue of P.E. to consider the future of the electronics hobbyist. At the present time, nearly all electronics publications are fairly brimming with articles and courses to introduce the hobbyist to the latest arrival on the scene—microprocessors. It seems that any amateur so inclined, providing his pocket will stand it, can learn all about these devices. Fine!

But what of the future? In a few years, when the enthusiasm has died down, will you continue to deliver elementary articles on such matters? Will it be possible for beginners to work their way up from Ohm's law to micros as has now been the case? Or will you only publish articles which presume the reader to have a working knowledge of the interior of a microprocessor chip, program writing etc.? If that is the case, then the "amateur" electronics field will surely be limited to those already into electronics by virtue of their profession.

Let us hope that this will not be so, and that you will run a series of "Teach-In" articles every few years as *Everyday Electronics* does in an elementary way.

C. Nelms, Elstead, Surrey.

You are certainly looking ahead, but the point is very valid, and one that is well taken. No doubt many people from all walks of life will ultimately want, or need, to know more about microcomputers and we are sure you will not find this need overlooked.—Ed.

POINTS ARISING

DIGITAL MULTIMETER (October 1977)

The Siliconix LD130 i.c. used in this project is apparently prone to "lock-up" when the power is first switched on. This condition manifests itself by a constant display reading of 007, and it is necessary to re-cycle the power supplies to clear it.

A modification is illustrated (right) which will rectify the fault and not affect the accuracy of the Instrument. Adjustment of the instrument is best carried out with the meter set to DC CURRENT, no input applied, and well-used rather than new batteries. With the preset wiper towards 0V the display will flash as if on over-range. As the wiper is revolved towards the negative rail the display will cease flashing, and indicate a number which decreases as the control is further rotated. Correct setting of the preset is just beyond the point where zero is displayed.

Modification kits comprising the preset potentiometer, E204 f.e.t., and necessary stripboard and fixings (with instructions) are available from Sparks Developments, Dept. P.E., 53 North Street, Melbourne, Derby, DE7 1FZ. AUTO-RANGING D.C. VOLTMETER (February 1978) A few corrections are necessary in Fig. 5, the component layout (specifically the power supply). Zener diodes D5 and D6 should be transposed, and also R1 with R2, so that they conform to Fig. 3. These are drawing errors which will not affect operation, but a correction which must be made is to transpose the labelling of +6V OUT.

Also, note that in Fig. 2, VR2 is connected between pins 1 and 5 of IC2, and the output of IC3a is pin 3.





THE HEAVY MOB

Want to make a solenoid an offer it can't refuse? Then call in the heavy mob from Texas. Working under the "SN75416 series" alias, and not using their real names like "Crusher Hargreaves" and "Hammer Hoskins", these new arrivals are guaranteed to beef up any logic team. Each member comes in a smart black fourteen pin package and carries two drivers which can each sink 500mA and hold off over 70 volts. The four members currently in town cater for all four varieties of logic gating, NAND, AND, NOR and OR and have low current p.n.p. input stages so that they can makeout with TTL or MOS families when required. Any loads which try any funny business with inductive spikes get clamped with a 500mA diode, and remember these boys are fast, 100 nanoseconds fast.

THE QUIET ONES

Any advance in the art of hi-fi is sure to be popular, even though most advances these days are pretty marginal! For those who strive to eliminate all sources of noise from their system, so that they are better able to hear the noise generated by their records or tapes, Motorola have something new to offer. The BC650/651 transistors are claimed to be the lowest noise input transistors available today, generating noise voltages of only 4, 3 and 2.5 nanovolts at 10, 120 and 1,000 hertz respectively when run at collector currents of 200mA. The two new transistors are n.p.n. devices and are rated at 30 volts (BC650) and 45 volts (BC651) with a minimum hre of 380 at 2mA and an ft of 300MHz. They come in low cost TO92 plastic packages and should be ideal for use in the "front ends" of domestic hi-fi amplifiers and high power disco' music systems.

THE FAST ONE

I am including my next offering, not because I expect anyone to rush out and buy one (they are a little expensive), but because it is an interesting new device which has pushed out the frontiers of performance in the analogue amplifier field quite dramatically.

I am talking about the Teledyne Philbrick 1435 which is described as an "Ultrafast differential input operational amplifier" in the data sheet. Ultrafast it certainly is because it sports a gainbandwidth product approaching one gigahertz, and can be used in standard operational amplifier configurations to provide a 20db to 40db gain from d.c. to beyond 10MHz! Despite its high speed performance the 1435 is a precision device, offering 0.01% gain accuracies and rapid recovery from input voltage steps, making it ideal for the amplification of complex waveforms in video and other signal processing applications.

When I first read the data sheet I immediately saw in this device the makings of a very simple, but capable, Y amplifier for an oscilloscope, and there seems to be no reason why it should not be used for this purpose when provided with an f.e.t. source follower input stage and a switched attenuator. In this role its 75 nanosecond settling time and less than 1% overshoot on square pulses would be a great advantage.

The 1435 is made using hybrid technoogy and lives in a small 14-pin hermetic d.i.p. package.

BRIDGING THE GAP

It has been said that any signal conditioning that can be carried out with analogue circuitry, can (in theory anyway). be carried out just as well with digital circuitry. Of course, microprocessors have not yet reached the point at which we can connect an aerial to an input port and a speaker to an output port and then expect to write some software which will tune in "Desert Island Discs". Given a micro' that worked sufficiently fast however, this sort of high speed signal processing would become perfectly feasible because the processes involved in converting a modulated r.f. signal into a signal suitable for driving a loudspeaker can be described mathematically, and we all know just how nifty micro's can be at doing sums! Speed is not the only limiting factor though; the conversion of analogue signals to digital form has been an expensive business which precluded its use except for the most demanding

applications, even when only low frequencies were involved.

There have been major advances recently which have made the digital processing of analogue signals more practical; modules or circuit boards are now available which contain an analogue-todigital converter paired with analogue signal multiplexers which permit the converter to be shared by a number of analogue inputs. These modules and circuit boards have been made fully compatible with the popular microprocessor fuses, but costing several hundred pounds apiece they are still not the stuff of which revolutions are made. To take the analogue world by storm we need a low cost monolithic converter and multiplexer, and the first example of such a system-on-a-chip has just been announced by National. Termed "Single Chip Data Acquisition Systems", the National ADC0816 and ADC0817 devices really do represent an attempt to bridge the gap between the microprocessor and the essentially analogue world that it lives in. The chips accept up to sixteen separate analogue signal inputs and then select one by means of a multiplexer for conversion to an eight bit digital word. Signal selection is controlled by means of a four bit binary address word which can be latched in directly from a microprocessor bus, or fed via an existing output port.

The eight bit data outputs are tri-state and can also be connected directly to a data bus.

The multiplexer output is brought out to a package pin and must be linked externally to the converter input, allowing signal conditioning and/or multiplexer expansion to be achieved easily. The converter itself is a rather unusual 256R type (256 resistors of value R and a switch tree selection system) instead of the more usual R2R type. Conversion is achieved by means of a successive-approximation sequence, and takes only 100 μ s per channel which means that a.c. signals of up to 300Hz per channel can be coped with (proportionately higher if less channels used), as well as d.c. inputs.

The ADC0816 is specified over a wide temperature range whereas the ADC0817 is specified at 25°C only and has reduced performance, albeit at a lower cost. Both chips are CMOS and draw only about 1mA from a 5V supply.



The outline of the voicing system is shown in Fig. 4.1, and consists of two sections covering upper and lower parts of the keyboard separately. Outputs from the threshold diodes, following the diode gates, for the upper section of the keyboard are taken to staircase networks in the voicing system



Fig. 4.1. Schematic of the voicing system

which produce stepped waveforms suitable for the four instruments, three at 16ft pitch and one at 8ft pitch. Each instrument is controlled by a slider potentiometer and has its own filter. The string voicing consists of a combination of both high and low pass filters which in conjunction with the second filter, which processes both registers, are in active form. Brass and Woodwind voices are produced by ringing band pass active filters and following a common preamplifier all upper voices are adjusted by a level control on the front panel.

A similar staircase network section produces stepped waveforms for strings in the lower section of the keyboard at 16ft, 8ft, and 4ft, which are then controlled by the lower voice switches and followed by similar string filters to the upper section with a preset level control on the Voice Board.

The combined voices from the full keyboard are amplified together and set by the master level control on the front panel. This signal is passed to the Chorus Generator for processing and returned to the Voice Board for distribution to the Swell Pedal and output sockets.

STAIRCASE NETWORKS

The effect of the Staircase networks is shown in Fig. 4.2 where waveforms (a) to (d) are square waves at 2ft, 4ft, 8ft, and 16ft each coming from the respective diode gate busbar. The square waves contain odd harmonics only which have limited use in the generation of musical instrument tones. Since the square waves on each busbar are octave related even harmonics are available by mixing outputs from each busbar. Generally an amplitude relationship is used where the level of each successively higher even harmonic content is half that of the harmonic below. Waveform (e) in Fig. 4.2 is produced by mixing an input at 16ft (d) with half the level at 8ft (c), and is used as the base waveform for the woodwind. The description "staircase" can be understood from the shape obtained. Waveform (f) is produced by mixing an input at 16ft with half the level at 8ft, a quarter the level at 4ft, and one eighth the level at 2ft. This waveform is used for all 16ft strings and brass, giving the addition of higher even harmonics.

Waveform (g) is obtained by mixing a fundamental at 8ft with half the level at 4ft and a quarter the level at 2ft for the 8ft strings, whilst waveform (h) has a fundamental at 4ft with half the level at 2ft and is used for the 4ft strings in the lower section.

VOICE CIRCUITRY

Full circuit details are given in Fig. 4.3. Resistors R69 to R76 terminate the output busbars from the diode gate circuits and are essential in any tests of the diode gate system if the Voice Board is removed. R77 to R98 perform the staircasing function prior to slider or switch controllers. The upper string filters are associated with IC32, the brass with IC33, woodwind with IC34 and lower strings with IC35. VR16 and VR17 control the resonant frequencies of the brass and woodwind filters respectively and require setting to avoid the violent peak occurring within the keyboard range. IC36 amplifies all the upper voices and is followed by the upper level control VR18.

LOWER STRING CONTROLS

Switches S3 to S6 are interlinked. With S3 depressed the 16ft and 8ft waveforms from the lower section of the keyboard are linked to String I and II slider controls respectively, and



Fig. 4.2. Formation of staircase waveforms from octave related squarewaves







Fig. 4.4. Etching detail for the voice p.c.b.



Fig. 4.5. Showing component assembly and drillings

COMPONENTS ...

VOICING SYSTEM

Resistors			
R69-76	10kΩ	R105 ·	47kΩ
R77	120kΩ	R106	10kΩ
R78	47kΩ	R107	22kΩ
R79	22kΩ	R108	10kQ
R 80	220k ()	R109-111	47k 0
R81	120k 0	R112	A 7k0
R82	47k()	R112	1040
R83	994()	P114	1740
P84	33040	R115 116	1040
R85	12040	P117	104.0
P86	1204 0	P119 100	10132
D97	120132	R10-120	47836
Dog	12040	P100	4.1 K 22
	120832	D102	10K32 47k()
R09-90	4/8.52	R120	47K32
R91 B00	22R32	R124	270K32
R92	120K32	R125	TUKS2
R93	47K12	R120	4/KS2
R94	22K12	R127	10K12
R95	220K12	R128	4/KS2
R96	120K12	R129	10kΩ
R97	4/ks2	R130	47k\$2
R98	22k12	R131	150kΩ
R99	10kΩ	R132	10kΩ
R100	47ks2	R133	47kΩ
R101	10kΩ	R134	47kΩ
R102	47ks2	R135	220kΩ
R103	470kΩ	R136	22kΩ
R104	10kΩ	R137	2·2kΩ
‡ watt 5%	6 carbon film		
Capacitor	s		
C56	4.7nF ceramic	C82	47nF ceramic
C57	10nF ceramic	C83	47nF ceramic
C58	22nF ceramic	C84-85	4.7nF ceramic
C59	47nF ceramic	C86	10nF ceramic
C60	10nF ceramic	C87	4.7nF ceramic
C61	22nF ceramic	C88	10nF ceramic
C62-63	47nF ceramic	C89	0.22/4F polyester
C64	10nF ceramic	C90	180pF
C65	22nF ceramic	C91	22nF ceramic
C66-67	47nF ceramic	C92-93	4.7nF ceramic
C68-69	2.2nF ceramic	C94	47nF ceramic
C70-71	4.7nF ceramic	C95-96	10nF ceramic
C72	0.1µF polvester	C97	0.1µF polvester
C73	100pF	C98-99	22nF ceramic
C74	4.7nF ceramic	C100	0.22 #F polvester
C75	47nF ceramic	C101	470pF ceramic
C76	22nF ceramic	C102	4.7nF ceramic
C77-78	4.7nF ceramic	C103	47nF ceramic
C79	10nF ceramic	C104	47nF ceramic
C80	4-7nF ceramic	C105	180pF
C81	10nF ceramic	C106-112	10nF ceramic

Potentiometers

VR12–15 10k Ω lin Sliders, VR16–17 47k Ω Presets 100mW submin. VR18 10k Ω lin, VR19 4-7k Ω Preset, VR20 10k Ω lin, VR21 10k Ω Pedal

Integrated Circuits

IC32-37 741

Miscellaneous

SK2-4 Mono standard jack. S3-6 bank of two-pole two-way switches interlocked. 47 terminal pins 1 printed circuit board. the 4ft signal is inoperative. S4, 5 and 6 convert the lower section to 16ft, 8ft and 4ft strings only, but more than one control button may be depressed at the same time. Except when in the couple condition the Lower Voices have a fixed amplitude preset by VR19, and balancing of the two parts of the keyboard is achieved with the Upper Level Control.

PREAMPLIFIER

Upper and Lower Voices are fed to the complementary (anti-phase) inputs of preamplifier IC37 to compensate for the additional inverting amplifier, 1C36, in the upper voice channel. The main purpose of the Master Level Control VR20 is to compensate for the many modes and styles in which the instrument may be played, either melodic or chordal, single or multi-voiced, and it may be used to prevent overloading of the Chorus Generator input under extreme conditions.

OUTPUT AND SUPPLIES

After processing by the Chorus Generator the signal is returned to the Voice Board on which it is controlled by the Expression Pedal via socket SK2. Divider resistors R136 and R137 give high and low level outputs at SK3 and SK4.

The Voice Board is powered by +15 volt and -15 volt supplies obtained from the regulators on the PSU/Tone Generator Board, and capacitors C106 to C112 are incorporated to ensure stable operation to the 741 Operational Amplifiers.





Fig. 4.6. Voice and Chorus interwiring

VOICE BOARD CONSTRUCTION

The Voice circuits described are mounted on a printed circuit board, the etching and drilling details of which are given in Fig. 4.4, with the component assembly details in Fig. 4.5. To assemble the board the terminal pins should first be inserted followed by resistors, i.c.s, preset potentiometers, small capacitors, large capacitors and the wire link next to R103.

INTERWIRING OF THE VOICE AND CHORUS CIRCUITRY

The Chorus Generator interfaces with the Voice Board only, as shown in Fig. 4.6, whilst the Voice Board provides connections to all controls and output sockets. The wiring details given in Fig. 4.6 should be followed carefully, and it should be particularly noted that in some cases screen connections are made at one end of a cable only whilst in others both ends of the screen are connected.

Supply inputs to the Voice Board are taken direct from the PSU/Tone Generator at +15 volts, -15 volts, and 0 volts. Pedal and output signals are taken through a single 3-core screened cable to sockets SK2-SK4 with both ends of the screen connected.

High and low inputs are each taken from the diode gate busbars through a four-core screened cable with the screen connected at each end. The Upper and Master Level Controls are connected by two-core screened cables with the screen soldered at both ends.

UPPER VOICE CONTROLS

A ground lead is taken from the Voice Board and connected to one of the slider controls. A lead is then taken from this point to each voice potentiometer. The remaining terminals on VR12 and VR13 are connected via a four-core screened cable with the screen soldered at the Voice Board end, but not to the potentiometers. Similarly VR14 and VR15 are connected via a four-core screened cable.

LOWER VOICE SWITCHES

A ground lead is taken to the tags (or pins) shown on S4, S5 and S6, which are strapped together. Three multi-screened leads are then used to complete interconnection to the switches and in each case the screen is only soldered to the Voice Board end whilst the other end is cropped and cleaned up to prevent shorting to other switch connections.

The first lead is two-core and interconnects the relevant pins on the Voice Board to S3. The second lead is three-core and interconnects the Voice Board to S3. The second lead is also three-core and interconnects to the two tags shown on S3 and one tag on S6. Ordinary wire connections are then made as shown between S3 and S4, and between S3 and S5.

Note: Omissions from Part One Components List are C6, C7, C9, C12–10nF ceramic, C8–68pF.

In Fig. 2.5 diodes D23-30 should be reversed. Fig. 2.2 shows them correctly polarised.

In Fig. 3.6 IC28 should be a 14 pin device. The two extreme left pin connections should be ignored.

NEXT MONTH—Cabinet construction



Battery Voltage Monitor S.V.ESSEX

UTILISING one inexpensive CMOS integrated circuit, the device described in this article gives a positive indication, by means of a light emitting diode, whenever battery voltage falls below a certain preset level.

BATTERY LEAKAGE

Whenever a battery is allowed to discharge too far it usually leaks—as many people have found to their cost at one time or another. Not only is the leakage unsightly and messy, but it is also extremely hard to remove.

Occasionally batteries will leak even when the equipment being supplied gives no indication of falling battery voltage.

Battery voltage is also of paramount importance in connection with test instruments—although these may be stabilised by means of a Zener diode, once the battery voltage falls past a certain level the diode can no longer exercise control and inaccuracies result.

Both leakage and inaccuracy can be prevented by employing a battery voltage monitor, which gives a positive indication of when batteries should be changed well before these problems are encountered.

CIRCUIT OPERATION

The circuit is shown in Fig. 1. Each of the four 2-input NOR gates in the CD4001 CMOS i.c. is used as an inverter by connecting the two inputs together.

The negative supply connection to the four gates is taken to the battery negative line via the light-emitting diode D1, across which an almost constant voltage is developed; even when the circuit is in its quiescent state (i.e. the battery voltage is relatively high), a small leakage current flows through the i.c., which is sufficient to bias the l.e.d. on, although it is not enough to illuminate it beyond a faint glow.

Two of the four gates are connected in cascade, the output from the second gate being connected to the inputs of the two remaining gates, which are connected in parallel. The four gates form one high gain amplifier, such that the output switches very rapidly from high to low when the input voltage to the first gate exceeds a certain proportion—usually 45–50 per cent—of the voltage supplied to the i.c. In figures this is the battery voltage minus about 1.6V developed across the l.e.d.

When the output from the last two gates begins to change state the threshold voltage is thus about 5.3V at a battery voltage of 9V, dropping to about 4.3V when the battery voltage has fallen to 7V.

POTENTIAL DIVIDER

R1, VR1 and R2 form a potential divider across the supply lines; if VR1 is adjusted so that its slider is at a potential of 5.4V (relative to the battery negative line) when the battery voltage is 9V, it will be found that at 7V the slider voltage is 4.2V. Thus, although the slider voltage is higher than the threshold voltage when the battery voltage is high, as battery voltage falls a point is reached where the slider voltage is lower than the threshold voltage and the output of the gates goes from low to high.

Since R3 is connected between the output and the i.c. negative supply pin, it thus draws current which increases the current flowing through D1. This slightly increases the voltage developed across the l.e.d., which, in turn, increases the threshold voltage: a regenerative process is initiated and the i.c. output gates saturate, causing an appreciable current to flow through D1, which then becomes illuminated.

Because the circuit operation is regenerative, there is a certain amount of hysteresis present (about a fifth of a volt), so that if the l.e.d. lights up when the battery voltage drops to 7V, it will remain on until the battery voltage exceeds 7.2V. This characteristic could be useful where a load is drawing high current peaks from a partially discharged battery. Although the battery voltage in between current



Fig. 1. Circuit of Voltage Monitor



Fig. 2. Suggested printed circuit layout



Fig. 3. Showing component assembly and wiring

peaks may be above the safe limit, any excessive rise in the internal resistance of the battery—which in this case could be the deciding factor as to whether or not the battery requires changing—would still mean that the l.e.d. would light up, since the battery voltage would momentarily drop below the preset limit; the hysteresis in the circuit would hold the diode on.

If the hysteresis provided by the basic circuit is insufficient, it can be increased by including a low value resistor (less than 10 ohms) in series with the l.e.d.

WORKING RANGE

The prototype was set to switch on when a nominal 9V battery voltage dropped to 7.2V, although the circuit will work on any voltage in the range 6–15V. With the value of R4 given, the current through D1 is about 10mA just before the l.e.d. cuts out.



If a higher voltage is used, or a higher current through the l.e.d. is required, the value of R4 can be altered, although care must be taken not to exceed the current and dissipation ratings of the i.c.

The standby current is about 0.4mA at 10V, if this is felt to be excessive a small pushbutton switch can be included in the supply to the circuit, so that battery voltage is monitored only when the button is pressed.

The standby current is roughly proportional to battery voltage.

CONSTRUCTION

The circuit, being small, can easily be constructed on a scrap of printed circuit board or Veroboard left over from a larger project: the layout is entirely non-critical although a suggested p.c. board layout is shown (Fig. 2).

When assembling the circuit, small components should be soldered in first and the CMOS i.c. left to last to prevent damage due to overheating. An i.c. socket can be used if desired.

The i.c. should not be handled excessively; although static discharge protection circuits are built into the inputs to the gates, there is no point in taking unnecessary risks. Care should also be taken to connect the i.c. into the circuit the right way round; incorrect connection could lead to an excessively high current flowing through it and the l.e.d.

SETTING UP

The easiest way to set VR1 correctly is to connect the circuit up to a variable voltage power supply, and adjust VR1 so that D1 lights up at the required battery end voltage. The battery voltage monitor can then be connected into the piece of equipment which is to be protected.



Compiled by DJD.

Appearing every two months, Micro-Bus will present ideas, applications, and programs for the most popular microprocessors; ones that you are unlikely to find in the manufacturers' data books. The most original ideas will probably come from readers working on their own microcomputer systems, and payment will be made for any contribution featured here. This is also the place to air your views, in general, on this new technology, so let's be hearing from you!

MICRO-EUS

THIS month's Micro-Bus describes a system which was developed to enable a microprocessor to play waltzes as it composes them. The main part of the program, which will play polyphonic tunes containing up to three notes sounded simultaneously, is described in full, together with the codes to enable it to play the first eight bars of a waltz. With the addition of a random-number generator and some extra control routines the micro can be made to compose as it plays.

MOZART'S DICE WALTZES

The idea of composing music mechanically has its origins in the 18th century when a number of pieces were published which used the throws of dice to randomly select between possibilities at different points in the music. Perhaps the best known of these is the "Musikalisches Würfelspiel" or "Musical Dice Game" attributed to Mozart and published two years after his death in 1793. This pamphlet enables anyone without the least musical ability to compose an almost unlimited number of different waltzes with the aid of a pair of dice. There is some doubt as to whether Mozart actually designed the game, although he was undoubtedly interested in such subjects, and the manuscript for his Adagio KV516 shows the bars arranged by letters in a similar way to the Musical Dice Game.

The Musical Dice Game consists of a set of bars numbered from 1 to 176, and a pair of tables. To compose the first eight bars of a waltz a pair of dice is thrown eight times, and the first table gives the number of the bar to be used at each stage for each throw. The other table is used with a second set of eight dice throws to give the second half of the waltz. The bars are so contrived that, no matter what combination of them is put together, the result will be a pleasant waltz. Since the choices for the eighth bar in each part are the same there are a total of 1114 possible waltzes; a number so large that playing a different one every minute it would take 700 million years to hear them all!

MUSIC FROM A MICRO

Although it is simple enough to write a program which uses software timing to play monophonic tunes (only one note sounded at a time), most of the interest of music lies in the simultaneous sounding of several notes to produce harmony and counterpoint, and this dictates the use of some extra hardware to produce the notes independently of the micro. Originally a design was contemplated using a topoctave note generating chip, three 1-of-12 selectors for the note selection, three 6-stage binary dividers and three 1-of-6 selectors for the octave selection, together with latches and addressing logic for interfacing this with the micro. The scheme was soon abandoned when it was realised that the same capability is provided in one Motorola part: the MC6840 Programmable Timer Module. In the mode of operation used here it effectively acts as three independent programmable dividers, so that an organ based on one chip can sound up to three notes at once.

MC6840 PROGRAMMABLE TIMER MODULE

The Programmable Timer Module, or PTM, is a recent addition to the M6800 family, and is Motorola's answer to the Intel 8253 Counter/Timer which it resembles in many ways. It contains three 16-bit counters whose contents can be read at any time, and three 16-bit buffer registers which can be written to by the micro. The contents of these buffers can be transferred to the counters either immediately they are written, or else only under the control of an external gate input $\overline{G1}$, $\overline{G2}$, or $\overline{G3}$. The counters are decremented on each clock pulse derived either from the microprocessor clock $Ø_2$, or from an external signal of lower frequency presented at the respective clock input $\overline{C1}$, $\overline{C2}$, or $\overline{C3}$. When a counter reaches the count of zero, a time-out occurs and a bit is set in the status register. What happens next depends on the mode of operation programmed for that counter by its control register. There are four basic modes of

operation, and any of the three counters can operate in any mode. In single shot mode the counter's output goes low at time-out and remains low until the counter is re-initialized. In continuous mode the content of the buffer register is loaded into the counter at each time-out so that a continuous square-wave is produced at the output. Additionally the timers can be programmed to generate an interrupt when a time-out occurs. In frequency comparison mode an interrupt is generated if the input period is less than (or alternatively, greater than) the counter time-out, and finally, in pulsewidth comparison mode an interrupt is generated if the input "down time" is less than (or greater than) the counter time-out.

The PTM is addressed as eight consecutive memory locations, the first two of which provide access to the three control. registers and the status register, and the 'at three pairs of which are used to write to the buffer registers or read the current values of the counters. The MC6840 is available from Cramer Components Ltd., 16 Uxbridge Road, Ealing, London, W5 2BP, for £12.73 plus 80p postage (VAT extra).

MUSIC INTERFACE

The complete circuit for the music interface is shown in Fig. 1. The three counters of the PTM are used in continuous mode, and the gate inputs are grounded to enable the counters. The clocks are derived from the microprocessor clock, so the inputs $\overline{C1}$, $\overline{C2}$, and $\overline{C3}$ are left unconnected. The three outputs are taken to an adder circuit at the input of an LM386 1 watt audio amplifier chip which drives a loudspeaker. The two MT8T26 quad bidirectional buffer packages and the TTL gates are needed to interface the PTM to the D2 kit's bus. In a small system the PTM could be connected directly to the microprocessor's address and data lines in which case these parts could be dispensed with. As shown, the PTM is addressed as locations \$2010 to \$2017.

The music produced by the circuit sounds "organlike" since the amplifier

is fed with the unmodified squarewave outputs of the PTM, but with extra circuitry it would be possible to add filtering or envelope shaping, possibly under control of the micro, to simulate other instruments or sounds. Once configured, the three counter outputs will produce frequencies equal to half the clock frequency divided by N+1, where N is the 16-bit number loaded into the respective buffer register. To obtain silence from any output, zero can be loaded into the buffer to give an inaudibly high frequency. To play tunes a program is needed to read the music stored in some notation in memory, and from this, load the correct divisors chosen to give the musical scale of notes, into the three counters at the correct times.

MUSIC NOTATION

The notation used for encoding tunes to be played by the micro uses one byte for each note, and one byte for each change in duration. The codes are shown in Fig. 2. The note codes use the lower four bits to determine the note, and bits 4 to 6 to determine the octave of the note (7 for the highest octave and 1 for the lowest), giving a total range of 7 octaves. For silence, a code of \$60 is used. The top bit specifies whether the notes will be sounded. If the top bit is zero, the next note code is fetched immediately; if one, the program waits for a certain duration while the notes sound. This duration is set up by specifying a duration code with \$D in the lower fourbits. The upper four bits then give a duration of 1 to 16 units. Fig. 3 shows the note codes for a section of the keyboard (the values in brackets give the codes with the top bit set).

The notation is best explained by an example. Fig. 4 shows the music for the first eight bars of a dice waltz, together with the hex codes which, when supplied to the program to be described, will play the tune at the loudspeaker of the music interface.

MUSIC PROGRAM

The main part of the music program is subroutine PLAY, shown in Fig. 5, which converts the note codes into the correct divisors and loads them into the PTM to generate the music. Its operation is shown by the flowchart of Fig. 6. A duration code, when encountered at any time, sets the duration parameter DURN to the value of the top four bits. Note codes cause the divisors for the notes specified to be loaded into successive buffer registers of the PTM until a note code with the top bit set has been encountered; at this point the program delays for the currently standing duration, and the register pointer is reset. The table SCALE gives the divisors for a well-tempered top octave; these are doubled for each successive lower octave.

The main program to call subroutine PLAY and play a tune, such as the one given in Fig. 4, is shown in Fig. 7. This is







Fig. 2. The four types of 8-bit code used to encode tunes for the music program described

Fig. 3. Note codes corresponding to the notes of a section of the keyboard. The same values with the top bit set are shown in brackets

(D) 5(E1) 61	(E4 64) (E 6	(6) (6)	(E8) 68	(E1 61	B) (F1) 71	(F4 74) (1	-6) 76	(F8) 78	
(DA) 5A	(DC) 5C	(E2) 62	(E3) 63	(ES) 65	(E7) 67	(E9) 69	(EA) 6A	(EC) 6C	(F2) 72	(F3) 73	(F5) 75	(F7) 77	(F9) 79	



WALTI	PCB \$2D,\$72,\$4A,\$EO,\$EA,\$65,\$EO
FCB	\$72,\$4A,\$EO,\$EA,\$65,\$EO
FCB	\$1D,\$6C,\$55,\$C9,\$F2,\$F3,\$EC,\$6A,\$C5,\$E9
FCB	\$6A, \$52, \$CA, \$E9, \$EA, \$P2, \$2D, \$65, \$60, \$E0
FCB	\$74, \$CA, \$1D, \$F7, \$P4, \$6C, \$E0, \$P4
FCB	\$75,\$4C,\$CC,\$F4,\$75,\$4C,\$CC,\$F9,\$2D,\$6C,\$4C,\$C
PCB	\$1D,\$72,\$4A,\$EO,\$EA,\$69,\$CC,\$E7,\$65,\$BC,\$E4
FCB	\$2D,\$45,\$75,\$E9,\$1D,\$D5,\$D3,\$52,\$60,\$E0,\$CC,0

Fig. 4. The music for the first eight bars of one possible Mozart dice waltz, together with the hex codes for playing this on the system described in the article. Each line of codes corresponds to one bar of the music

			NAM	PLAY			
		* SUA! * 1	POINTED	TO PLAY LIST OF NOTE TO JY X.	s		
0000	0002	NOTE	Ч́МЭ ЗМЭ	2			
0004	0002	PTIME	1 9M3 7M8	5			
0007	0001	TOP	344	i			
	0240	TEMPO	EQU	5240 52010 MC 6840 PTM			
		* * ¥£LI	. TEMPER	ED SCALE .			
0008	0000	* SCALE	FD3	0,451,426,402,379			
000A 000C	01C3 01AA						
0010 000E	0192 0173			,			
0012	0166 0152		FDJ	358,338,319,301			
0016	013F 012D				Fig. 5. Si	ubroutine for the MC6800 micro	
001A	011C 010C		FDB	284,268,253,239	in coded	form	
0015	OOFD						
0022	DF 00	PLAY	STX ,	NOTE			
0027	DF 84	STINE	STX	PTIMER			
0023	A6 00	HURL	LDA A	0.X			
0 0 2 D 0 0 2 F	39		RTS	RETURN			
0030 0031	05 DF 00	NSTOP	STX	NOTE			
0033	16 C0 0D		TA-3 SU-3	£50D			
0036	C5 0F 26 04		BIT 3 BNE	ESOF 2ND DIGIT SET	D3	Fig. 6 Flowchart for subrouting (
0 D 3 A	D7 86	* D =	DURATIO	N CODE * DURN	•	shown in Fig. 5	
003C	20 EB 36	SET	BRA PSH A	MORE		energi e	
003F	16 84 0F		TAB AND A	ASOF NOTE PART			
8042	48		ASL A	·			
0045	97 03		STA A	PNOTE+1			
0046	89 00		ADC A	ESCALE/256			
804C	97 02 DE 02		LDX	PNOTE POINT TO D	IVISOR		
004E	A6 00 97 07		LDA A STA A	0.X TOP			
0052 8054	A6 01 58		LDA A ASL J	1.X			
0055	CO EO	+ DOUE SHIFT	LE DIVI	SOR FOR EACH LOWER OF	CTAVE +		
0057	24 06		BOL A	NSHIFT WITH CARRY	6 5 T	· · · · · · · · · · · · · · · · · · ·	
005A	79 000	7	ROL	TOP	321	Fig. 7. Main program for the MC68	00
DOSF	44	NSHIFT	DECA	AS DIVIDES	34 N+1	micro which configures the thr	ee
0060	D6 07		LDA 3	TOP		mode and then calls subjection	us
0064	E7 00 A7 01		STA B	1*X 0*X		PLAY	
0068 0069	08 08		INX INX				
806A 006B	32 4D		PUL A TST A			0080 ORG \$80 0080 CE 8283 BEGIN LDX 158283 CONFIGURE PTM	
806C	2A 39	* TOP	APL BIT SET	STIMER SOUND NOTES *		0063 FF 2010 STX TIMER 0066 86 82 LDA A 1382	
006E	96 86 CE 824		LDA A	DURN		0088 57 2010 STA A TIMER 0088 CE 0092 LDX SWALTZ POINT TO MUST	c
0073	19	VAIT	DEX	VALT		008E BD 0022 JSR PLAY 0091 3F SVI	-
0076	44		DEC A	WA41		•	
0079	20 A9		3RA	N EWS ET			
		•	END				

entered at BEGIN. These programs were developed and run on a Motorola D2 kit, which is based on the MC6800 micro, although it should be a simple matter to modify them to run on any other. To implement the composing of dice waltzes a random number generator is used to choose between several alternatives for each bar. One approach would be to use the random number as an

offset to a table of pointers to the alternative bars. The composition of music by computer is certainly not exhausted by the techniques described, and it is hoped that those interested will be stimulated to experiment further. It seems as unlikely that music generated in this linear way will be memorable any more than a line of random numbers will form a pleasing overall pattern. A more enlightened approach might be to compose from the top down; for example, starting with a simple theme which is developed and embellished. Perhaps the most amusing suggestion was to use the Musical Dice Game to generate continuous "musak" which would never repeat itself! When by chance an especially pleasing waltz cropped up, the sequence of bars could be noted down for posterity.

ON ENTRY Y POINTS TO TUNE POINT NOTE TO TUNE POINT PTIMER TO FIRST COUNTER REGISTER GET NEXT CODE FROM ADDRESS NOTE YES CODE=00 RETURN NO NOTE = NOTE +1 CODE = XO NO DURN = X0 A=LOWER 4 BITS OF CODE A=A = 2 USE A AS OFFSET IN SCALE TABLE AND GET DIVISOR B=OCTAVE NUMBER YES B =7 NO STORE DIVISOR IN COUNTER REGISTER AT PTIMER DOUBLE DIVISOR TOP BIT O POINT PTIMER TO NEXT COUNTER REGISTER YES DELAY FOR **DURN + TEMPO UNITS**

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The Common Good

International political and economic quarrels are constantly in the news. The USA and Britain are at odds with the Japanese in matters of trade, for example. And Britain is frequently in dispute with EEC partners, individually or collectively. Such squabbles tend to obscure some fine international cooperative ventures.

One such is Meteosat, 22,000 miles up, quietly doing its job of providing world weather maps and met. data for the common good of mankind. It was built for the European Space Agency by the COSMOS consortium of aerospace companies and launched by the Americans from Cape Canaveral. In all there will be five satellites to give complete global coverage. The Americans are supplying two and the Japanese and the Russians one each.

Japanese Imports

But having recorded with pleasure something that unites us all through science, we still cannot avoid considering what divides us internationally in trade and industry. In the UK, Japanese car imports have displaced colour TV as the main talking point. Not that the Japanese threat to British electronics goods manufacturers has disappeared although there is some easement in the TV sector where the agreement on restricting sales is reported to be working reasonably well.

The knock-out blow which closed Thorn's Skelmersdale picture-tube plant has left Mullard in a more favourable position as Britain's sole producer. Even so, Mullard have had to adopt an aggressive attitude (i.e. reduce prices and profits margins) to sell 650,000 tubes out of 1.8 million used in the UK last year. The Japanese, however, still managed to sell 583,000 with the rest coming mainly from Canada and the USA. Mullard also got some help from the rise in value of the Japanese yen. Mullard report having "turned the corner" with the Japanese market share in the UK dropping sharply in recent months.

The British Radio Equipment Manufacturers Federation (BREMA) has been negotiating an agreement on music centres where the Japanese have over half the UK market. Trade in this sector is said to be sluggish and with large stocks of Japanese products already in the UK it seems likely that even if agreement is reached there will be little effect in the short term.

Brain-power Exports

The Post Office Viewdata service, becoming operational in the UK a year earlier than expected, looks like being an export success. Already adopted by Germany, it is now being examined in the United States following its public showing at Atlanta, Georgia, last October. The system is being sold through Insac Data Systems Ltd, a company set up by the NEB to market British computer software overseas.

Another software company, Compeda Ltd, owned by the National Research Development Corporation, specialises in selling systems based on university and government-sponsored research. Exports of systems packages are expected to account for at least 60 per cent of turnover according to Keith Trickett, managing director of Compeda. One of the systems offered is called the Pipework Design Management System expected to appeal to the petrochemical, gas and process industries. Another is Gaelic, a design system for i.c.s and p.c.b.s.

Setting Up the Arabs

Closely allied to pure brain-power exports such as selling software and supplying consultancy services is technology exporting, a controversial subject because as well as transferring technology it can also transfer jobs, not politically attractive at periods of high domesic unemployment.

Egypt, for example, is anxious to move into high-technology industries such as electronics and aerospace and some British electronics technology has already been transferred to the Cairo area and is in production. Recently announced is a big helicopter deal in which the Anglo-French Lynx will be built in Egypt, both airframe and engines, initially from kits of parts but progressively building up an indigenous aerospace industry which eventually could conceivably compete in the world market and with lower labour costs pose a threat to the companies now supplying all the know-how.

The process of educating those nations aspiring to industrial expansion is something we have to learn to live with. It is not new. I remember being startled to discover an American multinational, well known for antagonism to the Communist political system, cheerfully selling technology, and production machinery to match, to some Eastern Bloc countries. The Americans involved were confident that by the time the Eastern Bloc manufacturers had mastered the technology, they themselves would already be well ahead on the next generation of products, always a step ahead. A supporting argument was that if they didn't supply the know-how and equipment, then somebody else would, so you might as well get the business while it is going.

IEA/Electrex

People are slowly becoming accustomed to the National Exhibition Centre near Birmingham. The IEA/Electrex show attracted a substantially greater attendance than formerly. Facilities for visitors are far greater than at the old London venue but it wasn't just this that brought the crowds along but a' more optimistic trade outlook and the stands were certainly busy with good enquiries for products and services. The electrical part of the exhibition was more dominant and busier but I heard no complaints from exhibitors in the electronics section.

An interesting feature was the number of new towns and industrial development areas which took exhibition space, some with very large stands, in an effort to encourage industrial investment. Their full-colour brochures are fully on a par with those from travel agents, and it seems hard to resist the advertised attractions. "When you're in Wakefield you are in the heart of Rugby League country—and among people who work as hard as they play."

Overstretch

Videomaster, who built a £4 million turnover company from scratch in four years has been unable to finance expansion and, although still trading, is in the hands of the Official Receiver. Fresh capital is being sought. Eighty per cent of the business was in the volatile TV games sector where promotional costs are high and pricecutting nothing short of savage.

Engineers' Pay

The IEE Salary Survey shows that the hypothetical average professional electrical/electronics engineer had a salary increase of 5 per cent since the last annual survey, well below the rate of inflation. But the gap between those employed in the public and private sectors narrowed. In the public sector the average Fellow or Member engineer was ahead by £1,180 p.a. and this has now fallen to £840. In the case of Associate Members the respective figures are £890 and £630 p.a.

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The Sinclair PDM35 is tailormade for anyone who needs to make rapid measurements. Development engineers, field service engineers, lab technicians, computer specialists, radio and electronic hobbyists will find it ideal.

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The PDM35 gives precise digital readings. So there's no need to interpret ambiguous scales, no parallax errors. There's no need to reverse leads for negative readings. There's no delicate meter movement to damage. And you can resolve current as low as 0.1 nA and measure transistor and diode junctions over 5 decades of current.

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DC Volts (4 ranges) Range: 1 mV to 1000 V. Accuracy of reading $1.0\% \pm 1$ count. Note: 10 M (1) input impedance. AC Volts (40 Hz-5 kHz) Range: 1 V to 500 V. Accuracy of reading: $1.0\% \pm 2$ counts. DC Current (6 ranges) Range: 1 nA to 200 mA. Accuracy of reading: $1.0\% \pm 1$ count. Note: Max, resolution 0.1 nA.

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Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.



MUSICAL CALCULATOR

THE rapid advances in pocket calculator technology has resulted in many cheap keyboards being available on the surplus market, several of which are suitable to form the basis of a small electronic organ.

Using the two CD4011 i.c.s as shown in Fig. 1 at least two octaves may be covered, each key selecting an individually adjusted miniature skeleton preset thus producing the required note. The oscillator drives a simple amplifying stage consisting of a Darlington pair and a balanced armature insert producing a more than adequate sound level.

IC2 provides a switchable tremolo

effect needing only one key to turn it either on or off; a further key being used to parallel C4 with C3 to produce the flats to any note pressed. The range covered by the keyboard can be extended by using another "control" key to double the value of the capacitor in the oscillator.

Of especial benefit when used by the forgetful child is the fact that no on/off switch is necessary. With a 6 volt supply, and high gain, low leakage transistors, the quiescent current is unmeasurable on a 50μ A meter, although the tremolo should be left in the off condition as it consumes nearly

0.5mA.

Only the simplest keyboards have an independent connection available to each key but the printed circuit of most is easily modified after a little patient examination. The circuit is, of course, monophonic, i.e. depression of two keys produces a third, unrelated note, nevertheless an amusing musical toy can be constructed at very little cost, its accuracy dependant only on careful tuning of each note.

> D. Ian, Hampton Court, Surrey.



His circuit will help in making low noise, high quality recordings on a good stereo tape recorder. It could also enhance the reproduction from an f.m. tuner or from discs. The input signals are mixed and buffered by IC1, which has a variable gain to enable inputs within the range 10mV to 1V r.m.s. to be accepted. The signal is then half-wave rectified by IC2, and further amplified by IC3, which can work in the inverting or non-inverting mode depending on the setting of VR2, which also dictates the gain of this stage. A control voltage then appears across C6. The attack and decay time of this voltage is controlled by VR3 within the range 20ms to 200ms.

The control voltage is fed to two voltage controlled amplifiers (v.c.a.s), one for each channel. These utilise the forward conducting resistance of silicon diodes, which can be varied according to the current being passed through them.

Looking at the left channel v.c.a., the current through diodes D3 and D4 is controlled by TR1, and hence by the control voltage. The audio signal is injected into TR2 which along with TR3 forms a long tailed pair. Anti-phase signals appear at the inputs of IC4, which amplifies the difference as the output signal. R18 tends to minimise this difference, and hence the gain of the v.c.a. As more current is passed through the diodes, the gain increases.

VR4 and VR5 are adjusted so that 550mV is dropped across each of the resistors R17 and R29. VR1 is adjusted so that when a signal of normal listening level is fed into the inputs, adjustment of VR2 makes little difference to the overall output level.

P. R. Williams, Stevenage, Herts.

Here is a portable, high precision frequency counter up to 100MHz guaranteed,* typically 110MHz. It has 8 big, bright (0.6") LED displays, so there's no range changing. The crystal time base has 3 ppm accuracy and updates the display every second. Sensitivity is astounding. It will trigger at 30 mV, yet is protected to 200V peaks. It comes complete with clip lead input cable. An antenna for coupling to RF equipment indirectly, and a low-loss in-line RF tap are optionally available. Take it with you anywhere. Run it on internal rechargeable NiCad's, 110 or 220V AC, 12V from your car digarette lighter socket or from any external 7.2 to 12V DC supply.

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Fig. 1

FADE UP/DOWN IN THYRISTOR CIRCUITS

A MONG the many published thyristor circuits for power control, I do not recall seeing one with a "fade-up/ fade-down" feature. The circuit depicted in Fig. 1 will do this; as shown it is applicable to a load requiring 12V, but it could probably be adapted to higher power requirements.

The main controls are VR1 for thyristor firing control, and switch S1 for fade-up/down control. The setting of VR1 determines maximum load current, via two stages of amplification TR1 and TR2, then through a conventional u.j.t. firing circuit based on TR3. Fade-up/down is provided by the charging and discharging of C1, as regulated by S1, R1 and R2. With S1 switched to position (a), CSR1 output will gradually rise to the maximum governed by VR1, whilst with S1 switched to position (b), C2 will discharge and fade-down will occur.

Fade times of about 30 seconds were obtained with R1 or R2 equal to $5m\Omega$, whilst at $68k\Omega$ fade times were nil. R1 and R2 could be made variable if required.

VR2 and VR3 are included for

setting the bias on TR2; they should be adjusted when setting up so that zero output and full output from the s.c.r. are obtained from the extreme settings of VR1 (do this with C1 disconnected and S1 connected to the positive rail).

> J. Duffill, Cheltenham, Glos.



SIMPLE WAA-WAA

THIS system is very cheap but very effective, and comparable with many commercial circuits now on the market. It was designed chiefly for use with an electric guitar, though it is also very effective on other instruments.

It consists of a basic "T"-filter network in the feedback bias of a transistor amplifier. According to the component values, a small range of frequencies are boosted while others around are attenuated. As VR1 is rotated, the range is shifted and a different band of frequencies are boosted.

In the circuit shown, the component values have been selected so that with potentiometer rotation the narrow frequency-selective band sweeps over the entire range of the guitar, so giving the desired "Waa-Waa" effect.

The value of VR1 should not be more than $5k\Omega$ as the degree of rotation would then be too small for the musician to be able to control the effect confidently. If the guitar used with the unit has a particularly weak output signal then RI can be reduced. The circuit will run on a PP3 9V battery. S. D. Le Maistre, St. Laurence, Jersey.

SPEED CONTROLLER



This system was designed to control the speed of a radio controlled car capable of speeds of up to 35 m.p.h. The circuitry is intended for use in conjunction with the "Proportional Radio Control System" published in P.E.

One of the main disadvantages of using a variable resistor between the supply and the motor to provide speed control is that any control action which varies the speed of the car, also varies the current available to the motor and consequently the torque or pulling power. The speed controller shown in Fig. 1 uses pulse width modulation techniques which not only overcome these problems, but also provide reduced current consumption from the supply of nickel cadmium batteries used in the car.

Since the racing car is to be driven in one direction, i.e. for forward control, only one half of the servo amplifier circuit is required for use. This is shown for convenience in Fig. 1 and comprises transistors TR1, TR2 and TR3. A $1k\Omega$ resistor acts as a dummy load in place of the servo motor. The servo feedback potentiometer on the servo drive board is replaced by a trimming potentiometer of the same value in order to trim the completed system with respect to the transmitter.

A variable d.c. output of 0 to 4V is available between points C and 0V rail when the corresponding joystick movement in the transmitter is 0 to $5k\Omega$. This available voltage is used to switch transistors TR4 and TR5 to provide a maximum d.c. output of 8V which is necessary to provide the



variable speed control of the car as follows:

TR6 and TR7 form a Schmitt trigger and TR8 provides the phase inversion for oscillations to occur. The output at TR8C is a square wave whose mark/space ratio depends on the d.c. voltage present at point C. When mark/space ratio is 50 per cent, the time that the voltage is on is the same as the time that the voltage is off, giving an average value of the voltage as shown in Fig. 2. If the mark/space ratio is altered so that the on time is increased the average value of the voltage will rise and hence the car will speed up. Consequently when the mark/space ratio is decreased the car will slow down.

The output of the Schmitt circuit feeds a Darlington connected driver and output stage TR9 and TR10 that drives the motor. The motor used in the car is the popular "Bullett" motor supplied by most radio control shops. This motor was found to have a "start" current of 10A at 18V and a "run" current of approximately 6A which varies at different motor speeds. Diode D1 suppresses inductive kickback of the motor while D2 protects TR10 from voltage transients of the motor.

Transistor TR11 serves as a current sense amplifier in order to compensate via the feedback resistor Rx for variations in the frequency of the Schmitt trigger output.

A stack of 2 ampere-hour nickel cadmium batteries have been used to provide the 18V supply. The circuit can also be used with a 12V supply by decreasing R11 to approximately $100\Omega_{\odot}$.

Transistors TR9 and TR10 must be mounted on a heatsink.

L. Sadarangani, W. Ealing.

WAVEFORM GENERATOR



WHEN the circuit shown in Fig. 1 is switched on, the input of IClb is held "low" by C1, setting all the outputs of the 7496 shift registers to the "low" state, and setting the Q output of flip-flop IC4 "high". The "Set Entry" of IC5 (pin 9) is, therefore, also "high". When C1 charges to the threshold value, IClb changes state, thus removing the reset instruction to the shift registers.

ICla forms a clock whose frequency is determined by CT its output passes via IC2a to the C_k input of the registers and to IC3a. On the first negative going clock pulse, the "high" on pin 9 of IC5 is transferred to output number 1 (pin 15). This "high" is fed to IC3a, and when the clock output next goes "high" the output of IC3a goes "low" resetting IC4 and presenting a "low" to the set entry of IC5. On subsequent clock pulses the "high" on output 1 is shifted through the registers (ICs 5-7), each of the outputs switching "high" in turn.

When pin 10 of IC7 switches "high", IC2c and IC3b apply a "high" to the SET ENTRY of IC5, thus starting the sequence once more.

IC6 may be duplicated to provide more outputs (circuitry within dotted lines). If this is required, IC2a and IC2d should have one or more inverters connected in parallel with them to facilitate the extra load.

A waveform may be divided into X portions, each portion having a specific amplitude (Fig. 2). By connecting each output of Fig. 1 to a potential divider (Fig. 3), the amplitude of each output pulse may be pre-set, thus enabling the desired waveform to be taken off at point Y. The more

they.

outputs used, the more accurate will be the resulting waveform.

R1, R2, R3...can be around $2\cdot 2k\Omega$ and RA, RB, RC...may be varied to provide the required amplitude.

A unit was built in which the variable resistors were built onto a piece of Veroboard which plugged into an edge connector mounted on the case. Each waveform needed could then have its own program card.

The circuit of Fig. 1 could also be used to "scan" a solid state display etc.

> A. Damper, Carshalton, Surrey.




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