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## MAY 1978

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- INPUT IMPEDANCE - 47 K ohms
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- CONTROLS - Mains on-off, master volume, base $\pm 15 \mathrm{db}$ treble $\pm 15 \mathrm{db}, L$ and $R$ mixing, $L$ and $R$ motor switches
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Kit of basic modules less power pack, pots. 5 jack sockets, and 3 ains switches, but with fron
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 Sensitivity -60 mV THD $-0.3 \%$. $3 \frac{1}{n}^{n} \times 2^{\prime \prime} \times 1^{\prime \prime}$.
20 watts R.M.S. into $4 \Omega$ using 34 V . Sensitivity - 80 mV . THD $-0.3 \%$
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SS. $140 \quad 40$ watts R.M.S. Into $4 \Omega$ using 45 V . Sensitivity -300 mV . Distortion typically $0.1 \% .5$ " $\times 3 \frac{1}{1 "}^{\prime \prime} \times 1 \frac{1^{\prime \prime}}{4} \ldots \ldots$. 64 watts R.M.S. into $4 \Omega$ using 50 V . Sensitivity - 350 mV . Distortion typically $0.1 \% .5^{\prime \prime} \times 3 \frac{1}{4 \prime}^{\prime \prime} \times 1 \frac{1}{4}^{\prime \prime}{ }^{2} . . .$. $70 \mathrm{~V} / 2 \mathrm{~A}$. Input sensitivity -500 mV $70 \mathrm{~V} / 2 \mathrm{~A}$. Input sensitivity -500 mV . Distortion at half-power, typically $0.1 \% 5^{\prime \prime} \times 3 \frac{1}{2} \times 1 \frac{1}{4}$............
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| SS.334 | $\mathbf{3 4 V} / 2 \mathrm{~A}$ | $£ 8.75$ |
| SS.345 | $45 \mathrm{~V} / 2 \mathrm{~A}$ | $£ 10.75$ |
| SS. 350 | $50 \mathrm{~V} / 2 \mathrm{~A}$ | $£ 11.75$ |
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CIRCUIT AND LAYOUT DIAGRAMS are supplied free with all PCBs unless "as published"
PHOTOCOPIES of all P.E. texts for most of the kits are available-prices in our lists.

MAIL ORDER SUPPLIERS OF QUALITY PRINTED CIRCUIT BOARDS, KITS AND COMPONENTS TO A WORLD-WIDE MARKET
P.E. MINISONIC MK. 2 SYNTHESISER

A portable mains-operated Miniature Sound Synthesiser. with keyooard circuits. Although having slightly fowe affered by this designe P.E Synthesiser the function orrered by this design give it great scope and versatility. Consisis of $2 \log$ VCOs. VCF. 2 envelope shapers. 2 voltage
controlled amps. keyboard hold and control circuits. HF oscillator and detector. ring modulator. mixer, power supply.

Set of basic component kits from $£ 62.23$
Set of printed circuit boards
£9.71
P.E. SYNTHESISER (P.E. Feb. 73 to Feb. 74)

The well acclaimed and hignly versatile large-scale mains-operated Sound Synthesiser complete with keyboard Synthesiser to good advantage.
The" Maln Synthesiser: PSU. 2 linear VCOs. 2 ramp generators. 2 input amps. sample hold, noise generator reverb amp. ring modulator. peak level circuit. envelope shaper, voltage controiled amp.
Set of printed circuit boards
E83. 03
$\varepsilon 13.20$
The Synthesiser Keyboard Circuits (can be used without the Main Synthesiser to make an independent musical instrument): 2 logarithmic VCOs, divider, 2 hold circuits, 2 modulation amps, mixer. 2 envelope shapers and PSU.
$\begin{array}{lr}\text { Set of basic component kits } & \text { £48.18 } \\ \text { Set of printed circuit boards } & £ 7.88\end{array}$

## GUITAR EFFECTS PEDAL (P.E. July 75)

Modulates the attack. decay and filter characteristics of an audio signal not only from a guitar but from any audio source. producing 8 differen $t$ switchable effects that can be further modified by manual controls Possibly the most interesting of all the low-priced sound effects units in our Overdive Unit

Component set with special foot operated switches
Alternative component ser with panel switches
Printed circuit board
¢7.59

SOUND BENDER (P.E. May 74)
A multi-purpose sound controller. the functions of which include envelope shaper, tremolo. vorce-operated fader. automatic fader and frequency-doubler. Component set for ab

## P.E. JOANNA PLUS ORGAN VOICING

The basic five octave electronic piano (P.E. May/Sept 75 anc Sound Design) has switchable alternative voicings for HonkyTonk, ordinary piano, and Harpsichord or a mixture of any of these three, rogether with facintes including fast and slow tremolo, loud and soft pedal switching, and sustain pedal switening. The modicalit relain all he ancuin absociatod with piano but in addition provides an organ-voice envelope facility with 5 switchable pitches, variable attack and sustain, phasing and vibrato.

Set of components (excl switches) for PSU. Frequency generator, Pitch and Note Divider, Envelope Shapers, Voicings. and Control circuitries. (Order as KIT 71-5) £109.75 Set of PCBs (Order as PCB SET 71-6
c29.18

## SYNTHESISER TUNING INDICATOR (P.E. July 77)

A simple 4 -octave frequency comparator for use with ynthesisers and other instruments where the full versatility Component and PCB (but excl sw.)

GUITAR FREQUENCY DOUBLER (P.E. Aug. 77)
A modified and extended version of the circuit published
Component get and PCB

## GUITAR SUSTAIN (P.E. Oct 77)

Maintains the natural attack whilst extending note duration. Component set, PCB and foot switches £4.90 Component set, PCB and panel switches

## WIND AND RAIN UNIT

A manually controlled unit for producing the above-named sounds.
Compo
Component set (iricl. PCB)
£3. 72

GUITAR OVERDRIVE UNIT (P.E. Aug. 76 )
Sophisticated, versatile Fuzz unit. including variable and switchable controls affecting the fuzz qually whilst retaining the attack and decay. and also providing filtering. Does not duplicate the effects from the Guitar Effects Pedal and can be used with it and with other electronic instruments Component set using dual slider. por $\begin{array}{ll}\text { Printed circuit board } & \text { E6.20 } \\ \text { E1-62 }\end{array}$

FUZZ UNIT
Simple Fuzz unit based upon P.E. "Sound Design" circuit. Component set (incl. PCB)
P.E. SYNCHRONOME (P.E. Mar. 76)

An accented-beat electronic metronome. providing duple triple and quadruple times with full control over the beat rate Can also be used as a simple drum-beat rhythm Component set (inct loudspeaken
Printed circuit board 811.62

## TAPE NDISE LIMITER

Very effective circuit for reducing the hiss found in most tape recordings. All kits include PCBs
Standard tolerance set of components
Regulated power supply (will drive 2 sets)

ENVELDPE SHAPER WITHOUT VCA (P.E. Oct. 75)
Provides full manual control over atlack. decay. sustain and release functions, and is for use with an existing voltage Controlled amplitie
Component set (incl PCB)

ENVELOPE SHAPER WITH VCA (P.E. Apr. 76)
This unit has its own voltage controlled amplifier and has full manual control over attack, decay. sustann and release functions.

Component set ( $1 \mathrm{ncl} . \mathrm{PCB}$ )
£6.68
TRANSIENT GENERATOR (P.E. Apr 77)
An envelope shaper, without VCA, having the usual attack decay. sustain and release functions. and in addition it also provirma Repeat enabling a synthesiser to be banjo
Component set
Printed circuit boaro

## WAVEFORM CONVERTER

Slightly modified from a circuit published in "Elektor". Converts a saw-tooth waveform into four different waveforms: sine-wave mark-space saw-tooth, regular triangle form, and squarewave with an externally variable mark-space ratio.

Component set (incl. PCB but excl. sw/s)

VOLTAGE CONTROLLED FILTER (P.E. Dec. 74)
Part of the P.E Minisonic now released as an independen kit for use with other synthesisers.
Component set (incl PCB) (Order as Kit 65-1) 22

RING MODULATOR (P.E Jan 75)
Part of the P.E. Minisonic now released as an independen Component set (incl. PCB) (Order

## NOISE GENERATOR (P.E. Jan. 75)

Part of the P.E. Minisonic nów released as an independent
kit for use with other synthesisers.
Component set (incl. PCE) (Order as Kıt 60-1) $\quad$ \&3. 35

## SOPHISTICATED POWER SUPPLIES

A wide range of highly stabilised low noise power supply kits s available-details in our lists.

MICROPHONE PRE-AMP (P.E Apr. 77:
Component set (incl. PCB)
E3.78
Produces 84 switch-selected frequency-accurate tones. A LED monitor clearly displays all beat note adjustments. Ideal for tuning acoustic or electronic musical in struments. Main component set (incl. PCB)
Power supply set (incl. PCB)
£
$\mathbf{6} .03$

SEE OTHER PAGE FOR KEYBOARDS, AND OUR LISTS FOR OTHER COMPONENTS AND ACCESSORIES STOCKED

VOICE OPERATED FADER (P.E Oec. 73)
For automatically reducing music volume during talk-over - particularly useful for Disco work or for home-movie shows.
Component set (incl. PCB)

OYNAMIC RANGE LIMITER (P.E. Apr. 77)
Automatically controls sound output to within a preset

Component set (incl. PCB)
[4.58

EXPORT ORDERS are welcome, though we advise that a current copy of our list should be obtained before ordering as it also shows Export postage rates. All payments must be cash-with-order, in Sterling and preferably by International Money Order or through an English Bank. To obtain list send 50p.

## AND OTHER PROJECTS

PHOTOGAAPHS in this advertisement how two of our units containing some of PCBs. The cases were buill by ourselves and are not for sale. though a small selection of other cases is available.

LIST-Send stamped addressed envelope with atI U.K. requests for free ist giving fuller details of PCBs. kits and ther components.

OVERSEAS enquiries for list Eurode send 20p: other countries send 50p.


## KIMBER-ALLEN

## KEYBOARDS AND CONTACTS

Kimber-Allon Keyboards as required for many published circuits. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C, the keys are plastic, spring-loaded, fitted with actuators, and mounted on a robust aluminiump frame. 3 Octave ( 37 notes
£25.50
4 Octave (49 notes)
f32.25
5 Octave ( 61 notes)
£39.75
Contact Assemblies (gold-clad wire) for use with the above keyboards (1 required for each nota):
Type GJ: Single-pole change-over

Type $\mathrm{GB}: 2$ pairs of contacts, each pair normally open
Type GC: 3 pairs of contacts, each pair normally open
Type GE: 4 pairs of contacts, each pair normally open
Type GH: 5 pairs of contacts, each pair normally open
Type 4PS: 3 pairs of contacts plus single-pole chengeover
Printed Circuit Boards for use with CJ. GB and 4PS contacts (thus eliminating much interwiring) are available. Details in our lists.

## RHYTHM GENERATOR

15-Rhythm Tempo, Timing and Logic control unit (excl. sw's but incl. PCB)
PCB for Effects circuits
812.90

ع13.66
Power Supply incl. PCB

## 128-NOTE TUNE-PROGRAMMABLE SEQUENCER

(P.E. Nov/Dec 77)

Enabies a voltage controlled synthesiser to automatically play pre-programmed tunes of up to 32 pitches and 128 notes long. Programs are keyboard initiated and note length and rhythmic pattern are externally variable. (Please use order codes quoted in brackets.)

Main Circuit (Nov) excl. sw's (KIT 76-1)
Power Supply \{ KIT 76-3 \}
Trigoer Inverter and Alt Output (KIT 76-2)
PCB (as published) for $K$
CB to KITS 76d for KITS 76-1 \& 3 (PCB 76A)
P.E. STRING-ENSEMBLE (P.E. commencing Mar 78)

The new keyboard string-instrument synthesiser. - - - -
Tone Generators (incl. Test components)
PC8 for PSU and Tone Generator
Details of further kits and PCBs in our list

## FORMANT SYNTHESISER (Elektor 1977/78)

Very sophisticated music synthesiser for the advanced con structor who puts performance before price. Details in our lists.

3-CHANNEL SOUND-TO-LIGHT (P.E. Apr. 76)
A simple but effective sound-to-light controller capable of operating 3 lamps each of approximately 700 watts. Includes ower supply, thyristors, and by-pass switches. Component set (incl. PCB)

C11.95
DISCOSTROBE (P.E. Nov. 78)
4-channel light-show controller giving a choice of sequential, random, or full strobe mode of operation asic component set

ع18.19
Printed circuit board
f3.45
BIOLOGICAL AMPLIFIER (P.E. Jan/Feb. 73)
Multi-function circuits that, with the use of other extema equipment, can serve as lie-detector, alphaphone, cardiophone stc.

Pro-Amp Module Components set (incl. PCB) $\quad$ E4.22 Basic Output Circuit -combined component sel with PCBs, for alphaphone, cardiophone, frequency meter and visual feed-back tempdriver circuits. $\mathbf{E 6 . 5}$ Audio Amplifier Modula Type PC7
Recio ampimer mocuio Yype

## 10\% DISCOUNT VOUCHER (PE85)

TERMS: Correctly costed, C.W.O., U.K. orders over $\mathbf{E 4 0}$ goods value. Valid until end of month on covar of P.E. This voucher must accompany order.


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Excelient job prospects await those who hold one of these recognised certificates. ICS can coach you for
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Technical Communications
Radio Servicing Theory
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Computer Engineering and Programming
Radio, T.V. and Audio, Engineering \& Servicing
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## Other Career Courses

A wide range of other technical and professional courses are available including GCE.



## STEREO <br> PRE-AMPLIFIER <br> PA 100

The 450 Tuner provides instant program selection at the touch of a button ensuring accurate tuning of 4 pre-selected stations, any of which may be altered as often as you choose, by simply changing the settings of the preset controls.
Used with your existing audio equipment or with the BI-KITS STEREO 30 or the MK60 Kit etc. Alternatively the PS12 can be used if no suitable supply is available, together with the Transformer T461.
The S450 is supplied fully built, tested and aligned. The unit is easily instalied using the simple instructions supplied.


25 Watts (RMS)

- Max Heat Sink temp 90C. Frequency response 20 Hz to 100 kHz - Distortion better than 0.1 at 1 kHz - Supply voltage $15-50 \mathrm{v}$. Thermal Feedback Latest Design Improvements Load-3, 4, 5 or 160 hms. Signal to noise ratio 80 db Overall size 63 mm 105 mm .13 mm .

Especially designed to a strict specification. Only the finest components have been used and the latest solid-state circuitry incorporated in this powerful little amplifier which should satisfy the most critical A.F enthusiast.

## £4.55

## Stabilised Power Supply Type SPM80

SPM80 is especially designed to power 2 of the AL60 Amplifiers, up to 15 watts (r.m.s.) per channel simultaneously. With the addition of the Mains Transformer BMT80, the unit will provide outputs of up to 1.5A at 35 V . Size: $63 \mathrm{~mm}, 105 \mathrm{~mm}$. 30 mm . Incorporating short circuit protection.
Input Voltage: 33-40 V.A.C.
Output Voltage: 33 V D.C. Nominal
Output Current: $10 \mathrm{~mA}-1.5 \mathrm{amps}$
Overlead Current: 1.7 amps approx
Dimensions:
$105 \mathrm{~mm} \times 63 \mathrm{~mm} \times 30 \mathrm{~mm}$
Transformer BMT80:
$£ 5.40+86$ p postage
£4.25
3. Magnetic P.U. 3 mV into 50 K ohms
P.U. Input qqualises to R1AA curve within $1 d B$ from 20 Hz to 20 KHz Supply-20-35V at 20 mA . Dimensions
$299 \mathrm{~mm} \times 89 \mathrm{~mm} \times 35 \mathrm{~mm}$ $20 \mathrm{~Hz}-20 \mathrm{KHz}$ Sensitivity of inputs:

1. Tape input 100 mV into 100 K ohms
2. Radio Tuner 100 mV into 100 K ohms

## £15.80

 MK60 AUDIO KIT: Comprising: $2 \times$ AL60 $1 \times$ PPM80 $P$ PAP45p panel and knobs. 1 Kit of parts to include on $x$ SPM80. $1 \times$ PA100. 1 fron headphone sockets plus instruction onfich switch, neon indicator, sterao 62 postage. TEAK 60 AU$3 \frac{3}{4}$ ". other parts include aluminium chassis heatsink and size $16 \frac{3}{4}{ }^{\prime \prime} \times 11 \frac{1^{n}}{2} \mathrm{X}$ plus back panel and appropriate sockesis, heatsink and front panel bracket postage.

## STEREO 30 gawa <br>  <br> $7+7$ WATTS R.M.S

 power supply. This produce a high quality audio unit suitabler or overwind will high quality ceramic pick install, capable of pro full instructions, black front pang really first class results, this unit is supplied with universal mounting brackt panel, knobs, mains switch, fuse and fuse holder and cabinets of yourting brackets enabling it to be installed in a record plinth. cabinets of your own construction or the cabinet available. Ideal for the beginner or the advanced constructor who requires Hi-Fi performance with a minimum of installation (can be installed in 30 minutes)TRANSFORMER £3.25

$$
\text { plus } 50 p \text { p \& p }
$$

TEAK CASE E5.45 plus 70p $\mathrm{p} \& \mathrm{p}$
£18.95
p. \& p. 45 p

## uipment mono and other modules for Stereo

## NOW BI－PAK BRINGS YOU－ The AL80 <br> MPA 30

 $35{ }_{w}^{\text {Rss }}$ power Amp！ ONLY £7．15 $+8 \%$ VAT


Enjoy the quality of a magnetic cartridge with your existing ceramic equipment using the new BI－PAK M．P．A． 30 which is a high quality pre－amplifier enabling magnetic cartridges to be used where facilities exist for the use of ceramic cartridges only．Used in conjunction are 4 low noise high gain silicon transistors．It is provided with a standard DIN input socket for ease of connection．Supplied with full，easy－to－ follow instructions．

A High Fidelity Power Amplifier with a maxi－ mum Power Output of 35 watt R．M．S．， which has a maximum operating voltage of 60 v ．A MUST for all HI－FI users．

Maximum supply voltage
Power output for $2 \%$ THD
Harmonic distortion
Load impedance
nput impedance
Frequency response +3 dB
Sensitivity for 25 watt $0 / P$
Max．Heat sink temperature Dimensions
Mounting
Fuse requirements
$15-60 \mathrm{v}$
35 watts R．M．S．
0．1\％
3－8－16 ohm
50 K ohm
$20 \mathrm{~Hz}-40 \mathrm{KHz}$
280 mV R．M．S． $90^{\circ} \mathrm{C}$
$102 \mathrm{~mm} \times 64 \mathrm{~mm} \times 15 \mathrm{~mm}$ 2，4BA fixing holes in heat sink 1．5A

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POWER AMP
Specially designed for use in－
Disco Units，P．A．Systems，high power Hi－Fi，Sound reinforcement systems SPECIFICATION：

Output Power： 125 watt RMS Continuous

Operating voltage：50－80
Loads：4－16 ohms
Frequency response： $25 \mathrm{~Hz}-$ 20 kHz Measured at 100 watts Sensitivity for 100 watts output at $1 \mathrm{kHz}: 450 \mathrm{mV}$
Input impedance： 33 K ohms

Total harmonic distortion 50 watts into 4 ohms： $0.1 \%$ 50 watts into 8 ohms： $0.06 \%$ $\mathrm{S} / \mathrm{N}$ ratio：better than 80 dBs Damping factor， 8 ohms：65 Semiconductor complement： 13 transistors 5 diodes
Overall size：Heatsink width 190 mm ，length 205 mm ，height 40 mm

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illustratian shows GXL Centaur System
These systems feature full mixing for twa decks tape \& mic with monitaring facilities - override ond are supplied camplete with sound to light + sequencer, display, speoker leads etc.

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Similar in appearance to the Centaur and camplete with loudspeakers and leads.

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| Baam Stand | $\mathbf{£ 1 5 . 5 0}$ |
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c 7.50
$£ 7.50$ £ 1.90

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## $033000000 \begin{aligned} & \text { MONO OR STEREO } \\ & \text { WITH AUTOFADE }\end{aligned}$ <br> MODULES

Avoiloble complete and ready to plug in or as an easy to connect module with all controls except monitor switch already fitted - full instructions supplied.

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PIEZO HORNS only $£ 7.50$ YES! - only $£ 7.50$
(As fitted to our package PA system) Direct from Motorolo Inc., USA ot on UNBEATABIE PRICE

No crossover required $4 \mathrm{kHz}-30 \mathrm{kHz}$ rated $75 \mathrm{~W} / 8$ ohms $150 \mathrm{~W} / 4$ ohms use two per 100 W amplifier - Full instructions supplied.
 Mono module 5250 Stereo module $\quad £ 33.50$
Panel Kit of knobs/sockets ats $\quad \mathbf{E 3 . 9 5}$ COMPLETE MIXERS (with case) Mono l8V E39.50 Stereo 18V E57.50 Monomains $£ 45.75$ Stereo mains E63.75

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12 Manths @ $£ 14.60$ ar 24 Months @ $£ 8.14$ Includes 4 Channel 100 Watt Amplifier with Treble, Bass and Master Cantrals plus Leads and Twin Pieza Horn Columns (shown an right).

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## Melos Echo Unit $£ 59.00$

A high quality Cassette Tape Echo Unit giving long tape life, infinitely variable echo depth and speed control. Suitable for all mics. and instruments.
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Make your own mixer - Mono/Stereo - up to 20 channels with these, easy to wire modules - Available as $\mathrm{PCB}^{\prime}$ s or assembled on panels.


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| :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { Stereo } \\ & P C B \end{aligned}$ | £9.50 | Stereo (/W <br> panel etc. | £12.50 |
| Mixer/Monitor (One only | $\begin{aligned} & \text { Mono } \\ & \text { PCB } \end{aligned}$ | $£ 5.95$ | Mono (/W panel etc. | £8.95 |
|  | $\begin{aligned} & \text { Stereo } \\ & P C B \end{aligned}$ | £9.50 | Stereo C/W <br> panel etc. | f12.50 |
| Power supply for up 1020 channel |  | £9.50 | Blonk ponel | £1.00 |

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## SPECIAL

THIS month we bring you yet another special issue-the third in as many months. We have enclosed a Guide to the Language of Microcomputers in this issue and feel sure that this will be of interest to virtually all readers. The booklet contains over 120 words allied to microcomputers and gives a concise meaning to each. Even those with little or no interest in the field at present will, we are sure, soon find it necessary to keep up with the language as these devices enter into our everyday lives from all directions at an increasing rate.

The next two issues of P.E. will also be rather special as they contain details and entry coupons for a "free entry" competition. This is special because it could result in financial backing for a reader's idea. Some inventive readers could thus be financially rewarded as well as win a couple-of-hundred pounds worth of oscilloscope.

This competition is the result of an exclusive arrangement between P.E. and a Venture Capitalist. The aim is to attract ideas that could be developed to form commercial products. The presenter of such an idea will be involved in
the development and will reap the benefit in the form of a stake in any company set up to handle the product or in the form of a royalty or other payment.

The competition is also open to companies, who could thus win backing for their prototype designs. We think you are an inventive lot and remember -it is often the simple ideas that are the best ones. So put your thinking caps on and watch out for full details in the next two issues of P.E. only.

## SAVING

In this day and age it seems that one thing is becoming more and more im-portant-that is the best possible use of available finances. For the electronics man this often means the construction of a piece of equipment rather than its purchase and also means the use of electronics to save energy by providing automatic control of such things as central heating, etc.

It is probably true to say that it is now easier to get into electronic construction than ever before-the tools for the job are readily available as are the necessary components. Most construction can be carried out with the
minimum of metal bashing and the use of readily available plastic cases, knobs and finishing materials can result in a very professional end product.

So, all those regular readers who enjoy the theory, or just reading all about it, perhaps now is the time to get stuck in! If you want further encouragement there's an item under Strictly instrumental that tells you how to save about $£ 4,500$ !

## SUBSCRIPTIONS

As some alert readers may have noticed we have recently reinstated the subscription service for P.E. Although this may not interest many "home" readers, we know that a large number of overseas readers have problems in getting regular issues. This should no longer be the case as you can now have them posted to you each month with the minimum of fuss-see the foot of this page for details.

The subscription service is of course also available to British readers; if you have problems getting issues this service should guarantee you a copy: The next best thing is to place an order with your local newsagent.

Mike Kenward

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## Letters

Queries regarding articles published in PE should be addressed to the Editor, at the Editorial Offices, and a stamped, addressed envelope enclosed. We cannot undertake to answer questions regarding other items, nor to answer technical queries over the telephone.

FOR some years advertising signs have used strings of lights wired up and controlled in such a way as to make lights appear to move in a continuous movement along the string. Such effects are also fairly popular as backgrounds in television shows and on the stage. Smaller units are also used to create the same effects in discos.

The device to be described is designed to provide the same effect using either three strings of low voltage pygmy lights or up to a 3 kW total power loading of mains voltage bulbs wired in three circuits.
The movement effect is obtained by wiring the lamps as three circuits so spaced out that every third bulb in the series of bulbs is wired to the same circuit. The three circuits are switched on and off in sequence and the effect created is that of a moving point of light if one circuit is on at a time and the other two off.

If the circuits are so arranged that two circuits are on and one is off at any one time the effect is the opposite in that a moving gap in a string of lights is created.

Here, both possibilities can be obtained, depending on the position of a selector switch. A sprint facility is included which, if switched into operation, will make the speed of change of the output circuits increase for a short period at regular intervals in the cycle.

## POWER SUPPLIES

Two voltages are required to power the circuitry: +5 V stabilised which is used to power the TTL circuits and the lamp changing multivibrator and +12 V which is used to power the reay driver circuitry and to provide part of the output for the sprint circuit.

The power supply is shown at the left of Fig. 1. The two voltages are derived from the secondary of a single 9 V transformer the oulput of which is rectified by a bridge rectifier and smoothed.

This circuit gives 12 V , the negative connection of which is grounded. The +5 V stabilised supply is derived from the smoothed 12 V raii by $\mathrm{IC1}$. This device is a 100 mA stabiliser mounted in a TO92 case and only requires the addition of C2, C3 and R1 to form a complete stabilisation circuit.

TR1, TR2, C4. C5. R2. R3, R4, R5 and VR1 form a conventional multivibrator circuit. The speed of the lamp change is governed by the frequency of the output, which is set by VR1.

## SELECTION AND OUTPUT CIRCUITS

The output sequence is obtained by counting the output pulses from the multivibrator and decoding the numerical sequence obtained to switch the lamp circuits.

This circuit uses trl logic and is shown in Fig. 2.
In this an input to a gate is logic 1 when a voltage of more than 2.4 V is present and at logic 0 when less than 0.4 V is present. As most logic circuits require a clean transition between these two states the output from the multivibrator circuit is cleaned up by means of a Schmitt trigger which gives a clean square waveform.

This is achieved by using half of a $\overline{7413 \text { (IC2a). }}$
As any unused TTL input automatically floats to the logic 1 condition it is customary to tie any unused input to a used input on the same gate so the used inputs of IC2s are connected to the output of the multivibrator.

The square wave output from IC2a passes to IC3 which is a 7492 divide-by-twelve counter. The truth table for this is given in Table 1.

Table 1

| Count <br> (Pin 14) | Output <br> (Pin 8) |  |  |  |  | C <br> (Pin 9) | B <br> (Pin 11) | A <br> (Pin 12) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 0 | 0 | 0 | 0 |  |  |  |  |
|  | 0 | 0 | 0 | 1 |  |  |  |  |
| 2 | 0 | 0 | 1 | 0 |  |  |  |  |
| 3 | 0 | 0 | 1 | 1 |  |  |  |  |
| 4 | 0 | 1 | 0 | 0 |  |  |  |  |
| 5 | 0 | 1 | 0 | 1 |  |  |  |  |
| 6 | 1 | 0 | 0 | 0 |  |  |  |  |
| 7 | 1 | 0 | 0 | 1 |  |  |  |  |
| 8 | 1 | 0 | 1 | 0 |  |  |  |  |
| 9 | 1 | 0 | 1 | 1 |  |  |  |  |
| 10 | 1 | 1 | 0 | 0 |  |  |  |  |
| 11 | 1 | 1 | 0 | 1 |  |  |  |  |
| 12 | 1 | 1 | 1 | 1 |  |  |  |  |

After 12 the counter resets to 0 and runs through the sequence again.

Outputs A and B are used to drive relays 1 and 2 respectively each of which controls one circuit of lamps. The third circuit of lamps needs to be on when neither


Fig. 1. Power supply and lamp changing multivibrator

A or B is at logic 1 . This would be most easily obtained by using a NOR gate, but to do so would require an additional i.c. To implement this an alternative circuit using three inverters from unused gates are used.

A two input NAND gate has a truth table as shown in Table 2.

Table 2

| B | A | Output |
| :---: | :---: | :---: |
| $\mathbf{0}$ | 0 | 1 |
| 1 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 1 | 0 |

Although this circuit gives us the required output at logic 1 when both $A$ and $B$ are at logic 0 , it gives us the same output when either of the inputs are at logic 0 and the other is at logic 1. However, we can make use of the fact that the gate gives us an output of logic 0 when both of the inputs are at logic 1. By inverting outputs A and B of the counter and connecting the inverted outputs to the inputs of the NAND gate we can obtain an output from the gate which changes from logic 1 to logic 0 when the counter outputs are both at logic 0 . This output is the opposite of what we wanted but this can easily be rectified by inverting the output of the gate.

If we allowed the 7492 counter to continue further through its cycle after it had reached the count of two we would obtain a very strange light display and we therefore need to return the counter to zero when it would normally go onto a count of three. This is achieved by making use of the reset line connected to pins 6 and 7. When both of these pins are connected to logic 0 the circuit counts but if the reset pins are taken to logic 1 the counter resets all outputs to logic 0 .

## COMPONENTS . . .

Resistors

| R1 | $4 \cdot 7 \mathrm{k} \Omega$ |
| :--- | :--- |
| R2-8 | $2 \cdot 2 \mathrm{k} \Omega$ |
| R9 | $470 \Omega$ |
| R10 | $1 \mathrm{k} \Omega$ |
| $\frac{1}{2} \mathrm{~W}$ | $10 \%$ carbon |

Potentiometer
VR1 $5 k \Omega$ dual linear potentiometer

## Capacitors

| C1 | $2,200 \mu \mathrm{~F} 25 \mathrm{~V}$ electrolytic |
| :--- | :--- |
| C 2 | $0.22 \mu \mathrm{~F} 250 \mathrm{~V}$ polyester |
| C 3 | $0.47 \mu \mathrm{~F} 250 \mathrm{~V}$ polyester |
| C 4 | $1,000 \mu \mathrm{~F} 10 \mathrm{~V}$ electrolytic |
| C 5 | $100 \mu \mathrm{~F} 10 \mathrm{~V}$ electrolytic |
| C 6 | $0.1 \mu \mathrm{~F}$ disc ceramic |

Semiconductors

| TR1-11 | BC109 (11 off) |
| :--- | :--- |
| IC1 | 7805 regulator |
| IC2 | 7413 |
| IC3, IC6 | 7492 |
| IC4 | 7404 |
| IC5 | 7400 |
| D1-7 | OA81 (7 off) |

## Miscellaneous

T1 $\quad 240 \mathrm{~V}$ primary 9 V secondary 6 VA
S1 D.p.s.t. rocker switch with neon indicator
S2 S.p.d.t. rocker switch
S3 S.p.s.t. rocker switch
FS1 10A fuse
RLA-C 12V $110 \Omega$. Contacts rated $10 \mathrm{~A}, 240 \mathrm{~V}$ according to requirement
SK1 $\quad 5$ way output socket with plug to match. Each pin rated 5A 240V
Double sided printed circuit board $140 \mathrm{~mm} \times 102 \mathrm{~mm}$ Metal case $204 \mathrm{~mm} \times 152 \mathrm{~mm} \times 76 \mathrm{~mm}$

A two input nand gate, IC5b, is connected with its inputs to the outputs A and B of the counter. When both of these inputs are at logic 1 (when the counter goes to a count of three) the gate gives an output of logic 0 ; this is inverted so that the counter is reset to zero every time it goes to a count of three. As the reset only takes about 8 ns the unwanted display never appears and the counter appears to count $0,1,2,0,1,2 \ldots$.

## SPRINT CIRCUIT

A sprint circuit can be engaged so that the speed of the display will increase for eighteen counts and then continue at the set speed for a further eighteen counts before speeding up again.

The circuit takes an output from the reset circuit to a 7492 counter. The output of this counter at pin 8 (output D) goes to logic 1 for counts 6 to 12 and as the counter is incremented every time the first counter is reset this corresponds to 18 counts in all. This output is used to drive TR10 and TR11 which are arranged to form saturated transistor switches which short out the variable resistance VR1 of the multivibrator circuit.

When the collector voltage of the transistor is lower than the base voltage the transistor saturates and the emitter collector path behaves as a short circuit. R3 and R4 are therefore in effect connected to the +5 V line and set the frequency of the multivibrator.
In order to operate the transistor switches the output voltage of the 7492 is used to lift the base voltage of the two transistors to be above the collector voltage. Unfortunately the logic 1 voltage of TTL gates is typically only $+2 \cdot 4 \mathrm{~V}$ and so voltage amplification is required. TR9, R9 and R10 form a simple voltage amplifier.
This single stage amplifier inverts the output from the 7492 in the process of amplifying the voltage and therefore the circuit sprints when the output of IC6 is at logic 0.
Switch S3 disengages the output of TR9 from the bases of TR10 and TR11 and when this switch is in the off position the sprint circuit is inoperative.

## OUTPUT RELAY DRIVE

The use of s.c.r.s as the output elements of this device was considered but was abandoned in order to reduce the


[^2]

Fig. 2. Logic and drive circuit


Fig. 3. Etching details shown full size for p.c.b.


Fig. 4. Component layout and assembly on p.c.b.


Fig. 3. Wiring for a large display
cost of the device since relay drives are easily inverted to give the two light or one light circuit operative at a time.

The output from the trl logic circuits is insufficient to drive the relays directly and the relays are driven by transistors connected in the standard Darlington pair configuration, controlled by the output from the TTL circuits. The drive circuits for each relay are identical.

In order to provide the option of one light circuit or two light circuits active at a time the relays are wired with
the common terminal connected to the lamps and the normally closed contacts of each relay wired together. If two lamp circuits are required to be on at any one time and the third circuit off (giving a moving gap effect) the mains connection is made to the normally closed contacts of each relay so that the selection by the TTL logic circuits switches off the required circuit. If one light circuit is required to be on and the other two circuits off, giving a moving light effect, the mains is connected to the normally open contacts of the relays and the tTL logic circuits activate the relay to switch the required lamp on.

## LIGHT DISPLAY

An easy way to obtain the necessary light display is to use three strings of low voltage bulbs connected with a common wire from each string to the neutral connection of the output socket and the live connection to the appropriate output pin of the output socket. The strings should be taped together so that the lamps of each string are interleaved to give the correct impression of movement. If a bigger display is required this could be obtained by wiring mains bulb holders together as shown in Fig. 3. $\star$

## Riondout A SEIECTION FROM OUR POSTBAG

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

## CHamping of The Bit

Sir-I have read the first parts of the CHAMP project with great interest, but am somewhat reluctant to spend, to me, a fair amount of money on the system shown for the following reasons:
(1) The seemingly poor availability of the Intel devices, in fact, all the i.c.s not in the 74 range. I have noticed that in the past, a project has been published, and, next month, the retailers adverts are full of "complete kit of parts for . . . project" but not for CHAMP.
(2) The fact that the 4040 is a 4 -bit device as opposed to the 8 -bit devices of which there seem to be more types available at the moment. Coupled with this the 4040 is an "old" device-will there be spares available for this 4 -bit device in the years (months?) to come amid all the parts for 8 -bit and now 12-bit (16-bit? 20-bit?) devices on the market?
I am very keen to learn more about microprocessors and thought that CHAMP might provide a good start.
P. C. Chamberlain,

Anglesey.

Point 1:- The "chip kit" mentioned in part II of CHAMP contains all the necessary Intel i.c.s and is available for a very reasonable price. These i.c.s are also available separately from Rapid Recall, G.E.C. Semiconductors, Jermyn, and several other suppliers.

Point 2:- The Intel 4040 has been a very successful microprocessor, and has been incorporated in a very wide range of commercial equipment which guarantees its availability for many years to come. I would urge Mr Chamberlain not to be misled by current advertising which concerns the "latest and the greatest" microprocessor chips. It has been the trend to "push" these chips often before they are available in useful quantities, and this can cause frustration for intending users. I would also point out that to use a sixteen bit microprocessor for example, a considerable investment in equipment and software is necessary before anything like full use can be made of its admittedly powerful facilities. CHAMP does not, and will not, require teletypes, VDUs, bulk memory, assembler software or high level com-
pilers for its effective employment.
The writer is currently using the 4040 in a number of applications which involve the control of printers, paper tape stations, and keyboards, all of which involve binary arithmetic, B.C.D. arithmetic and binary to B.C.D. conversion along with other tasks such as time measurement and analogue output. All these applications were programmed directly in hexadecimal and involve the simplest basic 4040 hardware. I have the highest regard for the more powerful microprocessors, but believe me, they are only worth having if you invest in a powerful memory, software, and input output onvironment to do them justice. RW Coles.

## Quality

Sir-I feel that the quality of projects in P.E. is not as good as a year ago. For instance Dec. 1977 three out of four projects were continuations of projects from previous months. Jan. 1978 had six projects in, one of which was a continuation and another three were suitable only for (your usually excellent) Ingenuity Unlimited!
P.E. Champ is all very nice and educational but, at the cost indicated not very many people can afford to build it. For this reason I think that Champ should be a General Features/Project, rather than just a project. This would allow for four projects plus P.E. Champ.

## R. A. Austin,

 Brentwood.Although we endeavour to present a varied selection of constructional projects each month we cannot promise to please "all of the people all of the time". It is also necessary to give a good selection of additional news and features as we are sure most readers would not like a purely constructional magazine.-Ed.


There is a case on record of the businessman who visited his doctor, to be told that he was suffering from strain, hypertension and was likely to have heart trouble. The treatment prescribed was not the usual course of drugs: instead, he was advised to buy an electronic organ. Perhaps a bit expensive, but good advice as a method of unwinding from the effects of the rat-race.

## HATH CHARMS

It appears that the treatment worked, though learning to play from scratch demands a good deal of concentration and co-ordination of mind and limbs. For the perfectionist in particular, this will be hard going at first but before long the tiro can become completely absorbed. As a large percentage of organ sales are to first-time owners, manufacturers compete with each other to offer aids to the beginner-from a special pedal Legato to illuminated keys. Many of these features can also be put to good use by the skilled player, Arpeggiators and A.O.C. as two examples. Aside from organ features, specialist magazines and sheet music scored for electronic keyboard instruments are more common than hitherto.

## WERSI D.I.Y.

Klaus Wunderlich, who records for Telefunken, has many best selling records to his name. One of his more recent discs is "In the Miller Mood". played on the Wersi "Helios" organ and multi-tracked to obtain an orchestral effect (Selecta 6-23026AS). Though many of his past records were made using Hammond or Lowrey organs, he appears to be confining his attentions to Wersi at present and, although he would not claim to be an electronics engineer, I am certain that the Wersi design team have taken heed of his suggestions.

Since Wersi organs were mentioned in "Market Place" last October I have had a chance of hearing and examining a "Helios" W2T assembled from a kit. There is no doubt that this is an outstanding instrument, embodying the
latest state-of-the-art circuitry. The Wersi concern is some eight years old, comprising an enthusiastic team of engineers and musicians: it provides both assembly kits and the complete instrument.

Readers of practical magazines need no reminder that it is usually cheaper to construct than buy a ready-made product. Organ building is one of the more complex projects, especially where the shopping list is concerned, so that buying a kit really makes sense. In the case of the "Helios" W2T the saving is very considerable as the finished product costs over $£ 7,000$, whilst the kit retails for about $£ 2,500$. If even the kit price sounds expensive, 1 would point out that this is a very comprehensive instrument and that Klaus Wunderlich's opinion of it may be verified by hearing the record previously mentioned.

Keying is fully electronic, the Transposer allows brilliant key changes while the special effects include autoWah, Repeat, Contracussion and Second Voice. There are three outstanding features of the W2T, in my opinion. "Wersivoice" is an electronic doppler-effect superior to any I have heard, based on several "bucket brigade" devices: two speeds at three intensities provide a perfect rotating baffle effect. "Wersidatal" is a system for programming preset sounds, each of the 20 programs having random access: thus, favourite combinations may be memorised and altered if superseded. Lastly, the "Wersimatic" rhythm unit is one of the best in its field. Based on 24 patterns and 15 instruments, it can produce alternating bars, stereo effects, drum breaks and has touch and automatic control.

Despite the complexity of the "Helios", it can easily be disassembled into three parts-top, base and chrome legs. Perhaps the only musical criticism is that the pedal section consists only of 13 notes, though a larger clavier is available in the "Zenith" series, if required. Potential constructors can obtain further information on the kits from Aura Sounds, Copthorne Bank, Crawley, West Sussex.

Building from a kit or published design has the advantage that the circuitry will have been proven beforehand, though the shopping list will call for careful planning in the latter case. By carefully following instructions and taking care to understand what is being assembled at each stage, this option should prove both successful and educational in familiarising the organist with the routing and processing of signals. Once the instrument is playable, he will not be deterred from changing component values-especially in the formant stages-to suit his taste and musical requirements. In this respect, a commercial organ under guarantee tends to: preclude getting to work with an iron land sidecutters!

For most owners, the professionally built instrument will tend to be the natural choice but, because organs are happily obeying the cost of living graph, a great deal of thought is required before parting with one's money.

## CAVEAT

The buyer must beware of himself, so se ipsum caveat emptor would be more appropriate! Whether a new or used model is in prospect, a few guidelines may not be out of place in this column.

The amount of money available will narrow the field and, having decided on what the bank balance can stand, a specification commensurate with the musical requirements should be aimed for. In this respect, salesmen are not only helpful but extremely honest as they know that you will recommend the firm if satisfied-or perhaps buy a more expensive model at a later date.

Arrange for a demonstration of the models within the range that can be afforded and, having heard them, ask for detailed brochures of those that lived up to expectations. Then take these home and study them carefully, with time no object: after all, having been without an instrument for years, what does another week matter? By all means take advice from a musical friend but remember that your money is involved and that the final decision lies with you. Look at the accompaniment manual in particular: the solo manual may offer a number of pitches, but the lower manual is often thread-bare-perhaps one pitch only-and could begin to sound monotonous. Check the dimensions of the instrument against the available room space and confer with the "household management" as to the appearance of the cabinet as a piece of furniture.

Those who are classically inclined will look for fewer trimmings and more "straight" organ in the specification. A spinet organ, with short manuals and 13 stub pedals, will not suit anyone aspiring to Widor and Bach.


Assuming that a single tone frequency is fed into the input of the chorus generator whilst one delay line is at maximum and the other at minimum delay, then two separate sounds will be produced at the generator outputs. This would be unlikely to be noticeable, but if the length of the two lines is slowly changed towards the opposite extremes then the phase relationship of the two sounds will be changing which will then be detectable.

Dependent upon the input frequency the phase relationship between the two sounds may pass through a cancellation point ( $180^{\circ} \times$ odd number) or be additive ( $180^{\circ} \times$ even number), and with a number of input frequencies present a phasing effect, sweeping through the frequency range is obtained. Superimposing a faster modulation on the v.c.o. control voltage enhances the multiple image, causing relatively rapid changes in phase relationship which when combined with the slow sweep give a complex pattern of relative phase simulating more than two sources and resulting in a rich chorus sound. The sweep rates of the slow and fast modulators are approximately 0.5 Hz and 10 Hz respectively.

## BUCKET BRIGADE

The term "Bucket Brigade Delay Line", is derived from the anology of a number of people, each with a bucket, forming a chain along which it is desired to transmit water.


Fig. 3.1. Schematic of Chorus Generation system


Fig. 3.2. Circuit of TDA1022 analogue delay line

Assuming that the first person has a full bucket, and all others are empty, it is possible to pour the water from bucket one into bucket two, then bucket two into bucket three, and so on until eventually all the water from the first bucket, excluding spillage, is transferred to the last bucket.
This description infers a delay which is dependent on both the speed at which each person reacts in filling his neighbour's bucket, and the number of buckets in the chain. In the String Ensemble it is fundamental that this delay line is controllable at will, and since the number of buckets, or stages in the device, is constant it is necessary to instruct each person how quickly to react before pouring the contents of their bucket into the next bucket, thus controlling the overall delay. The reaction time is quoted since in the electronic version the speed of pouring is very high such that variation in the "stage delay" is controlled by introducing a pause before the instruction to pour. Electronically the pause is created by an instruction to pour constituting the leading edge of a square wave which is known as the "clock". An increase in clock frequency corresponds to shouting "pour" at greater frequency thus shortening the stage and overall delay.

Carrying the analogy further, two instructions are used, which equate to two clocks, where one can visualise one male and one female instructor each instructing persons of their own sex in a line where the sexes are alternated. This is only a matter of electronic convenience and in future generations of bucket brigade delay line i.c.s one can expect that the required conversion from a single clock will be carried out in the same package.

## ANALOGUE DELAY

The system described above can be digital or analogue, in the first case either full or empty buckets would always be concerned and in the second case the amount of water in the last bucket would directly relate to the amount contained in the first bucket as it commenced its journey.

In order to fully understand the electronic analogue delay line, sometimes called the a nalogue shift register, an alternative method of operation within our chain of bucket carriers and water pourers should be considered. Since we are not concerned with the actual transfer of water along the chain, but simply require to know how much water was in the first bucket when the chain commenced its sequence of operation, we can start with all buckets full apart from the first one which will be filled to the amount (analogue) of interest. On
the first instruction (leading edge of Clock 1) the first person (male) puts the required amount of water into his bucket, which is equivalent to the level of the input signal at that moment, and on the second instruction the second person (female) fills up the first bucket leaving her with the same quantity of water previously contained in the first bucket. On the next instruction the third person fills up the second bucket and this continues down the line until the last bucket contains the same quantity of water as was present in the first bucket at the commencement of the sequence.

This can of course be a continuous process such that whilst the third person is topping up the second bucket, the first person is correcting the quantity in his bucket to match the new analogue or signal level.

## BUCKET BRIGADE DEVICES

Many of the earliest instruments incorporating analogue delay line i.c.s used an ITT device, the TGA350, which contains 185 stages of delay in the package, but since that time Reticon, Matsuchita and Phillips (Signetics) have produced devices in various configurations ranging from a single 512 stage line in a package to $2 \times 512$ stage lines, tapped lines, and now rumours of considerably longer lines in a package. The potential application for A.D.L.s are numerous including echo, reverberation, double tracking, flanging and phasing, vibrato, chorus generation, speech delay matching in P.A., signal scrambling, time compression, pseudu-stereo, voice threshold switching and test equipment circuitry particularly associated with oscilloscope storage displays.

## THE CHORUS MODE

Circuits have been proposed in which chorus is achieved by mixing a direct signal with the output of one delay line and the output of a second line fed from the first, both lines using the same changing clock frequency, but for the greatest effect the outputs from two or more lines should be mixed using clock frequencies modulated in an out of phase rela-tionship-e.g. $180^{\circ}$ for two lines, $120^{\circ}$ for three lines. In practice this poses a problem for dual packaged lines in that on-chip intermodulation occurs in the form of both audio frequency tones and high noise. It is therefore necessary to use a separate package for each line, although noise advantages can be gained by using parallel dual lines in each position providing only one clock frequency is fed to the package.

## FREQUENCY CONSIDERATIONS

The bucket brigade principle described earlier relies on sampling the input waveform at discrete moments in time, and since a bucket cannot be involved in both filling and emptying operations at the same time, Bucket 1 must wait for the transaction between Buckets 2 and 3 to be completed before it can again be involved with Bucket 2, and half the information from the input is automatically lost. This imposes a relationship between the bandwidth (DC to maximum input frequency) and the clock frequency, such that the input bandwidth should be limited to less than one-half of the clock frequency, and normally to less than one-third. The resulting sampled waveform at the output of the delay line requires heavy filtering to recover the original waveform and remove the clock frequency content.

## TDA1022

The internal circuitry of the Signetics A.D.L. is shown in Fig. 3.2, using mos technology f.e.t.s to switch the charge in the required manner between capacitors at each stage. The supply required is a nominal -15 volts, and at the clock frequencies used in the Ensemble ( $50-100 \mathrm{kHz}$ ), the average delay for the 512 stages totals approximately 3.5 ms , and for a distortion level of less than $\frac{1}{2}$ percent the input level can slightly exceed 2 V r.m.s., with a band width of $12-15 \mathrm{kHz}$, and attenuation through a line will be typically 4 dB .

Fig. 3.3(a)-(h) indicate the operation of the delay line in conjunction with Fig. 3.2. Clocks 1 and 2 are in anti-phase, odd number stages linked to Clock 2 and even number stages, together with the input gate, connected to Clock 1.

Taking a waveform as shown in Fig. 3.3(b), the voltage present whilst Clock 1 is up is transferred direct to $C_{0}$ in Fig. 3.2. When Clock 2 rises the charge in $\mathrm{C}_{0}$ is topped up reducing the charge in C to that which was previously present on $\mathrm{C}_{0}$. Thus in Fig. 3.3(c) the voltage on. $\mathrm{C}_{0}$ rises to V and in Fig. 3.3(d) the voltage at the output of Stage 1 falls to the value at the input immediately prior to the rise of Clock 2. This situation now prevails until Clock 1 rises again at which time C is topped up reducing the charge on $\mathrm{C}_{2}$ to that which was previously on $\mathrm{C}_{1}$.

With the rise of Clock 1 again $C_{0}$ continues to monitor the input voltage such that when Clock 2 rises again the new voltage level (second sample) at the input, immediately prior to the rise of Clock 2, is transferred to the output of Stage 1, whilst the voltage at the output of Stage 2, which is equal to the first sample, is transferred to the output of Stage 3.

Thus it can be seen that the time taken for each sample to move from one stage to the next is half a clock period, and input samples are taken once per clock cycle with the input blocked for one half of each clock cycle.

When stage 512 is reached, a further stage (513) is used to fill in the half of the clock cycle during which a sample has not been passed through the delay line giving the stepped waveform shown in Fig. 3.3(h) which is then filtered to reconstitute the input waveform.

## CHORUS GENERATOR CIRCUITRY

The complete circuit of the Chorus Generator is given in Fig. 3.4. The bandwidth of the incoming signal is first limited by the low pass filter associated with IC19, and parallel connections taken to Channels A and B incorporating. TDA 1022 delay lines IC25 and IC26 respectively. The delay lines in each channel are followed by two low pass active filters based on IC20 and IC21 in Channel A and IC22 and IC23 in Channel B.
Clock frequencies are generated in IC28 and IC30 for Channel A and IC29 and IC31 for Channel B using the
conventional v.c.o. configuration based on the CMOS 4007. In Channel $A$ the variable resistance with voltage of the $n$-channel f.e.t. (pins 3,4 and 5 ) is used to control the frequency of the oscillator comprising two gates of IC28 by virtue of its effect on the value of R59 which in combination with C45 determines its frequency of operation.

Two gates within IC30 are used to shape the waveform and produce an inverted version for the second phase of the clock.

In Channel B the $p$-channel f.e.t. (pins 1, 2 and 3 ) is used to control the oscillator comprising two gates in IC29, such that for the same modulation waveforms as pin 3 of IC30 and IC31, the oscillators work in anti-phase with respect to frequency variation.

Some gate wastage occurs in IC28 and IC29 due to the necessity to provide good decoupling of clock frequencies between the two channels, without which clock intermodulation would occur leading to a high noise level and swept audio frequencies at the output of the delay lines.

The modulation signals at pin 3 of the 4007's are generated by IC27 and amplified by IC24. IC27 is connected as two oscillators, similar to the clock oscillation but without voltage control, one operating at approximately 0.5 Hz and the other at approximately 10 Hz .

Filters, consisting of R47, C36, R48 and C37 for the slow modulator and R52, C40, R53 and C41, provide smooth modulation waveforms the level of which is controlled by VR9 and VR10 for slow and fast modulations respectively.

signal is delayed ay half a clock period for each stage.


Fig. 3.3. Waveshapes showing operation of delay line


Fig. 3.4. Circuit of Chorus Generator


## CHORUS GENERATOR



## Capacitors

| C14 | $0.22 \mu \mathrm{~F}$ polyester |
| :---: | :---: |
| C15 | $4.7 \mu \mathrm{~F}-\mathrm{V}$ electrolytic |
| C16 | 100 pF polystyrene |
| C17 | 470 pF ceramic |
| C18 | $0.1 \mu \mathrm{~F}$ polyester |
| C19 | 220 pF polystyrene |
| C20 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C21 | 47pF polystyrene |
| C22 | $2 \cdot 2 \mathrm{nF}$ ceramic |
| C23 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C24 | $2 \cdot 2 \mathrm{nF}$ ceramic |
| C25 | $0.22 \mu \mathrm{~F}$ polyester |
| C26 | 470 pF ceramic |
| C27 | $0.1 \mu \mathrm{~F}$ polyester |
| C28 | 220 pF polystyrene |
| C29 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C30 | 47pF polystyrene |
| C31 | 2.2 nF ceramic |
| C32 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| С33 | 2.2 nF ceramic |
| C34 | $0.22 \mu \mathrm{~F}$ polyester |
| C35 | $2 \mu \mathrm{~F}$ non polarised |
| C36-38 | $47 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C39 | $0.1 \mu \mathrm{~F}$ polyester |
| C40-43 | $4.7 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C44 | 10 nF ceramic |
| C45-46 | 470 pF ceramic |
| C47-49 | $100 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C50 | 10 nF ceramic |
| C51 | $100 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C52 | 10 nF ceramic |
| C53 | $100 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic |
| C54-55 | 10 nF ceramic |

Potentiometers
VR8-11 $47 \mathrm{k} \Omega$ presets 100 mW subminiature
Diodes
D31 12 volt 300 mW Zener
Integrated Circuits

| IC19-24 | 741 |
| :--- | :--- |
| IC25-26 | TDA1022 |
| IC27-29 | 4011 |
| 1C30-31 | 4007 |

IC30-31 4007

## Miscellaneous

1 Printed circuit board; 2-16 lead d.i.l. sockets; 5-14 lead d.i.l. sockets; 5 terminal pins


## Photo of Chorus Generation board

## SETTING UP THE CHORUS GENERATOR

VR8 provides a d.c. control to the input filter which sets the input bias on both delay lines. This preset potentiometer should be adjusted such that with a signal present at the input, the combined $\mathrm{A}+\mathrm{B}$ outputs will move from zero, through a distorted period, through a clear range, a further distorted period and back to zero. VR8 should be finally set for the centre of the clear transmission range to give maximum signal handling capacity for the delay lines.

With VR9 and VR10 at minimum, VR11 adjusts the centre frequency of the two v.c.o.s to approximately the same value. This is achieved by initially setting VR11 near its midpoint and VR9 slowly increased. The combined A and B output signals should be subject to a phasing effect with a smooth sweep and sweep turn-around characteristic. If the sweep appears to pause at one end, VR11 should be adjusted to recover the even sweep. VR9 should then be reduced and VR10 increased to mix in the fast modulator, the levels of both being adjusted to taste.

All the adjustments associated with the clock modulation are slow to take effect due to the long time constants associated with the slow modulator filters. This time constant also produces a turn on delay of a few seconds, before the chorus modulation commences, after switching on the instrument. Rapid adjustment will stop the chorus modulation which will then recover after a few seconds.

## CHORUS GENERATOR CONSTRUCTION

All the chorus generation circuits are mounted on a single printed circuit board, the etching and drilling details of which are given in Fig. 3.5 with the component assembly details in Fig. 3.6.

To assemble the board the previously recommended order of terminals, pins, resistors, Zener (D31), i.c. sockets, preset potentiometers, small capacitors, and finally large capacitors may be used. Sockets are recommended for the 14 and 16 lead i.c.s which are all of mos type and therefore sensitive to handling, but these are not necessary for the 741 type i.c.s.

Careful attention should be paid to correct orientation of the i.c.s.
Note-the track cutting amendment given finally last month refers to IC3
NEXT MONTH: Voice/preamp board construction


## SOUND GONTROL

BP 1479516
Patents continue to give an interesting insight into the areas under research by Sony in Tokyo. In BP 1476 516, Sony describe how a.g.c. circuits in a stereo amplifier must act on both channels if image swing from left to right, through one channel level dipping in volume while the other remains untouched, is to be avoided.

However, there is a difficulty with this approach, because a high level transient in one channel can dip the level of both channels and in so doing push quiet sounds down to inaudible levels. Sony have patented a circuit which is claimed to overcome this problem. The block diagram, Fig. 1,


Fig. 1
shows left channel amplifiers feeding detector and filter circuits to produce gain control signals. Diode D1 connects the output of the detector circuit 2 to the base of gain control transistor TR1. A right channel is arranged in corresponding fashion, with the output of detector circuit 4 connected to gain control transistor TR2 via D4. There is also, however, a crossfeed of the outputs of detector circuits 1 and 3 between channels, via further blend diodes, D2 and D3.

In action, control signals of high amplitude applied to the base of the gain control transistors reduce the collector-emitter impedance and thus
reduce the gain of the signals passing between the amplifiers. The attack time of both detector circuits 2 and 4 is made small (around 0.1 second) and their recovery time is made large (between 20 and 30 seconds). Thus the circuits are capable of passing relatively short transient signals which control the gain of their own respective left and right channels.

The attack time of the cross-channel circuits 1 and 3 is however approximately 5 second, i.e, substantially longer than the attack time of circuits 2 and 4. The recovery time of the cross-channel circuits is also much shorter, for instance, around 5 seconds.

When a transient of high amplitude is applied to one channel only, the a.g.c. function of that channel begins to operate in less than 0.1 second; but the gain control of the other channel transistor will not be affected. During normal operation, the a.g.c. functions of both channels are controlled by the average voltage. In this way there is overall a.g.c. and a transient peak in one channel should cause neither an image swing nor inordinate depression of the other channel's level.

## HIHH VOLTAEE SWITCH BP 1486804.

Siemens AG, in BP 1486 804, suggests a clever way of controlling high voltage remote switching, for instarice in the order of 1 MV , without corona discharge.

A series of thyristors, each suited to control a voltage of 1 kV , are arranged in a circle. A second series of similar thyristors are arranged in a second, smaller circle, spaced from the first, so that the two circles lie as if on the exterior of an imaginary cone (see Fig. 1). To enable remote operation, the thyristors are associated with light or other electromagnetic radiation-sensitive circuitry. A radiation source, " $s$ " is arranged at the focal point of a parabolic reflector " $r$ "', with a firing control signal " $z$ " supplied to a radiation generator " $e$ ". The emitted signal " $h$ " is beamed towards the two rings of thyristors, which by virtue of their arrangement (as if on an imaginary cone) all
receive similar amounts of radiation, with no one thyristor and its sensor shielding another.

The individual thyristors are connected in series with each thyristor switching the next-higher potential, so that in the first circle t 1 switches 1 kV , t2 switches 2 kV relative to 1 kV , and so on, up to t 8 , which switches 8 kV relative to 7 kV . A similar arrangement is obtained in the lower circle of thyristors, so that there is between two adjacent group planes no greater potential than 8 kV , and between the neighbours of a group plane no more than 1 kV . In this way corona discharge and flashover are avoided, with the single safe firing signal " $z$ " remotely switching a voltage of level governed only by the number of thyristors placed on the notional cone.


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Timer Facility: Display Seconds, Stopwatch use, Sleep Delay: 50p extra DISPLAYS: $\frac{1}{2}^{\prime \prime}$ Red LED FND500 £1.20 each. 6 for $\mathbf{~} 6.48$. NSB5430 $\frac{1}{2}^{\prime \prime}$ red LED. Stick of $\mathbf{4} \mathbf{£ 4 . 3 2 .} 5$ LTO2 stick of 4 Green £5.40.
CLOCK CHIPS: MK50253 Alarm 12/24hr 4/6 digit £5.40. MM5385 £4.32. MK50362N calendar £7.56. 6 digit counter $50395 / 6 / 7 £ 9.18$
MICRO PROCESSOR: $Z 80$ CPU £21.60. CTC \& PIO £15.12. MK4096N 300nS 4 KXI DUn RAM £6.75. 1702A UV PROM 450 nS £10.80 BATTERY RECHARGER: Mains Adaptor with 4-way Plug. 4XAA (1.2v) Ni Cads plus holder $£ 8.64$. AA Ni Cads separately $£ 1.20$

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With eight decade ranges it is possible to read capacitance from 300 pF to $3,000 \mu \mathrm{~F}$ on a linear scale, due to integrator circuit operating techniques. An absolute accuracy +2.5 per cent can be attained if suitable components are used, and for matching, comparative measurements can be made to an accuracy of as little as +0.25 percent.

## KIN CONTROLLER

Pottery is now a popular hobby and many amateur potters, schools, etc. possess small kilns. Most kilns are only supplied with an indicating pyrometer and have no temperature control; this can be provided by our project, described next month.


## PRACTICAL



## marige PLALE

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

## TUNE SEQUENCER

To the numerous electronics kits for the home constructor available from Phonosonics, has been added the 128 Note Sequencer of P.E. November and December 77.

This programmable sequencer was designed with the $P . E$. Minisonic in mind, for which Phonosonics can provide the various kits, and Kimber-Allen Keyboard.

A kit comprising components for the Sequencer Main Board ( K it 76-1) is now available. Control switches and p.c.b.s are not included. Kit 76-2 provides the cemponents for three "Optional Trigger Inverters" and one "Alternative Output Circuit"

The same company are also able to supply kits for the $P . E$. String Ensemble.

Details of the above component kits, p.c.b.s, and prices can be obtained by sending a s.a.e. to Phonosonics, 22 High Street, Sidcup, Kent, DA14 6EH.

## TOMORROW'S TECHNICAL LANGUAGE

The International Electrotechnical Commission, the organisation responsible for the preparation of world-wide standards in the electrical and electronic fields announces the publication of the 1978 edition of their International Electrotechnical Vocabulary (IEV).

The purpose of the IEV "dictionary" is for there to be one common technical language for the scientists and engineers of tomorrow.

Containing originally some 2,000 terms, today the IEV, in line with the unparalleled growth in electrotechnology, accounts for more than seventy thousand internationally agreed terms.

The International Electrotechnical Commission comprises member countries representing 80 per cent of the world's population consuming 95 per cent of electrical energy. At present there are some twenty thousand pages of IEC standards in each of the official languages of the Commission, the largest set of international standards existing in the world.

Details from IEC, 1 Rue de Varembé, 1211 Geneva 20, Switzerland.


## BEAUTIFUL AND CLEVER

A slim and elegant accessory (present) for the modern woman. That's the Casio MQ-1 multi-function micro-computer.
lis time display is constantly visible even in bright light and quartz accuracy is plus or minus fifteen seconds per month.

The calendar function is capable of helping the busy female to be in the right place on the right day up until 31st December 2099. If she wishes to compute whether a friend was born "full of grace", the same function can do calculations back to 1 st January 1901.
A genuine stopwatch allows a full 24 hours of continuous timing of one
tenth of a second accuracy
The timer function can count down enabling, say, remaining parking time to be seen at a glance. Also, by setting the timer to count up from a set origin a continuous read out of an overseas time zone can be run without altering home time.

Finally, the four basic mathematical functions are possible on the petite calculator panel which is beautifully designed for a slim feminine finger.
£34.95, including leatherette wallet available from Tempus, 19-21 Fitzroy Street, Cambridge, CB1 1EH.


An American test clip which doubles as an insertion and removal tool for 14 and 16 d.i.1. packages is available, priced $£ 9.04$ ( 14 pin ) and $£ 9.38$ ( 16 pin ), from BCE Ltd., Briticent House, New Street, Ringwood, Hants.

## IIY ALARM SYSTEM

Motor vehicle alarms fitted by garages tend to be expensive. These systems usually consist of switches fitted to the doors, bonnet and boot or operate with a trembler switch.

An install it yourself (IIY) alarm by Photain Controls operates on an entirely new current sensing principle. This unit monitors the outflow of current from the battery of a vehicle. Current flow can be caused by anyone attempting to start the vehicle or when a lamp is illuminated by the opening of a door, the bonnet or the boot. The alarm can operate the existing vehicle horn or a separate siren from Photain.

When activated the horn will sound for a period of sixty seconds and the unit will then automatically reset for the next operation. To enable the owner to leave and enter the vehicle quietly two time delays are incorporated.

The easily installed unit operates from the 12 volts DC vehicle battery (negative earth) and has a current consumption of only 1 mA in the set condition. The complete unit measures $55 \mathrm{~mm} \times 100 \mathrm{~mm}$ $\times 64 \mathrm{~mm}$, weighs 227 grams and will operate over a temperature range of minus 20 degrees to plus 60 degrees centigrade.

Price complete with on/off switch, mounting brackets, fixing screws and connecting cable is $£ 20$ plus $12 \frac{1}{2}$ per cent VAT. Siren costs $£ 12$ plus VAT. Photain Controls Ltd., Unit 18, Hangar No. 3, The Aerodrome, Ford, Arundel, West Sussex.


## A NEW PET FOR YOUR HOME

The Commodore PET is now available in the U.K. although you will have to wait at least 30 days to get one-provided of course you have $£ 695$. The PET is a "low priced" all-in-one microcomputer designed with the user in mind. The package incorporates a 9 inch CRT which will accommodate 25 lines of 40 characters and a standard cassette system for program storage and entry. A keyboard containing 73 alpha numeric keys plus 64 additional graphic characters for plots, games or "artwork", lower case letters is also available.

PET operates in BASIC and comes with

8 k of RAM and 14 k of rom which includes 4 k operating system and 1 k diagnostic routine.

Commodore will be marketing PET from office/showroom premises in London and they will also be setting up a PET Owners Club. They hope to offer such items as extra RAM ( 24 k we believe), floppy disks, printers etc., at a later stage.

We also hear from Commodore that they have been able to reduce the price of KIM1 to $£ 149.00$ plus VAT.

More details from Commodore Systems, 446 Bath Road, Slough, Berkshire.


## BACKGAMMON COMPUTER

The latest microprocessor-based game now available from Gemini Electronics is backgammon. The unit which is called the Gammon Master II is based on the Motorola 6800 microprocessor and is designed to change its strategy depending on the type of game you choose: running block and hit, back games, it is capable of playing them all.

It is claimed that the unit will on average defeat an intermediate player and
compete evenly with experts. Each game is "charted" with regular pieces and the location of every man on the board can be verified by the touch of a button. The dice is also electronically "rolled" ensuring each game is different

The total cost of the game including VAT and post and packing is $£ 175.00$. It is available by mail order only from Gemini Electronics, 3 Branksome Avenue, Prestwich, Manchester.

## MAINLINER

Co-axial cable de-braiding tools handting cables from 3.3 mm to 13.5 mm and having the memorable name of "Mainliner" are available at $£ 5.58$ plus VAT from Eraser International Ltd, $2 / 3$ Hampton Court Parade, East Molesey, Surrey, KT8 9HB.



Fig. 1. Wiring diagram of Random Decision Unit.
The outputs of IC3 feed the l.e.d. segments of the display.
To be safe, $100 \Omega$ resistors should be inserted in series with the four cathode leads to the display, however, in the prototype these were not used as all the i.c.s tried appeared to limit their own output current without the inclusion of these resistors. They have not been shown in the diagram nor in the layout drawing but can be included if it is desired to play safe.

IC1c and D1 serve to gate the oscillator (IC1a and b). Attempts were made to use one of the spare inputs of these gates, but in two of the i.c.s tried, this made start up of the oscillator unreliable so the present method was adopted.

When the junction of IC1c and D2 is taken high, C3 charges through D2 and hence the display is activated, also IC1c goes


Fig. 2. Circuit diagram.


Fig. 3. Printed circuit board design
low and the oscillator starts up. As the above mentioned junction goes low, the oscillator stops immediately, however the display remains active until C3 has discharged through R4, hence sufficient time is given (5-6 seconds) to read the resulting display. ICld serves as a buffer with its input normally kept high via R3.

Touching the plate takes ICld input low (due to skin conductivity) and the above explained process occurs.

In the quiescent state, the i.c.s take virtually no current, and the only real flow is that through D1 and R1, this amounting to around $\frac{1}{2} \mathrm{~mA}$. Hence an on-off (mechanical) switch is not really justified.

## CONSTRUCTION

The touch plate is shown full size and the copper lines should not be made any closer than that shown, otherwise dampness caused by breathing near the plate could lead to shortened battery life. If trouble is experienced, the value of R3 can be reduced slightly without significantly affecting the sensitivity of the touch plate. See Fig. 1.

Construction methods will vary depending on the type of case available, however in the prototype construction of both the p.c. board and the touch plate was on copper coated p.c. board. An excellent system to use for etch resisting is the Alfac range of electronic symbols (lines, bends, etc), however these are expensive, but if a professional looking finish is desired, this system is excellent. The i.c.s should be mounted on sockets if space permits and they should be mounted last, and must not be handled until you are ready to use them as they are liable to damage by static, and the more often handled, the greater the chance of damage. The battery should not be fitted until all parts and all connections have been checked.


COMPONENTS . . .

## Resistors

R1-R2 $15 \mathrm{k} \Omega \frac{1}{4} \mathrm{~W} 10 \%$ (2 off)
R3-R4 $10 \mathrm{M} \Omega \frac{1}{4} \mathrm{~W} 10 \%$ ( 2 off)
Capacitors
C1-C2 $\quad 0.01 \mu \mathrm{~F}$ polyester $\mathrm{C} 3 \quad 0.47 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum
Integrated Circuits
IC1/IC3 4011 (2 off)
IC2 4027
XI FND 507 (common anode)
Diodes
D1-D2 1N914 (2 off)
Miscellaneous
Printed circuit board, sockets for i.c.'s (if req.), PP3 battery, suitable battery press studs.

## OPERATION

In use, the plate is touched and all 4 segments will light up, appearing to be all on at once due to the high oscillator speed used. On release of the plate, 2 of the 4 segments will remain alight. Which two is totally random and there should be no bias toward any particular combination (of the four possible alternatives). If segments bce and $f$ are used as in the prototype, it could be said that a straight line represents "EVENS" and a staggered line represents "ODDS".

Keep the unit in a reasonably dry atmosphere and the battery should last for many months. A


## THE SAGA OF SALYUT, SOYUZ AND PROGRESS

The year 1978 will mark a special place in the history of Soviet space progress. On Valentine's day Yuri Rmanenko and Georgi Grechko began using equipment brought up to them by the cargo spacecraft Progress 1 .

They began erecting an electric heating chamber in a special lock compartment. Photographic and other monitoring equipment was set up also. Part of the programme included the photography of Earth and space phenomena. Optical instruments for navigation were tested in the new mode where operation can be automatic or manual depending whether the station is with or without crew. This facility can be operated whether the vehicle is on the dark side or the light side of the Earth.

The furnace was brought up by Progress 1 and assembled by the two cosmonauts. This furnace, whose temperature is in excess of $1,000^{\circ} \mathrm{C}$ is controlled by a computer. An accuracy of plus or minus five degrees can be maintained. The unit is installed in the lock compartment which is arranged so that the rear end of the furnace faces into space.

The first experiment in smelting was to study the diffusion process of molten metals in weightless conditions. A capsule of aluminium-magnesium, copperindium and indium-antimonide was introduced into the heating chamber. The airlock was depressurised and the heating switched on. At the same time the control of the spacecraft for orientation was switched off to allow the vehicle to drift. The process of crystallization was completed and the control engines took over again. The materials obtained during the experiment will be brought back to Earth for study.
A second experiment was carried out;
research materials were aluminium-tungsten, molybdenum-gallium and semiconductor materials. These results will also be examined when they are returned to Earth. Information about the interaction between liquid and solid metals will show what reaction occurred under weightless conditions. The aim of these experiments is to gain knowledge of welding and soldering and also the possibility of the creation of new composite materials.

Optical observations have also been carried out particularly with regard to silver clouds, which may provide information about the state of the atmosphere. These clouds tend to appear at about 80 kilometres above the poles. Many drawings have been made as well as photographs. It is clear that these clouds are in distinct layers. Three of these layers have been observed between $-130^{\circ}$ and $-150^{\circ} \mathrm{C}$. During this time the Aurora was very prominent and reached a height of about 500 kilometres.

In the mission there was a programme for the study of biological effects involving drosophila, micro-organisms and tissue culture. Two day larvae of drosophila were taken aboard the spacecraft in a nutrient medium, contained in a special thermostatically controlled container, Bio-therm-4. The temperature was held constant at $24^{\circ} \mathrm{C}$. The first flies appeared in December 1977 and the reproduction cycle began again. This time it was in the condition of weightlessness. The object of this experiment is to determine effect of weightlessness on the insects' hereditary systems.
An important phenomenon was confirmed regarding the productivity level of the cosmonauts. There was an increase in their output of 10 per cent. This was noticed with the activity of the previous long term team, Sevastyanov and Klimuk, which lasted 63 days. It would appear that prolonged weightlessness is like getting second wind.
Another point which is being closely watched is the calcium loss in the bone structure. It would seem that as the skeleton is no longer needed to support the body the amount of calcium reduces. This was of course also noticed with the American teams.

## RADIO TELESCOPE

The largest telescope of its kind has been installed in the Salyut-Soyuz space station and is to operate in the 1.5 millimetres band. The detector, which consists of crystals, is cooled in a closed circuit using liquid helium at a temperature of $-269^{\circ} \mathrm{C}$ : The liquid helium is made on board and the final cooling is by an expanding throttle valve.
The telescope is being used to detect radiation in the infrared part of the spectrum. This particular wavelength is not observable from Earth because of the cut-off caused by the atmosphere. The instrument will be turned towards Earth to examine the upper layers which are important in weather forecasting. The control system uses Jupiter and

Sirius as test locations.
Also included in this programme are observations of the centre of the galaxy, the Orion nebulosity and interstellar clouds.

## THE PROGRESS 1 DESIGN

The development of Progress 1 was based on economy and reliability. Both these parameters were best served by making use of existing tried materials and units. For example the carrier was similar to that of an ordinary manned Soyuz. Only the emergency rescue system was removed. Since there were no crewmen this was redundant.
The main re-design was on the spacecraft itself. In order that the maximum payload might be carried it was decided that the Progress 1 should be nonrecoverable. This decision enabled the heavy heat protection shield to be discarded.

Progress 1 weighed 7,020 kilogrammes and could carry 2,300 kilogrammes of cargo. This amounted to 30 per cent of the lift-off weight, a very high proportion for a spacecraft. Progress was 2.2 metres in diameter and 8 metres long. Without solar panels the flight time was for eight days, in independent operation. However its design was such that it could have remained a month in space if linked to the space station. The vehicle was indeed a tanker for it carried upwards of half a ton of fuel, plus 1,300 kilogrammes of dry cargo.
In order that the dynamic characteristics should not be affected a new power scheme was developed.

A number of frames supporting the hull were removed to make economical use of the space. These were replaced by a structural framework supporting shelves to which containers were attached by quick release locks.

New systems in the craft included a pumping installation to transfer the fuel to the space station. Control systems were extended to ensure reliability in the absence of manual control.
It took the cosmonauts twelve trips to unload Progress 1 on its arrival and docking with the space station. Before the re-fuelling operation, the cosmonauts had replenished the air supplies from that brought up by the cargo craft. Stock air had been depleted by the disposal of waste and the space walk.
While still locked to the space station another series of 'resonance' experiments were carried out. No doubt the cosmonauts enjoyed using the cargo ship as a trampoline.

The Progress engines were used to make a correction to the orbit of the space station. After that the cosmonauts set about the separation of the two craft.

Before the Progress 1 was allowed to enter the atmosphere it performed one more task. One orbit after separation, when it was 12 to 15 kilometres away, the back-up automatic and approach systems were tested. This had never been tried before.

# Semiconductor UPDATITEm <br> FEATURING: 8086, 28000, MC6809, TLI70C, 355 

## THE 16 BIT CHALLENGE

You may remember that I was less than enthusiastic about the Texas Instruments announcement that the ... "end of the two-bit eight bit" was imminent. There is still no doubt in my mind that the eight bit micros, such as the 6800 , the 8080 and the Z80, will be with us for many years yet, as indeed will the four bit chips such as the 4040 and the TMS 1000.

Certainly the current sixteen bit contenders, represented by the Texas TMS 9900, the General Instrument CP1600, and the Ferranti F100L, are hardly taking the micro world by storm, and have so far proved to be of little interest to computer hobbyists who still have plenty of elbow room left in their existing eight bit systems.

But time marches on, and the sixteen bit challenge is soon to be reinforced by the three giants in microprocessor technology, who have at last decided that the market, and their own technology, are now ready. First of the giants on the scene will be Intel with its new 8086, a powerful 16 bit design which is software compatible with its 8080 and 8085 predecessors and yet offers on-chip multiply and divide, a one megabyte address range and internal clock rates of up to 8 Mhz . Close on the heels of Intel are Zilog with their new $\mathbf{Z 8 0 0 0}$ chip which, like the 8086 , offers software compatibility with its eight bit predecessors. Rumour has it that one version of the $\mathbf{Z 8 0 0 0}$ will be able to address eight megabytes of memory!

## FROM MOTOROLA

Last but not least come Motorola with their MC6809 device which has the interesting distinction of being at an "in-between" stage in microprocessor development because, while it uses 16 bit internal architecture, externally it interfaces with the eight bit data bus common to all current 6800 systems. This "best of both worlds" design, while not as powerful as the Intel or Zilog chips, has the advantage that existing hardware and memory investment can be carried through into the more capable sixteen bit arena.

If you relish the thought of a powerful home computer based on a sixteen bit chip, then this news from the three giants will please you, because confident predictions of price erosion are already being
made. Before throwing your old micro away though, try calculating the cost of eight megabytes of RAM!

## HALL SWITCH

Becoming increasingly popular in the professional electronics market these days, and looking extremely attractive for amateur applications are the new generation of magnetic sensors, the Hall effect switches.

These useful devices have been mentioned once before in this column, but at that time they were difficult to obtain. Now Texas Instruments have introduced the TL170C which is likely to become freely available at a very low price, and so there are no longer any excuses for not sampling the delights of this robust and useful switching device!

Hall effect switches like the TL170C are basically silicon integrated circuits which include a Hall sensor able to sense the presence or absence of steady, state magnetic fields, and a transistor switching stage which provides a logic type output. The TL170C itself is packaged in a tiny plastic three lead transistor package and operates from-standard five volt supplies. The output switching stage is a basecollector transistor with a 30 volt rating so that the output voltage swing can be tailored with the aid of a pull-up resistor and a suitable supply rail to suit most applications. Output sink current is a respectable 20 mA .

## EASILY INFLUENCED

Magnetic sensitivity is rated in milliTeslas, with a positive threshold of about 35 mT and a negative threshold of about minus 35 mT . If, like me, you do not have much of a feel for milli-Teslas, suffice to say that the TL170C can be reliably operated with quite a small magnet! To prevent erratic switching or threshold oscillations the new chip has a built in hysteresis of 20 mT .

Applications for these devices must be legion, and are surely not limited to the main commercial use which is as contactless keyboard switches for high quality teletypes and VDUs. All kinds of clever, perhaps concealed, magnetic switches are possible, and how about a solid state
magnetic replacement for the mechanical or reed switch car contact breakers used with electronic ignition systems? And don't forget model train layouts, and slot cars, and ... well I am sure you can see that it really is amazing how you have been able to manage without these devices for so long!

## SPIKELESS TIMER

No doubt everyone will have used that great little 555 timer integrated circuit by now, probably with great success. You may have used it as a monostable, an astable, a voltage controlled oscillator, a long period timer or any number of the other jobs at which it excels, but if you used it on a board with TTL flip-flops, you may have come unstuck.

The trouble is that the 555 generates a large current spike in the supply lines when it switches to the high output state, a spike which can be as large as 300 mA and last for 100 nanoseconds or more. This sort of spike can cause glitches in the Vcc line which will have unfortunate effects on TTL or other flip-flops, particularly if the decoupling arrangements are not of the best.

The only way out of this problem until now has been to hang a few hundred microfarads of capacitance across the 555 supply pins so that it could guzzle current from its own personal supply during a spike, with out communicating its bad habits to the other occupants of the board!

But now a rather more elegant solution has appeared from Teledyne Semiconductor in the shape of their 355 timer.

Yes, that's right folks! It's a pin for pin replacement for the 555 chip without the current drinking problem! Actually, the 355 does take a tiny sip when it switches but the current spike generated is only about 1 mA .

The 355 not only tackles the 555's drinking problem, but also solves another couple of problems on the side. The 555 has a tendency not to reset reliably on command, and can get too hot on 15 volt supplies. The goody-goody 355 never puts a foot wrong. Personally my sympathies are with my red-faced, overheated, intemperate, 555's who sometimes forget to reset. I think it's an identity problem!

## 27bIESTM P.A.BIRNIE

This unit gives a usual indication of the logic states of any 14 or 16 pin i.c. in the 74 family

T- His tester was designed for use on all 14 and 16 pin integrated circuits in the 74 TTL series. It is powered from the circuit under test and uses TTL hex inverters with light emitting diodes to indicate the logic states of the i.c. under test.

## POWER SQURCE

As the tester is powered from the circuit under test and because in the 74 series the pins used for the power supply connections vary from device to device, it was necessary to ensure that the tester received a voltage of the correct polarity irrespective of the i:c. being tested.

This was achieved by listing all the pins used to supply power in the 74 series and then designing a diode circuit capable of providing the correct voltage.

## CIRCUIT OPERATION

The complete circuit diagram of the tester is shown in Fig. 1 with the four hex inverters enclosed within the dotted lines.

Under typical operating conditions with output voltages over 0.6 V the 74 L 04 hex inverter acts as a 13 mA constant current generator. This current is sufficient to illuminate an l.e.d. and so indicate a "logic 1 " state.

When the testcr is applied to an i.c. its positive and negative supply rails are obtained by two of the diodes D18 to D23 being forward biased from the supply pins of the i.c. under test. If for example a 16 pin i.c. with its +5 V and earth connected to pins 5 and 12 respectively is to be tested, then diodes D18 and D23 will be forward biased establishing the supply rails.

If any inverter senses a logic 1 input its output is switched low and the l.e.d. connected to it is illuminated. When a logic 0 is sensed by an inverter its output is switched high and the appropriate l.e.d. turned off.


Fig. 1. Circuit diagram of the Test Clip. Pins 14 and 17 are used as the positive and negative connections of the 7404


Fig. 2. Printed circuit board pattern

## CONSTRUCTION

The two printed circuit boards (Fig. 2) used in the tester are identical and their respective component layouts are shown in Figs. 3 and 4. The components should be soldered as close as possible to the p.c.b.s to allow easy mounting of the boards into the case.
The unused pins of each i.c. should be cut off before the i.c. is soldered into the p.c.b.s.

Each l.e.d. should have its cathode lead cut to about $\ddagger$ in and its anode lead to about $\frac{1}{8} \mathrm{in}$. A piece of sleeving should be fitted over the cathode lead to prevent it shorting out to the positive supply line.

When soldering the l.e.d.s in place care should be taken to keep the spacing between them even to ensure neat indicator rows in the finished tester.

## TEST CLIP

The test clip was made using $0 \cdot 1$ in edge connector which was modified as shown in Fig. 4. One side of the connector was carefully' cut off, leaving a strip of 8 contacts with con-


Fig. 5. Modifications and mounting details for the edge connectors

## COMPONENTS <br> - -

Diodes
D1-D16 TIL 209 (16 off)
D17-D23 OA47 (7 off)
Integrated Circuits
IC1-IC4 74L04 (4 off)

## Miscellaneous

Printed circuit board, 4 in length of 0.1 in pitch edge connector for probe, clear and opaque Perspex, Araldite, Tensol No. 6 cement


Fig. 3. Component layout for Board A


Fig. 4. Component layout for Board B


SIDES AND ENOS - 1 mm OPACUE PERSPEX

Fig. 6. Case cutting details
venient "spills" which were used to solder the connectors to the p.c.b.s. Araldite was then used to securely hold the connectors in place.

## TESTING

The tester should be checked before it is fitted into the case to ensure that it is working correctly. $A+5$ volt supply should be connected to the positive and negative supply lines of the boards and all the l.e.d.s should light up. If a lead is taken from the negative supply to each inverter input in turn the corresponding l.e.d. should be turned off.

## CASE ASSEMBLY

The case was constructed using 1 mm Perspex (Fig. 6). A special solvent is available for fixing Perspex and this should be used rather than an adhesive.

The two p.c.b.s are first Araldited to the sides taking care to align the connectors in the centre of the case. The top of the case, which should be made of clear Perspex, may then be fitted to one side.

The two ends of the case can be curved by warming them over a hot soldering iron and gently bending them until they match the curves of the sides. These may now be fitted to the top and side of the test clip. The other side can now be fitted and all the corners rounded off with wet and dry sandpaper. The Perspex can be restored to its original finish using Brasso. Finally the top of the tester should be marked with a spot of white paint to indicate the position of pin 1. A stimulate further thought.

Why not submit your idea? Any idea published will be awarded payment according to its merits.
Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.

Here are a number of circuits contributed by E. F. Flint which should be of interest to constructors of the Minisonic 2 synthesiser
Copies of Sound Design containing this are still available from Post Sales Dept., IPC Magazines Ltd., Lavington Street, London SE1 OPF at $£ 1 \cdot 20$ each including Inland/Overseas p. \& p.)

## VOLTAGE CONTROLLED OSGILLATORS

Fig. 1


One of the modifications incorporated in Minisonic 2 was in the design of the v.c.o.s. The new circuit used an LM318 N op-amp as a comparator on the grounds that this device has a much faster slewing rate than the $741-70 \mu \mathrm{~V} / \mathrm{sec}$. It is however both expensive and hard to obtain, and an alternative approach is to use a 555 timer, which contains two comparators with slewing rates comparable to the 318 , and is cheap and readily available. Two designs are possible, the second being more complex and having two output waveforms.

This version (see Fig, 1) provides only a ramp waveform as in the original design. The two comparators in the 555 have thresholds of $\frac{1}{3}$ and $\frac{2}{3}$ of $V_{\text {ce }}$, so that by operating the device with supplies of -3 volts and +6 volts the thresholds become zero volts and +3 volts.
The mode of operation is as follows: the 741 integrator ramps in a positive direction as before until the ramp level reaches +3 volts. At this point the output of the 555 goes rapidly to the negative supply rail; this switches on TR2, connecting the gate of the f.e.t. to
+6 volts and discharging the integrating capacitor. When the ramp output reaches zero volts, the 555 output goes positive, switching off the f.e.t., and integration begins again. Thus the oscillator produces a ramp of 3 volts amplitude, rising from zero volts.

The synchronisation network is slightly different from the published design; since the 555 produces negativegoing pulses the gating diode must be reversed, and the sync. pulses are fed to the inverting input of the integrator.

Fig. 2


This version provides triangle and square wave outputs; in addition to the 555 and 741 it uses a cmos chip 4007 as a two-way switch. The switch works as follows: the 4007 contains two complementary pairs of f.e.t.s and an inverter. The two complementary pairs are wired as a pair of bilateral switches with one common terminal, and the inverter controls their on/off state so that when one is on the other is off, and vice versa. (Fig. 2).
The 4007 is used in this application to switch the constant current generator between the inverting and non-inverting
inputs of the 741 integrator, so that it can ramp both positively and negatively. The 555 is connected to $\pm 6$ volts, so the threshold levels are +2 and -2 volts.

If we consider first the ramp rising positively, the 555 output is also positive and the 4007 is in position $2+9$. When the ramp reaches +2 volts, the output of the 555 swings to -6 volts, the 4007 switches to position $11+4$, and the integrator begins to ramp negatively. When the ramp reaches -2 volts the 555 output swings back to +6 volts, the 4007 switches back to
position $2+9$, and the integrator once more ranips in a positive direction.
This oscillator uses only negativegoing sync pulses so the gating diode is reversed and the pulses are routed to the common terminal of the switch; this means that when the ramp is positive-going the spikes from the sync pulses are too, and when the ramp is negative-going, the spikes are again in the same direction as the ramp, i.e. the sync pulses always augment the ramp voltage to induce triggering of the comparators at a sub-threshold voltage.

Fig. 3


A completely different approach to v.c.o. construction is to use the 8038 (Fig. 3), a 14 d.i.l. package which contains a v.c.o. with sine, square and triangle outputs, having variable symmetry, adjustable harmonic distortion on the sine-wave output, and an extremely wide frequency range of 0.001 Hz to 1 MHz (by changing the component values in the timing network then in the voltage controlled mode the sweep range is $1,000: 1$ ).

The internal workings of the package are far too complex to describe in detail but it can be considered as a black box whose output frequency depends on the values of R3, R4, Cl and the voltage between pin 8 and the positive supply rail. It is the last of these variables with which we are concerned. The frequency is a linear function of this voltage, but a logarithmic function can be introduced using a modified version

## THE 8038 V.c.0.

of the well-known Minisonic v.c.o. control node.

Here the constant current generator (c.c.g.) instead of working from an integrator draws its current from the positive rail via resistor $\mathrm{R}_{\mathrm{X}}$. Since the collector current of the transistor is a logarithmic function of the voltage on its base, it follows from Ohm's Law that the voltage across $\mathrm{R}_{\mathrm{X}}$ follows the same log function, and therefore the frequency of the oscillator becomes a log function of the voltage on the transistor base.

The control node works exactly as before, and setting up therefore follows the established procedure. Symmetry is adjusted by varying either R3, R4 or both, but note that this also affects the frequency. This is best done using an oscilloscope to monitor the square wave output.


Fig. 4
Sine wave distortion can be adjusted in two ways-the first is simpler and should be used where it may be desired to introduce some harmonics during use, for example in simulating flutes etc. A variable resistor is connected
from pin 12 to the negative rail (Fig. 4) and adjusted for the required purity of waveform or harmonic content.
Where a pure sine wave is required the circuit of Fig. 5 is used and the two

potentiometers adjusted for minimum distortion-this requires an oscilloscope and a sine wave generator for comparison, and distortion as low as 0.5 per cent can be achieved.

The circuit of Fig. 6 produces a train of pulses of variable width and repetition frequency, for so long as a key is pressed. These can be used to repeatedly trigger an envelope shaper so as to produce an effect like a banjo, mandolin or xylophone (all of these require an ADSR envelope shaper).

The circuit uses a 741 in the astable mode, with a few alterations from the usual design. It is basically a comparator, with R1 and R2 added to allow operation from a single supply.

The two diodes allow the charging and discharging times of Cl to be adjusted independently. TR1 connects R1 to the negative supply when a key trigger pulse is present, allowing the multivibrator to oscillate. VR2 controls the charging time of C 1 , and therefore the width of the output pulses, and VRI controls the discharging time and therefore the width of the spaces between the pulses.

The circuit of a sawtooth to triangle converter is shown as Fig. 7 with examples of waveforms produced (Fig. 7a). The sawtooth wave first passes through a d.c. blocking capacitor so that it is symmetrical about zero volts. A pair of diodes then gate the signal so that positive half waves are applied to the non-inverting input of a differential amplifier, and negative halves to the inverting input. As shown, this produces an output train of alternate positive and nega-tive-going ramps, i.e. a triangle wave.

Since this rectification process effectively halves the signal amplitude, the resistor values in the amplifier are chosen to give a gain of two. A second d.c. blocking capacitor in the output again makes the output symmetrical about ground.

The same circuit can also be used as a frequency doubler for triangle wave signals, as shown in Fig. 7b.

REPEAT/PERCUSSION CIRCUIT


To trigger point on
Envelope Shaper.
 CONVERTER CIRCUIT
SAWTOOTH TO thiangle

Fig. 6


Fig. 7

REaders of this magazine who have observed the sudden rise of digital clocks and quartz watches over the last few years may be surprised to learn that electric timekeeping goes back over 150 years. Last year an exhibition at the Science Museum, "Electrifying Time", commemorated the centenary of the death of the Father of Electric Horology, Alexander Bain. The exhibition surveyed electric timekeeping from its infancy at the beginning of the 19th Century through to the atomic clocks of the present day.

## ELECTROSTATIC

Zamboni and others in Europe experimented with electrostatically maintained clocks at the start of the 19th Century. They relied on the repulsion between a charged ball at the end of a pendulum and two oppositely charged plates at each end of the pendulum's swing. A high voltage battery such as a Zamboni pile maintained the potential difference between the ball and the plates. As the ball swung to one plate it would be electrostatically repelled to the other and so on, thereby maintaining the pendulum. This system was so highly temperature

sensitive that Zamboni concluded that it was a better thermometer than a clock and this approach was soon abandoned.

In 1819 Oersted had demonstrated the principle of electromagnetism and in 1841 a Scotsman, Alexander Bain, produced the first successful electric clock using an electromagnetically maintained pendulum. One of the difficulties associated with these early electric clocks was the temperature dependence of the cell driving the clock, usually a Daniell cell. In 1843 Bain hit on the idea of the Earth Cell in which copper and zinc electrodes were buried several feet down in the soil which acted as the electrolyte. At a depth of six or more feet the temperature of the soil is constant and so Bain managed to eliminate at least one of the causes of poor electric timekeeping.

## SYNCHRONISATION

One of the great virtues of electric clocks is that the pulses of electrical energy which maintain the pendulum swinging may also be used to synchronise other clocks in the same building or at greater distances. Bain demonstrated such a system with a master clock in Edinburgh synchronising a slave clock in Glasgow, the synchronising pulses being transmitted over telegraph wires.

Despite Bain's innovations, it appears that he did not meet with any great commercial success. He installed an electric turret clock in St. John's Church, Loughton, Essex, in 1846, but within four years it was replaced by a mechanical clock. A similar fate befell Shepherd's electric clock for the 1851 Great Exhibition in Hyde Park.

By the end of the 19th Century new methods of electrifying time were being evolved. In 1881 Chester H. Pond of the U.S.A. produced the first electrically maintained spring driven clock in which the spring mechanism was wound every hour by an electric motor. The "Synchronome" master clock using a deadbeat escapement was patented in 1895 , but was not practically realised until $1905-7$ by F. Hope-Jones and G. B. Bowell. In this clock a gravity arm falls every half minute to give the pendulum


Alexander Bain 1810-1877 (Photo courtesy Science Museum)
a push. This done, the gravity arm is restored to its original position by an electromagnet. At the same time as the elecromagnet is energised, a pulse is sent to move the slave clocks. The great advantage of this scheme over all previous ones is that the master pendulum itself does not have to provide the synchronising pulses.
William Hamilton Shortt carried this principle further in 1921. Shortt's master pendulum was placed in a vacuum and did not even have to turn a counting wheel. A slave clock carries out the counting and every half minute releases a light arm carrying a jewel which falls on to a small wheel mounted on the master pendulum. In rolling off this wheel it imparts a light impulse to it and after transmits a synchronising signal to the slave clock. The master pendulum runs completely free except for this impulse every half minute and maintains an accuracy of 1 part in 107 . This type of clock was in use at the Greenwich Observatory until 1942, when it was replaced by a quartz clock.


Electric clock of the type patented by Bain in 1845 (Photo courtesy Science Museum)


Rear view of Scott clock (Photo courtesy Science Museum)

The idea of using the mains to distribute time occurred to Ferranti in 1895 when the first alternating current generator was installed at Deptford. At that time, however, there was no means of controlling the mains frequency with sufficient accuracy and the idea had to wait until 1916 when H. E. Warren of the Warren Telechron Company devised a method of precision frequency measurement and a low power-consumption synchronous motor which led to the first mains driven electric clock.
Quartz oscillators were first constructed in the early 1920s and in 1927-30 Warren Alvin Morrison of the Bell Telephone Laboratories produced the first quartz clock. The reliability of electronics at that time was low and consequently three such independent clocks were used to confirm the time. The accuracy of these quartz clocks was improved and now accuracies of better than 1 part in $10^{8}$ are achievable, ten times better than the best pendulum clock

The quartz oscillator which is the timekeeping element of the quartz clock relies upon the vibration of a quartz crystal between two metal electrodes. This high frequency vibration, which usually takes place above 100 kHz , is amplified by an active device, originally a valve but nowadays a semiconductor device, and a portion of this amplified output is fed back to the quartz crystal to maintain its oscillation. The high frequency signal is then divided to provide a low frequency signal (hetween 50 Hz and 1 Hz ) to drive a synchronous or stepping motor to provide an analogue display of time or a digital display via counter and decoder circuits.

## ATOMIC CLOCK

Up until the early 1950 s the method of timekeeping had always relied on the oscillation of a solid body whether it be a pendulum. a balance wheel or quartz crystal. These are calibrated with respect to the rotation of the Earth on its axis which is irregular. In 1955, Dr Louis Essen and Mr Parry of the National Physical Laboratory in England produced the first atomic clock. This essen-


A typical domestic electric clock of the 1920's
(Photo courtesy Science Museum)


Hamilton electric analogue watch (Photo courtesy Science Museum)
tially was a quartz clock controlled by the vibration of cæsium atoms and was accurate to within 1 second in 300 years ( 1 part in $10^{10}$ ). As a result, in 1967, the second was re-defined in terms of the cæsium vibrations and in 1971 the International Atomic Time Scale was adopted. Currently casium clocks are accurate to within 1 second in 100,000 years (3 parts in 1013)-a quite mind-boggling accuracy which, if it surprises us, would have shaken Alexander Bain. Rubidium clocks which rely on the vibrations of rubidium atoms are used as secondary standards and are at present accurate to within 1 second in 1,000 years ( 3 parts in 1011).

## DIGITAL

On the domestic scene Max Hetzel devised a clock employing an electrically maintained tuning fork. This was the forerunner of the Bulova Accutron, a wristwatch in which the tuning fork was maintained by a transistor circuit. The first domestic quartz clock was manufactured by Junghan's in 1967. Only two years later, Seiko introduced the world's first electronic watch and later that same vear Longines introduced their electronic watch. All three of these timepieces used a motor driving the hands of an analogue display. In 1972 Hamilton produced the first quartz watch with a digital l.e.d display and, in the same year, a Swiss Company, Societe des Gardes-Temps, produced a quartz watch using a liquid crystal digital display.

All of these brings us just about up to date. The accuracy of even a cheap quartz watch is beyond the dreams of watchmakers of a hundred years ago. Although it is improbable that atomic clocks will ever be used domestically, their accuracy can be tapped by receiving and decoding the radio clock signals transmitted by MSF, Rugby. Such radio clocks and designs for the experienced constructor are available, so if you ever feel the need for a clock with an accuracy of 1 second in 100,000 years, that's the way to achieve it.


## CB RADIO

Last year's clamour from pressure groups advocating Citizens Band Radio is now somewhat muted, perhaps because the problems of introducing a CB "service" in a smallish country like the UK have now been more fully examined and debated.

An interesting sidelight on $C B$ in the United States is that Texas Instruments has been working with the FCC on developing a high-performance TV receiver which is less susceptible to interference from $C B$ radios, the implication being that interference is a major problem to viewers. Improving front-end performance of mass-produced TV receivers is a worthy end in itself but it costs money.

Ninety-five per cent of Britain's 20 million households have a TV receiver and the average viewing time is 18 hours per week. The great mass of the population would not take kindly to interruption of their principal recreation and even less kindly to having to spend more to overcome the difficulties caused by enthusiastic chatterers on CB.

## PRELUDE

Project Prelude, now running on a trial basis in the United States could be the shape of things to come in Business Communications. Big companies need to talk from their headquarters to their factories and offices at remote locations and often to each other. The modern way to do this is by communications satellite which can provide high-speed data, facsimile and teleconference transmission, all over the same links. In the teleconference mode, for example, you can have largescreen projection colour TV for group discussions and even person-to-person viewphones.

Three big businesses are involved in the trials, Texaco, Rockwell International and Montgomery Ward. Using small transportable earth terminals they are currently operating through the Communications Technology Satellite (CTS) which operates on the same frequencies as a proposed Satellite Business Systems (SBS) satellite.

## LUCAS ELECTRICAL

Lucas seldom gets in the news as an electronics company but the company is strong in thick film technology and has large semiconductor manufacturing operations in Birmingham to supply devices for alternators, electronic ignition and other automotive electronic applications. Lucas has now increased its shareholding in Ducellier, France, from 49 per cent to 100 per cent, having bought out, DBA (the American Bendix Automotive subsidiary) for a reported 26 million dellars.

Ducellier has 7,000 employees in four factories. Lucas-France already has 4,300 employees in six factories as well as a 49 per cent share in ThomsonLucas which serves the aerospace market, making Lucas a major company in France. The Lucas move appears to be following the trend of investment overseas into areas where the industrial climate is less restrictive than in Britain under the present regime.

## DISTRIBUTION

The Association of Franchised Distributors of Electronic Components (AFDEC) have forecast a total component market this year worth $£ 531$ million of which about 16 per cent will be served through distributors, the balance being bulk direct supplies from manufacturers.
Despite talk of overcrowding in the distribution business there are still some eager entrants. One such is Jack Evans Electronic Distribution Ltd, scheduled to start up in April near London Airport, an area already heavily populated with distributors. JEE Distribution, as the company will be called, is to concentrate on electronic hardware which, according to company hand-outs, is a neglected area.

Jack Evans is a familiar name in the business. He was the prime mover in setting up ITT Electronics Services and was general manager there from 1964 to 1970. Most new companies in distribution start off with one or two good franchises as the foundation of the business. Evans is starting with over 30.

JEE's hardware catalogue is said to be a real engineering manual rather than a list of products and is expected to be a prime addition to the equipment design engineer's technical library. The first edition has some 150 pages of products and technical data.

## HEWLETT-PACKARD

John A. Young, newly elected president and chief operating officer of Hewlett-Packard has inherited a strong financial and technological position. He is only 45 so has many years ahead of him to seek further growth in what is one of the most powerful and respected companies in world electronics. He heads up over $35,000 \mathrm{H}-\mathrm{P}$ people of whom 10,000 are outside the United States. The two founders of the company who started up in a backyard garage are continuing to exercise influence on long-term policy.

Ten years ago H-P was still the classical test and measurement company with 86 per cent turnover in that field out of a total turnover of less than 300 million dollars. Today the company is approaching 1.5 billion dollar turnover and test and measurement activities are only 42 per cent of the total, having now been overhauled by an equal share in electronic data products which was only 4 per cent of the total 10 years ago.

It's now full speed ahead on computational technology in H-P, which means "smart" instruments, computers, terminals and analytical tools. To keep the whole effort moving H-P has invested in component technology, one example being H-P's silicon-on-sapphire microprocessor which has over 10,000 circuit elements on one chip. But above all H-P invests in people. Graduate student intake last year totalled nearly 400, and over 10,000 employees took advantage of educational programmes to develop technical and management skills. $R$ and $D$ spend last year was running at the rate of about $£ 1$ million a week. Nothing succeeds like success.

## BIG BATTERIES

With so much emphasis on micromin in electronics we tend to overlook that some things get bigger. In contrast to the tiny cells which power our watches and calculators, Chloride Industrial Batteries have recently roped in £4 million of orders for submarine batteries. Individual cells are said to be nearly as tall as a man and weigh about half a ton. A complete set for a submarine can weigh as much as 250 tons.

## AVO's DMM

AVO, the oldest name in multimeters, is having another crack at the digital multimeter market with a unit, the DA116, with liquid crystal display and a price tag of £99. An important feature is high-speed resistance measurement, claimed to be ten times faster in response than conventional dmms. A single i.c. is used for analogue-digital conversion.

provides a variable power source limit

AN essential piece of equipment for any home lab is a power supply as batteries come extremely expensive-particularly the high power types.
This design is stabilised and has a performance which vies with a number of much higher priced commercial designs.
The maximum power available is 25 V at 1.5 A . The voltage output is continuously variable and the protection offered by an adjustable over-current limit is very reassuring-especially when experimenting with breadboards that have been inadvertently wired with shorts.
To cut cost, meters have been deliberately excluded but the ingenious use of l.e.d.s compensate in part for this omission.

## STABILISER

Transistors TR1, 6, 7 and 8 constitute the constant voltage stabiliser. The reference voltage for TR1 is provided by D3 with the decoupling components C3 and R2 removing any hum that could be fed back.
Assuming S2 in the B switched position then TR8 is providing current to the load. For a full rated 1.5 A the base input to this comes via R11, R14 and R16.
The output voltage seen at the terminal is sampled by the preset VR5 and R18 and if too negative (dependent on VR1 setting) switches on TR1. As a result of this TR6 and TR7 are turned on so that some of the base current available to TR8 is shunted.

This feedback ensures that the output voltage will be such that the base of TR1 is always just negative with respect to the slider of VR1, the voltage setting potentiometer.
Returning TR6 collector to a tapping on the divider chain feeding TR8 base ensures that dissipation in this transistor is kept low. Note that R16 cannot be omitted, otherwise TR7 would be unable to bottom sufficiently to control TR8 base current.

Stability in the feedback loop is ensured by the roll off provided by C6 and C5, although the main role of the latter is to maintain a low output impedance at high frequencies. C4 provides this additional h.f. roll off when the output is switched off.

## CURRENT LIMITING

The active components in the current limit loop are TR2, 6, 7 and 8. To understand the working, assume calibration has been made-VR2, VR3 adjusted-and a current limit value has been set on VR6. If the volt drop across D4 and R3 is such as to turn on TR2 then TR6 and TR7 will turn on, subtracting base current from TR8 and preventing any further increase in load currents.

If the whole of the volt drop across R3 were fed to TR2 base, it would be necessary to vary R3 to adjust the current limit and to obtain a wide control range-say 20 mA to full load-requiring a low value non-linear wirewound potentiometer. This has been done in some commercial stabilised supplies, but such a specialised component is hard to come by. Therefore, a power diode D4 is included and this still drops an appreciable voltage even at low currents, enabling front panel control VR6 to be calibrated directly in current down to low values.
VR2 sets the maximum current the stabiliser can supply (with VR6 fully clockwise) and VR3 is set to make the anticlockwise position of VR6 correspond to virtually zero short circuit output current.



Fig. 1. Circuit of Power Supply Unit

R21 ensures that C5 discharges when the output is turned off at $\mathbf{S} 2$. Consequently, when $\mathbf{S} 2$ is switched on again, the charging current into C 5 momentarily takes the power supply into current limit, causing the output voltage to ramp up linearly from zero. This feature in conjunction with the adjustable current limit provides complete protection of any circuit connected to the stabiliser.

## L.E.D. INDICATORS

Whilst the omission of meters saves a considerable amount of money, one needs to know if the output voltage set by front panel control VR1 is really appearing across the load or whether the supply is in current limit. TR 3 and 4 detect whether TR1 or TR2 is controlling the output and light an appropriate l.e.d. Thus, if the output voltage really is as

## COMPONENTS

| Resistors |  |  |
| :---: | :---: | :---: |
| R1 | $1 \mathrm{k} \Omega$ |  |
| R2 | $3.3 \mathrm{k} \Omega$ |  |
| R3 | $0.33 \Omega$. | 7 in of $24 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. Eureka wire |
| R4 | 47 k ת | may be used. Eureka wire is |
| R5 | $1 \mathrm{k} \Omega$ | obtainable from The Scien- |
| R6 | $27 \mathrm{k} \Omega$ | tific Wire Co., PO Box 30, |
| R7 | $33 \mathrm{k} \Omega$ | London E4 9BW. |
| R8 | $120 \mathrm{k} \Omega$ |  |
| R9 | $68 \mathrm{k} \Omega$ |  |
| R10 | $100 \mathrm{k} \Omega$ |  |
| R11-14 | $470 \Omega \frac{1}{2} W$ |  |
| R15 | $1 \mathrm{k} \Omega$ |  |
| R16 | 68 k ת |  |
| R17 | $3.9 \mathrm{k} \Omega$ |  |
| R18 | $4.7 \mathrm{k} \Omega$ |  |
| R19 | 8.2k $\Omega$ |  |
| R20 | $3.9 \mathrm{k} \Omega$ |  |
| R21 | $2 \cdot 2 \mathrm{k} \Omega$ |  |
| (1)W 5\% | i stab exc | ept where stated otherwise) |

Potentiometers

| OR1 | $5 \mathrm{k} \Omega$ linear |
| :--- | :--- |
| VR1 |  |
| VR2, 3 | $4.7 \mathrm{k} \Omega$ vertical miniature skeletal |
| VR4 | $100 \mathrm{k} \Omega$ |
| vertical miniature skeletal |  |
| VR5 | $4.7 \mathrm{k} \Omega$ vertical miniature skeletal |
| VR6 | $2.5 \mathrm{k} \Omega$ linear |

## Capacitors

| C1, C2 | $2 \times 4,700 \mu \mathrm{~F} 40 \mathrm{~V}$ |
| :--- | :--- |
| C3 | $150 \mu \mathrm{~F} 15 \mathrm{~V}$ |
| C4, C5 | $2 \times 22 \mu \mathrm{~F} 40 \mathrm{~V}$ |
| C6 | 330 pF |

Semiconductors

| TR1, | 2N2905A (2 off) |
| :--- | :--- |
| TR3, | BC109 (2 off) |
| TR5 | BC214 |
| TR6 | BC109 |
| TR7 | BFY50 |
| TR8 | 2N3055 with mica insulating set |
| TR9 | BC109 |
| D1,2 | 1N5402 (2 off) |
| D3 | BZY88 C7V5 |
| D4 | 1N5402 |
| D5 | 1N916 |
| D6, | TIL209 (2 off) |
| D8 | 1N5402 |

## Miscellaneous

T1 MT104AT pri. 240 V ; sec. $25-0-25 \mathrm{~V} 1 \frac{1}{2} \mathrm{~A}$, FS1 1 A , S1 On/off switch, S2 S.P.C.O. switch, Heatsink 401-807 (Radiospares). LK301 Side Plate No. 1 (2 off) LK211 Chassis rail (4 off) LK431 Front panel LK521 short perforated plate (3 off) (Home Radio)


Fig. 3. (above) Component layout and wiring details for p.s.u. board

Fig. 2. (left) Printed circuit board layout. Holes should be drilled to conform with the component wire inserts as shown above


indicated by VR1 setting, TR1 is controlling the loop and the volt drop across R4 due to its collector current will ensure that TR3 is on and TR4 is off. Therefore TR5 and TR9 will be off and D7 lit. In current limit TR2 takes over control of the loop, so the drop across VR4 will cause TR4 and hence TR5 and TR9 to turn on and l.e.d. D6 will light, extinguishing D7.
S2 enables the output to be turned off and R19 ensures that even with little or no load the output voltage will then collapse to zero within a second or so. If the mains switch were used instead, then the output voltage could hold up for several seconds when lightly loaded, leading to possible damage to a circuit being worked on whilst still connected to the supply. D8 protects the stabiliser when switching off a highly inductive load and also when the supply is being used in series with another to obtain a higher voltage. Under these circumstances, if the stabiliser goes into current limit, the voltage at its terminals could reverse if it were not for D8.

## CONSTRUCTION

Figs. 2 and 3 show the track layout and component assembly of the main board. In the prototype a Lektrokit chassis system was used for the case with TR8 heat sink mounted to the rear. A free air circulation must be ensured around this.
The sink specified will cope with the dissipation in TR8 at 1.5 A output current into a short circuit at ambient temperatures in excess of 50 degrees centigrade if heat sink compound is used on both sides of the insulating mica washer.
As D4 can get rather warm on full load it should be mounted with its body away from the board.


Various views showing p.s.u. prototype assembly. Four lower value reservoir capacitors were used in this

## CALIBRATION

On completion, set VR1, 2, 3, 4 and 5 midway, VR6 fully clockwise and check the raw supply voltage across CI. Off load it should be 37 V . With $\mathbf{S} 2$ on, monitor the output voltage. Increase it to maximum by rotating the voltage control VR1 fully clockwise and set VR5 to give 25 V output. VR1 may now be calibrated directly in output volts.

With output voltage control VR1 set fully anti-clockwise, the minimum output voltage is about 1.75 V . Next set VR1 fully clockwise and current limit control VR6 fully anticlockwise and measure the short circuit output current. Adjust VR3 to set this to 1 or 2 mA . Now adjust VR6 clockwise until the short circuit current reaches 1.5 A . Adjust VR2 so that 1-5A short circuit current occurs with VR6 fully clockwise. As the settings of VR2 and 3 will interact somewhat, readjust them alternately so that the short circuit current with the current limit control VR6 fully anti-clockwise and clockwise is 2 mA and 1.5 A respectively. VR6 may now be calibrated directly in short circuit current.
Next connect a suitable load to draw 1.25 A at 25 V and adjust current limit control VR6 so that the output voltage just begins to fall. Adjust VR4 so that l.e.d. D6 just takes over from D7. The l.e.d.s now indicate the mode of operation, i.e. if D 7 is !it the output voltage is as indicated by the setting of voltage control VR1, whilst if D6 is lit the power supply is in current limit. The current limit control VR6 can thus be used as indication of the current drawn by the load, by noting its reading at the point where the current limit l.e.d. D6 just lights.

## USE

Whilst the power supply can look after itself, with 25 V at 1.5 A available, care is needed to avoid damage to low power circuits being run from the supply. Always connect up with $\mathbf{S} 2$ switched off and check the voltage and current limit settings before switching the output on. Set the current limit at about 50 per cent more than the current you expect the circuit to take and you will then seldom damage it even if it has been assembled wrongly.

Note that the current limit control has been calibrated in terms of the short circuit output current. Of course, when the output is short circuited, R21 draws no current, but at 25 V output it will draw about 11 mA . Consequently, if the current limit is set to less than 11 mA , even though the voltage control VR1 is set to maximum, the output voltage will be less than 25 V . At high current limit settings, the 11 mA difference between current at the onset of limiting at 25 V and the current at short circuit is of course barely perceptible.


WITH the circuit details of CHAMP-PROG behind us, this month we can move on to consider the construction of the main board, power supply and plinth, and to an examination of the PROMPT firmware program. Anyone who has already built CHAMP itself should have no problems with CHAMP-PROG, because the techniques required will have already become familiar.

## CIRCUIT BOARD

Most of the CHAMP-PROG circuitry is mounted on a piece of Veroboard measuring 165 mm by 225 mm , and this has to be cut from a larger sheet, of the same type as was used for the CHAMP main board. When the board has been cut to size, the three unperforated copper strips which run along the two long edges should be removed by easing up each of their ends with a sharp knife and then carefully pulling them away from the board along their entire length. The removal of these strips makes for easier insertion of the board into the card guide supports, and insertion can be further eased by chamfering the four board corners with a fine file.

The required track cuts and component positions are detailed in Fig. 9.3 and it is a good idea to study the component layout at this stage so that when construction starts you will "know-your-way-around". There are three distinct circuit areas on the board, the top quarter being occupied by the regulator components, the next quarter by the timing generator and the lower half by the CHAMP interface and the data and address circuits. The layout chosen provides plenty of room in which to work and makes fault finding fairly easy.

Track cuts can be made right at the start, which makes life easier later but requires great care initially to prevent errors creeping in. Alternatively they can be incorporated as construction proceeds so that some layout flexibility is retained.

## CONNECTING UP

The circuitry on the CHAMP-PROG board is a fair mixture of digital and analogue integrated circuits and discrete components, and of course high voltages will be present during operation. Needless to say, great care must be taken during wiring-up to avoid expensive mistakes. A wiring error on the prototype caused a transistor to quite literally "blow its top" when power was first
applied! One consolation though, faults on this kind of circuitry are usually easy to locate, just watch out for the smoke signals!

As with the CHAMP main board, Soldercon pins are recommended for every integrated circuit, but not for the 16 and 24 way connector socket positions where standard or low-profile sockets are best. Remember to leave the bandolier attached to the Soldercon pins until construction is complete, and be sure not to plug in the 4265 mos chips until the debugging process is over.
Using the Soldercon pins and sockets as a reference framework, wiring up is carried out using Fig. 8.2. Kynar wire is highly recommended for the interconnection of all logic circuitry address and data drivers, although sturdier single core PVC covered wire is better for the $+5 \mathrm{~V},-10 \mathrm{~V}$ and +80 V interconnections because of its higher current rating and higher voltage insulation.

## TESTING

The complex timing generator and voltage regulator circuitry lends itself well to being tested in isolation without benefit of PROMPT software or 4265 interface chip. Before testing can take place, +5 V and -10 V supplies must be connected, and the 80 V supply will have to be built using the circuit of Fig. 9.1 and the layout shown in Fig. 9.2.

To start the testing procedure, first connect pin 1 of ICl to OV temporarily to enable the timing generator to free-run (this can be achieved by grounding pin 11 of the vacant IC8 socket if desired). Next apply the +5 V and -10 V power, but not the +80 V supply and examine the timing generator waveforms at the Q and $\overline{\mathrm{Q}}$ outputs of the 74123 using an oscilloscope set to measure pulse amplitudes of a few volts and pulse durations of a few milliseconds.

At ICl pin 13 you should be able to see narrow pulses with a 15 ms separation. If the pulses you see are separated by much more or less than this, the value of fixed resistor R3 should be changed to compensate. A timing accuracy of $\pm 10$ per cent should be the target.

On IC2 pin 13 a series of 3.25 ms wide pulses should be obtained, and of course the width of these pulses can be accurately set using VR1. On IC3 pin 12 the pulses should be set to a width of 3.0 ms using VR2, and on IC2 pin 12 pulses about $60 \mu \mathrm{~s}$ wide should be observed. Finally on IC3 pin 13, pulses of about $155 \mu \mathrm{~s}$ width should be visible.

## REGULATOR OUTPUTS

If the monostable circuits are operating correctly and VR1 and VR2 have been properly adjusted, the next step is to set VR3 to its mid-travel position (Remember that all three adjustment pots are of the 10 turn variety) and connect up the +80 V supply. Providing that the
fuse does not blow (and that no wisps of smoke are observed!), the next step is to examine the 7405 outputs, IC4 pins $2,4,8$, and 10 and compare these with Fig. 8.5 published last month.

If these drive pulse outputs are correct switch the 'scope probe to the junction of D7 and D8 in the regulator area and decrease the 'scope sensitivity to show pulses of about 50 V . The waveform at this point should consist of a steady +4.5 V level with pulses 3.25 ms wide superimposed every 15.0 ms . The amplitude of these pulses can be set by means of VR3, and this should be adjusted to give a peak of +47 V . (Note that the +47 V should be measured with respect to 0 V and not with respect to +4.5 V .)

At this point you can relax a little, because the worst is over! All that remains is to check the remaining Cs, Vbb, Vgg, Prgm and Vdd outputs and to ensure that they all conform to the timing and amplitudes specified in Fig. 8.5 last month. The Prgm pulse must have the characteristic "two-eared" shape and you will find that final trimming of this pulse can be achieved using VR1 and VR2. The high voltage programming waveforms can all be found on the board-mounted 24 pin socket as well as on the regulator transistors themselves of course.

## CASE CONSTRUCTION

The CHAMP-PROG case, or plinth, is made of plywood and aluminium and is relatively easy to construct using the techniques described in part four for the CHAMP case. The plywood framework should be pinned and glued together, and mated carefully with the aluminium top cover and the separate aluminium back


Fig. 9.1. Circuit diagram of 80 V supply


Fig. 9.2. Component layout of 80 V Supply
panel. When the overall fit is satisfactory all necessary holes can be drilled in accordance with Fig. 9.4 and the board runners made up and pop riveted or bolted in place on the cover.

It is of course essential at this stage to ensure that the board runners are positioned so as to provide a satisfactory sliding fit on the CHAMP-PROG circuit board. A great deal of care was taken to ensure that the CHAMP-PROG prototype finish matched that of CHAMP itself, and again the process of applying several coats of primer, sanding it smooth and finishing off with a couple of colour coats, was followed. After allowing the paint to harden for a couple of days Letraset lettering was applied along with outlines drawn with a spirit based pen. Finally a coat or two of polyurethane clear varnish was applied to bring out a high gloss and to protect the lettering.

## ZERO INSERTION FORCE SOCKET

With the plinth hardware completed, overall assembly can begin with items such as the ON/OFF rocker switch and the zero insertion force socket mounted on the front panel section of the cover.

If an economy CHAMP-PROG is required, the zero insertion force socket could be left out, and Proms programmed directly in the 24 -pin socket on the board, but levering expensive proms in and out of this type of socket is less than satisfactory, and the sheer convenience of the lever action type is well worth the few pounds it costs. If used, the front panel socket can be mounted either by adhesive, or more securely by first removing the two small Phillips screws from the socket, and ivith the socket lever in the upright position removing the face of the socket so that two holes can be drilled at the top and bottom of the socket. These should clear the two Phillips screw holes to take two 8BA countersunk screws. Using the socket as a template, two 8BA clear holes can be drilled in the plinth cover so that the socket can be mounted securely and its faceplate replaced.

## EIGHTY VOLT SUPPLY

The programming voltages are derived from a simple power supply which consists of a transformer with a $25-0-25 \mathrm{~V} 2 \mathrm{~A}$ secondary, a 200 V 2 A bridge rectifier, and a $3,300 \mu \mathrm{~F}, \quad 100 \mathrm{~V}$ electrolytic capacitor. These bulky components are mounted inside the plinth using the separate aluminium back panel as a support and as a heat sink for the transformer and the rectifier. Mains input is via a three pin connector and, of course, the rocker switch on the front panel. The 80 volt output is routed to two wander sockets on the back panel, and the CHAMP-PROG board connects to these via a couple of flying leads.

The 80 V generated by this circuit is of course sufficient to give the unwary quite a tingle, and caution is advisable when making the back panel connections! A fully insulated connector could of course be used instead of the Wander plugs and sockets if required. Since the 80 V is also present on the CHAMP-PROG circuit board some protection against prying fingers has been provided by mounting a tailored sheet of perspex over the parts of the board where danger exists. Five 4BA plastic mounting pillars were cut to size and cemented to the Veroboard using cyanoacrylate adhesive. The perspex safety cover is screwed to these pillars when construction is complete.



Fig. 9.3. CHAMP-PROG component layout. For interconnections see Fig. 8.2.

| Row | Positions |
| :---: | :---: |
| 4 | $X, Y, Z, A A, A B, A C, B K$ |
| $5{ }^{\text {' }}$ | $A T, A V, A X$ |
| 9 | $X, Y, Z, A A, A B, A C$ |
| 11 | $A S, A T, A U, A V, A W, A X$ |
| 12 | BA |
| 13 | $A F, A H, A L, B P$ |
| 16 | AR-AY |
| 17 | $W, X, Y, Z, A A, A B, A C, A F$ |
| 20 | BL |
| 21 | AF, AH, AL |
| 22 | AN, AR-AY, BA |
| 24 | $B K, B L, B M, B P$ |
| 25 | AF, AH, AL |
| 26 | $D-Z, A A, A B, A C$ |
| 27 | AR-AY |
| 28 | AN, BN |
| 29 | AF, AH, AL, BJ, BK |
| 30 | BL |
| 31 | AR-AY, BA |
| 33 | $A F, A H, A L, B A, B P$ |
| 35 | W-Z, AA, AB, AC, BE, BF, BH, BJ |
| 37 | $A F, A H, A L, A R-A Y, B A$ |
| 39 | BM |
| 42 | AR-AY |
| 43 | $D-U, W-Z, A A, A B, A C, B A, B E-B N$ |
| 44 | AF-AK |
| 47 | $B L, B M$ |
| 48 | $A F-A K, A N, A R-A Y$ |
| 50 | $W-Z, A A, A B, A C$ |
| 51 | BE-BJ, BM |
| 52 | $A F-A K, A N$ |
| 56 | $A F-A K, A N, B E, B H-B N$ |
| 57 | AR-AY |
| , 60 | $A F-A K, A N, A T, B C, B D$ |
| 61 | $D-U, W-Z, A A, A B, A C$ |
| 63 | AS, BJ |
| 64 | AF-AK, AN, AT |
| 65 | BN |
| 66 | AS, BA |
| 68 | $A F-A K, A N, A T, B D$ |
| 70 | AS, BE, BF |
| 72 | $\begin{aligned} & \text { W-Z, AA, AB, AC, AF, AH, AJ, AK, AN, } \\ & \text { AT, BD } \end{aligned}$ |
| 74 | BN |
| 75 | AN-AZ |
| 76 | BF, BK |
| 79 | BC, BD, BF |
| 81 | BN |
| 82 | $A M-A Z$ |
| 84 | $X, A A$ |
|  | of track cut positions on CHAMP. PROG Veroboard |

## TWENTY-FOUR WAY CONNECTOR

The zero insertion force socket on the front panel is connected via a flying lead to the socket on the circuit board, and in addition to the 24 wires required for the PROM three others are needed for the "PROM POWER" switch and l.e.d. On the prototype a 27 -way wiring loom was made up with the three extra wires being terminated at the board end with individual sleeved Soldercon sockets which provided a very convenient means of connection to terminal pins soldered to the Veroboard (Fig. 9.5).

The 24 -pin plug required was actually made using a "header plug with top" which is available from Doram. Fine flexible wire was used for the interconnection, and this was soldered to the plug pins and brought out through a hole cut in the right hand side of the header plug top. When fully wired the header plug pins were potted in quick-set Araldite and the top clamped in position until the epoxy hardened. This method of construction has provided a satisfactory and trouble free plug which is a less expensive but more time consuming alternative to the flat-strip cable connectors used on CHAMP itself.

## PROMPT FIRMWARE

When construction is complete, and the timing circuits and voltage regulator outputs have been set up correctly, you are ready to plug in the 4265 chips and run the PROMPT firmware program using the control sequence detailed last month. The 4702A containing PROMPT must be in the Chip One socket on the CHAMP main board, and arrangements have been made to enable CHAMP-PROG constructors to get their own devices programmed with PROMPT by using the CHAMP PROM programming service. Of course, once CHAMPPROG is operating with PROMPT, CHAMP programmers will be totally independent and will never again have to rely on outsiders for programming facilities. The full listing of PROMPT is given in Fig. 9.7 and as you can see there is no wasted space in the 256 line Prom. All users of CHAMP will find it useful to study the operation of the PROMPT software, and to make this easier to follow, some words of explanation might be helpful.

## DESIGN AIMS

A prime objective of the software design was that it should make CHAMP-PROG simple to use and preferably self explanatory. To this end the program has been made "interactive" with the programmer. PROMPT issues a prompting message via the keyboard display and waits for a response from the programmer, this process is repeated three times for address entry. At the termination of a programming run, CHAMP-PROG will issue the message "done" or "fail".

For address entry the CHOMP keyboard interrupt routine is "borrowed", and for the display of messages and keyboard entries the CHOMP DDRV subroutine is used. Here is a good example of why it pays to make software routines as general-purpose as possible from the outset, and to code them as subroutines callable from anywhere in program memory! The DDRV subroutine is segment, rather than BCD based and so it is quite capable of refreshing a display of alphanumeric characters when required. The generation of text messages is of course a new facility, and PROMPT includes a new subroutine, TEXT, to handle this job.

## MAIN FLOWCHART

Referring to Fig. 9.6 PROMPT is entered at the top via a JUN instruction, from CHOMP. Since CHOMP itself was unaware of the presence of CHAMP-PROG during CHAMP initialisation, the first job here is to set the modes of the two new 4265 chips via WMP instructions (box 1). Next, (box 2) the prompt message "Adr 1 " is loaded into the display ram buffer register by the subroutine TEXT, and then an interruptible display loop is entered to await keyboard response (boxes 3 and 4). The continuous looping automatically refreshes the display via DDRV, while accepting up to three hexadecimal digits via the INTER routine.

An exit from the loop is made via box 4 by pressing

the enter data button. At this time, a three digit hexadecimal address should be resident in 4040 registers $C, D$ and $E$, and of course visible on the left of the display. The next job is to store Adr 1 away in its appointed storage locations (box 5) and to change the display message to Adr 2 (box 6). In this case it is not necessary to change the message radically, and so rather than employ TEXT once more, the display buffer is modified directly to save program lines. Boxes 7 and 8 are of course identical to boxes 3 and 4 also boxes 11 and 12 further down, and this makes them ideal subroutine candidates. (In fact a subroutine ENTERL does contain this pair of boxes, but for the purposes of our flow chart these activities have been included individually, to clarify program action.)


Fig. 9.4. Physical dimensions of CHAMP-PROG chassis


Fig. 9.5. Cable loom arrangement for z.i.f. socket

With three new hexadecimal digits entered, ENTER data is pressed once more and the Adr 2 data stored away in Ram register 2 where it will later be needed for comparison with Adr 1 (box 9). The display is now modified to show Adr 3, and the interruptible display loop again used for address entry. Adr 3 data is left in the 4040 registers C, D and E, and so box 13 does not so much represent an action but rather it is a reminder that no action takes place in this case.

Next the keyboard display is blanked by writing OOH to ports X and Y of the CHAMP 4265 so that in the absence of a refresh loop, a single display digit does not remain on continuously.

Box 15 represents a largish subroutine called WUNBYTE which has the job of programming a single PROM location each time it is called. This subroutine reads data from the appointed area in program RAM, sends it out together with Adr 3 to the CHAM-PROG 4265 chips, initiates a program cycle via 4265 number 1 port $\mathrm{Z3}$, and waits during a software delay of about 540 milliseconds before reading the results of its programming and storing them away in RaM register 2 before returning to the main program.


In box 16 a subroutine called MATCH is used to compare a copy of the PROM input data stored in ram with the output data read by WUNBYTE after its cycle. MATCH returns a flag which is tested in box 17 , where a conditional jump (JCN) either aborts operations if the output is bad, or passes on to an address compare operation using MATCH (box 18) if the output is good.

Address comparison is needed to check whether all necessary locations have been programmed. In this case
further programming is suspended if a good comparison results. If on the other hand Adr 1 and Adr 2 are not yet the same, then Adr 1 and Adr 3 are incremented to point to the next source address and the next destination (PROM) address respectively, before a JUN loop to the label MORE is carried out (boxes 20 and 21 ).

Boxes 22 and 23 load the base addresses of text messages stored in PROM before jumping to a routine which loads the message into the display using TEXT

## PROMPT

| paise | ITE | POM | CODINE | Larse | OFRration | OPERAM | Sumixas |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | D9 |  | PROMPT | LDM | 9 | LOAD DCLRSEC CODF |
|  | - | FD |  |  | DCL |  | SELFCT RAM BANKI |
|  |  | B8 |  |  | XCH | 8 | $\mathrm{q}_{\mathrm{H}} \rightarrow \mathrm{R}_{8}$ |
|  |  | 29 |  |  | SRC | 9 | SEWECT 4265 CHIP |
|  | 4 | D4 |  |  | LDH | 4 |  |
|  | 5 | E1 |  |  | WMP |  | SET TO MODE 4 |
|  |  | DF |  |  | LDM | F | BIT SET PORT 23 |
|  |  | EO |  |  | WRM |  |  |
|  | 8 | D2 |  |  | LDM | 2 |  |
|  | \% | FD |  |  | DCL |  | SERET RAM BANK 2 |
|  | $\wedge$ | 29 |  |  | SRC | 9 | SELECT 4265 CHIP |
|  | B | D6 |  |  | LDM | 6 |  |
|  | c | El |  |  | WMP |  | SET TO MODE 6 |
|  |  | DO |  |  | LDM | 0 |  |
|  | $\pm$ | FD |  |  | DCL |  | SELECT RAM BANK 0 |
|  | $\stackrel{\square}{\text { P }}$ | OB |  |  | SB 1 |  | SKLFCT PEG. BANK 1 |
| 1 | 1. | 20 |  |  | FIM | 0 | StT UP ADDRESS |
|  |  | F4 |  |  | F | 4 | of Mbsace "Adrl" |
|  |  | 51 |  |  | JMS |  | LOAD MESAEE INTO |
|  |  | E1 |  |  | TEX | T | DISPIAY |
|  |  | 51 |  |  | JMS |  | Loop For Adrl Teriont |
|  |  | CO |  |  | ENT | RL | Entry 2 'ence OATA' |
|  | - | AD |  |  | LD | D | Get Adrl Lenst Sif |
|  |  | 83 |  |  | XCH | 3 | Put in $\mathrm{R}_{3}$ |
|  | \% | AC |  |  | LD | c | GETAdrl MID |
|  | - | 82 |  |  | XCH | 2 | Putin R2 |
|  | $\wedge$ | $A E$ |  |  | LD | E | Cet Adri most sis |
|  | B | B4 |  |  | KCH | 4 | Purin $\mathrm{R}_{4}$ |
|  | - | 51 |  |  | JHS |  | COFY LERST SIG\& HID |
|  | D | Do |  |  | LAD | R1 | OF AdF 1 TO RAM |
|  | g | 20 |  |  | FIM | 0 | CODE FOR TEKT |
|  | F | 68 |  |  | 6 | B | OF "2" |
| 1 | 2. | 51 |  |  | JMS |  | CHANGE DISAAT TO |
|  | 1 | B6 |  |  |  | 10 | "Adr 2" |
|  | a | 51 |  |  | JMS |  | LOOP For Adr 2 KEYPOARD |
|  | 3 | CO |  |  | ENT | RL | ENTRT ${ }^{\prime \prime}$ ENTER DATA" |
|  | 4 | 28 |  |  | FIM | 8 | SET UP Ad-2 RAM |
|  | 5 | 22 |  |  | 2 | 2 | ADPress |
|  | 6 | 29 |  |  | SRC | 9 |  |
|  |  | AD |  |  | LD | D | LEASTSIE AdF-2 TO RAM |
|  | 8 | EO |  |  | WRM |  |  |
|  | 9 | 69 |  |  | INC | 9 |  |
|  | 1 | 29 |  |  | SRC | 9 |  |
|  | B | AC |  |  | LD | c | Mid Adr2 To RAM |
|  | $c$ | EO |  |  | WRM |  |  |
|  |  | 20 |  |  | FIM | 0 | GET CODE FOR TEX |
|  | 8 | E9 |  |  | E | 9 | of "3" |
|  | $\stackrel{5}{5}$ | 51 |  |  | TMS |  | CMANGE DISPLAT TO |
| 1 | 3 | B6 |  |  | AD | 0 | "Adr3" ${ }^{\text {a }}$ |
|  | 1 | 51 |  |  | JMS |  | LOOP FOR Mdr 3 KETDOARD |
|  | 2 | co |  |  | ENT | RL | Entries lenter dafa |
|  |  | 28 |  |  | FIM | 8 |  |
|  | 4 | 80 |  |  | 8 | 0 |  |
|  | 5 | 29 |  |  | SRC | 9 | SELELT CHAMP 4265 |
|  | 。 | Fo |  |  | CLB |  |  |
|  |  | ES |  |  | WRI |  | 2 BLANK |
|  | 8 | E6 |  |  | WR? |  | $\int$ Displat |
| 1 | 3. | 51 |  | More | JMS |  | Pbogram one priom |
|  | 1 | 54 |  |  | WUNB | TE | Location |
|  | B | 20 |  |  | FIM | 0 | SET UP ADORES OF |
|  | $c$ | 10 |  |  | - | 0 | IN' DAMA For match |
|  | D | 22 |  |  | FIM | 2 | SET UP ADPress of |
|  | 8 | 20 |  |  | 2 | 0 | BUT' DATA FORMATSH. |
|  | r | 51 |  |  | JMS |  |  |


| Pace | 12x | ROL | Sontw | LLAEEL | Opzeatios | OPERME | mavase |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 4 | A6 |  |  | MATEH |  |  |
|  | ? | 20 |  |  | FIM | 0 | SET UPGASE ADPEESS |
|  | : | F8 |  |  | F | 8 | OF TOT MBSAGE"FAIL" |
|  | 1 | 1 C |  |  | JNZ |  | STOP \& PASAAY "EAIL" |
|  | 4 | DA |  |  | FINIS |  | IF AS NOT zero. |
|  | $\pm$ | 20 |  |  | FIM | $\bigcirc$ | SET UP ADORESS OF Adrl |
|  | : | 12 |  |  | I | 2 | For match. |
|  |  | 22 |  |  | Fim | 2 | SET UP ADDRESS OF Adr 2 |
|  | B | 22 |  |  | 2 | 2 | FOR MATCH. |
|  | ? | 51 |  |  | JMS |  | compare current Adti |
|  | $\wedge$ | A6 |  |  | MATCH |  | with Adr 2 |
| $\cdots$ | B | 20 |  |  | Fim | 0 | SFT UP BASE ADREESS |
|  | $\bigcirc$ | Fc |  |  | $F$ | c | OF TEXTHEXKFE" "done" |
|  |  | 14 |  |  | Ј |  | STCPR DISRAAY |
|  | E | DA |  |  | Fin | is | "done" |
|  | F | 0a |  |  | SB6 |  | SAET RESEBNK O |
| 1 | 5 | 73 |  |  | 152 | 3 |  |
|  | 1 | 53 |  |  | PAST |  | Cinkreliment Adri |
|  |  | 62 |  |  | INC | 2 | $\}$ ) |
| 1 | 5. | 51 |  | PAST | JMS |  | NEW Adrl TO RAM |
|  | 4 | Do |  |  | LADR1 |  |  |
|  |  | 70 |  |  | 152 | D |  |
|  | ¢ | 39 |  |  | MORE |  | Increment Adr 3 |
|  | , | $6 c$ |  |  | INC | c |  |
|  | ? | 41 |  |  | JUN |  | LOOP BACK FOR NEXT BME |
|  | 5 | 39 |  |  | MORE |  | END OF MAIN PROG. |
| 1 | 5. | 28 |  | WUNETE. | FIM | 8 | SUBPONTE PROCO 1 BYTE |
|  | B | 00 |  |  | 0 | $\bigcirc$ |  |
|  | $c$ | 29 |  |  | SRC | 9 | SELEET PROCRAM Meart |
|  | 2 | A4 |  |  | LD | 4 | SOURCE CHIP |
|  | 3 | E1 |  |  | WHP |  |  |
|  | 9 | 23 |  |  | SRC | 3 | SEAD OUT AdI I |
| 1 | 6. | OE |  |  | RPM |  | READ EIRST NIBRLE. |
|  | $\pm$ | F4 |  |  | CMA |  | Combement IT. |
|  | $\stackrel{1}{2}$ | B1 |  |  | $\times$ CH | 1 | Putitin Ri |
|  | 3 | OE |  |  | RPM |  | READ SECOND NIEBLE. |
|  | 7 | F4 |  |  | CMA |  | COMPEMENT IT |
|  | 5 | BO |  |  | XCH | 0 | PUT IT IN RO |
|  | $\dot{\square}$ | 28 |  |  | FiH | 8 | PU ITIN RO. |
|  | 1 | 10 |  |  | 1 | $\bigcirc$ |  |
|  | 8 | 29 |  |  | SRC | 9 | Put in data in |
|  | 3 | A 1 |  |  | LD | 1 | (RAM FOR USE |
|  | $\wedge$ | EO |  |  | WRM |  | NITH MATCH |
|  | B | 69 |  |  | INC | 9 | later. |
|  | ${ }^{\circ}$ | 29 |  |  | SRC | 9 |  |
|  |  | AO |  |  | LD | 0 |  |
|  | \& | EO |  |  | WRM |  | ) |
|  | $\stackrel{\square}{8}$ | D2 |  |  | LDM | 2 |  |
| 1 | 7 | FD |  |  | DCL |  | SFLECT RAM BANKZ |
|  | 1 | 28 |  |  | FIM | 8 |  |
|  | 2 | 80 |  |  | 8 | 0 | SERRT 4265 No 2 |
|  | 3 | 29 |  |  | SRC | 9 |  |
|  | 4 | A1 |  |  | LD | 1 |  |
|  |  | E6 |  |  | WR | 2 | - IN data to Ports |
|  | 。 | AO |  |  | LD | 0 | $Y \& \geq$. |
|  |  | E7 |  |  | WR | 3 |  |
|  | 3 | D1 |  |  | LDM | 1 |  |
|  |  | FD |  |  | DCL |  | LOAD Adr 3 TO POETS |
|  | $\stackrel{ }{ }$ | AD |  |  | LD | D | Wex 4265 NO 1 |
|  | 3 | E4 |  |  | WR O |  | $\}$ |
|  | : | Ac |  |  | LD | c |  |
|  | D | E5 |  |  | WR I |  |  |
|  | 8 | DE |  |  | LDM | $E$ | TURN ON PROSRAM |
|  | F | EO |  |  | WRM |  | Pulses. |

Fig. 9.7. PROMPT program listing

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(box 24) and then enters a display loop using DDRV (box 25 ). From the foregoing you can see that the program always terminates with one of the two messages "fail" or "done" displayed, and that return to CHOMP must be carried out by use of the reset button.

## SUBROUTINE FLOWCHARTS

The flowchart of the ENTERL or "interruptible loop" subroutine is shown in Fig. 9.8, note the use of EIN and

DIN to control interrupts, and the way that the ENTER data flag is read using RDR and tested using JCN; the WRR before the BBL is used to reset the ENTER data flag. Fig. 9.9 shows the TEXT subroutine and its look-up tables. This subroutine is passed the base address of a text message table in register pair 0,1 so that the FIN command can be used to fetch the message a byte at a time.

In PROMPT only four-character messages are per-

## PROMPT

| Paic | Live |  | codina | LLABEL | Opremiton | Opreant | comesis |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 8 | OB |  |  | SBI |  |  |
|  | 1 | 22 |  |  | FIM | 2 |  |
|  |  | 19 |  |  | 1 | 9 | Preset delat |
|  | 3 | 24 |  |  | FIM | 4 | COUNTER. |
|  | 4 | 9 A |  |  | 9 | A | f countr |
| 1 | 85 | 72 |  | WAIT | is 2 | 2 |  |
|  | - | 85 |  |  | WA1T |  |  |
|  |  | 73 |  |  | is 2 | 3 |  |
|  | B | 85 |  |  | WA | T | SAOM.S. DELAY |
|  | 3 | 74 |  |  | 152 | 4 | Soms. DELAY |
|  | $\wedge$ | 85 |  |  | WA1 | I |  |
|  | 8 | 75 |  |  | 152 | 5 |  |
|  | $\bigcirc$ | 85 |  |  | WA1 | $T$ |  |
|  |  | DF |  |  | LDH | F | TURN OFF |
|  | E | EO |  |  | WRM |  | Program pulses |
| 1 | 8 8 | 74 |  | DELAY | 152 | 4 |  |
|  |  | 8F |  |  | Del |  | DELAT BEFORE |
|  | 1 | 75 |  |  | 1sz | 5 | $\text { READ } 16 \mathrm{~m} . \mathrm{s} \text {. }$ |
|  |  | 8 F |  |  | DEL | AY |  |
|  |  | D2 |  |  | LDM | 2 | $)$ |
|  |  | FD |  |  | DCL |  | Saret 4265 NO 2 |
|  |  | 29 |  |  | SRC | 9 |  |
|  | - | EC |  |  | RDO |  |  |
|  |  | 85 |  |  | XCH | 5 | REAT Prom 'our data |
|  | 8 | ED |  |  | RDI |  | Put in R5, R4. |
|  | 4 | B4 |  |  | XCH | 4 |  |
|  | 1 | DO |  |  | LDM | $\bigcirc$ |  |
|  | B | FD |  |  | DCL |  | SELETRAM BANK O |
|  | c | 28 |  |  | FIM | 8 | CMIPO REGZ 2 |
|  | . | 20 |  |  | 2 | 0 |  |
|  | B | 29 |  |  | SRC | 9 | ) |
|  | F | AS |  |  | LD | 5 |  |
| 1 | A | EO |  |  | WRM |  |  |
|  | - | 69 |  |  | INC | 9 | Putóot' data into |
|  | $\therefore$ | 29 |  |  | SRC | 9 | RAM. |
|  | 3 | A4 |  |  | LD | 4 |  |
|  | 4 | EO |  |  | WRM |  | $\int$ |
|  | , | CO |  |  | BBL | $\bigcirc$ | END OF WUNBYTE. |
| 1 | A ${ }^{\text {e }}$ | FA |  | MATCH | STC |  | SURROUTINE COMPMRES 2 PMES. |
|  |  | DE |  |  | LDM | E | PRETET MIPFLF COUNTER. |
|  | , | B5 |  |  | XCH | 5 |  |
| 1 | A. | F3 |  | Loop 2 | CmC |  |  |
|  | $\stackrel{1}{1}$ | 21 |  |  | SRC | 1 |  |
|  | B | E9 |  |  | RDM |  | Read first nisple. |
|  | c | 23 |  |  | SRC | 3 |  |
|  |  | E 8 |  |  | SBM |  | SuETPACT SFROND NIBPLE. |
|  | $\Sigma$ | 61 |  |  | INC | 1 | 3 Increment Ram |
|  | ${ }^{5}$ | 63 |  |  | INC | 3 | ADPRRSES. |
| 1 | B 0 | 14 |  |  | 丁2 |  |  |
|  |  | 83 |  |  | SK1 | P |  |
|  |  | $\mathrm{Cl}^{7}$ |  |  | BBL | 1 | 15 AC. Noto BBLI |
| 1 | B | 75 |  | Skip | 152 | 5 | NEXT PAIP OF |
|  | 4 | A9 |  |  | LOOP | 2 | NIBELES? |
|  | - | CO |  |  | BBL | - | If AC snce 0 B8LO. |
| 1 | B | 28 |  | ADNO | Fin | 8 | Suprainne noypies displat |
|  |  | OE |  |  | 0 | E | SLERT RAMBMK O, |
|  | 8 | 29 |  |  | SRC | 9 | CHIP O, CHARE. |
|  | * | Ao |  |  | LD | 0 | Put first nibblein |
|  | A | EO |  |  | WRM |  | CHAR E. |
|  | B | 69 |  |  | INC | 9 | NEXT GHAR |
|  |  | 29 |  |  | SRC | 9 | NEXT GAAR |
|  | D | A1 |  |  | LD | , | Put sezond NibBle |
|  | B | EO |  |  | WRM |  | INCHAR F. |
|  | F | co |  |  | BRL | 0 | END OF ADNO |


| Pais | - $\mathrm{SR}^{\text {P }}$ | RCY | cosing | LAEEL | OPrRation | Oprabal | conems |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | c | 0 |  | ENTERL | EIN |  | SuRPOINE: KEY ENTR TCOPR |
|  |  | 28 |  |  | FIM | 8 | ) |
|  |  | 40 |  |  | 4 | 0 | READ SWITEH RAGS. |
|  |  | 29 |  |  | SRC | 9 |  |
|  | 4 | EA |  |  | RDR |  | J |
|  | 5 | F6 |  |  | RAR |  | Rutentr anai in cy |
|  | - | OD |  |  | DIN |  | DISABLE INTERUPTS. |
|  | 1 | 50 |  |  | JMS |  | dispar next digit. |
|  | 8 | B1 |  |  | DDR | v |  |
|  | 3 | 12 |  |  | JC |  | IF Éenter matai not |
|  | 1 | co |  |  | ENTE | 21 | PRESSED TIEN LDOP |
|  | B | 28 |  |  | FIM | 8 | Now Pressed so |
|  | c | 50 |  |  | 5 | 0 | clear switeh rlags. |
|  |  | 29 |  |  | SRC | 9 |  |
|  | E | E2 |  |  | WRR |  |  |
|  | F | co |  |  | BBL | 0 | END OF ENTERL |
| 1 | D. | 28 |  | LADRI | Fim | 8 | Supeoun ${ }^{\text {a }}$ : AdrI TO RAM. |
|  | 1 | 12 |  |  | 1 | 2 | SELECT RAM BANKO |
|  | ? | 29 |  |  | SRC | 9 | KHIP O RET 1, CMAR 2 |
|  | 3 | A3 |  |  | LD | 3 |  |
|  | 4 | EO |  |  | WRM |  | Uinat sie to ram. |
|  | 5 | 69 |  |  | INC | 9 |  |
|  | 6 | 29 |  |  | SRC | 9 | I NEXT CHAR |
|  |  | A2 |  |  | LD | 2 | 3 NID TO RAM |
|  | - | EO |  |  | WRM |  | MID TO RAM. |
|  | - | co |  |  | $B B^{\prime}$ | 0 | END OF LADR1 |
| 1 | DA | 51 |  | Finls | JMS |  | SUPTMNE:END OF PROCG |
|  | ${ }^{\text {B }}$ | El |  |  | TEX |  | lCad text to dishay |
| 1 | D 0 | 0 A . |  | LooPx | SBO |  |  |
|  | , | 50 |  |  | JMS |  | DISPLAY NEET PIGT |
|  | B | B1 |  |  | DDR | $v$ |  |
|  | $\stackrel{5}{1}$ | 41 |  |  | IUN |  | Loop until Rezer. |
| 1 | E |  |  |  | LOO |  | ENS OF FINIS. |
| 1 | E 1 |  |  | TEXT | FIM | 2 | SUROUTINE: LOADS TEXT |
|  | : |  |  |  | - | 8 | SEET RAM BANK $Q, C 0, R 0,18$ |
|  | 3 |  |  |  | LDM | $c$ | Preat rute connt. |
|  | ${ }_{5}$ |  |  |  | - CH | 5 |  |
| 1 | E5 |  |  | LOOP I | Fin | 6 | LOOK UP INTEXT TABLE. |
|  | 6 |  |  |  | INC | 1 | NEKT TABLE LINE |
|  | 1 |  |  |  | SRC | 3 | RAM SRC |
|  | B |  |  |  | LD | 7 | First miprlf To RAM |
|  | 9 |  |  |  | WRM |  | DISAT BUFFER. |
|  | 1 |  |  |  | INC | 3 | NEETRAM CHAR |
|  | B |  |  |  | SEC | 3 |  |
|  | c |  |  |  | LD | 6 | SECOND NIPBLE TO |
|  |  |  |  |  | WRM |  | ram displal buffer. |
|  | E |  |  |  | INC | 3 | NEXTRAM CHAR. |
|  | ${ }_{\text {F }}{ }^{\text {F }}$ |  |  |  | 152 | 5 |  |
| 1 | $F$ |  |  |  | L00 | 1 | NEXT BYTE? |
|  | 1 |  |  |  | SBO |  |  |
|  | 2 |  |  |  | BBL | 0 | END OF TEXT |
|  |  |  |  |  | Nof |  |  |
| 1 | $\mathrm{F}_{4}$ |  |  | ADE1 | $E$ | $E$ | teattagle: A |
|  | 5 |  |  |  | $B$ | $c$ | d |
|  | - |  |  |  | A | 0 | $r$ |
|  | ${ }^{7}$ |  |  |  | 0 | c | 1 |
| 1 | F: |  |  | FAIL | E | 2 | F |
|  | 4 |  |  |  | E | E | A |
|  | $\wedge$ |  |  |  | 6 | 0 | 1 |
|  | ${ }^{\text {B }}$ |  |  |  | 7 | 0 | $L$ |
| 1 | Fo |  |  | DONE | $B$ | c | d |
|  | D |  |  |  | B | 8 | $\square$ |
|  | B |  |  |  | A | 8 | $\square$ |
|  | F |  |  |  | F | 2 | $E$ |



Fig. 9.10. MATCH subroutine
mitted to save program space, but any message of up to eight characters could be used in other applications, with appropriate table entries and a different preset for the byte counter, register 5 . The messages themselves were worked out to give the most pleasing display within the restrictions of a seven segment format.

The MATCH subroutine is shown in Fig. 9.10, and here the ploy is to subtract one byte of data from the other and check if the result is zero. The result of the comparison is flagged to the main program via alternative BBL exits, BBL 0 means match, BBL 1 means no match. The addresses of the data to be compared are passed to MATCH in register pairs 0,1 and 2,3 by the main program.


Fig. 9.9. TEXT subroutine

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\end{aligned}
$$

Please ensure PROM is securely packaged, and state clearly whether CHOMP or PROMPT firmware is required.

There is not sufficient room to fully detail WUNBYTE, but since this subroutine is so important, readers may like to draw up their own flowchart using the listing in Fig. 9.7. Fortunately WUNBYTE is relatively straightforward and should pose few problems.

Note that source data is complemented using CMA before programming to compensate for the 74 L 00 in version. Notice also the way the registers 2, 3, 4, 5 are preset using FIM instructions to give a 16 bit counter which produces a delay of 540 milliseconds. For further details see page 2.17 of the MCS40 manual.

## PROMPT LISTING

For PROMPT we have chosen to list the program code in the format introduced in Part 7 for the TONE program. This format differs from the cross assembler listing given in Part 6, and is used to demonstrate to all budding CHAMP programmers that hand-coding of long programs is perfectly feasible! No facilities other than CHAMP itself were used in the development of the PROMPT firmware.
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| 2N696 | 0.39 | 2 N 2 | 0.3 | 2N3397 | 0.19 | 2N4062 | 0.20 | 2N5247 | 0.44 | 40410 |  |  |  |  |  |  |  | ${ }^{\text {BC5 } 5498}$ |  |  |  |  | $0.37$ |  |  |  | $\begin{aligned} & 0.50 \\ & 0.70 \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2N697 | 0.31 | 2N2219A | 0.39 | 2N3438 | 0.85 | 2 N 4084 | 1.35 | 2N5248 | 0.44 | 40411 |  |  |  |  |  |  | ${ }_{0}^{0.17}$ | ${ }^{\text {BC5 }}$ B49C | 0.15 0.14 | ${ }_{802454}^{8024}$ | ${ }^{0.87}$ | $\begin{aligned} & \text { BF183 } \\ & \text { BF184 } \end{aligned}$ | $\begin{aligned} & 0.44 \\ & 0.41 \end{aligned}$ | BFY5 | $\begin{aligned} & 0.27 \\ & 0.27 \end{aligned}$ | MJE521 | ${ }_{1}^{0.85}$ |
| 2N698 | 0.49 | 2N2220 | 0.39 | 2N3440 | 0.75 | 2N4074 | 2.65 | 2N5294 | 0.44 | 40594 | 0.87 | ${ }^{\text {BCl }} 4$ | 0.13 | 8 C | 0.15 | BC214L8 | ${ }_{0}^{0.181}$ | ${ }_{\text {BC558 }}$ | 0.13 | B02454 | 0.69 0.85 | 8F184 | 0.41 | BFY52 | 0.27 | MPF3055 | 1.05 |
| 2N699 | 0.58 | 2N2221 | 0.25 | 2N344 | 0.92 | 2 N 4121 | 0.27 | 2 N 5295 | 0.44 | 40595 | 0.98 |  | 0.13 |  | 0.15 | BC214LC | 0.18 | BC559 | 0.15 | BD246A | 0.72 | BF194 | 0.18 | 8FY90 | 1.35 | MPF102 | 0.3i |
| 2N706 | 0.30 | 2N2221A | 0.25 | 2N3442 | 1.45 | 2 N 4122 | 0.27 | 2N5298 |  | 40869 | 0 | ${ }_{\text {BC1488 }}$ | 0.13 0.13 | BC182LA | 0.15 | ${ }_{\text {BC2378 }}$ | 0.15 | BCY70 | 0.21 | BD24BC | 0.93 | ${ }_{\text {BF1 }} 195$ | 0.16 | 8R101 | 0.55 | MPF103 | 0.44 |
| 2N706A | 0.30 | 2 N 2222 | 0.25 | ${ }^{2 N} \mathbf{N} 3638$ |  |  |  |  |  | ${ }_{\text {AC128 }}{ }^{4069}$ | 1.30 0.48 | ${ }_{8 C 1488}^{8 C 1}$ | 0.13 | ${ }_{\mathrm{BC} 183}^{\mathrm{BC}} 182 \mathrm{l}$ | 0.12 | BC238A | 0.13 | ECY71 | 0.26 | ${ }_{80433}$ | 0.4 | 8F198 | 0.16 | 8RY39 | 0.55 | MPF104 | 0.44 |
| 2N708 | 0.30 | 2 N 2222 A | 025 | 2N3638A | 0.17 | 2N4124 | 0.19 | 2N5448 | 0.16 | ${ }_{\text {AC127 }}$ | 0.48 | BC149 | 0.15 | BC183A | 0.12 | BC2388 | 0.13 | 8CY72 | 0.18 | 80434 | 0.46 | BF197 | 0.18 | BSX19 | 0.35 | MPS 105 | 0.44 |
| 2N718 | 0.30 | 2 N 2368 A | 0.27 | 2N3703 | 0.14 | 2N4128 | 0.19 | 2N5449 | 0.20 | AC128 | 0.48 | BC149C | 0.15 | BC1838 | 0.13 | BC238C | 0.13 | BD115 | 0.88 | 80435 | 0.46 | BF198 | 0.19 | BSX20 | 0.35 | MPSAOS | 0.27 |
| 2N720A | 0.85 | 2N2846 | 0.80 | 2N3704 | 0.14 | 2N4284 | 0.38 | 2N5457 | 0.38 | AC151 | 0.43 | BC157A | 0.15 | BC183C | 0.13 | BC2398 | 0.16 | 80131 | 0.55 | B0436 | 0.46 | BF199 | 0.19 | BS $\times 21$ | 35 | 6 | 0.27 |
| 2N722 | 0.45 | 2N2647 | 1.55 | 2N37 | 0.14 | 2N4286 | 0.22 | 2N5458 | 0.35 | AC152 | 0.54 | BC158A | 0.15 | BC1831 | 0.15 | BC239C | 0.11 | 80132 | 0.75 | BR | 0.55 | BF224J | 0.22 | 8 8104 | . 00 |  | . 33 |
| 2N727 | 0.50 | 2N2903 | 1.60 | 2N3706 | 0.14 | 2N4287 | 0.22 | 2N5459 | 0.32 | AC153 | 0.59 | BC158B | 0.15 | 8Cib3La | 0.15 |  | 0.18 | 880135 | 0 |  | 0.4 | BF2 | ${ }_{0}^{0.38}$ | BU126 | . 08 |  | 0.27 |
| 2N914 | 0.36 | 2N2904 | 0.31 | 2N3707 | 14 | 2N4288 | 0.22 | 2N5480 | 0.65 | AC.153k |  | BC159A | 17 | BC1 | 15 | BC2588 | 0.19 | 80137 |  | ${ }_{80530}$ | 0.55 | BF244B | 0.33 |  | 2.20 | MPSA5 | 0.27 |
| 2N916 | 0.33 | 2N2904 | 31 | 2N3708 | 12 | 2N4289 | 0.22 | 2N5484 | 0.37 | AC176 | 0.70 | BC159 | 0.17 | BC184 | 0.12 | 8С300 | 0.4 | 80138 | 0.41 | B0535 | 0.70 | BF 2454 | 0.44 | 8U205 | 2.40 | A2008B | 2.45 |
| 2 N 917 | 0.38 | 2N2905 | 0.31 | 2 N3709 | 0.12 | 2 N 4347 | 2.20 | 2N5485 | 0.40 | AC187 | 0.54 | 8C161 | 0.38 | BC184B | 0.13 | BC301 | 0.43 | 8D139 | 0.43 | BD536 | 0.70 | BF245B | 0.44 | BU206 | 2.70 | R20108 | 2.15 |
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| ${ }_{2}^{2 N 9294}$ | 0.37 | 2 N 2907 | 0.25 | 2N3819 | 36 | 2N4920 | 0.83 | 2N5494 | 0.65 | AC188K | 0.55 | BC168A | 0.13 | BC184LB | 0.15 | BC307 | 0.18 | B0182 | 2.20 | 80539 | 0.60 | BF259 | 0.35 | ME0402 | 0.22 | TIP30A | 0.54 |
| 2N930A | 0.95 | 2N2907A | 0.25 | 2N3820 | 0.39 | 2N4921 | 0.54 | 2N5496 | 0.67 | AD161 | 1.00 | BC1688 | 0.13 | BC184LC | 0.15 | BC307A | 0.16 | ${ }^{80183}$ | 2.35 | 80540 | 0.60 | 8F336 | 0.42 | ME0404 | 0.17 | TIP31A |  |
| 2N1711 | 0.30 | 2N2923 | 0.17 | 2N3821 | 0.96 | 2 N 4922 | 0.60 | 6027 |  | AD162 | . 00 | BC168C | 0.13 | 8C212 | 0.15 | ${ }^{\text {BC307 }} 308$ | 0.15 | ${ }_{8}^{80187}$ | 0.95 | B0X14 <br> 8018 <br> 18 | 1.32 | ${ }_{8}^{8 F 337}$ | 0.49 | ME0414 | 0.22 | TIP31C | 0.72 |
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| 2N1893 | 0.30 | 2N2926 | 0.17 | 2N3903 | 0.20 | 2 N | 30 | ${ }^{2}$ 2N6111 | 0.49 | BC107A | 0.16 | BC177A | 0.22 | ${ }_{\text {BC212L }}$ | 0.18 | ВС3098 | 0.16 | BD238 | 0.44 | BOY56 | 10 | BFAd 1 | 0.30 | ME4003 | 0.16 | TIP41A | 0.76 |
| 2N2102 | 0.50 | 2 N 305 | 0.25 | 2 N 3904 | 0.18 | 2N50 | 0 | 2NB121 | 0.41 | ${ }_{\text {BC107 }}$ | 0.16 | ${ }_{\text {BCLI7 }}$ | 0.25 | BC212LB | 0.18 | вС309C | 0.16 | 90239A | 0.44 | 8F115 | 0.39 | BFR79 | 0.30 | ME4101 | 0.11 | IIP41C | 0.97 |
| 2N2192 | 0.58 | 2 N3055 | 0.72 | 2N3905 | 0.18 | ${ }^{2 N 5089}$ | 0.30 | 2N6122 | 0.44 | BC108 | 0.16 | ${ }_{8} \mathrm{C} 178$ | 0.22 | BC213 | 0.15 | 8С327 | 0.22 | BD239C | 0.59 | BF180 | 0.33 | BFA80 | 0.30 | ME4102 | 0.11 | TIP42A | 0.86 |
| ${ }^{2} \mathrm{~N} 2193 \mathrm{~A}$ | 0.52 | 2N3390 | 0.50 | 2N4031 | 0.55 | 2N5190 | 0.85 | 2N6123 | 0.48 | BC108A | 0.16 | BC178A | 0.25 | BC213A | 0.15 | BC328 | 0.20 | BD240A | 0.49 | 8F161 | 0.65 | BFR81 | 0.30 | ME4103 | 0.11 | IP42 | 1.08 |
| 2N2194 | 0.42 | 2N3391 | 0.40 | 2N4032 | 0.65 | 2N5191 | 0.75 | 2N6124 | 0.45 | BC108B | 0.16 | 8С178B | 0.35 | BC2138 | 0.15 | BC337 | 0.20 | BD240C | 0.59 | 8F167 | 0.37 | BFX29 | 0.34 | ME |  |  | 59 |
| 2N2194A | 0.45 | 2N3391A | 0.45 | 2 N 4036 | . 12 | 2N5192 | 0.80 | 2N6125 | 0.47 | 8C108C | 0.17 | 8C179 | 0.25 | BC213C | 0.15 | BC338 | 0.23 |  | 0.49 | $8 \mathrm{BFI7}$ | 0.37 | 8 Bx 30 | 0.34 |  | 0.22 | 1P34 | 1.55 |
| 2N2195 | 0.40 | 2N3392 | 0.17 | 2N4037 | 0.60 | 2N5193 | 0.75 | 40361 | 0.55 | 8 Cl 09 | 0.16 | BC179A | 0.25 | 8C213 | 0.17 | BC | . 13 | ${ }^{802} 2424$ |  | 8F178 | 0.27 | 8FX85 | 0.38 | MJ2955 | , | TIS42 | 0.50 |
| 2N2195A | 0.40 | 2N3393 | 0.17 | 2N4058 | 0.22 | 2N5194 | 0.80 | 40382 | 0.55 | 8 8.1098 | 0.17 | BC1798 | 0.25 | BC2 31 | 0.17 | BC547 | 0.13 |  |  | F179 | 0.33 | BY886 | 0.30 | MJE340 | 0.62 | TIS43 | 0.47 |
| 2N2217 | 0.55 | 2N3394 | 0.17 | 2N4059 | 0.17 | 2N5195 | 0.97 | 40363 | 1.45 | ${ }^{8 C 109 C}$ | 0.18 | ${ }_{8 C 1785}$ | 0.26 | BC213LC | 0.17 |  | 0.13 | B0243A | 0.65 | BF180 | 0.37 | 8 Ex 87 | 0.35 | MJE370 | 0.62 | IIS90 | 0.22 |
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| 74585 N | 1.10 | 74 CABN | . 38 | 7440 | 0.20 | 74163 A | 1.10 |
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 everything for the mcdern D.I.Y. electronics enthusiast and more.


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[^1]:    Open daily to callers: Mon.-Fri. 9 a.m.-5 p.m.
    Valves, Tubes and Transistors . Closed Saturday Terms C.W.O. only - Tel. 01-677 2424-7 Quotations for any types not listed S.A.E.
    Post and Packing 25p per order $+8 \%$ V.A.T.
    when going
    to press Items marked * $12 \frac{1}{2} \%$

[^2]:    Showing component assembly to front and back panels with p.c.b. and transformer which fixes to box base panel

[^3]:    Texas Instruments Ltd. Supply Division. Manton Lane. Bedford. ' Tel: Bedford (0234) 67466. Branch Offices at: Slough 0753-33411. Edinburgh 031-229 5573. Slockyort 061-442 7000. Southampton 070327267

[^4]:    Suits loads 4-16 ohms
    $20-20,000 \mathrm{~Hz} \pm 1 \mathrm{~dB}$
    Siticon circuitry throughout
    Glass fibre P.C.B
    High sensitivity ( 100 mV 10 k )

[^5]:    LEADER" Electronic kits enable even the inexperienced constructor to produce

[^6]:    Meil order only. P. \& P. 2sp. Add ©\% VAT. CWO
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