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35 CAR	DIFF	ROAL	D, WAT	FORD,	HERTS., E	NGLAND	7400 7401	74*	7494 E 7495 7 7496 E 7497 26	5 74 0 74 2 74 2 74	94 140 95 95 96 118 97 118	4056 4057 4059 4060	134 2570 480	4513 2 4514 2 4515 2	206 4518 265 4519 299 4520	102 59 108	4526 4527 4528	199 152 99
MAIL ORD	ER, C	ALLE	RS WE	LCOME	E. Tel. Wat	ord 40588/9	7402 7403 7404	16 16 20	74100 12 74105 6 74107 3	5 74 2 75 3 75	98 250 50 110 91 75	4061 4062 4063	2380 999 110	4517 3	82 4521	199	4529	85
DESPATCHED BY P.O.s OR BANKE	RETU	RN OF AFT W	POST. 1	ERMS D	FOLLY GUARA DF BUSINESS: ERNMENT AN	CASH/CHEQUE/ D EDUCATIONAL	7405 7406 7407	22 38 38	74109 5 74110 5 74111 7	4 754	92 80	4066 4067 4068	58 380 22	LINEAR 702	IC'S 75	MC13	303L 304P	148 260
WELCOME. P&P	DFFICIA ADD 30 T. AIR/S	L ORD p* TO URFAC	ERS ACCI All Orde F.	RS UNDE	RADE AND E	RSEAS ORDERS	7408 7409 7410	20 20 15	74112 9 74116 19 74118 9	0 CN 8 400 0 400	105+ 0 15 1 17	4069E 4070 4071	E 22 32 21	709C 14 710★ 741C★8	pin 35 48 pin 22	MC1 MC1	310P 312PQ 458P★	185 195 90
VAT Export ord	ers no VA	MINIM T. Applic	UM ORDE able to U.K.	R £2.00 P Customers	LEASE. only. Unless state	id otherwise, all prices	7411 7412 7413	24 23 37	74119 9 74120 11 74121 2	0 400 5 400 8 400	2 17 6 105 7 18	4072 4073 4075	21 21 23	747C 748C 753	70 36 150	MC1 MC1 MC1	496 4433L 710CG	101 1250 79
We stock many m Ground. Nearest U	ore item ndergrou	s. It pa ind/BR	dd 8% to devi γs to visit Station: Wi	ces marked us. We ar atford Hig	* To the rest add e situated behin h Street. Open l	12∦%. nd Watford Footbali Monday to Saturday	7414 7416 7417 7420	35 39	74122 4 74123 7 74125 6	0 400 5 401	8 92 9 58 0 58	4076 4077 4078	129 40 21	8038CC	355 12 580	MC3 MC3	340P 360P	150 89
POLYESTER CAPA 400V: 0.001 0.001	Ample F	S: Axial li	Parking spa ead type. (Va	lues are in	μF) 168 0.01 0.015	0.018 95: 0.023	7421 7422 7423	33 24 32	74128 7 74132 7 74136 7	7 401 3 401	19 2 18 3 55 4 99	4081 4082 4085	22 21 74	AY-1-508 AY-1-672 AY-3-850	51 145 21/6 195	MFC	00008 5040★ 0253★ 0362★	97 550
0.033 10p; 0.047 160V: 0.039, 0.15, DUBILIER: 1000V	0 068 0.22 13	14p; 0 ; 0 33	10 15 p; 0 0.47 22 p; 0.022 18	15, 0.22 0.68, 1.0	22p; 033.04 29p; 1.5 33p; 15p: 0134p	7 39 p; 0.68 45 p. 2.2 38 p; 4.7 47 p.	7425 7426 7427	30 36 36	74141 7 74142 28 74143 31	2 401 0 401 4 401	5 93 6 52 7 99	4089 4093 4094	150 85 190	AY-3-87 AY-5-122 AY-5-123	10 * 978 24 * 349 30 * 490	MM2 MM2 NE51	102-2 112-2N	190 * 310 210
POLYESTER RADI 0-01 0-015 6p, 0-0 0-22 0 33 14p; 0-4	AL LEAI 22.0.02 7.16p;0) (Values 7 7p; 0-0	in μF) 250 033 0-047 1 0 24 ρ; 1	0-068.0-1 -5 27 p; 2-1	8p; 0-15 12p; 2 31p.	FEED THROUGH CAPACITORS 100µF 350V 8p	7428 7430 7432 7433	38 18 32 40	74144 31 74145 8 74147 17 74148 14	4 401 5 401 5 402 3 402	8 99 9 60 0 102 1 99	4095 4096 4097 4098	105 105 372 110	CA3011 CA3018 CA3020 CA3023 CA3023	82 82 170 170	NE55 NE56 NE56	55 * 608* 50*	36 90 325 410
ELECTROLYTIC CA	2 2 2 2 5	DRS: Ax	ial lead type 7. 6.8, 8, 1	(Values are 0, 15, 22	e in μF) 9p; 47, 32, 50	12p; 63, 100 27p;	7438 7440 7441	33 17 74	74150 7 74151 7 74153 7 74154 14	5 402 5 402 5 402 0 402	2 90 3 20 4 76 5 19	4099 4160 4161 4162	109 109 109	CA3035 CA3036 CA3043	140 180 190	NE56	52★ 554★ 56★	185 180 157
50V: 1.0 7p; 50 1 62p; 4700 64p; 35 8p; 220 250 13p; 4	00.220 V:10.3 70.640	25p; 47 3 7p; 33 25p; 10	0 50p; 100 0. 470 32p 00 27p; 15	2200 68 ; 1000 49 00 300:22	3p; 40V: 22, 33 p; 25V: 10, 22, 4 00 41p; 3300 5	9p; 100 12p; 3300 17 6p; 80, 100, 160 2p; 4700 54p; 16V	7442 7443 7444	68 115 112	74155 8 74156 8 74157 8	2 402 0 402 0 402	6 180 7 55 8 90	4163 4174 4175	109 110 99	CA3046 CA3048 CA3075	80 200 175	RAM RC41	/1 2102-2★ 36D★	450 210 120
640 10p; 1000 14p; TAG-END TYPE: 7	0V: 200	ор; 330 0 98р; 4 юм: 200	4700 121p	50V: 300	00 75 p; 40V : 40	ю;10V: 4, 100 бр; 00 70р; 2500 б5р;	7445 7446 7447	94 94 82	74159 22 74160 11 74161 11	5 402 6 403 6 403	9 108 0 58 1 230	4194 4408 4409	108 720 720	CA3080E CA3081 CA3089E	80 190 210	ROM SG34 SN72	2513★ 02★ 2733N	700 255 125
TANTALUM BEAD	CAPAC	ITORS	POTENTIO	METERS IA	AB or EGEN!		7448 7450 7451	78 17 17	74162 11 74163 11 74164 12	6 403 6 403 1 403	2 100 3 145 4 196	4410 4411 4412F	720 995 1650	CA3090A CA3123E CA3130#	10 390 200 200 200	SN76 SN76 SN76	5003N 5013N 5023N	211 140 140
$10 \ 2 \ 2_{\mu}F \ 3 \ 3 \ 47 \ 6$ $20V: \ 1.5 \ 16V: \ 10$ $47 \ 100 \ 40n \ 10V: \ 2$	58 25V: 0µF 13p 2µF 33	15 10 each 47 6V	500Ω, 1K & 5KΩ-2MΩ sii 5KΩ 2MΩ sii	2K (lin oniy). 19le gang 19le gang D/F	Single 26p 26p	LEDs - chp TIL209 Red 13 TIL211 Grn 24	7453 7454 7460 7470	17	74165 13 74166 14 74167 19 74170 24	403 1 403 8 403	5 120 6 325 7 100	4412F 4415F 4415V	1380 795 795	ICL7106E ICM7205	55 Ev★ 975 5★ 1150 170	SN76 SN76	5033N 5115N 5227N	211 215 175
47, 68, 100, 3V: 68, 1	00µF. 20	p each	5K0 2M0 d	ual gang ster	eo 70p	Tit212 Yellow 27 7110 20 2" Red 17	7472 7473 7474	28 32 32	74172 62 74173 17 74173 17 74174 11	5 403 5 403 5 404 3 404	9 320 0 105	4419 4422 4433 4435	545 1225 825	LM301A LM308 LM318	39 140 195	TAAC TAAC TAA7	61A 00	155 353 300
0 015 0 02 0 04 0 05 0 1, F 0 15 0 2 7p. 5	005001µ 0056µF 0V 047µ	и+ 5р 6р F 10р	0-25W log ar 5K() 500K() 10K() 500K	d linear value single gang O dual gang	15 60mm 70p 80p	2 Yellow Green 21 OCP70 40 ORP12 68	7475 747 6 7480	42 30 50	74175 9 74176 12 74177 11	0 404 0 404 6 404	2 81 3 96 4 95	4440 4450 4451	1275 295 295	LM 324 LM 339 LM 348	98 55 120	TBA1 TBA5 TBA5	20S 40Q 50Q	90 220 355
CERAMIC CAPACITO 0 5pF to 10nF 15nF 22nF 33nF 47nF	DRS: 50V	3p each 4p each	Self Stick Gra	duated Bezel	s 20p	7 Seg Oisplays TIL312 C An 3 125 TIL313 C Cth 3 125	7481 7482 7483	97 82 95	74178 10 74180 14 74181 29	2 404 2 404 9 404	5 145 6 130 7 99	4490F 4490V 4501	695 525 17	LM379 LM380 LM381	398 95 170	TBAG	41BX11 51 100	250 180 90
POLYSTYRENE CAPA 10pF to 1nF 6p: 15nF 1	ACITORS	Бр р.	Vertical & Hi 0.1W 50() 0.25W 1000	5MΩ Minia 5MΩ Minia = 3.3MΩ He	ture 8p priz 10p	11L321 C An 5 130 T1L322 C Cth 5 130 DL704 C Cth 99	7485 7486 7489	110 36 320	74182 12 74184 22 74185 14 74188 6	404 404 405 405	8 58 9 52 0 52 1 89	4502 4503 4506 4507	69 51 55	LM382 LM1459 LM3900	125 425 65	TBAS	20 200 022	80 350 595
SILVER MICA (Values 6 8 10 12 18 22 33 82 85 100 120 150 22	in pF) 3 47 50	3 4 7 68 25	RESISTORS	4 7ΜΩ Ve	nake 5% Carbon	DL707 C A 99 DL747 C A 180 FND 357 140 LCD 31 digit 975	7490 7491 7492	36 80 53	74190 14 74191 14 74192 14	405 405 405	2 89 3 89 4 120	4508 4510 4511	298 135 168	LM3909N LM3911 M252AA1	N 70 125 ★ 750	TDA2 UAA ZN41	020 70 4	320 198 110
250 300 330 360 39 0 1000 1 80 0, 2000, 22 00	600 820 1 2	6p each Op each	Miniature RAN W 2-20-4	High Stabil IGE VAL IM E24	199 100 . 15p 1p	LCO 4 digit 975	7493 TRA	35 NSI	5TORS	405	5 134	4512	98	M253AA1	* 795	ZN42	4	130
2 7pF 4 15pF 6-25pl	CAPACI F 8 30pF	TORS 20p	1W 2 20-4 1W 2 20 10 2% Metal File	M E12 M E12 n 10Ω-1ΜΩ	2p 1.5p 5p 4p 6p 4p	10GGLE 2A 250V SP51 26p DPD1 35p	AC117 AC125 AC126	35 19 19	BC168C BC169C BC170	12 14 13	BF195 BF196 BF197	10 12 15	OC43≢ OC44≢ OC45≢	65 35 35	ZTX501 ZTX502 ZTX503	15 2 19 2 19 2	N3614+ N3615+ N3663	169 135 26
2.5 6pF 3-10pF 10 40 5.25pF 5.45pF 60pF 8	pF 38pF	22p 30p	100 - price not mixed val	applies to Re ues	8p sistors of each type	4 pole on off 58p SUB-MIN TOGGLE SP changeover 59p	AC127 AC128 AC141	18 18 24	BC171 HC172 BC177 •	11 10 18	B£198 B£200 B£224A	18 30 18	0C46 + 0C70 + 0C71 +	35 30 30	ZTX504 ZTX531 ZTX550	25 2 28 2 25 2	N3702 N3703 N3704	10 11 10 e
COMPRESSION TRIN 3 40pF 10 80pF 25 1 100 500pF 1250pF	4 M E R S 190 p F	25p 38p	PE String Componen	ENSEMB ts now ava	LE ilable.	SPST on off 54p DPDT 6 tags 78p DPDT Centre off 92p	AC141 AC142 AC142 AC142	* 38 • 24 • 38 • 18	BC178★ 8C179★ BC182 BC182	16 18 10	BF256 + BF258 + BF258 +	50 29 29	0C72 + 0C77 + 0C79 +	30 76 76 28	2N526 + 2N696 + 2N697 + 2N698 +	58 2 35 2 21 2 39 2	N3705 N3706 N3707 N3708	11 10 10
JACK PLI	UGS Placti			SOCKET	S	SLIDE 250V: 1A DPD* 14p 1A DP crover 16p	AC187 AC188 ACY17	20 20 35	BC183 BC183L BC184	10 12 11	BF394 BF594 BF595	22 40 38	0C82 + 0C82D + 0C83 +	45 35 48	2N699 + 2N706A + 2N707 +	50 2 19 2 50 2	N3709 N3710 N3711	11 16 12
2.5mm 12p 3.5mm 15p	body 8p	~	metal 8p Bn	with break	couplers 11p 12p	A DPDT 13p A pole clover 24p PUSH BUTTON	ACY18 ACY19 ACY20	40 40 40	BC184L BC186 BC187 •	14 24 28	BFR39 BFR40 BFR79	25 28 28	0C84 + 0C123 + 0C139 +	44 115 140	2N708 + 2N914 + 2N916 +	19 2 32 2 27 2	N3715 + N3772 + N3773 +	25C 17C 288
MONO 23p STEREO 31p	15p 18p		13p 15p	20p 24p	18p 22p	Spring loaded SPST on/off 55p SPDT c/over 65p	ACY21 ACY22 ACY28 ACY39	35 40 40 78	BC212 BC2121 BC213 BC213	11 13 11	BFR80 8FX29+ 8FX81+ RFX84+	28 28 130 74	0C141 + 0C170 + 0C170 +	125 157 40	2N918+ 2N920+ 2N930+ 2N961+	30 2 51 2 18 2 61	N3819 N3820 N3823 •	22 38 65 70
DIN 2 PIN Loudspeaker	PLUGS	SOCKET	S In Line	SWITC Push to J	HES + Miniature N	on-Locking Push to Break 25n	ACY40 ACY44 AD140	48 39 69	BC214 BC214L BC307B	14 15 20	BFX85 + BFX86 + BFX87 +	28 28 23	0C201 + 0C202 + 0C203 +	125 140 150	2N1131 + 2N1132 + 2N1302 +	22 2 22 2 35 2	N3866 + N3903 N3904	90 20 18
3. 4. 5 Pin Audio CO-AXIAL (TV)	13p 14p	8p 14p	20p	ROCKE SP chan ROCKE	R (white) 10A 250V geover centre off R: (black) on/off 10A	28p 250v 23p	AD149 AD161 AD162	70 42 42	BC328 BC338 • BC441 •	15 15 45	BF X88 + BFY18 BFY50 +	26 50 17	OC204 + TIP29 TIP29A	150 43 44	2N1303 + 2N1304 + 2N1305 +	50 2 50 2 28 2	N3905 N3906 N4037 ◆	18 17 52
PHONO assorted colours Metal Screened	9p 12p	5p singl 8p doubl 10p 3-wa	• 15p	ROCKE Lights w ROTAF	R: Illuminated (white when on 3A 240V RY: (ADJUSTA8LE	52p STOPI 1 pole / 2-12	AF114 AF115 AF116 AF117	20	BC461 + BC462 + BC547 BC548	38 45 12	BFY51 + BFY52 + BFY71 BFY70	17 17 47	TIP29C TIP30 TIP30A	60 52 52	2N1306+ 2N1307+ 2N1308+ 2N1612+	35 2 50 2 46 2	N4058 N4061 N4064 ±	17 17 120
BANANA 4mm 2mm	10p 10p	12p 10p	=	ROTAR DIL SO	CKETS + (Low Profil	W 4p/2-3W 41p 4 Amp 42p e - Texas)	AF118 AF121 AF124	55 48 55	BC549C BC557 BC558	13 15 15	BSX20+ BSY65+ BSY954+	18 30 18	IP30C IP31 + TIP31A +	70 50 50	2N1671 + 2N16718 + 2N16718 +	190 2 195 2 28 2	14289 N4859 N5135	20 65 42
WANDER 3mm	8р 9р	8 p 9p	-	8 pin 1 20 pin 2	0 p; 14 pin 12 p; 16 6 p; 24 pin 30 p; 28 p	i pin 13p; 18 pin 20p; in 42p; 40 pin 58p.	AF125 AF127 AF139	35 35 35	8CY30+ 8CY34+ 8CY39+	57 75 78	BU105 BU205 BU208	140 190 225	TIP31B + TIP31C + TIP32 +	58 66 55	2N1986 2N2160★ 2N2217★	60 2 105 2 48 2	N5136 N5138 N5179◆	42 20 60
						DALO ETCH Resist Pen Späre Tip 75p+	AF180 AF180 AF186 AF239	70 70 50 42	BCY59 • BCY70 • BCY71 •	90 15 20	E5567 MD8001 • MJ400 • MJ400 •	65 158 90	TIP32A TIP32B TIP32C TIP33	60 75 77 85	2N2218A ± 2N2219A ± 2N2220A ± 2N2220A ±	35 2 22 2 26 2 25 2	N5180 N5191 N5305 N5357	60 65 40
RATTI F				RE	1222222	COPPER	ASY26 ASY27 BC107	40	BCY72 + BD115 + BD121 +	15 62 78	MJ2955 + MJE340 + MJE370 +	120 45 80	TIP338 + TIP33C + TIP34 +	100 110 95	2N2222 * 2N2297 2N2303 *	20 2 45 2 50 2	N5458 N5459 N5485	36 36 38
Build this for	ntastio	τ.	DIN.	(왕 주 ⁴) T 14 14		Fibre Glass Single Sided 5 x 5 75p	8C1078 8C108 8C1088	* 10 9 * 12	BD123 BD124 = BD131 =	98 115 38	MJE371 + MJE520 + MJE521 +	80 65 74	TIP34A • TIP34B • TIP35 •	95 120 219	2N2368 2N2369 + 2N2476	25 2 15 2 125 4	N5777 N6027 0311 +	56 40 56
GAME with rea	alistic t	oattie	GEN	ERAT	OR	6 x 12 130p S R B P 8 x 10 ¹ / ₄ 70p	BC1080 BC109 BC1098 BC1098	12 9 12	BD133+ BD135 BD136	43 38 36	MUE2955 MUE3055 MPF102 MPF102	80 36	TIP35C + TIP35C + TIP36A +	270 280 325	2N2483 2N2484 * 2N2646 * 2N2784	28 4 30 4 48 4 55 4	0313 + 0316 + 0317 + 0325 +	95 52
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HF 7948 FRONT END

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TECHNICAL CHARACTERISTICS:

Output terminal for digital frequency meter; Antenna impedance - 75 to 300 Ohms; Frequency ranges 87.5 to 104 MHz or to 108 MHz; Sensitivity - 0.9 uV 26dB signal to noise ratio + 75 kHz deviation; Inter-To holse ratio + 7.5 kHz deviation; inter-modulation 80dB Image rejection - 60dB; Tuning voltage - 1V to 11V; Total gain -33dB; Intermediate frequency - 10.7 MHz; Power supply voltage + 15V; Power con-sumption 15mA; Dimensions104 \times 50 mm.

TECHNOLOGY:

Double sided epoxy printed circuit board with plated through holes. Dual gate effect transistors; Silvered coils.

FI 2846



TECHNICAL CHARACTERISTICS:

TECHNICAL CHARACTERISTICS: Intermediate frequency – 10.7MHz. IF Bandwidth – 280kHz; Signal to noise ratio – 70dB with 1mV input; Distortion – mono 0.1%, stereo 0.3%; Sensitivity – 30uV up to the 3dB limit; Channel separation – 40dB at 1kHz; Pass band – 20 to 15,000Hz; Rejection at 38kHz greater than 55dB; Am rejection – 45dB; De-emphasis – 50 to 75µs; Pilot capture at 19kHz + 4%; Channel matching within less than 0.3dB; Output impedance – 100 Ohms; Output Output impedance - 100 Ohms; Output voltage – 500mV; Phase locked loop stereo decoder; Output for LED VU-meter; Null indicator; Output for LED VO-meter; Null indicator; Outputs for AGC AFC and inter-station muting; Consumption – 55mA LEDs extinguished; 100mA LEDs illuminated; Power supply – 15V; Dimensions 195 × 76mm.

CIRCUIT TECHNOLOGY

Epoxy printed circuit board; Monolithic integrated circuits; ceramic filter.

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COMPREHENSIVE RANGE

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£2.53 Inc. VAT P&P

TECHNICAL CHARACTERISTICS:

Output voltage - 15V; Max. output current - 500mA; Thermal coefficient less than 1mV/C; 15V power supply for modules HF 7948 and Fl 2846; Supply protected against short circuit (power and current protection); Dimensions – 65 × 55mm.

TECHNOLOGY:

Double sided epoxy circuit board; Monolithic integrated circuit.





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Introductory SPECIAL OFFER £2 OFF Kit

Smoother running Instant all-weather starting Continual peak performance Longer coil/battery/plug life Improved acceleration/top speeds Optimum fuel consumption

Sparkrite X4 is a high performance, high quality capacitive discharge, electronic ignition system in kit form. Tried, tested, proven, reliable and complete. It can be assembled in two or three hours and fitted in 1/3 mins. Because of the superb design of the Sparkrite circuit it completely eliminates problems of the contact breaker. There is no misfire due to contact breaker burnes with in all and the superbalance of problems of the contact breaker. There is no misfire due to contact breaker bounce which is eliminated electronically by a pulse suppression circuit which prevents the unit firing if the points bounce open at high R.P.M. Contact breaker burn is eliminated by reducing the current to about 1/50th of the norm. It will perform equally well with new, oid, or even badly pitted points and is not dependent upon the dwell time of the contact breakers for recharging the system. Sparkrite incorporates a short circuit protected inverter which eliminates the problems of SCR lock on and, therefore, eliminates the possibility of blowing the transistors or the SCR. (Most canactive discharge inpitting are not completely transistors or the SCR. proviews of SCh lock of and, therefore, eliminates the possibility of blowing the transistors or the SCR. (Most capacitive discharge ignitions are not completely foolproof in this respect). The circuit incorporates a voltage regulated output for greatly improved cold starting. The circuit includes built in static timing light, systems function light, and security changeover switch. All kits fit vehicles with coil/distributor ignition up to 8 cylinders.

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Die pressed epoxy coated case. Ready drilled, aluminium extruded base and heat sink, coil mounting clips, and accessories. Top quality 5 year guaranteed transformer and components, cables, connectors, P.C.B., nuts, bolts and silicon grease. Full instructions to assemble kit neg, or pos. earth and fully illustrated stallation instructions.

NOTE – Vehicles with current impulse tachometers (Smiths code on dial RV1) will require a tachometer pulse slave unit. Price £3.35 inc. VAT. post & packing Electronic Design Associates, 82 Bath Street, Walsall, WS1 3DE. Phone: (0922) 614791



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Switches any oppliance of up to 1kW on and off at preset times ance a day. KIT contains: AY-5-1230 Clock/Appliance Timer IC, 0.5" LED display, mains supply, display larivers, switches, LEDs, triac, complete with PCBs and full instructions. **E14.85** Special white box (56 x 131 x 71mm) with red Applications and the 20 at 20 Acrylic window - undrilled £2.38 drilled £2.70

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Set of basic component kits	from £62.23
Set of printed circuit boards	£9.71

P.E. SYNTHESISER (P.E. Feb. 73 to Feb. 74)

The well acclaimed and highly versatile large-scale mains-operated Sound Synthesiser complete with keyboard circuits. Other circuits in our lists may be used with the Synthesiser to good advantage. The Main Synthesiaer: PSU, 2 linear VCOs, 2 ramp generators, 2 input amps, sample hold, noise generator, reverb amp, ring modulator, peak level circuit, envelope shaper, voltage controlled amp. Set of basic component kits E83-03 Set of printed circuit boards £13-20 The Synthesiser Keyboard Circuits (can be used without the Main Synthesiser to make an independent musical instrument): 2 logarithmic VCOs, divider, 2 hold circuits, 2 modulation amps. mixer, 2 envelope shapers and PSU. Set of basic component kits Set of printed circuit boards £7.66 GUITAR EFFECTS PEDAL (P.E. July 75) Modulates the attack, decay and filter characteristics of an audio signal not only from a guitar but from any audio source. producing 8 different switchable effects that can be further modified by manual controls. Possibly the most interesting of all the low-priced sound effects units in our range. Circuit does not duplicate effects from the Guitar Overdrive Unit Component set with special foot operated switches £7-59 Alternative component set with panel switches Printed circuit board £4-98 £1-43 SOUND BENDER (P.E. May 74) A multi-purpose sound controller, the functions of which include envelope shaper, tremolo, voice-operated fader, automatic fader and frequency-doubler, Component set for above functions (excl. SWs) Printed circuit board Defined extra defunction Mediulter, the 11-42 E1-41 Component set (incl PCB) 22-88 PHASING UNIT (P.E. Sept. 73) A simple but effective manually controlled unit for introducing the phasing' sound into live or recorded music. Component set (incl. PCB) \$2.87
 PHASING CONTROL UNIT (P.E. Oct. 74)

 For use with the above Phasing Unit to automatically control the rate of phasing.

 Component set (incl. PCB)
 £4:48
 SOPHISTICATED PHASING AND VIBRATO UNIT A slightly modified version of the circuit published in "Elektor". December 1976, and includes manual and automatic control over the rate of phasing and vibrato. Component set Printed circuit board £17-89 62.33 WAH-WAH UNIT (P.E. Apr. 76) The Wah-Wah effect produced by this unit can be controlled manually or by the integral automatic controller. Component set (incl. PCB) £3-55 AUTOWAH UNIT (P.E. Mar. 77) Automatically produces Wah-pedal and Swell-pedal sounds each time a new note is played Component set, PCB, special foot switches 17-27 Component set and PCB, with panel switches 14-63 POST AND HANDLING U.K. orders-under £15 add 25p plus VAT, over £15 add 50p U.K. orders—under £15 add 25p plus VAT, over £15 add 50p plus VAT. Keyboards £2·00 plus VAT. Optional Insurance for compensation against loss o⊩ dam age in post, add extra 50p for cover up to £50, £1·00 for £100 cover, £2.00 for £200 cover. Eire, C.I., B.F.P.O., and other countries are subject to Export postage rates.

COMPONENTS SETS include all necessary resistors. capacitors, semiconductors, potentiometers and transformers. Hardware such as cases, sockets, knobs, keyboards, etc. are not included but most of these may be bought separately. Fuller details of kits, PCBs and parts are shown in our lists.

CIRCUIT AND LAYOUT DIAGRAMS are supplied free with all PCBs unless "as published".

PHOTOCOPIES of all P.E. texts for most of the kits are available prices in our lists.

P.E. JOANNA PLUS ORGAN VOICING

The basic five octave electronic piano (P.E. May/Sept 75 and Sound Design) has switchable alternative voicings for Honky-Tonk, ordinary piano, and Harpsichord or a mixture of any of these three, together with facilities including fast and slow tremolo, loud and soft pedal switching, and sustain pedal switching. The modification retains all the circuitry associated with the plano but in addition provides an organ-voice envelope facility with 5 switchable pitches, variable attack and sustain, phasing and

Set of components (excl switches) for PSU, Frequency generator, Pitch and Note Divider, Envelope Shapers, Voicings, and Control circultries. (Order as KIT 71-5) £109-75 Set of PCBs (Order as PCB SET 71-6) £29-18

SYNTHESISER TUNING INDICATOR (P.E. July 77) A simple 4-octave frequency comparator for use with synthesisers and other instruments where the full versatility of the P.E. Tuning Fork is not required.

Component and PCB (but excl sw.)

£7-45

GUITAR FREQUENCY DOUBLER (P.E. Aug. 77)

A modified and extended version of the circuit publish Component set and PCB £4-£4-22

GUITAR	SUS	TAIN (P	.E. Oct	77)
Mainteine	44-0	a a true a l	a tha a k	

Maintains	the	natural	attack	whilst	extending	note	duration.
Com por	nent	set, PCB	and for	t switcl	ies		£4-90
Compor	hent	set, PCB	and par	nel swit	ches		13-48

WIND AND RAIN UNIT

A manually controlled unit for producing the above-named sounds Component set (incl. PCB) \$3.72

GUITAR OVERDRIVE UNIT (P.E. Aug. 76)
Sophisticated, versatile Fuzz unit, including variable and
switchable controls affecting the fuzz quality whilst retainin
the attack and decay, and also providing filtering. Does no
duplicate the effects from the Guitar Effects Pedal and ca

be used with it and with other electronic	instruments.
Component set using dual slider pot	£6 · 86
Component set using dual rotary pot	£6·20
Printed circuit board	£1-62

FUZZ UNIT

- Simple Fuzz unit based upon P.E. "Sound Design" circuit Component set (incl. PCB) £2.05
- TREMOLO UNIT nemolu UNII ased upon P.E. "Sound Design" circuit. Component set (incl. PCB) E3-64

 TREBLE BODST UNIT (P.E. Apr. 76)

 Gives a much shriller quality to audio signals fed through it.

 The depth of boost is manually adjustable.

 Component set (incl. PCB)

P.E. TUNING FORK (P.E. Nov. 75)

Produces 84 switch-selected frequency-accurate tones. A LED monitor clearly displays all beat note adjustments. Ideal for tuning acoustic or electronic musical instruments. £15-59 Main component set (incl. PCB)

Power supply set (incl. PCB)

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An accented-beat electronic metronome, providir	g duple.							
triple and quadruple times with full control over the beat								
rate. Can also be used as a simple drum-bea	t rhythm							
generator. Includes power supply								
Component set (incl. loudspeaker)	£11-62							
Printed circuit board	£2 · 04							
TARE NOISE LIMITER								
TAPE NVIDE EIMITEN								
very effective circuit for reducing the hiss found in r	nost tape							
recordings. All kits include PCBs								
Standard tolerance set of components	£2 · 96							
Superior tolerance set of components	£3-76							
Regulated power supply (will drive 2 sets)	£4-69							

ENVELDPE SHAPER WITHOUT VCA (P.E. Oct. 75) Provides full manual control over attack, decay, sustain and release functions, and is for use with an existing voltage controlled amplifier. Component set (incl. PCB) E4-66

ENVELOPE SHAPER WITH VCA (P.E. Apr. 76)

This unit	has its o	own vo	ltage co	ontrolled	amplifie	r and	has full
manual	control	over	attäck,	decay.	sustain	and	release
Compo	onen tse	t (incl	PCB				F6 - 68

TRANSIENT GENERATOR (P.E. Apr 77) An envelope shaper, without VCA, having the usual attack, decay, sustain and release functions, and in addition it also provides a "Repeat Effect" enabling a synthesiser to be programmed to imitate such instruments as a mandolin or banjo

omponent set	£4·5
rinted circuit board	£1-8

WAVEFORM CONVERTER

Slightly modified from a circuit published in "Elektor". Converts a saw-tooth waveform into four different waveforms: sine-wave, mark-space saw-tooth, regular triangle form, and squarewave with an externally variable mark-space ratio. with an externally variable mark-space ratio Component set (incl. PCB but excl. sw/s) £8-19

VOLTAGE CONTROLLED FILTER (P.E. Dec. 74)

it for use with other synthesisers.	
Component set (incl PCB) (Order as Kit 65-1) E8·22
NING MODULATOR (P.E. Jan. 75) art of the P.E. Minisonic now released as an	independent
Component set (incl. PCB) (Order as Kit 59-1) £5·50

NOISE GENERATOR (P.E. Jan. 75) Part of the P.E. Minisonic now released as an independent kit for use with other synthesisers. Component set (incl. PCB) (Order as Kit 60-1) £3:35 ~

SOPHISTICATED POWER SUPPLIES

£7.03

A wide range of highly stabilised low noise power supply kits is available—details in our lists.

MICROPHONE PRE-AMP (P.E. Apr. 77; Component set (incl. PCB)	£3·78
VOICE OPERATED FADER (P.E. Oec. 73) For automatically reducing music volume 'talk-over'-particularly useful for Disco work	during or for
Component set (Incl. PCB)	£3 · 97

DYNAMIC RANGE LIMITER (P.E. Apr. 77) Automatically controls sound output to within a preset

Component set (incl. PCB) £4-58

EXPORT ORDERS are welcome, though we advise that a current copy of our list should be obtained before ordering as it also shows Export postage rates. All payments must be cash-with-order, in Sterling and preferably by International Money Order or through an English Bank. To obtain list send 50p.

MAIL ORDER AND C.W.O. ONLY SORRY BUT NO CALLERS PLEASE PHONOSONICS · DEPT. PE65 · 22 HIGH STREET · SIDCUP · KENT DA14 6EH

AND OTHER PROJECTS

PHOTOGRAPHS in this advertisement show two of our units containing some of the P.E. projects built from our kits and PCBs. The cases were built by ourselves and are not for sale, though a small selection of other cases is available.

LIST—Send stamped addressed envelope with all U.K. requests for free list giving fuller details of PCBs, kits and other components.

OVERSEAS enquiries for list Europe send 20p: other countries—send 50p.

KIMBER-ALLEN KEYBOARDS AND CONTACTS

Kimber-Allen Keyboards as required for many published circuits. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C, the keys are plastic, spring-loaded, fitted with actuators, and mounted on a robust aluminium frame \$25.50

4 Octave (49 notes) 5 Octave (61 notes)	£32-25 £39-75
Contact Assemblies (gold-clad wire) for use with the above key	boards (1 required for each
note): Type G I: Single-pole change-over	each 24p

The dat outline batt and Be and	
Type GB: 2 pairs of contacts, each pair normally open	each 27
Type GC: 3 pairs of contacts, each pair normally open	each 36
Type GE: 4 pairs of contacts, each pair normally open	each 45
Type GH: 5 pairs of contacts, each pair normally open	each 57
Turne APS: 3 pairs of contacts plus single-pole changeover	each 53

Printed Circuit Boards for use with CJ. GB and 4PS contacts (thus eliminating much interwiring) are available. Details in our lists.

	TRANSISTORS
RHYTHM GENERATOR	AC128
15-Rhythm Tempo, Timing and Logic control unit (excl. sw's	AC176
but incl. PCB) £12-90	BC108
10-Instrument Effects circuits £13-56	BC109
PCB for Effects circuits £4-25	BC109C10p
Power Supply Incl. PCB E12-00	BC18412p
128-NOTE TUNE-PROGRAMMABLE SEQUENCER	BC18725p
(PE Nov/Dec 77)	BC209C14p
Enables a voltage controlled synthesiser to automatically play	BC21315p
pre-programmed tunes of up to 32 pitches and 12B notes long.	BC262
Programs are keyboard initiated and note length and rhythmic	BC415
pattern are externally variable. (Please use order codes quoted in	BD131
brackets.)	BD132
Rower Supply (KIT 76-3) 6005	BF244A24p
Trigger Inverter and Alt, Output (KIT 76-2) £1-35	8SY95A
LED Counter (KIT 76-4) £2-45	MD8001172p
PCB (as published) for KITS 76-1 & 3 (PCB 76A) £2-61	0072
PCB for KITS 76-2 & 4 (PCB 76B) £2-64	RPY58A
PE STRING-ENSEMBLE (PE commencing Mar 78)	ZTX108 90
The new keyboard string-instrument somthesizer	ZTX30113p
Power Supply Basic component set £9-22	ZTX384164p
Tone Generators (incl. Test components) £14-93	2N221927p
PC8 for PSU and Tone Generator £3-40	2N2646
Details of further kits and PCBs in our list.	2N2905A
	2N2906
FURMANT SYNTHESISER (Elektor 1977/78)	2N3054
Very sophisticated music synthesiser for the advanced con-	2N3055
structor who puts performance before price. Details in our lists.	2N3704
3-CHANNEL SOUND-TO-LIGHT (P.E. Apr. 76)	2N381935p
A simple but effective sound-to-light controller capable of	2N3823E
operating 3 lamps each of approximately 700 watts. Includes	2N5459 45 p
power supply, thyristors, and by-pass switches.	
Component set (Incl. PCB) 211-30	INTEGRATED CIRCUITS
DISCOSTRORF (P.F. Nov. 76)	301 8-pin DIL 48p 318 8-pin DIL 230p
A-channel light-show controller giving a choice of sequential	320-15 58p
random, or full strobe mode of operation.	324 14-pin DIL 87p 341-15 195p
Sasic component set £18-19	709 8-pin DIL 48p
Printed circuit board £3-45	723 T05 108p
	726 TO5 980p
BIOLOGICAL AMPLIFIEN (F.C. Jan/reo. 73)	741 8-pin DIL 32p
Multi-function circuits that, with the use of other external	4024 14-pin DIL 481p
equipment, can serve as ne-detector, arpitaphone, cardiophone	4069 14-pin DIL 18p
Pre-Amp Module Components set (incl. PCB) £4-22	7805 TO220 205p
Basic Output Circuits-combined component set	7806 TO220 205p
with PCBs, for alphaphone, cardiophone, frequency	7812 TO220 205p
meter and visual feed-back lampdriver circuits. 10-03	7815 TO220 205p
Audio Amplimer Module Type PC7	AY10212 16-pin DIL 650p
	AY16721/6 195p
	CA3080 8-pin DIL 80p
10% DISCOUNT VOUCHER (PER5)	CA3084 14-pin DIL 209p
	MC3340 8-pin DIL 150p
TERMS: Correctly costed, C.W.O., U.K. orders over £40	MCM6810 24-pin DIL870p
goods value. Valid until end of month on cover of P.E. This yourcher must accompany order	STK025 5950
	TDA1022 16-pin DIL 672p
	ZN425E 16-pin DIL 375p
PRICES ARE CORRECT AT TIME OF PRESS. E. & O. E. DELIVERY SUBJECT TO AVAILABILITY.	PHONOSONICS

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2



High quality audio eq

STEREO FM TUNER Fitted with Phase Lock-loop Decoder

PRE-AMPLIFIER PA 100

- ☆ FET Input Stage
- ☆ VARI-CAP diode tuning
- ☆ Switched AFC

STEREO

- ☆ Multi turn pre-sets
- ☆ LED Stereo Indicator

Typical Specification: Sensitivity 3µ volts Stereo separation 30db Supply required 20-30v at 90 Ma max.

3. Magnetic P.U. 3mV

P.U. Input equalises to

R1AA curve within 1dB

from 20Hz to 20KHz.

Supply - 20 - 35V at 20mA.

299mm × 89mm × 35mm

into 50K ohms

Dimensions:

$\mathbf{f}_{22.30}^{\text{our price}}$

The 450 Tuner provides instant program selection at the touch of a button ensuring accurate tuning of 4 pre-selected stations, any of which may be altered as often as you choose, by simply changing the settings of the pre-set controls.

Used with your existing audio equipment or with the BI-KITS STEREO 30 or the MK60 Kit etc. Alternatively the PS12 can be used if no suitable supply is available, together with the Transformer T461.

The S450 is supplied fully built, tested and aligned. The unit is easily installed using the simple instructions supplied.



Max Heat Sink temp 90C. ● Frequency response 20Hz to 100kHz
 Distortion better than 0.1 at 1kHz ● Supply voltage 15-50v. ●
 Thermal Feedback ● Latest Design Improvements ● Load - 3, 4, 5 or 16ohms. ● Signal to noise ratio 80db ● Overall size 63mm.
 105mm. 13mm.

Especially designed to a strict specification. Only the finest components have been used and the latest solid-state circuitry incorporated in this powerful little amplifier which should satisfy the most critical A.F. enthusiast.

£4.55

£4.25

Stabilised Power Supply Type SPM80

SPM80 is especially designed to power 2 of the AL60 Amplifiers, up to 15 watts (r.m.s.) per channel simultaneously. With the addition of the Mains Transformer **BMT80**, the unit will provide outputs of up to 1.5A at 35V. Size: 63mm. 105mm. 30mm. Incorporating short circuit protection.

Input Voltage: 33-40 V.A.C. Output Voltage: 33V D.C. Nominal Output Current: 10mA-1.5 amps Overlead Current: 1.7 amps approx. Dimensions: 105mm x 63mm x 30mm Transformer BMT80:

£5.40 + 86p postage

A top quality stereo pre-amplifier and tone control unit. The six pushbutton selector switch provides a choice of inputs together with two really effective filters for high and low frequencies, plus tape output.

Frequency response + 1 dB 20Hz-20KHz

Sensitivity of inputs:

1. Tape input 100mV into 100K ohms 2. Radio Tuner 100mV into 100K ohms

P&P 45p panel and knobs. 1 Kit of parts to include on/off switch, neon indicator, stereo headphone sockets plus instruction booklet. COMPLETE PRICE £36.75 plus

TEAK 60 AUDIO KIT: Comprising: Teak veneered cabinet size $16\frac{3}{4}$ " x $11\frac{1}{2}$ " x $3\frac{3}{4}$ ", other parts include aluminium chassis, heatsink and front panel bracket plus back panel and appropriate sockets etc. KIT PRICE £13.25 plus 85p postage.



The Stereo 30 comprises a complete stereo pre-amplifier, power amplifiers and power supply. This, with only the addition of a transformer or overwind will produce a high quality audio unit suitable for use with a wide range of inputs i.e. high quality ceramic pick-up, stereo tuner, stereo tape deck, etc. Simple to install, capable of producing really first class results, this unit is supplied with full instructions, black front panel, knobs, mains switch, fuse and fuse holder and universal mounting brackets enabling it to be installed in a record plinth, cabinets of your own construction or the cabinet available. Ideal for the beginner or the advanced constructor who requires Hi-Fi performance with a minimum of **TRANSFORMER £3.25**

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Practical Electronics May 1978

uipment mono and other modules for Stereo

MPA

NOW BI-PAK BRINGS YOU The AL80 35^{RMS} power Amp!



+8% VAT

A High Fidelity Power Amplifier with a maximum Power Output of 35 watt R.M.S., which has a maximum operating voltage of 60v. A MUST for all HI-FI users.

Maximum supply voltage Power output for 2% THD Harmonic distortion Load impedance Input impedance Frequency response +3dB Sensitivity for 25 watt O/P Max. Heat sink temperature Dimensions Mounting Fuse requirements

15-60v35 watts R.M.S. 0.1% 3-8-16 ohm 50K ohm 20Hz-40KHz 280mV R.M.S. 90°C

102mm x 64mm x 15mm 2, 4BA fixing holes in heat sink 1.5A

Enjoy the quality of a magnetic cartridge with your existing ceramic equipment using the new BI-PAK M.P.A. 30 which is a high quality pre-amplifier enabling magnetic cartridges to be used where facilities exist for the use of ceramic cartridges only. Used in conjunction are 4 low noise high gain

silicon transistors. It is provided with a standard DIN input socket for ease of connection. Supplied with full, easy-tofollow instructions.

£2.95



10w R.M.S. AUDIO AMPLIFIER MODULE

The AL30A is a high quality audio amplifier module replacing our AL20 & 30. The versatility of its design makes it ideal for use in record players, tape recorders, stereo amps, cassette & cartridge players. A power supply is available comprising of PS12 together with a transformer T538, also for stereo, the pre

full output

2dB

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AND for those who need more amp PA12. P-O-W-E-R 125 RMS AL250 **ONLY £3.75 PA12 POWER AMP** Specially designed for use in-Disco Units, P.A. Systems, high power £7.10 Hi-Fi, Sound reinforcement systems SPECIFICATION: Output Power: 125 watt RMS Total harmonic distortion 50 watts into 4 ohms: 0.1% Continuous 50 watts into 8 ohms: 0.06% Operating voltage: 50-80 S/N ratio: better than 80dBs Loads: 4-16 ohms Damping factor, 8 ohms:65 Frequency response: 25Hz-Semiconductor complement: 13 20kHz Measured at 100 watts transistors 5 diodes Sensitivity for 100 watts output Overall size: Heatsink width at 1kHz: 450mV 190mm, length 205mm, height Input impedance: 33K ohms 40mm

8% VAT

SPECIFICATION:

- Output Power 10w Supply 22 to 32 volts R.M.S.
 Impedance 50K Input Impedance 50K
 - Load Impedance 8 to 16 ohms Sensitivity 90mV for ۰
 - Total Harmonic Distortion less than less than .5% (Typically .3%)
 - Frequency Response 60Hz to 25KHz Response Max Heat Sink Temp 80°C
 - Dimensions 90 x 64 x 27mm

NEW PA12 Stereo Pre-Amplifier completely redesigned for use with AL30A Amplifier Modules. Features include on/off volume. Balance, Bass and Treble controls. Complete with tape output. Frequency Response 20Hz-20HJ Response 20Hz-20KHz

tape output. Frequency Response 20Hz-2 (--3dB) Bass and Treble range ± 12dB input Impedence 1 meg ohm Input Sensitivity 300mV Supply requirements 24V. 5mA Size 152mm x 84mm x 33mm

ransformer T538 £3.20 Size 60mm x 43mm x 26mm.

Power supply for AL30A, PA12, S450 etc. Input voltage 15-20v A.C. Output voltage 22-30v D.C. Output Current 800 mA Max. £1.30



COMPONENT SHOP: 18 BALDOCK STREET, WARE.





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A high quality Cassette Tape Echo Unit giving long tape life, infinitely variable echo depth and speed control. Suitable for all mics. and instruments

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Make your own mixer - Mono/Stereo - up to 20 channels with these, easy to wire modules - Available as PCB's or assembled on panels.



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Four mixing inputs - 100W into 4 ohms Wide range bass & treble controls + master – Twin outputs

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Miniature transistorised f.m. front end with integral tuning gang 88-108MHz **£2.50**. New U.H.F. transistor TV tuners 4 pushbutton

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obrasive cleaner, 2 mini drill bits, etch	n tray and
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150 sg. in. fibre glass board	£2.00
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Miniature mains transformers, fully :	shrouded.
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100 mixed diodes IN4148, etc. **£1.20**. 100 mixed diodes including zener, power and bridge types £3.30. Bridge rypes £3.30. Bridge rectifier 100V 2.5 omp. 4 for £1.00. Brond new ITT 25kV T.V. triplers for Decca Brodford chossis £2.50.5 for £10.00.

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50 Germanium diodes ideal for crystal sets etc. £1.00.

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200 unmarked mixed transistors, interesting types including power. Send 60p for samples. £4.50. BR101 full spec. 5 for £1:00.

TBA 120A 50p eoch.

20 mm anii-surge fuses, your selection 800mA to 3.15A. 12 for £1.00. Full spec. I.R. diodes, 200V 1A. 20 for £1.00. 100 for £4.50. SN761J5N £1.00.

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40p P & P ON ALL ABOVE ITEMS. SEND CHEQUE OR POSTAL ORDER WITH ORDER TO SENTINEL SUPPLY, DEPT PE, 149A BROOKMILL RD., DEPTFORD SE8





EASY BUILD SPEAKER DIY KITS Specially designed by RT-VC for cost- conscious hi-fi enthusiasts, these kits incorporate two teak-simulate enclosures, two texts and a pair of matching crossover Supplied complete with an easy-to-follow circuit diagram, and crossover components. STERED PAIR Input 15 watts rms. 30 watts peak, each unit. + p & p f5.50 Cabine Isize 20' \times 11' \times 9 [°] / ₂ (approx). SPAERES AVAILABLE WITHOUT CABINETS. It's the units which we supply with the enclosures illustrated Sure 13' \times 8' (approx) wooler. [EMI, 2 [°] / ₂ app. f1700 per fweeter, and matching crossover components. Stere of the stars rms. 30 watts peak, + p & p f3.40	20 x 20 WATT STEREO AMPLIFIER Superb Viscount IV unit in teak-finished cabinet. Silver fascia with aluminium rotary controls and pushbuttons, red mains indicator and stereo jack (2.50 socket. Function switch for mic. magnetic and crystal pick-ups. tape. funer, and auxiliary Rear panel features two mains outlets. DIN speaker and input sockets, plus fuse. 20 + 20 watts rms. 40 + 40 watts peak. 30 x 30 WATT AMPLIFIER KIT Specially designed by RT-VC for the experienced constructor.	45 WATT MON DISCO AMP STOP & 0 (2,50) Size approx. 13 3^{+} 5 4^{+} \times 6 3^{+} 45 watts rms, 90 watts peak output. Big leatures include two disc inputs both for ceramic cartridges, tape input and microphone input. Level mixing controls fitted with integral push pull switches. Independent bass and treble controls and master volume. 70 & 100 WATT MOND DISCO AMP Size approx. 14' \times 4' \times 10 3^{+} Brushed aluminum
$\begin{array}{l} \textbf{CDMPACT FOR TOP VALUE These infinite baffle enclosures come to you ready mitred and professionally finished. Each cabinet measures approx. per stere o pair 12" x 9" x 5" deep, and is in wood simulate. Complete with two 8" (approx.) speakers for maximum power handling of 1 watts. 80. + p & p f 2.20 \\ \textbf{SPEAKERS Two models - Ouo 10. teak veneer. 12 watts rms, 24 watts, peak, 18\frac{1}{2}" x 13\frac{1}{2}" x 7\frac{1}{2}" (approx.)$	$\begin{array}{c} \mbox{complete in every detail. Same facilities as} \\ Viscount IV amplifier. 60 + 60 peak, p & p (2.50 for $200 $000 $100 $100 $100 $100 $100 100	fascia and rotary controls. Five vertical slide coptrols master volume. tape level, mic level, deck level, PLUS INTER DECK FADER for perfect graduated change from record deck No. 1 to No. 2, or vice versa. Pre fade level control 70 watt £57 (PFL) lets YOU hear next disc before fading ¹⁴⁰ watt beak it in. VU meter monitors output level. Dutput 100 watts RMS 200 watts peak. 100 watt £65
Use III. 20 watts ms. 40 watts peak. 27 \times 13 \times 14 \pm appx Due III b f 7 p 8 p 16 . Due III f 52 p 8 p f 7.50 DECCA 20 WATTS STERED SPEAKER stereo pair This matching loudspeaker system is hand made. kit comprises of two 8" diameter approx. base drive unit, with heavy die cast chassis laminated cones with rolled P.V.C. surrounds. two 3 \pm diameter approx. domed tweepers complete with classiver networks 80. f 4.00 p 8 p. f 2000	ADD-ON STEREO CASSETTE TAPE DECK KIT Designed for the experienced 0.1 Y. man. This kit comprises of a tape transport mechanism, ready built and tested record/leptay electronics with twin V. U. meters and level control for mating with mechanism. Specifications. Sensitivity – Mic. 0.85 mV ar 20K 0HMS: Din, 40mV	CHASSIS RECORD PLAYER DECKS Record changer with cue; stereo ceramic cartridge. p & p £2.00 BSR MP60 TYPE Single play record deck 12" × 8j" 12" × 8j"
PERSONAL SHOPPERS STEREO CASSETTE record /repair willy built P.C. boaid £195 Used without guarantee. (Ex Equipment.) AM. FM. TUNER P.C. 8. with Mullard L.P. 1185. 1185. 1181. modules. 100K Multiturn Varicap tuning pots. 6 for £100 PAIR STERED 8 WATT SPEAKERS 8' bass units with 3' approx. tweeters £95	$\label{eq:constraint} \begin{array}{c} \alpha \ 400k\ DMMS: \ 0utput - 300m\ VRMS\ per channel \ \ 1KH_2 \\ from 2K\ DMMS\ source: Cross Tatk - 30db. Tape Counter - 3 \ Digit- Resettable: frequency Response - 40H_2 - 8KH_2 \pm 6db. \\ Deck\ Motor - 9\ Volt\ DC\ with electronic speed regulations: \\ Key Functions - Record, Rewind, flow of the source of the $	$\begin{array}{c} \text{BSR automatic record player deck} \\ \text{cueing device and stereo ceramic head, } p \ p \ f \ 2.55 \ f \ 995 \\ \text{BSR MP 60 type, complete with magnetic cartridge, } \ f \ 29 \\ \text{diamond stylus, and de luxe plinth and cover.} \ p \ p \ f \ 4.50 \\ \text{Home 8 Track cartridge player This unit will match} \ f \ 1650 \\ \text{with the Viscount IV } \ 9^\times 8^\times \times 3\frac{1}{2}^\circ, \ p \ p \ f \ 2.50 \\ \end{array}$
$\label{eq:size16} \begin{array}{llllllllllllllllllllllllllllllllllll$	Credit card and Subject to availability. Price correct at 1.2.78 and Subject to change without notice	EASY TO BUILD RECORD PLAYER KIT for the D-I-Y man who requires a stereo unit at a budget price, comprising ready assembled stereo amp, module, Garard auto / manual deck with cueing device, pre-cut and finished cabinet work Dut- put 4 watts per channel, phones socket and record/replay socket. Without Speakers. £1795 p & p £2.00

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SPECIAL

HIS month we bring you yet another special issue-the third in as many months. We have enclosed a Guide to the Language of Microcomputers in this issue and feel sure that this will be of interest to virtually all readers. The booklet contains over 120 words allied to microcomputers and gives a concise meaning to each. Even those with little or no interest in the field at present will, we are sure, soon find it necessary to keep up with the language as these devices enter into our everyday lives from all directions at an increasing rate.

The next two issues of P.E. will also be rather special as they contain details and entry coupons for a "free entry" competition. This is special because it could result in financial backing for a reader's idea. Some inventive readers could thus be financially rewarded as well as win a couple-of-hundred pounds worth of oscilloscope.

This competition is the result of an exclusive arrangement between P.E. and a Venture Capitalist. The aim is to attract ideas that could be developed to form commercial products. The presenter of such an idea will be involved in the development and will reap the benefit in the form of a stake in any company set up to handle the product or in the form of a royalty or other payment.

The competition is also open to companies, who could thus win backing for their prototype designs. We think you are an inventive lot and remember -it is often the simple ideas that are the best ones. So put your thinking caps on and watch out for full details in the next two issues of P.E. only.

SAVING

In this day and age it seems that one thing is becoming more and more important-that is the best possible use of available finances. For the electronics man this often means the construction of a piece of equipment rather than its purchase and also means the use of electronics to save energy by providing automatic control of such things as central heating, etc.

It is probably true to say that it is now easier to get into electronic construction than ever before-the tools for the job are readily available as are the necessary components. Most construction can be carried out with the

minimum of metal bashing and the use of readily available plastic cases, knobs and finishing materials can result in a very professional end product.

So, all those regular readers who enjoy the theory, or just reading all about it, perhaps now is the time to get stuck in! If you want further encouragement there's an item under Strictly Instrumental that tells you how to save about £4,500!

SUBSCRIPTIONS

As some alert readers may have noticed we have recently reinstated the subscription service for P.E. Although this may not interest many "home" readers, we know that a large number of overseas readers have problems in getting regular issues. This should no longer be the case as you can now have them posted to you each month with the minimum of fuss-see the foot of this page for details.

The subscription service is of course also available to British readers; if you have problems getting issues this service should guarantee you a copy. The next best thing is to place an order with your local newsagent.

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Back Numbers and Binders

Copies of most of our recent issues are available from: Post Sales Department, IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SE1 OPF, at 65p each including Inland/Overseas p & p.

Binders for PE are available from the same address at £2.85 each to UK addresses, £3.45 overseas, including postage and packing, and VAT where appropriate. Orders should state the year and volume required.

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made payable to IPC Magazines Limited.

Letters

Queries regarding articles published in PE should be addressed to the Editor, at the Editorial Offices, and a stamped, addressed envelope enclosed. We cannot undertake to answer questions regarding other items, nor to answer technical queries over the telephone.



For some years advertising signs have used strings of lights wired up and controlled in such a way as to make lights appear to move in a continuous movement along the string. Such effects are also fairly popular as backgrounds in television shows and on the stage. Smaller units are also used to create the same effects in discos.

The device to be described is designed to provide the same effect using either three strings of low voltage pygmy lights or up to a 3kW total power loading of mains voltage bulbs wired in three circuits.

The movement effect is obtained by wiring the lamps as three circuits so spaced out that every third bulb in the series of bulbs is wired to the same circuit. The three circuits are switched cn and off in sequence and the effect created is that of a moving point of light if one circuit is on at a time and the other two off.

If the circuits are so arranged that two circuits are on and one is off at any one time the effect is the opposite in that a moving gap in a string of lights is created.

Here, both possibilities can be obtained, depending on the position of a selector switch. A sprint facility is included which, if switched into operation, will make the speed of change of the output circuits increase for a short period at regular intervals in the cycle.

POWER SUPPLIES

Two voltages are required to power the circuitry; + 5V stabilised which is used to power the TTL circuits and the lamp changing multivibrator and + 12V which is used to power the relay driver circuitry and to provide part of the output for the sprint circuit.

The power supply is shown at the left of Fig. 1. The two voltages are derived from the secondary of a single 9V transformer the output of which is rectified by a bridge rectifier and smoothed.

This circuit gives 12V, the negative connection of which is grounded. The +5V stabilised supply is derived from the smoothed 12V rail by IC1. This device is a 100mA stabiliser mounted in a TO92 case and only requires the addition of C2, C3 and R1 to form a complete stabilisation circuit.

TR1, TR2, C4, C5, R2, R3, R4, R5 and VR1 form a conventional multivibrator circuit. The speed of the lamp change is governed by the frequency of the output, which is set by VR1.

SELECTION AND OUTPUT CIRCUITS

The output sequence is obtained by counting the output pulses from the multivibrator and decoding the numerical sequence obtained to switch the lamp circuits.

This circuit uses TTL logic and is shown in Fig. 2.

In this an input to a gate is logic 1 when a voltage of more than $2 \cdot 4V$ is present and at logic 0 when less than $0 \cdot 4V$ is present. As most logic circuits require a clean transition between these two states the output from the multivibrator circuit is cleaned up by means of a Schmitt trigger which gives a clean square waveform.

This is achieved by using half of a 7413 (IC2a).

As any unused TTL input automatically floats to the logic 1 condition it is customary to tie any unused input to a used input on the same gate so the used inputs of IC2a are connected to the output of the multivibrator.

The square wave output from IC2a passes to IC3 which is a 7492 divide-by-twelve counter. The truth table for this is given in Table 1.

Table 1

0	Output				
(Pin 14)	D (Pin 8)	C (Pin 9)	B (Pin 11)	A (Pin 12)	
0	0	0	0	0	
1	0	0	0	1	
2	0	0	-1	0	
3	0	0	1	1	
4	0	1	0	0	
5	0	1	0	1	
6	1	0	0	0	
7	1 -	0	0	1	
8		0	1	0	
9	1	0	1	1	
10	1	1	0	0	
11	1	1	0	1	
12	1	1	1	1	

After 12 the counter resets to 0 and runs through the sequence again.

Outputs A and B are used to drive relays 1 and 2 respectively each of which controls one circuit of lamps. The third circuit of lamps needs to be on when neither



Fig. 1. Power supply and lamp changing multivibrator

A or B is at logic 1. This would be most easily obtained by using a NOR gate, but to do so would require an additional i.c. To implement this an alternative circuit using three inverters from unused gates are used.

A two input NAND gate has a truth table as shown in Table 2.

Table 2

В	Α	Output
0	0	1
1	0	1
0	1	1
1	1	0

Although this circuit gives us the required output at logic 1 when both A and B are at logic 0, it gives us the same output when either of the inputs are at logic 0 and the other is at logic 1. However, we can make use of the fact that the gate gives us an output of logic 0 when both of the inputs are at logic 1. By inverting outputs A and B of the counter and connecting the inverted outputs to the inputs of the NAND gate we can obtain an output from the gate which changes from logic 1 to logic 0 when the counter outputs are both at logic 0. This output is the opposite of what we wanted but this can easily be rectified by inverting the output of the gate.

If we allowed the 7492 counter to continue further through its cycle after it had reached the count of two we would obtain a very strange light display and we therefore need to return the counter to zero when it would normally go onto a count of three. This is achieved by making use of the reset line connected to pins 6 and 7. When both of these pins are connected to logic 0 the circuit counts but if the reset pins are taken to logic 1 the counter resets all outputs to logic 0.

COMPONENTS					
Resisto R1 R2–8 R9 R10 ≟W	rs 4·7kΩ 2·2kΩ 470Ω 1kΩ 10% carbon				
Potenti VR1	ometer 5k Ω dual linear potentiometer				
Capacit C1 C2 C3 C4 C5 C6	tors 2,200μF 25V electrolytic 0·22μF 250V polyester 0·47μF 250V polyester 1,000μF 10V electrolytic 100μF 10V electrolytic 0·1μF disc ceramic				
Semico TR1-1 IC1 IC2 IC3, IC IC4 IC5 D1-7	Inductors 1 BC109 (11 off) 7805 regulator 7413 C6 7492 7404 7400 OA81 (7 off)				
Miscell T1 S2 S3 FS1 RLA- SK1	 aneous 240V primary 9V secondary 6VA D.p.s.t. rocker switch with neon indicator S.p.d.t. rocker switch S.p.s.t. rocker switch 10A fuse C 12V 110Ω. Contacts rated 10A, 240V according to requirement 5 way output socket with plug to match. Each pin rated 5A 240V 				
Double sided printed circuit board 140mm \times 102mm Metal case 204mm \times 152mm \times 76mm					



Fig. 2. Logic and drive circuit

A two input NAND gate, IC5b, is connected with its inputs to the outputs A and B of the counter. When both of these inputs are at logic 1 (when the counter goes to a count of three) the gate gives an output of logic 0; this is inverted so that the counter is reset to zero every time it goes to a count of three. As the reset only takes about 8ns the unwanted display never appears and the counter appears to count 0, 1, 2, 0, 1, 2...

SPRINT CIRCUIT

• A sprint circuit can be engaged so that the speed of the display will increase for eighteen counts and then continue at the set speed for a further eighteen counts before speeding up again.

The circuit takes an output from the reset circuit to a 7492 counter. The output of this counter at pin 8 (output D) goes to logic 1 for counts 6 to 12 and as the counter is incremented every time the first counter is reset this corresponds to 18 counts in all. This output is used to drive TR10 and TR11 which are arranged to form saturated transistor switches which short out the variable resistance VR1 of the multivibrator circuit.

When the collector voltage of the transistor is lower than the base voltage the transistor saturates and the emitter collector path behaves as a short circuit. R3 and R4 are therefore in effect connected to the +5V line and set the frequency of the multivibrator.

In order to operate the transistor switches the output voltage of the 7492 is used to lift the base voltage of the two transistors to be above the collector voltage. Unfortunately the logic 1 voltage of TTL gates is typically only +2.4Vand so voltage amplification is required. TR9, R9 and R10 form a simple voltage amplifier.

This single stage amplifier inverts the output from the 7492 in the process of amplifying the voltage and therefore the circuit sprints when the output of IC6 is at logic 0.

Switch S3 disengages the output of TR9 from the bases of TR10 and TR11 and when this switch is in the off position the sprint circuit is inoperative.

OUTPUT RELAY DRIVE

The use of s.c.r.s as the output elements of this device was considered but was abandoned in order to reduce the



Showing component assembly to front and back panels with p.c.b. and transformer which fixes to box base panel

Control panel layout



Fig. 3. Etching details shown full size for p.c.b.







Fig. 3. Wiring for a large display

cost of the device since relay drives are easily inverted to give the two light or one light circuit operative at a time.

The output from the TTL logic circuits is insufficient to drive the relays directly and the relays are driven by transistors connected in the standard Darlington pair configuration, controlled by the output from the TTL circuits. The drive circuits for each relay are identical.

In order to provide the option of one light circuit or two light circuits active at a time the relays are wired with



Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

CHAMPing at The Bit

Sir—I have read the first parts of the CHAMP project with great interest, but am somewhat reluctant to spend, to me, a fair amount of money on the system shown for the following reasons:

(1) The seemingly poor availability of the Intel devices, in fact, all the i.c.s not in the 74 range. I have noticed that in the past, a project has been published, and, next month, the retailers adverts are full of "complete kit of parts for . . . project" but not for CHAMP.

(2) The fact that the 4040 is a 4-bit device as opposed to the 8-bit devices of which there seem to be more types available at the moment. Coupled with this the 4040 is an "old" device—will there be spares available for this 4-bit device in the years (months?) to come amid all the parts for 8-bit and now 12-bit (16-bit? 20-bit?) devices on the market?

I am very keen to learn more about microprocessors and thought that CHAMP might provide a good start.

> P. C. Chamberlain, Anglesey.

Point 1:— The "chip kit" mentioned in part II of CHAMP contains all the necessary Intel i.c.s and is available for a very reasonable price. These i.c.s are also available separately from Rapid Recall, G.E.C. Semiconductors, Jermyn, and several other suppliers.

Point 2:- The Intel 4040 has been a very successful microprocessor, and has been incorporated in a very wide range of commercial equipment which guarantees its availability for many years to come. I would urge Mr Chamberlain not to be misled by current advertising which concerns the "latest and the greatest" microprocessor chips. It has been the trend to "push" these chips often before they are available in useful quantities, and this can cause frustration for intending users. I would also point out that to use a sixteen bit microprocessor for example, a considerable investment in equipment and software is necessary before anything like full use can be made of its admittedly powerful facilities. CHAMP does not, and will not, require teletypes, VDUs, bulk memory, assembler software or high level com-

the common terminal connected to the lamps and the normally closed contacts of each relay wired together. If two lamp circuits are required to be on at any one time and the third circuit off (giving a moving gap effect) the mains connection is made to the normally closed contacts of each relay so that the selection by the TTL logic circuits switches off the required circuit. If one light circuit is required to be on and the other two circuits off, giving a moving light effect, the mains is connected to the normally open contacts of the relays and the TTL logic circuits activate the relay to switch the required lamp on.

LIGHT DISPLAY

An easy way to obtain the necessary light display is to use three strings of low voltage bulbs connected with a common wire from each string to the neutral connection of the output socket and the live connection to the appropriate output pin of the output socket. The strings should be taped together so that the lamps of each string are interleaved to give the correct impression of movement. If a bigger display is required this could be obtained by wiring mains bulb holders together as shown in Fig. 3.

pilers for its effective employment.

T

The writer is currently using the 4040 in a number of applications which involve the control of printers, paper tape stations, and keyboards, all of which involve binary arithmetic, B.C.D. arithmetic and binary to B.C.D. conversion along with other tasks such as time measurement and analogue output. All these applications were programmed directly in hexadecimal and involve the simplest basic 4040 hardware. I have the highest regard for the more powerful microprocessors, but believe me, they are only worth having if you invest in a powerful memory, software, and input output environment to do them justice. R W Coles.

Quality

Sir—I feel that the quality of projects in P.E. is not as good as a year ago. For instance Dec. 1977 three out of four projects were continuations of projects from previous months. Jan. 1978 had six projects in, one of which was a continuation and another three were suitable only for (your usually excellent) *Ingenuity Unlimited!*

P.E. Champ is all very nice and educational but, at the cost indicated not very many people can afford to build it. For this reason I think that *Champ* should be a General Features/Project, rather than just a project. This would allow for four projects plus *P.E. Champ*.

R. A. Austin, Brentwood.

Although we endeavour to present a varied selection of constructional projects each month we cannot promise to please "all of the people all of the time". It is also necessary to give a good selection of additional news and features as we are sure most readers would not like a purely constructional magazine.—Ed.



There is a case on record of the businessman who visited his doctor, to be told that he was suffering from strain, hypertension and was likely to have heart trouble. The treatment prescribed was not the usual course of drugs: instead, he was advised to buy an electronic organ. Perhaps a bit expensive, but good advice as a method of unwinding from the effects of the rat-race.

HATH CHARMS

It appears that the treatment worked, though learning to play from scratch demands a good deal of concentration and co-ordination of mind and limbs. For the perfectionist in particular, this will be hard going at first but before long the tiro can become completely absorbed. As a large percentage of organ sales are to first-time owners, manufacturers compete with each other to offer aids to the beginner-from a special pedal Legato to illuminated keys. Many of these features can also be put to good use by the skilled player, Arpeggiators and A.O.C. as two examples. Aside from organ features, specialist magazines and sheet music scored for electronic keyboard instruments are more common than hitherto.

WERSI D.I.Y.

Klaus Wunderlich, who records for Telefunken, has many best selling records to his name. One of his more recent discs is "In the Miller Mood", played on the Wersi "Helios" organ and multi-tracked to obtain an orchestral effect (Selecta 6-23026AS). Though many of his past records were made using Hammond or Lowrey organs, he appears to be confining his attentions to Wersi at present and, although he would not claim to be an electronics engineer, I am certain that the Wersi design team have taken heed of his suggestions.

Since Wersi organs were mentioned in "Market Place" last October I have had a chance of hearing and examining a "Helios" W2T assembled from a kit. There is no doubt that this is an outstanding instrument, embodying the latest state-of-the-art circuitry. The Wersi concern is some eight years old, comprising an enthusiastic team of engineers and musicians: it provides both assembly kits and the complete instrument.

Readers of practical magazines need no reminder that it is usually cheaper to construct than buy a ready-made product. Organ building is one of the more complex projects, especially where the shopping list is concerned, so that buying a kit really makes sense. In the case of the "Helios" W2T the saving is very considerable as the finished product costs over £7,000, whilst the kit retails for about £2,500. If even the kit price sounds expensive, I would point out that this is a very comprehensive instrument and that Klaus Wunderlich's opinion of it may be verified by hearing the record previously mentioned.

Keying is fully electronic, the Transposer allows brilliant key changes while the special effects include auto-Wah, Repeat, Contracussion and Second Voice. There are three outstanding features of the W2T, in my opinion, "Wersivoice" is an electronic doppler-effect superior to any I have heard, based on several "bucket brigade" devices: two speeds at three intensities provide a perfect rotating baffle effect. "Wersidatal" is a system for programming preset sounds, each of the 20 programs having random access: thus, favourite combinations may be memorised and altered if superseded. Lastly, the "Wersimatic" rhythm unit is one of the best in its field. Based on 24 patterns and 15 instruments, it can produce alternating bars, stereo effects, drum breaks and has touch and automatic control.

Despite the complexity of the "Helios", it can easily be disassembled into three parts—top, base and chrome legs. Perhaps the only musical criticism is that the pedal section consists only of 13 notes, though a larger clavier is available in the "Zenith" series, if required. Potential constructors can obtain further information on the kits from Aura Sounds, Copthorne Bank, Crawley, West Sussex.

Building from a kit or published design has the advantage that the circuitry will have been proven beforehand, though the shopping list will call for careful planning in the latter case. By carefully following instructions and taking care to understand what is being assembled at each stage, this option should prove both successful and educational in familiarising the organist with the routing and processing of signals. Once the instrument is playable, he will not be deterred from changing component values-especially in the formant stages-to suit his taste and musical requirements. In this respect, a commercial organ under guarantee tends to preclude getting to work with an iron and sidecutters!

For most owners, the professionally built instrument will tend to be the natural choice but, because organs are happily obeying the cost of living graph, a great deal of thought is required before parting with one's money.

CAVEAT

The buyer must beware of himself, so se *ipsum caveat emptor* would be more appropriate! Whether a new or used model is in prospect, a few guidelines may not be out of place in this column.

The amount of money available will narrow the field and, having decided on what the bank balance can stand, a specification commensurate with the musical requirements should be aimed for. In this respect, salesmen are not only helpful but extremely honest as they know that you will recommend the firm if satisfied—or perhaps buy a more expensive model at a later date.

Arrange for a demonstration of the models within the range that can be afforded and, having heard them, ask for detailed brochures of those that lived up to expectations. Then take these home and study them carefully, with time no object: after all, having been without an instrument for years, what does another week matter? By all means take advice from a musical friend but remember that your money is involved and that the final decision lies with you. Look at the accompaniment manual in particular: the solo manual may offer a number of pitches, but the lower manual is often threadbare-perhaps one pitch only-and could begin to sound monotonous. Check the dimensions of the instrument against the available room space and confer with the "household management" as to the appearance of the cabinet as a piece of furniture.

Those who are classically inclined will look for fewer trimmings and more "straight" organ in the specification. A spinet organ, with short manuals and 13 stub pedals, will not suit anyone aspiring to Widor and Bach.

N order to provide the apparent multiplicity of sound sources, producing similarly pitched notes, required to simulate the orchestral string section effect, a Chorus Generation System is described which relies on the use of bucket brigade analogue delay lines for its fundamental operation.

CHORUS GENERATION SYSTEM

The system is shown in block schematic form in Figure 3.1. For reasons described later the input is processed by a low pass filter thus reducing the maximum frequency fed into the delay lines. Two active low pass filters follow each of the delay lines to complete the signal paths, extracting the clock frequency break through, and reconstituting the sampled waveform.

The two-phase clock circuits produce square waves in anti-phase from v.c.o.s A and B (see inset example of waveforms associated with Delay Line A), and both v.c.o.s are controlled in frequency by the combination of slow and fast modulator outputs which have been amplified to a suitable drive level. The two v.c.o.s work in an opposing manner in that as the control voltage increases, the frequency for one v.c.o. increases whilst the frequency for the other v.c.o. decreases, and vice versa.

Since the delay in each line is inversely proportional to the clock frequency, the delay in one line reduces whilst the delay in the second line increases and vice versa. When the control voltage is in the mid position the two delay lines have equal delays of approximately 3.5ms, varying between approximately 2.5ms and 5ms at the extremes.

Assuming that a single tone frequency is fed into the input of the chorus generator whilst one delay line is at maximum and the other at minimum delay, then two separate sounds will be produced at the generator outputs. This would be unlikely to be noticeable, but if the length of the two lines is slowly changed towards the opposite extremes then the phase relationship of the two sounds will be changing which will then be detectable.

SEMBL

Á.J. BOOTHMAN B.Sc.

Part 3

Dependent upon the input frequency the phase relationship between the two sounds may pass through a cancellation point ($180^\circ \times$ odd number) or be additive ($180^\circ \times$ even number), and with a number of input frequencies present a phasing effect, sweeping through the frequency range is obtained. Superimposing a faster modulation on the v.c.o. control voltage enhances the multiple image, causing relatively rapid changes in phase relationship which when combined with the slow sweep give a complex pattern of relative phase simulating more than two sources and resulting in a rich chorus sound. The sweep rates of the slow and fast modulators are approximately 0.5Hz and 10Hz respectively.

BUCKET BRIGADE

The term "Bucket Brigade Delay Line", is derived from the anology of a number of people, each with a bucket, forming a chain along which it is desired to transmit water.



Fig. 3.1. Schematic of Chorus Generation system



Fig. 3.2. Circuit of TDA1022 analogue delay line

Assuming that the first person has a full bucket, and all others are empty, it is possible to pour the water from bucket one into bucket two, then bucket two into bucket three, and so on until eventually all the water from the first bucket, excluding spillage, is transferred to the last bucket.

This description infers a delay which is dependent on both the speed at which each person reacts in filling his neighbour's bucket, and the number of buckets in the chain. In the String Ensemble it is fundamental that this delay line is controllable at will, and since the number of buckets. or stages in the device, is constant it is necessary to instruct each person how quickly to react before pouring the contents of their bucket into the next bucket, thus controlling the overall delay. The reaction time is quoted since in the electronic version the speed of pouring is very high such that variation in the "stage delay" is controlled by introducing a pause before the instruction to pour. Electronically the pause is created by an instruction to pour constituting the leading edge of a square wave which is known as the "clock". An increase in clock frequency corresponds to shouting "pour" at greater frequency thus shortening the stage and overall delay.

Carrying the analogy further, two instructions are used, which equate to two clocks, where one can visualise one male and one female instructor each instructing persons of their own sex in a line where the sexes are alternated. This is only a matter of electronic convenience and in future generations of bucket brigade delay line i.c.s one can expect that the required conversion from a single clock will be carried out in the same package.

ANALOGUE DELAY

The system described above can be digital or analogue, in the first case either full or empty buckets would always be concerned and in the second case the amount of water in the last bucket would directly relate to the amount contained in the first bucket as it commenced its journey.

In order to fully understand the electronic analogue delay line, sometimes called the analogue shift register, an alternative method of operation within our chain of bucket carriers and water pourers should be considered. Since we are not concerned with the actual transfer of water along the chain, but simply require to know how much water was in the first bucket when the chain commenced its sequence of operation, we can start with all buckets full apart from the first one which will be filled to the amount (analogue) of interest. On the first instruction (leading edge of Clock 1) the first person (male) puts the required amount of water into his bucket, which is equivalent to the level of the input signal at that moment, and on the second instruction the second person (female) fills up the first bucket leaving her with the same quantity of water previously contained in the first bucket. On the next instruction the third person fills up the second bucket and this continues down the line until the last bucket contains the same quantity of water as was present in the first bucket at the commencement of the sequence.

This can of course be a continuous process such that whilst the third person is topping up the second bucket, the first person is correcting the quantity in his bucket to match the new analogue or signal level.

BUCKET BRIGADE DEVICES

Many of the earliest instruments incorporating analogue delay line i.c.s used an ITT device, the TGA350, which contains 185 stages of delay in the package, but since that time Reticon, Matsuchita and Phillips (Signetics) have produced devices in various configurations ranging from a single 512 stage line in a package to 2×512 stage lines, tapped lines, and now rumours of considerably longer lines in a package. The potential application for A.D.L.s are numerous including echo, reverberation, double tracking, flanging and phasing, vibrato, chorus generation, speech delay matching in P.A., signal scrambling, time compression, pseudo-stereo, voice threshold switching and test equipment circuitry particularly associated with oscilloscope storage displays.

THE CHORUS MODE

Circuits have been proposed in which chorus is achieved by mixing a direct signal with the output of one delay line and the output of a second line fed from the first, both lines using the same changing clock frequency, but for the greatest effect the outputs from two or more lines should be mixed using clock frequencies modulated in an out of phase relationship—e.g. 180° for two lines, 120° for three lines. In practice this poses a problem for dual packaged lines in that on-chip intermodulation occurs in the form of both audio frequency tones and high noise. It is therefore necessary to use a separate package for each line, although noise advantages can be gained by using parallel dual lines in each position providing only one clock frequency is fed to the package.

FREQUENCY CONSIDERATIONS

The bucket brigade principle described earlier relies on sampling the input waveform at discrete moments in time, and since a bucket cannot be involved in both filling and emptying operations at the same time, Bucket 1 must wait for the transaction between Buckets 2 and 3 to be completed before it can again be involved with Bucket 2, and half the information from the input is automatically lost. This imposes a relationship between the bandwidth (DC to maximum input frequency) and the clock frequency, such that the input bandwidth should be limited to less than one-half of the clock frequency, and normally to less than one-third. The resulting sampled waveform at the output of the delay line requires heavy filtering to recover the original waveform and remove the clock frequency content.

TDA1022

The internal circuitry of the Signetics A.D.L. is shown in Fig. 3.2, using MOS technology f.e.t.s to switch the charge in the required manner between capacitors at each stage. The supply required is a nominal -15 volts, and at the clock frequencies used in the Ensemble (50–100kHz), the average delay for the 512 stages totals approximately 3.5ms, and for a distortion level of less than $\frac{1}{2}$ per cent the input level can slightly exceed 2V r.m.s., with a band width of 12–15kHz, and attenuation through a line will be typically 4dB.

Fig. 3.3(a)-(h) indicate the operation of the delay line in conjunction with Fig. 3.2. Clocks 1 and 2 are in anti-phase, odd number stages linked to Clock 2 and even number stages, together with the input gate, connected to Clock 1.

Taking a waveform as shown in Fig. 3.3(b), the voltage present whilst Clock 1 is up is transferred direct to C_0 in Fig. 3.2. When Clock 2 rises the charge in C_0 is topped up reducing the charge in C to that which was previously present on C_0 . Thus in Fig. 3.3(c) the voltage on C_0 rises to V and in Fig. 3.3(d) the voltage at the output of Stage 1 falls to the value at the input immediately prior to the rise of Clock 2. This situation now prevails until Clock 1 rises again at which time C is topped up reducing the charge on C_2 to that which was previously on C_1 .

With the rise of Clock 1 again C_0 continues to monitor the input voltage such that when Clock 2 rises again the new voltage level (second sample) at the input, immediately prior to the rise of Clock 2, is transferred to the output of Stage 1, whilst the voltage at the output of Stage 2, which is equal to the first sample, is transferred to the output of Stage 3.

Thus it can be seen that the time taken for each sample to move from one stage to the next is half a clock period, and input samples are taken once per clock cycle with the input blocked for one half of each clock cycle.

When stage 512 is reached, a further stage (513) is used to fill in the half of the clock cycle during which a sample has not been passed through the delay line giving the stepped waveform shown in Fig. 3.3(h) which is then filtered to reconstitute the input waveform.

CHORUS GENERATOR CIRCUITRY

The complete circuit of the Chorus Generator is given in Fig. 3.4. The bandwidth of the incoming signal is first limited by the low pass filter associated with IC19, and parallel connections taken to Channels A and B incorporating TDA1022 delay lines IC25 and IC26 respectively. The delay lines in each channel are followed by two low pass active filters based on IC20 and IC21 in Channel A and IC22 and IC23 in Channel B.

Clock frequencies are generated in IC28 and IC30 for Channel A and IC29 and IC31 for Channel B using the conventional v.c.o. configuration based on the CMOS 4007. In Channel A the variable resistance with voltage of the n-channel f.e.t. (pins 3, 4 and 5) is used to control the frequency of the oscillator comprising two gates of IC28 by virtue of its effect on the value of R59 which in combination with C45 determines its frequency of operation.

Two gates within IC30 are used to shape the waveform and produce an inverted version for the second phase of the clock.

In Channel B the *p*-channel f.e.t. (pins 1, 2 and 3) is used to control the oscillator comprising two gates in IC29, such that for the same modulation waveforms as pin 3 of IC30 and IC31, the oscillators work in anti-phase with respect to frequency variation.

Some gate wastage occurs in IC28 and IC29 due to the necessity to provide good decoupling of clock frequencies between the two channels, without which clock intermodulation would occur leading to a high noise level and swept audio frequencies at the output of the delay lines.

The modulation signals at pin 3 of the 4007's are generated by IC27 and amplified by IC24. IC27 is connected as two oscillators, similar to the clock oscillation but without voltage control, one operating at approximately 0.5Hz and the other at approximately 10Hz.

Filters, consisting of R47, C36, R48 and C37 for the slow modulator and R52, C40, R53 and C41, provide smooth modulation waveforms the level of which is controlled by VR9 and VR10 for slow and fast modulations respectively.



Fig. 3.3. Waveshapes showing operation of delay line

650







COMPONENTS . . .

CHORUS GENERATOR

Posistors						
P15	22k ()	R39	15kO	R49	10k Q	
D16	10040	P33	140	R50	1.9MO	
D17	120K32	P24	17k0	R51	110	
D10	1540	D25	12040	R52_54	1740	
RIO D10	10832	N35	120834	DEE	1040	
RI9 Doo	1 1 52	R30-37	ZZKSZ	DEC	100.52	
R20	47K32	R38	08K32	R30	1 IVI 52	
R21	120k12	R39	180K12	R5/	47832	
R22-23	22kΩ	R40-41	5.6K12	R58	120832	
R24	68k12	R42	10k12	R59	TUKI	
R25	180k Ω	R43	5.6K12	R60	4/K12	
R26-27	5.6kΩ	R44	22kΩ	R61	120k \$2	
R28	$10k\Omega$	R45	1·2MΩ	R62	10kΩ	
R29	5-6kΩ	R46	820k Ω	R63-64	220Ω	
R30-31	$22k\Omega$	R47–48	47kΩ	R65-67	1kΩ	
att 5%	% carbo	n film		R68	470Ω	
C						
Capacitor	'S	E walkend				
C14	0.224	F polyes	ter trolutio			
C15	4.1µF V electrolytic					
C16	100pF polystyrene					
C17	470pF ceramic					
C18	0·1µH	- polyeste	er			
C19	220p	F polysty	rene			
C20	4·7μ	16V elec	ctrolytic			
C21	47pF polystyrene					
C22	2·2n	ceramic				
C23	4.7µF 16V electrolytic					
C24	2.2nF ceramic					
C25	0.22µF polyester					
C26	470pF ceramic					
C27	0.1µF polyester					
C28	220pF polystyrene					
C29	4.7µF 16V electrolytic					
C30	47pF polystyrene					
C31	2.2nF ceramic					
C32	4.7#F 16V electrolytic					
C33	2.2nF ceramic					
C34	0.22µF polvester					
C35	2µF non polarised					
C36-38	47 <i>u</i> F 16V electrolytic					
C39	0.1#E polvester					
C40-43	4.7μ F 16V electrolytic					
C44	10nF ceramic					
C45-46	470nF ceramic					
C47-49	100#F 16V electrolytic					
C50	10nF ceramic					
C51	100% F 16V electrolytic					
C52	10nE ceramic					
C53	100%F 16V electrolytic					
C54 55	10nE coramic					
004-00	TOTIF	ceramic				

Potentiometers

47kΩ presets 100mW subminiature VR8-11

Diodes

D31 12 volt 300mW Zener

Integrated Circuits

IC19-24	741
IC25-26	TDA1022
IC27-29	4011
IC30-31	4007

Miscellaneous

1 Printed circuit board; 2-16 lead d.i.l. sockets; -14 lead d.i.l. sockets; 5 terminal pins



Photo of Chorus Generation board

SETTING UP THE CHORUS GENERATOR

VR8 provides a d.c. control to the input filter which sets the input bias on both delay lines. This preset potentiometer should be adjusted such that with a signal present at the input, the combined A+B outputs will move from zero, through a distorted period, through a clear range, a further distorted period and back to zero. VR8 should be finally set for the centre of the clear transmission range to give maximum signal handling capacity for the delay lines.

With VR9 and VR10 at minimum, VR11 adjusts the centre frequency of the two v.c.o.s to approximately the same value. This is achieved by initially setting VR11 near its midpoint and VR9 slowly increased. The combined A and B output signals should be subject to a phasing effect with a smooth sweep and sweep turn-around characteristic. If the sweep appears to pause at one end, VR11 should be adjusted to recover the even sweep. VR9 should then be reduced and VR10 increased to mix in the fast modulator, the levels of both being adjusted to taste.

All the adjustments associated with the clock modulation are slow to take effect due to the long time constants associated with the slow modulator filters. This time constant also produces a turn on delay of a few seconds, before the chorus modulation commences, after switching on the instrument. Rapid adjustment will stop the chorus modulation which will then recover after a few seconds.

CHORUS GENERATOR CONSTRUCTION

All the chorus generation circuits are mounted on a single printed circuit board, the etching and drilling details of which are given in Fig. 3.5 with the component assembly details in Fig. 3.6.

To assemble the board the previously recommended order of terminals, pins, resistors, Zener (D31), i.c. sockets, preset potentiometers, small capacitors, and finally large capacitors may be used. Sockets are recommended for the 14 and 16 lead i.c.s which are all of Mos type and therefore sensitive to handling, but these are not necessary for the 741 type i.c.s.

Careful attention should be paid to correct orientation of theics

Note-the track cutting amendment given finally last month refers to IC3

NEXT MONTH: Voice/preamp board construction





Copies of Patents can be obtained from : the Patent Office Sales, St. Mary Cray, Orpington, Kent Pri-

Price 95p each

SOUND CONTROL BP 1 476 516

Patents continue to give an interesting insight into the areas under research by Sony in Tokyo. In BP 1 476 516, Sony describe how a.g.c. circuits in a stereo amplifier must act on both channels if image swing from left to right, through one channel level dipping in volume while the other remains untouched, is to be avoided.

However, there is a difficulty with this approach, because a high level transient in one channel can dip the level of both channels and in so doing push quiet sounds down to inaudible levels. Sony have patented a circuit which is claimed to overcome this problem. The block diagram, Fig. 1,



Fig. |

shows left channel amplifiers feeding detector and filter circuits to produce gain control signals. Diode D1 connects the output of the detector circuit 2 to the base of gain control transistor TR1. A right channel is arranged in corresponding fashion, with the output of detector circuit 4 connected to gain control transistor TR2 via D4. There is also, however, a crossfeed of the outputs of detector circuits 1 and 3 between channels, via further blend diodes, D2 and D3.

In action, control signals of high amplitude applied to the base of the gain control transistors reduce the collector-emitter impedance and thus reduce the gain of the signals passing between the amplifiers. The attack time of both detector circuits 2 and 4 is made small (around 0.1 second) and their recovery time is made large (between 20 and 30 seconds). Thus the circuits are capable of passing relatively short transient signals which control the gain of their own respective left and right channels.

The attack time of the cross-channel circuits 1 and 3 is however approximately 5 second, i.e. substantially longer than the attack time of circuits 2 and 4. The recovery time of the cross-channel circuits is also much shorter, for instance, around 5 seconds.

When a transient of high amplitude is applied to one channel only, the a.g.c. function of that channel begins to operate in less than 0.1 second; but the gain control of the other channel transistor will not be affected. During normal operation, the a.g.c. functions of both channels are controlled by the average voltage. In this way there is overall a.g.c. and a transient peak in one channel should cause neither an image swing nor inordinate depression of the other channel's level.

HIGH VOLTAGE SWITCH BP 1 486 804

Siemens AG, in BP 1 486 804, suggests a clever way of controlling high voltage remote switching, for instance in the order of 1MV, without corona discharge.

A series of thyristors, each suited to control a voltage of 1kV, are arranged in a circle. A second series of similar thyristors are arranged in a second, smaller circle, spaced from the first, so that the two circles lie as if on the exterior of an imaginary cone (see Fig. 1). To enable remote operation, the thyristors are associated with light or other electromagnetic radiation-sensitive circuitry. A radiation source, "s" is arranged at the focal point of a parabolic reflector "r", with a firing control signal "z" supplied to a radiation gener-ator "e". The emitted signal "h" is beamed towards the two rings of thyristors, which by virtue of their arrangement (as if on an imaginary cone) all receive similar amounts of radiation, with no one thyristor and its sensor shielding another.

The individual thyristors are connected in series with each thyristor switching the next-higher potential, so that in the first circle t1 switches 1kV, t2 switches 2kV relative to 1kV, and so on, up to t8, which switches 8kV relative to 7kV. A similar arrangement is obtained in the lower circle of thyristors, so that there is between two adjacent group planes no greater potential than 8kV, and between the neighbours of a group plane no more than 1kV. In this way corona discharge and flashover are avoided, with the single safe firing signal "z" remotely switching a voltage of level governed only by the number of thyristors placed on the notional cone.



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from 300 F to 3,000 μ F on a linear scale, due to integrator circuit operating techniques. An absolute accuracy ± 2.5 per cent can be attained if suitable components are used, and for matching, comparative measurements can be made to an accuracy of as little as +0.25 per cent.

Pottery is now a popular hobby and many amateur potters, schools, etc. possess small kilns. Most kilns are only supplied with an indicating pyrometer and have no temperature control; this can be provided by our project, described next month.







Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

TUNE SEQUENCER

To the numerous electronics kits for the home constructor available from **Phonosonics**, has been added the 128 Note Sequencer of *P.E.* November and December 77.

This programmable sequencer was designed with the *P.E.* Minisonic in mind, for which Phonosonics can provide the various kits, and Kimber-Allen Keyboard.

A kit comprising components for the Sequencer Main Board (Kit 76-1) is now available. Control switches and p.c.b.s are not included. Kit 76-2 provides the components for three "Optional Trigger Inverters" and one "Alternative Output Circuit".

The same company are also able to supply kits for the P.E. String Ensemble.

Details of the above component kits, p.c.b.s, and prices can be obtained by sending a s.a.e. to **Phonosonics**, 22 High Street, Sidcup, Kent, DA14 6EH.

TOMORROW'S TECHNICAL LANGUAGE

The International Electrotechnical Commission, the organisation responsible for the preparation of world-wide standards in the electrical and electronic fields announces the publication of the 1978 edition of their International Electrotechnical Vocabulary (IEV).

The purpose of the IEV "dictionary" is for there to be one common technical language for the scientists and engineers of tomorrow.

Containing originally some 2,000 terms, today the IEV, in line with the unparalleled growth in electrotechnology, accounts for more than seventy thousand internationally agreed terms.

The International Electrotechnical Commission comprises member countries representing 80 per cent of the world's population consuming 95 per cent of electrical energy. At present there are some twenty thousand pages of IEC standards in each of the official languages of the Commission, the largest set of international standards existing in the world.

Details from IEC, 1 Rue de Varembé, 1211 Geneva 20, Switzerland.



BEAUTIFUL AND CLEVER

A slim and elegant accessory (present) for the modern woman. That's the Casio MQ-1 multi-function micro-computer.

Its time display is constantly visible even in bright light and quartz accuracy is plus or minus fifteen seconds per month.

The calendar function is capable of helping the busy female to be in the right place on the right day up until 31st December 2099. If she wishes to compute whether a friend was born "full of grace", the same function can do calculations back to 1st January 1901.

A genuine stopwatch allows a full 24 hours of continuous timing of one tenth of a second accuracy.

The timer function can count down enabling, say, remaining parking time to be seen at a glance. Also, by setting the timer to count up from a set origin a continuous read out of an overseas time zone can be run without altering home time.

Finally, the four basic mathematical functions are possible on the petite calculator panel which is beautifully designed for a slim feminine finger.

£34.95, including leatherette wallet available from Tempus, 19-21 Fitzroy Street, Cambridge, CB1 1EH.



An American test clip which doubles as an insertion and removal tool for 14 and 16 d.i.l. packages is available, priced £9.04 (14 pin) and £9.38 (16 pin), from BCE Ltd., Briticent House, New Street, Ringwood, Hants.
IIY ALARM SYSTEM

Motor vehicle alarms fitted by garages tend to be expensive. These systems usually consist of switches fitted to the doors, bonnet and boot or operate with a trembler switch.

An install it yourself (IIY) alarm by Photain Controls operates on an entirely new current sensing principle. This unit monitors the outflow of current from the battery of a vehicle. Current flow can be caused by anyone attempting to start the vehicle or when a lamp is illuminated by the opening of a door, the bonnet or the boot. The alarm can operate the existing vehicle horn or a separate siren from Photain.

When activated the horn will sound for a period of sixty seconds and the unit will then automatically reset for the next operation. To enable the owner to leave and enter the vehicle quietly two time delays are incorporated.

The easily installed unit operates from the 12 volts DC vehicle battery (negative earth) and has a current consumption of only 1mA in the set condition. The complete unit measures $55mm \times 100mm$ × 64mm, weighs 227 grams and will operate over a temperature range of minus 20 degrees to plus 60 degrees centigrade.

Price complete with on/off switch, mounting brackets, fixing screws and connecting cable is £20 plus $12\frac{1}{2}$ per cent VAT. Siren costs £12 plus VAT. Photain Controls Ltd., Unit 18, Hangar No. 3, The Aerodrome, Ford, Arundel, West Sussex.



A NEW PET FOR YOUR HOME

The Commodore PET is now available in the U.K. although you will have to wait at least 30 days to get one—provided of course you have £695. The PET is a "low priced" all-in-one microcomputer designed with the user in mind. The package incorporates a 9 inch CRT which will accommodate 25 lines of 40 characters, and a standard cassette system for program storage and entry. A keyboard containing 73 alpha numeric keys plus 64 additional graphic characters for plots, games or "artwork", lower case letters is also available.

PET operates in BASIC and comes with

8k of RAM and 14k of ROM which includes 4k operating system and 1k diagnostic routine.

Commodore will be marketing PET from office/showroom premises in London and they will also be setting up a PET Owners Club. They hope to offer such items as extra RAM (24k we believe), floppy disks, printers etc., at a later stage.

We also hear from Commodore that they have been able to reduce the price of KIM1 to £149.00 plus VAT.

More details from Commodore Systems, 446 Bath Road, Slough, Berkshire.



MAINLINER

Co-axial cable de-braiding tools handling cables from 3.3mm to 13.5nm and having the memorable name of "Mainliner" are available at £5.58 plus VAT from Eraser International Ltd, 2/3 Hampton Court Parade, East Molesey, Surrey, KT8 9HB.

BACKGAMMON COMPUTER

The latest microprocessor-based game now available from Gemini Electronics is backgammon. The unit which is called the Gammon Master II is based on the Motorola 6800 microprocessor and is designed to change its strategy depending on the type of game you choose: running block and hit, back games, it is capable of playing them all.

It is claimed that the unit will on average defeat an intermediate player and

compete evenly with experts. Each game is "charted" with regular pieces and the location of every man on the board can be verified by the touch of a button. The dice is also electronically "rolled" ensuring each game is different.

The total cost of the game including VAT and post and packing is $\pounds 175.00$. It is available by mail order only from Gemini Electronics, 3 Branksome Avenue, Prestwich, Manchester.



THE circuit to be described has several advantages over previously published designs, in that most have used mechanical switches. If the unit is carried in a pocket, this is a major disadvantage as it can be switched on accidentally during movement, leading to shortened battery life. The touch switch alternative used here obviates this.

The circuit has no tendency towards biasing the outcome, and as the i.c.s are CMOS and the indicators are l.e.d.s, it is a gadget that will probably last for years.

The use of CMOS means that standby current is so low that an on-off switch is not necessary.

The whole unit including battery is built into a box of, in the case of the prototype, $120 \times 30 \times 20$ mm.

CIRCUIT

In Fig. 2. IC1a and b form an oscillator whose frequency is controlled by R1, R2, C1 and C2. The output of this oscillator feeds IC2a, a JK flip flop. The \overline{Q} output of IC2a connects to the clock input of IC2b, another flip flop. As each flip flop has two possible output states, there are four possible output conditions in total. The four outputs are fed to IC3, a quad NAND package, which acts not only as an inverting buffer to the outputs (although in this case the inversion feature has no relevance) but also it acts to gate the outputs hence acting as an on-off switch for the display.



Fig. 1. Wiring diagram of Random Decision Unit.

The outputs of IC3 feed the l.e.d. segments of the display. To be safe, 100Ω resistors should be inserted in series with the four cathode leads to the display, however, in the prototype these were not used as all the i.c.s tried appeared to limit their own output current without the inclusion of these resistors. They have not been shown in the diagram nor in the layout drawing but can be included if it is desired to play safe.

IC1c and D1 serve to gate the oscillator (IC1a and b). Attempts were made to use one of the spare inputs of these gates, but in two of the i.c.s tried, this made start up of the oscillator unreliable so the present method was adopted.

When the junction of IC1c and D2 is taken high, C3 charges through D2 and hence the display is activated, also IC1c goes



Fig. 2. Circuit diagram.





low and the oscillator starts up. As the above mentioned junction goes low, the oscillator stops immediately, however the display remains active until C3 has discharged through R4, hence sufficient time is given (5-6 seconds) to read the resulting display. IC1d serves as a buffer with its input normally kept high via R3.

Touching the plate takes IC1d input low (due to skin conductivity) and the above explained process occurs.

In the quiescent state, the i.c.s take virtually no current, and the only real flow is that through D1 and R1, this amounting to around $\frac{1}{2}mA$. Hence an on-off (mechanical) switch is not really justified.

CONSTRUCTION

The touch plate is shown full size and the copper lines should not be made any closer than that shown, otherwise dampness caused by breathing near the plate could lead to shortened battery life. If trouble is experienced, the value of R3 can be reduced slightly without significantly affecting the sensitivity of the touch plate. See Fig. 1.

Construction methods will vary depending on the type of case available, however in the prototype construction of both the p.c. board and the touch plate was on copper coated p.c. board. An excellent system to use for etch resisting is the Alfac range of electronic symbols (lines, bends, etc), however these are expensive, but if a professional looking finish is desired, this system is excellent. The i.c.s should be mounted on sockets if space permits and they should be mounted last, and must not be handled until you are ready to use them as they are liable to damage by static, and the more often handled, the greater the chance of damage. The battery should not be fitted until all parts and all connections have been checked.

COMPONENTS . . .



OPERATION

In use, the plate is touched and all 4 segments will light up, appearing to be all on at once due to the high oscillator speed used. On release of the plate, 2 of the 4 segments will remain alight. Which two is *totally* random and there should be *no* bias toward any particular combination (of the four possible alternatives). If segments bce and f are used as in the prototype, it could be said that a straight line represents "EVENS" and a staggered line represents "ODDS".

Keep the unit in a reasonably dry atmosphere and the battery should last for many months. \bigstar

ALL.



THE SAGA OF SALYUT, SOYUZ AND PROGRESS

The year 1978 will mark a special place in the history of Soviet space progress. On Valentine's day Yuri Rmanenko and Georgi Grechko began using equipment brought up to them by the cargo spacecraft Progress 1.

They began erecting an electric heating chamber in a special lock compartment. Photographic and other monitoring equipment was set up also. Part of the programme included the photography of Earth and space phenomena. Optical instruments for navigation were tested in the new mode where operation can be automatic or manual depending whether the station is with or without crew. This facility can be operated whether the vehicle is on the dark side or the light side of the Earth.

The furnace was brought up by Progress I and assembled by the two cosmonauts. This furnace, whose temperature is in excess of $1,000^{\circ}$ C is controlled by a computer. An accuracy of plus or minus five degrees can be maintained. The unit is installed in the lock compartment which is arranged so that the rear end of the furnace faces into space.

The first experiment in smelting was to study the diffusion process of molten metals in weightless conditions. A capsule of aluminium-magnesium, copperindium and indium-antimonide was introduced into the heating chamber. The airlock was depressurised and the heating switched on. At the same time the control of the spacecraft for orientation was switched off to allow the vehicle to drift. The process of crystallization was completed and the control engines took over again. The materials obtained during the experiment will be brought back to Earth for study.

A second experiment was carried out;

research materials were aluminium-tungsten, molybdenum-gallium and semiconductor materials. These results will also be examined when they are returned to Earth. Information about the interaction between liquid and solid metals will show what reaction occurred under weightless conditions. The aim of these experiments is to gain knowledge of welding and soldering and also the possibility of the creation of new composite materials.

Optical observations have also been carried out particularly with regard to silver clouds, which may provide information about the state of the atmosphere. These clouds tend to appear at about 80 kilometres above the poles. Many drawings have been made as well as photographs. It is clear that these clouds are in distinct layers. Three of these layers have been observed between -130° and -150° C. During this time the Aurora was very prominent and reached a height of about 500 kilometres.

In the mission there was a programme for the study of biological effects involving drosophila, micro-organisms and tissue culture. Two day larvae of drosophila were taken aboard the spacecraft in a nutrient medium, contained in a special thermostatically controlled container, Biotherm-4. The temperature was held constant at 24°C. The first flies appeared in December 1977 and the reproduction cycle began again. This time it was in the condition of weightlessness. The object of this experiment is to determine effect of weightlessness on the insects' hereditary systems.

An important phenomenon was confirmed regarding the productivity level of the cosmonauts. There was an increase in their output of 10 per cent. This was noticed with the activity of the previous long term team, Sevastyanov and Klimuk, which lasted 63 days. It would appear that prolonged weightlessness is like getting second wind.

Another point which is being closely watched is the calcium loss in the bone structure. It would seem that as the skeleton is no longer needed to support the body the amount of calcium reduces. This was of course also noticed with the American teams.

RADIO TELESCOPE

The largest telescope of its kind has been installed in the Salyut-Soyuz space station and is to operate in the 1.5 millimetres band. The detector, which consists of crystals, is cooled in a closed circuit using liquid helium at a temperature of -269° C: The liquid helium is made on board and the final cooling is by an expanding throttle valve.

The telescope is being used to detect radiation in the infrared part of the spectrum. This particular wavelength is not observable from Earth because of the cut-off caused by the atmosphere. The instrument will be turned towards Earth to examine the upper layers which are important in weather forecasting. The control system uses Jupiter and Sirius as test locations.

Also included in this programme are observations of the centre of the galaxy, the Orion nebulosity and interstellar clouds.

THE PROGRESS 1 DESIGN

The development of Progress I was based on economy and reliability. Both these parameters were best served by making use of existing tried materials and units. For example the carrier was similar to that of an ordinary manned Soyuz. Only the emergency rescue system was removed. Since there were no crewmen this was redundant.

The main re-design was on the spacecraft itself. In order that the maximum payload might be carried it was decided that the Progress I should be nonrecoverable. This decision enabled the heavy heat protection shield to be discarded.

Progress 1 weighed 7,020 kilogrammes and could carry 2,300 kilogrammes of cargo. This amounted to 30 per cent of the lift-off weight, a very high proportion for a spacecraft. Progress was 2·2 metres in diameter and 8 metres long. Without solar panels the flight time was for eight days, in independent operation. However its design was such that it could have remained a month in space if linked to the space station. The vehicle was indeed a tanker for it carried upwards of half a ton of fuel, plus 1,300 kilogrammes of dry cargo.

In order that the dynamic characteristics should not be affected a new power scheme was developed.

A number of frames supporting the hull were removed to make economical use of the space. These were replaced by a structural framework supporting shelves to which containers were attached by quick release locks.

New systems in the craft included a pumping installation to transfer the fuel to the space station. Control systems were extended to ensure reliability in the absence of manual control.

It took the cosmonauts twelve trips to unload Progress 1 on its arrival and docking with the space station. Before the re-fuelling operation, the cosmonauts had replenished the air supplies from that brought up by the cargo craft. Stock air had been depleted by the disposal of waste and the space walk.

While still locked to the space station another series of 'resonance' experiments were carried out. No doubt the cosmonauts enjoyed using the cargo ship as a trampoline.

The Progress engines were used to make a correction to the orbit of the space station. After that the cosmonauts set about the separation of the two craft.

Before the Progress 1 was allowed to enter the atmosphere it performed one more task. One orbit after separation, when it was 12 to 15 kilometres away, the back-up automatic and approach systems were tested. This had never been tried before.

Semiconductor UPDATE.... FEATURING : 8086, Z8000, MC6809, TL170C, 355 R.W. Coles

THE 16 BIT CHALLENGE

You may remember that I was less than enthusiastic about the Texas Instruments announcement that the ... "end of the two-bit eight bit" was imminent. There is still no doubt in my mind that the eight bit micros, such as the 6800, the 8080 and the Z80, will be with us for many years yet, as indeed will the four bit chips such as the 4040 and the TMS 1000.

Certainly the current sixteen bit contenders, represented by the Texas TMS 9900, the General Instrument CP1600, and the Ferranti F100L, are hardly taking the micro world by storm, and have so far proved to be of little interest to computer hobbyists who still have plenty of elbow room left in their existing eight bit systems.

But time marches on, and the sixteen bit challenge is soon to be reinforced by the three giants in microprocessor technology, who have at last decided that the market, and their own technology, are now ready. First of the giants on the scene will be Intel with its new 8086, a powerful 16 bit design which is software compatible with its 8080 and 8085 predecessors and yet offers on-chip multiply and divide, a one megabyte address range and internal clock rates of up to 8Mhz. Close on the heels of Intel are Zilog with their new Z8000 chip which, like the 8086, offers software compatibility with its eight bit predecessors. Rumour has it that one version of the Z8000 will be able to address eight megabytes of memory!

FROM MOTOROLA

Last but not least come Motorola with their MC6809 device which has the interesting distinction of being at an "in-between" stage in microprocessor development because, while it uses 16 bit internal architecture, externally it interfaces with the eight bit data bus common to all current 6800 systems. This "best of both worlds" design, while not as powerful as the Intel or Zilog chips, has the advantage that existing hardware and memory investment can be carried through into the more capable sixteen bit arena.

If you relish the thought of a powerful home computer based on a sixteen bit chip, then this news from the three giants will please you, because confident predictions of price erosion are already being made. Before throwing your old micro away though, try calculating the cost of eight megabytes of RAM!

HALL SWITCH

Becoming increasingly popular in the professional electronics market these days, and looking extremely attractive for amateur applications are the new generation of magnetic sensors, the Hall effect switches.

These useful devices have been mentioned once before in this column, but at that time they were difficult to obtain. Now Texas Instruments have introduced the **TL170C** which is likely to become freely available at a very low price, and so there are no longer any excuses for not sampling the delights of this robust and useful switching device!

Hall effect switches like the TL170C are basically silicon integrated circuits which include a Hall sensor able to sense the presence or absence of steady state magnetic fields, and a transistor switching stage which provides a logic type output. The TL170C itself is packaged in a tiny plastic three lead transistor package and operates from-standard five volt supplies. The output switching stage is a basecollector transistor with a 30 volt rating so that the output voltage swing can be tailored with the aid of a pull-up resistor and a suitable supply rail to suit most applications. Output sink current is a respectable 20mA.

EASILY INFLUENCED

Magnetic sensitivity is rated in milli-Teslas, with a positive threshold of about 35mT and a negative threshold of about minus 35mT. If, like me, you do not have much of a feel for milli-Teslas, suffice to say that the TL170C can be reliably operated with quite a small magnet! To prevent erratic switching or threshold oscillations the new chip has a built in hysteresis of 20mT.

Applications for these devices must be legion, and are surely not limited to the main commercial use which is as contactless keyboard switches for high quality teletypes and VDUs. All kinds of clever, perhaps concealed, magnetic switches are possible, and how about a solid state magnetic replacement for the mechanical or reed switch car contact breakers used with electronic ignition systems? And don't forget model train layouts, and slot cars, and ... well I am sure you can see that it really is amazing how you have been able to manage without these devices for so long!

SPIKELESS TIMER

No doubt everyone will have used that great little 555 timer integrated circuit by now, probably with great success. You may have used it as a monostable, an astable, a voltage controlled oscillator, a long period timer or any number of the other jobs at which it excels, but if you used it on a board with TTL flip-flops, you may have come unstuck.

The trouble is that the 555 generates a large current spike in the supply lines when it switches to the high output state, a spike which can be as large as 300mA and last for 100 nanoseconds or more. This sort of spike can cause glitches in the Vcc line which will have unfortunate effects on TTL or other flip-flops, particularly if the decoupling arrangements are not of the best.

The only way out of this problem until now has been to hang a few hundred microfarads of capacitance across the 555 supply pins so that it could guzzle current from its own personal supply during a spike, without communicating its bad habits to the other occupants of the board!

But now a rather more elegant solution has appeared from Teledyne Semiconductor in the shape of their **355** timer.

Yes, that's right folks! It's a pin for pin replacement for the 555 chip without the current drinking problem! Actually, the 355 does take a tiny *sip* when it switches but the current spike generated is only about 1mA.

The 355 not only tackles the 555's drinking problem, but also solves another couple of problems on the side. The 555 has a tendency not to reset reliably on command, and can get too hot on 15 volt supplies. The goody-goody 355 never puts a foot wrong. Personally my sympathies are with my red-faced, overheated, intemperate, 555's who sometimes forget to reset. I think it's an identity problem!

This unit gives a usual indication of the logic

states of any 14 or 16 pin i.c. in the 74 family

This tester was designed for use on all 14 and 16 pin integrated circuits in the 74 TTL series. It is powered from the circuit under test and uses TTL hex inverters with light emitting diodes to indicate the logic states of the i.c. under test.

POWER SOURCE

As the tester is powered from the circuit under test and because in the 74 series the pins used for the power supply connections vary from device to device, it was necessary to ensure that the tester received a voltage of the correct polarity irrespective of the i.c. being tested.

This was achieved by listing all the pins used to supply power in the 74 series and then designing a diode circuit capable of providing the correct voltage.

CIRCUIT OPERATION

The complete circuit diagram of the tester is shown in Fig. 1 with the four hex inverters enclosed within the dotted lines.

Under typical operating conditions with output voltages over 0.6V the 74L04 hex inverter acts as a 13mA constant current generator. This current is sufficient to illuminate an l.e.d. and so indicate a "logic 1" state.

When the tester is applied to an i.c. its positive and negative supply rails are obtained by two of the diodes D18 to D23 being forward biased from the supply pins of the i.c. under test. If for example a 16 pin i.c. with its +5V and earth connected to pins 5 and 12 respectively is to be tested, then diodes D18 and D23 will be forward biased establishing the supply rails.

If any inverter senses a logic 1 input its output is switched low and the l.e.d. connected to it is illuminated. When a logic 0 is sensed by an inverter its output is switched high and the appropriate l.e.d. turned off. IC1-IC4 7404

P.A.BIRNIE



Fig. 1. Circuit diagram of the Test Clip. Pins 14 and 17 are used as the positive and negative connections of the 7404









Fig. 3. Component layout for Board A

Fig. 4. Component layout for Board B







CONSTRUCTION

The two printed circuit boards (Fig. 2) used in the tester are identical and their respective component layouts are shown in Figs. 3 and 4. The components should be soldered as close as possible to the p.c.b.s to allow easy mounting of the boards into the case.

The unused pins of each i.c. should be cut off before the i.c. is soldered into the p.c.b.s.

Each l.e.d. should have its cathode lead cut to about $\frac{1}{2}$ in and its anode lead to about $\frac{1}{2}$ in. A piece of sleeving should be fitted over the cathode lead to prevent it shorting out to the positive supply line.

When soldering the l.e.d.s in place care should be taken to keep the spacing between them even to ensure neat indicator rows in the finished tester.

TEST CLIP

The test clip was made using 0-1in edge connector which was modified as shown in Fig. 4. One side of the connector was carefully cut off, leaving a strip of 8 contacts with con-



TOP - 1mm CLEAR PERSPEX SIDES AND ENDS - 1mm OPAQUE PERSPEX

Fig. 6. Case cutting details

venient "spills" which were used to solder the connectors to the p.c.b.s. Araldite was then used to securely hold the connectors in place.

TESTING

The tester should be checked before it is fitted into the case to ensure that it is working correctly. A +5 volt supply should be connected to the positive and negative supply lines of the boards and all the l.e.d.s should light up. If a lead is taken from the negative supply to each inverter input in turn the corresponding l.e.d. should be turned off.

CASE ASSEMBLY

The case was constructed using 1mm Perspex (Fig. 6). A special solvent is available for fixing Perspex and this should be used rather than an adhesive.

The two p.c.b.s are first Araldited to the sides taking care to align the connectors in the centre of the case. The top of the case, which should be made of clear Perspex, may then be fitted to one side.

The two ends of the case can be curved by warming them over a hot soldering iron and gently bending them until they match the curves of the sides. These may now be fitted to the top and side of the test clip. The other side can now be fitted and all the corners rounded off with wet and dry sandpaper. The Perspex can be restored to its original finish using Brasso. Finally the top of the tester should be marked with a spot of white paint to indicate the position of pin 1.



A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.

Why not submit your idea? Any idea published will be awarded payment according to its merits. Articles submitted for

Articles submitted for publication should conform to the usual practices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.

Here are a number of circuits contributed by E. F. Flint which should be of interest to constructors of the Minisonic 2 synthesiser

Copies of Sound Design containing this are still available from Post Sales Dept., IPC Magazines Ltd., Lavington Street, London SE1 0PF at £1:20 each including Inland/Overseas p. & p.)

VOLTAGE CONTROLLED OSCILLATORS

Fig. 1



One of the modifications incorporated in Minisonic 2 was in the design of the v.c.o.s. The new circuit used an LM318 N op-amp as a comparator on the grounds that this device has a much faster slewing rate than the 741-70 μ V/sec. It is however both expensive and hard to obtain, and an alternative approach is to use a 555 timer, which contains two comparators with slewing rates comparable to the 318, and is cheap and readily available. Two designs are possible, the second being more complex and having two output waveforms. This version (see Fig. 1) provides only a ramp waveform as in the original design. The two comparators in the 555 have thresholds of $\frac{1}{3}$ and $\frac{2}{3}$ of V_{cc}, so that by operating the device with supplies of -3 volts and +6 volts the thresholds become zero volts and +3 volts.

The mode of operation is as follows: the 741 integrator ramps in a positive direction as before until the ramp level reaches +3 volts. At this point the output of the 555 goes rapidly to the negative supply rail; this switches on TR2, connecting the gate of the f.e.t. to +6 volts and discharging the integrating capacitor. When the ramp output reaches zero volts, the 555 output goes positive, switching off the f.e.t., and integration begins again. Thus the oscillator produces a ramp of 3 volts amplitude, rising from zero volts.

The synchronisation network is slightly different from the published design; since the 555 produces negativegoing pulses the gating diode must be reversed, and the sync. pulses are fed to the inverting input of the integrator. Fig. 2



This version provides triangle and square wave outputs; in addition to the 555 and 741 it uses a CMOS chip 4007 as a two-way switch. The switch works as follows: the 4007 contains two complementary pairs of f.e.t.s and an inverter. The two complementary pairs are wired as a pair of bilateral switches with one common terminal, and the inverter controls their on/off state so that when one is on the other is off, and vice versa. (Fig. 2).

The 4007 is used in this application to switch the constant current generator between the inverting and non-inverting inputs of the 741 integrator, so that it can ramp both positively and negatively. The 555 is connected to ± 6 volts, so the threshold levels are +2 and -2 volts.

If we consider first the ramp rising positively, the 555 output is also positive and the 4007 is in position 2+9. When the ramp reaches +2 volts, the output of the 555 swings to -6 volts, the 4007 switches to position 11 + 4, and the integrator begins to ramp negatively. When the ramp reaches -2volts the 555 output swings back to +6 volts, the 4007 switches back to position 2 + 9, and the integrator once more ramps in a positive direction.

This oscillator uses only negativegoing sync pulses so the gating diode is reversed and the pulses are routed to the common terminal of the switch; this means that when the ramp is positive-going the spikes from the sync pulses are too, and when the ramp is negative-going, the spikes are again in the same direction as the ramp, i.e. the sync pulses always augment the ramp voltage to induce triggering of the comparators at a sub-threshold voltage.

Fig. 3



A completely different approach to v.c.o. construction is to use the 8038 (Fig. 3), a 14 d.i.l. package which contains a v.c.o. with sine, square and triangle outputs, having variable symmetry, adjustable harmonic distortion on the sine-wave output, and an extremely wide frequency range of 0.001Hz to 1MHz (by changing the component values in the timing network then in the voltage controlled mode the sweep range is 1,000:1). The internal workings of the package are far too complex to describe in detail but it can be considered as a black box whose output frequency depends on the values of R3, R4, C1 and the voltage between pin 8 and the positive supply rail. It is the last of these variables with which we are concerned. The frequency is a linear function of this voltage, but a logarithmic function can be introduced using a modified version

THE 8038 V.C.O.

of the well-known Minisonic v.c.o. control node.

Here the constant current generator (c.c.g.) instead of working from an integrator draws its current from the positive rail via resistor R_X . Since the collector current of the transistor is a logarithmic function of the voltage on its base, it follows from Ohm's Law that the voltage across R_X follows the same log function, and therefore the frequency of the oscillator becomes a log function of the voltage on the transistor base.

The control node works exactly as before, and setting up therefore follows the established procedure. Symmetry is adjusted by varying either R3, R4 or both, but note that this also affects the frequency. This is best done using an oscilloscope to monitor the square wave output.



Fig. 4

Sine wave distortion can be adjusted in two ways-the first is simpler and should be used where it may be desired to introduce some harmonics during use, for example in simulating flutes etc. A variable resistor is connected

The circuit of Fig. 6 produces a train of pulses of variable width and repetition frequency, for so long as a key is pressed. These can be used to repeatedly trigger an envelope shaper so as to produce an effect like a banjo. mandolin or xylophone (all of these require an ADSR envelope shaper).

The circuit uses a 741 in the astable mode, with a few alterations from the usual design. It is basically a comparator, with R1 and R2 added to allow operation from a single supply.

The two diodes allow the charging and discharging times of C1 to be adjusted independently. TR1 connects R1 to the negative supply when a key trigger pulse is present, allowing the multivibrator to oscillate. VR2 controls the charging time of C1, and therefore the width of the output pulses, and VR1 controls the discharging time and therefore the width of the spaces between the pulses.

The circuit of a sawtooth to triangle converter is shown as Fig. 7 with examples of waveforms produced (Fig. 7a). The sawtooth wave first passes through a d.c. blocking capacitor so that it is symmetrical about zero volts. A pair of diodes then gate the signal so that positive half waves are applied to the non-inverting input of a differential amplifier, and negative halves to the inverting input. As shown, this produces an output train of alternate positive and negative-going ramps, i.e. a triangle wave.

Since this rectification process effectively halves the signal amplitude, the resistor values in the amplifier are chosen to give a gain of two. A second d.c. blocking capacitor in the output again makes the output symmetrical about ground.

The same circuit can also be used as a frequency doubler for triangle wave signals, as shown in Fig. 7b.



from pin 12 to the negative rail

(Fig. 4) and adjusted for the required

purity of waveform or harmonic

Where a pure sine wave is required the

content.

potentiometers adjusted for minimum distortion-this requires an oscilloscope and a sine wave generator for comparison, and distortion as low as 0.5 per cent can be achieved.



R EADERS of this magazine who have observed the sudden rise of digital clocks and quartz watches over the last few years may be surprised to learn that electric timekeeping goes back over 150 years. Last year an exhibition at the Science Museum, "Electrifying Time", commemorated the centenary of the death of the Father of Electric Horology, Alexander Bain. The exhibition surveyed electric timekeeping from its infancy at the beginning of the 19th Century through to the atomic clocks of the present day.

ELECTROSTATIC

Zamboni and others in Europe experimented with electrostatically maintained clocks at the start of the 19th Century. They relied on the repulsion between a charged ball at the end of a pendulum and two oppositely charged plates at each end of the pendulum's swing. A high voltage battery such as a Zamboni pile maintained the potential difference between the ball and the plates. As the ball swung to one plate it would be electrostatically repelled to the other and so on, thereby maintaining the pendulum. This system was so highly temperature



sensitive that Zamboni concluded that it was a better thermometer than a clock and this approach was soon abandoned.

In 1819 Oersted had demonstrated the principle of electromagnetism and in 1841 a Scotsman, Alexander Bain, produced the first successful electric clock using an electromagnetically maintained pendulum. One of the difficulties asso-ciated with these early electric clocks was the temperature dependence of the cell driving the clock, usually a Daniell cell. In 1843 Bain hit on the idea of the Earth Cell in which copper and zinc electrodes were buried several feet down in the soil which acted as the electrolyte. At a depth of six or more feet the temperature of the soil is constant and so Bain managed to eliminate at least one of the causes of poor electric timekeeping.

SYNCHRONISATION

One of the great virtues of electric clocks is that the pulses of electrical energy which maintain the pendulum swinging may also be used to synchronise other clocks in the same building or at greater distances. Bain demonstrated such a system with a master clock in Edinburgh synchronising a slave clock in Glasgow, the synchronising pulses being transmitted over telegraph wires.

Despite Bain's innovations, it appears that he did not meet with any great commercial success. He installed an electric turret clock in St. John's Church, Loughton, Essex, in 1846, but within four years it was replaced by a mechanical clock. A similar fate befell Shepherd's electric clock for the 1851 Great Exhibition in Hyde Park.

By the end of the 19th Century new methods of electrifying time were being evolved. In 1881 Chester H. Pond of the U.S.A. produced the first electrically maintained spring driven clock in which the spring mechanism was wound every hour by an electric motor. The "Synchronome" master clock using a deadbeat escapement was patented in 1895, but was not practically realised until 1905-7 by F. Hope-Jones and G. B. Bowell. In this clock a gravity arm falls every half minute to give the pendulum



Alexander Bain 1810–1877 (Photo courtesy Science Museum)

a push. This done, the gravity arm is restored to its original position by an electromagnet. At the same time as the elecromagnet is energised, a pulse is sent to move the slave clocks. The great advantage of this scheme over all previous ones is that the master pendulum itself does not have to provide the synchronising pulses. William Hamilton Shortt carried this

William Hamilton Shortt carried this principle further in 1921. Shortt's master pendulum was placed in a vacuum and did not even have to turn a counting wheel. A slave clock carries out the counting and every half minute releases a light arm carrying a jewel which falls on to a small wheel mounted on the master pendulum. In rolling off this wheel it imparts a light impulse to it and after transmits a synchronising signal to the slave clock. The master pendulum runs completely free except for this impulse every half minute and maintains an accuracy of 1 part in 10⁷. This type of clock was in use at the Greenwich Observatory until 1942, when it was replaced by a quartz clock.



The technology of today's watches is relatively new but electro-assisted timekeeping has been progressing for 150 years through electrostatic, electromagnetic and mains frequency to quartz drive

Rolling ball clock attributed to Wheatstone (Photo courtesy Science Museum)



Electric clock of the type patented by Bain in 1845 (Photo courtesy Science Museum)



Rear view of Scott clock (Photo courtesy Science Museum)

The idea of using the mains to distribute time occurred to Ferranti in 1895 when the first alternating current generator was installed at Deptford. At that time, however, there was no means of controlling the mains frequency with sufficient accuracy and the idea had to wait until 1916 when H. E. Warren of the Warren Telechron Company devised a method of precision frequency measurement and a low power-consumption synchronous motor which led to the first mains driven electric clock.

Quartz oscillators were first constructed in the early 1920s and in 1927-30 Warren Alvin Morrison of the Bell Telephone Laboratories produced the first quartz clock. The reliability of electronics at that time was low and consequently three such independent clocks were used to confirm the time. The accuracy of these quartz clocks was improved and now accuracies of better than 1 part in 10^8 are achievable, ten times better than the best pendulum clock.

The quartz oscillator which is the timekeeping element of the quartz clock relies upon the vibration of a quartz crystal between two metal electrodes. This high frequency vibration, which usually takes place above 100kHz, is amplified by an active device, originally a valve but nowadays a semiconductor device, and a portion of this amplified output is fed back to the quartz crystal to maintain its oscillation. The high frequency signal is then divided to provide a low frequency signal (between 50Hz and 1Hz) to drive a synchronous or stepping motor to provide an analogue display of time or a digital display via counter and decoder circuits.

ATOMIC CLOCK

Up until the early 1950s the method of timekeeping had always relied on the oscillation of a solid body whether it be a pendulum, a balance wheel or quartz crystal. These are calibrated with respect to the rotation of the Earth on its axis which is irregular. In 1955, Dr Louis Essen and Mr Parry of the National Physical Laboratory in England produced the first atomic clock. This essen



A typical domestic electric clock of the 1920's (Photo courtesy Science Museum)



Hamilton electric analogue watch (Photo courtesy Science Museum)

tially was a quartz clock controlled by the vibration of cæsium atoms and was accurate to within 1 second in 300 years (1 part in 10¹⁰). As a result, in 1967, the second was re-defined in terms of the cæsium vibrations and in 1971 the International Atomic Time Scale was adopted. Currently cæsium clocks are accurate to within 1 second in 100,000 years (3 parts in 10¹³)—a quite mind-boggling accuracy which, if it surprises us, would have shaken Alexander Bain. Rubidium clocks which rely on the vibrations of rubidium atoms are used as secondary standards and are at present accurate to within 1 second in 1,000 years (3 parts in 10¹¹).

DIGITAL

On the domestic scene Max Hetzel devised a clock employing an electrically maintained tuning fork. This was the forerunner of the Bulova Accutron, a wristwatch in which the tuning fork was maintained by a transistor circuit. The first domestic quartz clock was manufactured by Junghan's in 1967. Only two years later, Seiko introduced the world's first electronic watch and later that same year Longines introduced their electronic watch. All three of these timepieces used a motor driving the hands of an analogue display. In 1972 Hamilton produced the first quartz watch with a digital l.e.d. display and, in the same year, a Swiss Company, Societe des Gardes-Temps, produced a quartz watch using a liquid crystal digital display.

All of these brings us just about up to date. The accuracy of even a cheap quartz watch is beyond the dreams of watchmakers of a hundred years ago. Although it is improbable that atomic clocks will ever be used domestically, their accuracy can be tapped by receiving and decoding the radio clock signals transmitted by MSF, Rugby. Such radio clocks and designs for the experienced constructor are available, so if you ever feel the need for a clock with an accuracy of 1 second in 100,000 years, that's the way to achieve it.



CB RADIO

Last year's clamour from pressure groups advocating Citizens Band Radio is now somewhat muted, perhaps because the problems of introducing a CB "service" in a smallish country like the UK have now been more fully examined and debated.

An interesting sidelight on CB in the United States is that Texas Instruments has been working with the FCC on developing a high-performance TV receiver which is less susceptible to interference from CB radios, the implication being that interference is a major problem to viewers. Improving front-end performance of mass-produced TV receivers is a worthy end in itself but it costs money.

Ninety-five per cent of Britain's 20 million households have a TV receiver and the average viewing time is 18 hours per week. The great mass of the population would not take kindly to interruption of their principal recreation and even less kindly to having to spend more to overcome the difficulties caused by enthusiastic chatterers on CB.

PRELUDE

Project Prelude, now running on a trial basis in the United States could be the shape of things to come in Business Communications. Big companies need to talk from their headquarters to their factories and offices at remote locations and often to each other. The modern way to do this is by communications satellite which can provide high-speed data, facsimile and teleconference transmission, all over the same links. In the teleconference mode, for example, you can have largescreen projection colour TV for group discussions and even person-to-person viewphones.

Three big businesses are involved in the trials, Texaco, Rockwell International and Montgomery Ward. Using small transportable earth terminals they are currently operating through the Communications Technology Satellite (CTS) which operates on the same frequencies as a proposed Satellite Business Systems (SBS) satellite.

LUCAS ELECTRICAL

Lucas seldom gets in the news as an electronics company but the company is strong in thick film technology and has large semiconductor manufacturing operations in Birmingham to supply devices for alternators, electronic ignition and other automotive electronic applications. Lucas has now increased its shareholding in Ducellier, France, from 49 per cent to 100 per cent, having bought out. DBA (the American Bendix Automotive subsidiary) for a reported 26 million dellars.

Ducellier has 7,000 employees in four factories. Lucas-France already has 4,300 employees in six factories as well as a 49 per cent share in Thomson-Lucas which serves the aerospace market, making Lucas a major company in France. The Lucas move appears to be following the trend of investment overseas into areas where the industrial climate is less restrictive than in Britain under the present regime.

DISTRIBUTION

The Association of Franchised Distributors of Electronic Components (AFDEC) have forecast a total component market this year worth £531 million of which about 16 per cent will be served through distributors, the balance being bulk direct supplies from manufacturers.

Despite talk of overcrowding in the distribution business there are still some eager entrants. One such is Jack Evans Electronic Distribution Ltd, scheduled to start up in April near London Airport, an area already heavily populated with distributors. JEE Distribution, as the company will be called, is to concentrate on electronic hardware which, according to company hand-outs, is a neglected area.

Jack Evans is a familiar name in the business. He was the prime mover in setting up ITT Electronics Services and was general manager there from 1964 to 1970. Most new companies in distribution start off with one or two good franchises as the foundation of the business. Evans is starting with over 30.

JEE's hardware catalogue is said to be a real engineering manual rather than a list of products and is expected to be a prime addition to the equipment design engineer's technical library. The first edition has some 150 pages of products and technical data.

HEWLETT-PACKARD

John A. Young, newly elected president and chief operating officer of Hewlett-Packard has inherited a strong financial and technological position. He is only 45 so has many years ahead of him to seek further growth in what is one of the most powerful and respected companies in world electronics. He heads up over 35,000 H-P people of whom 10,000 are outside the United States. The two founders of the company who started up in a backyard garage are continuing to exercise influence on long-term policy.

Ten years ago H-P was still the classical test and measurement company with 86 per cent turnover in that field out of a total turnover of less than 300 million dollars. Today the company is approaching 1-5 billion dollar turnover and test and measurement activities are only 42 per cent of the total, having now been overhauled by an equal share in electronic data products which was only 4 per cent of the total 10 years ago.

It's now full speed ahead on computational technology in H-P, which means "smart" instruments, computers, terminals and analytical tools. To keep the whole effort moving H-P has invested in component technology, one example being H-P's silicon-on-sapphire microprocessor which has over 10,000 circuit elements on one chip. But above all H-P invests in people. Graduate student intake last year totalled nearly 400, and over 10,000 employees took advantage of educational programmes to develop technical and management skills. R and D spend last year was running at the rate of about £1 million a week. Nothing succeeds like success.

BIG BATTERIES

With so much emphasis on micromin in electronics we tend to overlook that some things get bigger. In contrast to the tiny cells which power our watches and calculators, Chloride Industrial Batteries have recently roped in £4 million of orders for submarine batteries. Individual cells are said to be nearly as tall as a man and weigh about half a ton. A complete set for a submarine can weigh as much as 250 tons.

AVO's DMM

AVO, the oldest name in multimeters, is having another crack at the digital multimeter market with a unit, the DA116, with liquid crystal display and a price tag of £99. An important feature is high-speed resistance measurement, claimed to be ten times faster in response than conventional dmms. A single i.c. is used for analogue-digital conversion.



Provides a variable power source up to 25V at I·5A. Completely stabilised and with an adjustable over-current limit

A supply as batteries come extremely expensive—particularly the high power types.

This design is stabilised and has a performance which vies with a number of much higher priced commercial designs.

The maximum power available is 25V at 1.5A. The voltage output is continuously variable and the protection offered by an adjustable over-current limit is very reassuring—especially when experimenting with breadboards that have been inadvertently wired with shorts.

To cut cost, meters have been deliberately excluded but the ingenious use of l.e.d.s compensate in part for this omission.

STABILISER

Transistors TR1, 6, 7 and 8 constitute the constant voltage stabiliser. The reference voltage for TR1 is provided by D3 with the decoupling components C3 and R2 removing any hum that could be fed back.

Assuming S2 in the B switched position then TR8 is providing current to the load. For a full rated 1.5A the base input to this comes via R11, R14 and R16.

The output voltage seen at the terminal is sampled by the preset VR5 and R18 and if too negative (dependent on VR1 setting) switches on TR1. As a result of this TR6 and TR7 are turned on so that some of the base current available to TR8 is shunted.

This feedback ensures that the output voltage will be such that the base of TR1 is always just negative with respect to the slider of VR1, the voltage setting potentiometer.

Returning TR6 collector to a tapping on the divider chain feeding TR8 base ensures that dissipation in this transistor is kept low. Note that R16 cannot be omitted, otherwise TR7 would be unable to bottom sufficiently to control TR8 base current.

Stability in the feedback loop is ensured by the roll off provided by C6 and C5, although the main role of the latter is to maintain a low output impedance at high frequencies. C4 provides this additional h.f. roll off when the output is switched off.

CURRENT LIMITING

The active components in the current limit loop are TR2, 6, 7 and 8. To understand the working, assume calibration has been made—VR2, VR3 adjusted—and a current limit value has been set on VR6. If the volt drop across D4 and R3 is such as to turn on TR2 then TR6 and TR7 will turn on, subtracting base current from TR8 and preventing any further increase in load currents.

If the whole of the volt drop across R3 were fed to TR2 base, it would be necessary to vary R3 to adjust the current limit and to obtain a wide control range—say 20mA to full load—requiring a low value non-linear wirewound potentiometer. This has been done in some commercial stabilised supplies, but such a specialised component is hard to come by. Therefore, a power diode D4 is included and this still drops an appreciable voltage even at low currents, enabling front panel control VR6 to be calibrated directly in current down to low values.

VR2 sets the maximum current the stabiliser can supply (with VR6 fully clockwise) and VR3 is set to make the anticlockwise position of VR6 correspond to virtually zero short circuit output current.





Fig. 1. Circuit of Power Supply Unit

R21 ensures that C5 discharges when the output is turned off at S2. Consequently, when S2 is switched on again, the charging current into C5 momentarily takes the power supply into current limit, causing the output voltage to ramp up linearly from zero. This feature in conjunction with the adjustable current limit provides complete protection of any circuit connected to the stabiliser.

L.E.D. INDICATORS

Whilst the omission of meters saves a considerable amount of money, one needs to know if the output voltage set by front panel control VR1 is really appearing across the load or whether the supply is in current limit. TR3 and 4 detect whether TR1 or TR2 is controlling the output and light an appropriate l.e.d. Thus, if the output voltage really is as

COMPON	IENTS		
		Capacito	rs
Resistors		C1, C2	$2 \times 4,700 \mu F 40 V$
R1	1kΩ	C3	150µF 15V
R2	3·3kΩ	C4, C5	$2 \times 22 \mu F 40 V$
R3	0.33Ω. 7in of 24 s.w.g. Eureka wire	C6	330pF
R4	$47k\Omega$ may be used. Eureka wire is		
R5	1kΩ obtainable from The Scien-	Semicon	ductors
R6	27kΩ tific Wire Co., PO Box 30,	TR 1, 2	2N2905A (2 off)
R7	33kΩ London E4 9BW.	TR3, 4	BC109 (2 off)
R8	120kΩ	TR5	BC214
RQ	68k Q	TR6	BC109
P10	100kO	TR7	BFY50
P11_14	470.0 ±W	TR8	2N3055 with mica insulating set
D15	1k0	TR9	BC109
P16	68k ()	D1, 2	1N5402 (2 off)
P17	3.0k O	D3	BZY88 C7V5
D19	A.7kO	D4	1N5402
. R10	9.040	D5	1N916
R19 B00	2.040	D6, 7	TIL209 (2 off)
R20	0.040	D8	1N5402
(1) (1) (50/	Hi stab except where stated otherwise)	Miscella	neous
(4 VV 5%	HI Stab except where stated emernesy	T1 MT1	04AT pri. 240V; sec. 25-0-25V 12A, FS1 1A,
Potention	neters	S1 On	off switch, S2 S.P.C.O. switch, Heatsink
VR1	5kΩ linear	401-807	(Radiospares). LK301 Side Plate No. 1 (2 off)
VR2.3	4.7kΩ vertical miniature skeletal	LK211	Chassis rail (4 off) LK431 Front panel LK521
VR4	100kΩ vertical miniature skeletal	short o	erforated plate (3 off) (Home Radio)
VR5	4.7kΩ vertical miniature skeletal	0	
VR6	2·5kΩ linear		





Fig. 3. (above) Component layout and wiring details for p.s.u. board

Fig. 2. (left) Printed circuit board layout. Holes should be drilled to conform with the component wire inserts as shown above





indicated by VR1 setting, TR1 is controlling the loop and the volt drop across R4 due to its collector current will ensure that TR3 is on and TR4 is off. Therefore TR5 and TR9 will be off and D7 lit. In current limit TR2 takes over control of the loop, so the drop across VR4 will cause TR4 and hence TR5 and TR9 to turn on and l.e.d. D6 will light, extinguishing D7.

S2 enables the output to be turned off and R19 ensures that even with little or no load the output voltage will then collapse to zero within a second or so. If the mains switch were used instead, then the output voltage could hold up for several seconds when lightly loaded, leading to possible damage to a circuit being worked on whilst still connected to the supply. D8 protects the stabiliser when switching off a highly inductive load and also when the supply is being used in series with another to obtain a higher voltage. Under these circumstances, if the stabiliser goes into current limit, the voltage at its terminals could reverse if it were not for D8.

CONSTRUCTION

Figs. 2 and 3 show the track layout and component assembly of the main board. In the prototype a Lektrokit chassis system was used for the case with TR8 heat sink mounted to the rear. A free air circulation must be ensured around this.

The sink specified will cope with the dissipation in TR8 at 1.5A output current into a short circuit at ambient temperatures in excess of 50 degrees centigrade if heat sink compound is used on both sides of the insulating mica washer.

As D4 can get rather warm on full load it should be mounted with its body away from the board.



Various views showing p.s.u. prototype assembly. Four lower value reservoir capacitors were used in this

CALIBRATION

On completion, set VR1, 2, 3, 4 and 5 midway, VR6 fully clockwise and check the raw supply voltage across C1. Off load it should be 37V. With S2 on, monitor the output voltage. Increase it to maximum by rotating the voltage control VR1 fully clockwise and set VR5 to give 25V output. VR1 may now be calibrated directly in output volts.

With output voltage control VR1 set fully anti-clockwise, the minimum output voltage is about 1.75V. Next set VR1 fully clockwise and current limit control VR6 fully anticlockwise and measure the short circuit output current. Adjust VR3 to set this to 1 or 2mA. Now adjust VR6 clockwise until the short circuit current reaches 1.5A. Adjust VR2 so that 1.5A short circuit current occurs with VR6 fully clockwise. As the settings of VR2 and 3 will interact somewhat, readjust them alternately so that the short circuit current with the current limit control VR6 fully anti-clockwise and clockwise is 2mA and 1.5A respectively. VR6 may now be calibrated directly in short circuit current.

Next connect a suitable load to draw 1.25A at 25V and adjust current limit control VR6 so that the output voltage just begins to fall. Adjust VR4 so that l.e.d. D6 just takes over from D7. The l.e.d.s now indicate the mode of operation, i.e. if D7 is lit the output voltage is as indicated by the setting of voltage control VR1, whilst if D6 is lit the power supply is in current limit. The current limit control VR6 can thus be used as indication of the current drawn by the load, by noting its reading at the point where the current limit l.e.d. D6 just lights.

USE

Whilst the power supply can look after itself, with 25V at 1.5A available, care is needed to avoid damage to low power circuits being run from the supply. Always connect up with S2 switched off and check the voltage and current limit settings before switching the output on. Set the current limit at about 50 per cent more than the current you expect the circuit to take and you will then seldom damage it even if it has been assembled wrongly.

Note that the current limit control has been calibrated in terms of the short circuit output current. Of course, when the output is short circuited, R21 draws no current, but at 25V output it will draw about 11mA. Consequently, if the current limit is set to less than 11mA, even though the voltage control VR1 is set to maximum, the output voltage will be less than 25V. At high current limit settings, the 11mA difference between current at the onset of limiting at 25V and the current at short circuit is of course barely perceptible.

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PART NINE

WITH the circuit details of CHAMP-PROG behind us, this month we can move on to consider the construction of the main board, power supply and plinth, and to an examination of the PROMPT firmware program. Anyone who has already built CHAMP itself should have no problems with CHAMP-PROG, because the techniques required will have already become familiar.

CIRCUIT BOARD

Most of the CHAMP-PROG circuitry is mounted on a piece of Veroboard measuring 165mm by 225mm, and this has to be cut from a larger sheet, of the same type as was used for the CHAMP main board. When the board has been cut to size, the three unperforated copper strips which run along the two long edges should be removed by easing up each of their ends with a sharp knife and then carefully pulling them away from the board along their entire length. The removal of these strips makes for easier insertion of the board into the card guide supports, and insertion can be further eased by chamfering the four board corners with a fine file.

The required trackⁱ cuts and component positions are detailed in Fig. 9.3 and it is a good idea to study the component layout at this stage so that when construction starts you will "know-your-way-around". There are three distinct circuit areas on the board, the top quarter being occupied by the regulator components, the next quarter by the timing generator and the lower half by the CHAMP interface and the data and address circuits. The layout chosen provides plenty of room in which to work and makes fault finding fairly easy.

Track cuts can be made right at the start, which makes life easier later but requires great care initially to prevent errors creeping in. Alternatively they can be incorporated as construction proceeds so that some layout flexibility is retained.

CONNECTING UP

The circuitry on the CHAMP-PROG board is a fair mixture of digital and analogue integrated circuits and discrete components, and of course high voltages will be present during operation. Needless to say, great care must be taken during wiring-up to avoid expensive mistakes. A wiring error on the prototype caused a transistor to quite literally "blow its top" when power was first applied! One consolation though, faults on this kind of circuitry are usually easy to locate, just watch out for the smoke signals!

As with the CHAMP main board, Soldercon pins are recommended for every integrated circuit, but not for the 16 and 24 way connector socket positions where standard or low-profile sockets are best. Remember to leave the bandolier attached to the Soldercon pins until construction is complete, and be sure not to plug in the 4265 MOS chips until the debugging process is over.

Using the Soldercon pins and sockets as a reference framework, wiring up is carried out using Fig. 8.2. Kynar wire is highly recommended for the interconnection of all logic circuitry address and data drivers, although sturdier single core PVC covered wire is better for the +5V, -10V and +80V interconnections because of its higher current rating and higher voltage insulation.

TESTING

The complex timing generator and voltage regulator circuitry lends itself well to being tested in isolation without benefit of PROMPT software or 4265 interface chip. Before testing can take place, +5V and -10V supplies must be connected, and the 80V supply will have to be built using the circuit of Fig. 9.1 and the layout shown in Fig. 9.2.

To start the testing procedure, first connect pin 1 of IC1 to 0V temporarily to enable the timing generator to free-run (this can be achieved by grounding pin 11 of the vacant IC8 socket if desired). Next apply the +5V and -10V power, but *not* the +80V supply and examine the timing generator waveforms at the Q and \overline{Q} outputs of the 74123 using an oscilloscope set to measure pulse amplitudes of a few volts and pulse durations of a few milliseconds.

At IC1 pin 13 you should be able to see narrow pulses with a 15ms separation. If the pulses you see are separated by much more or less than this, the value of fixed resistor R3 should be changed to compensate. A timing accuracy of ± 10 per cent should be the target.

On IC2 pin 13 a series of 3.25ms wide pulses should be obtained, and of course the width of these pulses can be accurately set using VR1. On IC3 pin 12 the pulses should be set to a width of 3.0ms using VR2, and on IC2 pin 12 pulses about 60μ s wide should be observed. Finally on IC3 pin 13, pulses of about 155μ s width should be visible.

REGULATOR OUTPUTS

If the monostable circuits are operating correctly and VR1 and VR2 have been properly adjusted, the next step is to set VR3 to its mid-travel position (Remember that all three adjustment pots are of the 10 turn variety) and connect up the +80V supply. Providing that the

fuse does not blow (and that no wisps of smoke are observed!), the next step is to examine the 7405 outputs, IC4 pins 2, 4, 8, and 10 and compare these with Fig. 8.5 published last month.

If these drive pulse outputs are correct switch the 'scope probe to the junction of D7 and D8 in the regulator area and decrease the 'scope sensitivity to show pulses of about 50V. The waveform at this point should consist of a steady +4.5V level with pulses 3.25ms wide superimposed every 15.0 ms. The amplitude of these pulses can be set by means of VR3, and this should be adjusted to give a peak of +47V. (Note that the +47V should be measured with respect to 0V and *not* with respect to +4.5V.)

At this point you can relax a little, because the worst is over! All that remains is to check the remaining Cs, Vbb, Vgg, Prgm and Vdd outputs and to ensure that they all conform to the timing and amplitudes specified in Fig. 8.5 last month. The Prgm pulse must have the characteristic "two-eared" shape and you will find that final trimming of this pulse can be achieved using VR1 and VR2. The high voltage programming waveforms can all be found on the board-mounted 24 pin socket as well as on the regulator transistors themselves of course.

CASE CONSTRUCTION

The CHAMP-PROG case, or plinth, is made of plywood and aluminium and is relatively easy to construct using the techniques described in part four for the CHAMP case. The plywood framework should be pinned and glued together, and mated carefully with the aluminium top cover and the separate aluminium back



Fig. 9.1. Circuit diagram of 80V supply



Fig. 9.2. Component layout of 80V Supply

panel. When the overall fit is satisfactory all necessary holes can be drilled in accordance with Fig. 9.4 and the board runners made up and pop riveted or bolted in place on the cover.

It is of course essential at this stage to ensure that the board runners are positioned so as to provide a satisfactory sliding fit on the CHAMP-PROG circuit board. A great deal of care was taken to ensure that the CHAMP-PROG prototype finish matched that of CHAMP itself, and again the process of applying several coats of primer, sanding it smooth and finishing off with a couple of colour coats, was followed. After allowing the paint to harden for a couple of days Letraset lettering was applied along with outlines drawn with a spirit based pen. Finally a coat or two of polyurethane clear varnish was applied to bring out a high gloss and to protect the lettering.

ZERO INSERTION FORCE SOCKET

With the plinth hardware completed, overall assembly can begin with items such as the ON/OFF rocker switch and the zero insertion force socket mounted on the front panel section of the cover.

If an economy CHAMP-PROG is required, the zero insertion force socket could be left out, and PROMS programmed directly in the 24-pin socket on the board, but levering expensive PROMs in and out of this type of socket is less than satisfactory, and the sheer convenience of the lever action type is well worth the few pounds it costs. If used, the front panel socket can be mounted either by adhesive, or more securely by first removing the two small Phillips screws from the socket, and with the socket lever in the upright position removing the face of the socket so that two holes can be drilled at the top and bottom of the socket. These should clear the two Phillips screw holes to take two 8BA countersunk screws. Using the socket as a template, two 8BA clear holes can be drilled in the plinth cover so that the socket can be mounted securely and its faceplate replaced.

EIGHTY VOLT SUPPLY

The programming voltages are derived from a simple power supply which consists of a transformer with a 25-0-25V 2A secondary, a 200V 2A bridge rectifier, and a 3,300 μ F, 100V electrolytic capacitor. These bulky components are mounted inside the plinth using the separate aluminium back panel as a support and as a heat sink for the transformer and the rectifier. Mains input is via a three pin connector and, of course, the rocker switch on the front panel. The 80 volt output is routed to two wander sockets on the back panel, and the CHAMP-PROG board connects to these via a couple of flying leads.

The 80V generated by this circuit is of course sufficient to give the unwary quite a tingle, and caution is advisable when making the back panel connections! A fully insulated connector could of course be used instead of the Wander plugs and sockets if required. Since the 80V is also present on the CHAMP-PROG circuit board some protection against prying fingers has been provided by mounting a tailored sheet of perspex over the parts of the board where danger exists. Five 4BA plastic mounting pillars were cut to size and cemented to the Veroboard using cyanoacrylate adhesive. The perspex safety cover is screwed to these pillars when construction is complete.



Row	Positions
4	Х, Ү, Ζ, АА, АВ, АС, ВК
5	ΑΤ, Αν, ΑΧ
9	X, Y, Z, AA, AB, AC
11	AS, AT, AU, AV, AW, AX
12	BA
13	AF, AH, AL, BP
16	AR-AY
17	W, X, Y, Z, AA, AB, AC, AF
20	BL
21	AF, AH, AL
22	AN, AR-AY, BA
24	BK, BL, BM, BP
25	AF, AH, AL
26	D-Z, AA, AB, AC
27	AR-AY
28	AN, BN
29	AF, AH, AL, BJ, BK
30	BL
31	AR-AY, BA
33	AF, AH, AL, BA, BP
35	W–Z, AA, AB, AC, BE, BF, BH, BJ
37	AF, AH, AL, AR-AY, BA
39	ВМ
42	AR-AY
43	D-U, W-Z, AA, AB, AC, BA, BE-BN
44	AF-AK
47	BL, BM
48	AF-AK, AN, AR-AY
50	W-Z, AA, AB, AC
51	BEBJ, BM
52	AF-AK, AN
56	AF–AK, AN, BE, BH–BN
57	AR-AY
⁶⁰	AF-AK, AN, AT, BC, BD
61	D-U, W-Z, AA, AB, AC
63	AS, BJ
64	AF-AK, AN, AT
65	BN
66	AS, BA
68	AF-AK, AN, AT, BD
70 ·	AS, BE, BF
72	W-2, AA, AB, AC, AF, AH, AJ, AK, AN, AT, BD
74	BN
75	
/6	
/9	
81	
62 94	
04	
Table	e of track cut positions on CHAMP PROG Veroboard

TWENTY-FOUR WAY CONNECTOR

The zero insertion force socket on the front panel is connected via a flying lead to the socket on the circuit board, and in addition to the 24 wires required for the PROM three others are needed for the "PROM POWER" switch and l.e.d. On the prototype a 27-way wiring loom was made up with the three extra wires being terminated at the board end with individual sleeved Soldercon sockets which provided a very convenient means of connection to terminal pins soldered to the Veroboard (Fig. 9.5).

The 24-pin plug required was actually made using a "header plug with top" which is available from Doram. Fine flexible wire was used for the interconnection, and this was soldered to the plug pins and brought out through a hole cut in the right hand side of the header plug top. When fully wired the header plug pins were potted in quick-set Araldite and the top clamped in position until the epoxy hardened. This method of construction has provided a satisfactory and trouble free plug which is a less expensive but more time consuming alternative to the flat-strip cable connectors used on CHAMP itself.

PROMPT FIRMWARE

When construction is complete, and the timing circuits and voltage regulator outputs have been set up correctly, you are ready to plug in the 4265 chips and run the PROMPT firmware program using the control sequence detailed last month. The 4702A containing PROMPT must be in the Chip One socket on the CHAMP main board, and arrangements have been made to enable CHAMP-PROG constructors to get their own devices programmed with PROMPT by using the CHAMP PROM programming service. Of course, once CHAMP-PROG is operating with PROMPT, CHAMP programmers will be totally independent and will never again have to rely on outsiders for programming facilities. The full listing of PROMPT is given in Fig. 9.7 and as you can see there is no wasted space in the 256 line PROM. All users of CHAMP will find it useful to study the operation of the PROMPT software, and to make this easier to follow, some words of explanation might be helpful.

DESIGN AIMS

A prime objective of the software design was that it should make CHAMP-PROG simple to use and preferably self explanatory. To this end the program has been made "interactive" with the programmer. PROMPT issues a prompting message via the keyboard display and waits for a response from the programmer, this process is repeated three times for address entry. At the termination of a programming run, CHAMP-PROG will issue the message "done" or "fail".

For address entry the CHOMP keyboard interrupt routine is "borrowed", and for the display of messages and keyboard entries the CHOMP DDRV subroutine is used. Here is a good example of why it pays to make software routines as general-purpose as possible from the outset, and to code them as subroutines callable from anywhere in program memory! The DDRV subroutine is segment, rather than BCD based and so it is quite capable of refreshing a display of alphanumeric characters when required. The generation of text messages is of course a new facility, and PROMPT includes a new subroutine, TEXT, to handle this job.

MAIN FLOWCHART

Referring to Fig. 9.6 PROMPT is entered at the top via a JUN instruction, from CHOMP. Since CHOMP itself was unaware of the presence of CHAMP-PROG during CHAMP initialisation, the first job here is to set the modes of the two new 4265 chips via WMP instructions (box 1). Next, (box 2) the prompt message "Adr 1" is loaded into the display RAM buffer register by the subroutine TEXT, and then an interruptible display loop is entered to await keyboard response (boxes 3 and 4). The continuous looping automatically refreshes the display via DDRV, while accepting up to three hexadecimal digits via the INTER routine.

An exit from the loop is made via box 4 by pressing

the ENTER DATA button. At this time, a three digit hexadecimal address should be resident in 4040 registers C, D and E, and of course visible on the left of the display. The next job is to store Adr 1 away in its appointed storage locations (box 5) and to change the display message to Adr 2 (box 6). In this case it is not necessary to change the message radically, and so rather than employ TEXT once more, the display buffer is modified directly to save program lines. Boxes 7 and 8 are of course identical to boxes 3 and 4 also boxes 11 and 12 further down, and this makes them ideal subroutine candidates. (In fact a subroutine ENTERL *does* contain this pair of boxes, but for the purposes of our flow chart these activities have been included individually, to clarify program action.)



Fig. 9.4. Physical dimensions of CHAMP-PROG chassis

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Fig. 9.5. Cable loom arrangement for z.i.f. socket

With three new hexadecimal digits entered, ENTER DATA is pressed once more and the Adr 2 data stored away in RAM register 2 where it will later be needed for comparison with Adr 1 (box 9). The display is now modified to show Adr 3, and the interruptible display loop again used for address entry. Adr 3 data is left in the 4040 registers C, D and E, and so box 13 does not so much represent an *action* but rather it is a reminder that no action takes place in this case.

Next the keyboard display is blanked by writing OOH to ports X and Y of the CHAMP 4265 so that in the absence of a refresh loop, a single display digit does not remain on continuously.

Box 15 represents a largish subroutine called WUNBYTE which has the job of programming a single PROM location each time it is called. This subroutine reads data from the appointed area in program RAM, sends it out together with Adr 3 to the CHAM-PROG 4265 chips, initiates a program cycle via 4265 number 1 port Z3, and waits during a software delay of about 540 milliseconds before reading the results of its programming and storing them away in RAM register 2 before returning to the main program.



In box 16 a subroutine called MATCH is used to compare a copy of the PROM input data stored in RAM with the output data read by WUNBYTE after its cycle. MATCH returns a flag which is tested in box 17, where a conditional jump (JCN) either aborts operations if the output is bad, or passes on to an address compare operation using MATCH (box 18) if the output is good.

Address comparison is needed to check whether all necessary locations have been programmed. In this case

further programming is suspended if a *good* comparison results. If on the other hand Adr 1 and Adr 2 are not yet the same, then Adr 1 and Adr 3 are incremented to point to the next source address and the next destination (PROM) address respectively, before a JUN loop to the label MORE is carried out (boxes 20 and 21).

Boxes 22 and 23 load the base addresses of text messages stored in PROM before jumping to a routine which loads the message into the display using TEXT

PROMPT

	LINE	ROM CODIN	G LABEL	OFERATION	OPERAND	COMMENTS
	0.	D9	PROHPT	LDM	9	LOAD DELASRE CODE
	1	FD		DCL		SPET PAN BANK I
	ê	88		XCH	8	94 -> 99
-	1 3	29		SPC	a	
-	4	DA	-	IDH	7	SEGECT 4265 CHIP
	6	ET	_	EUT I	4	
			-	WMP		SET TO MODE 4
-	5	DF		LDM	F	BIT SET PORT Z3
-	- '	EO		WRM		
_	8	DZ		LDM	2	
_	9	FD		DCL		SERT RAM BANK 2
	A	29		SPC	q	SEDT AZIC CHIP
	P	DA		LDM	1	SELECT GLESS CHIT
-	0	FI		LUID		1
	-	DO		WHP	-	SET TO MODE 6
-	-	PO		LDM	0	
_	S	+D		DCL		SELECT RAMBANK O
	F	OB		SBI		SELECT REG. BANK I
1	1 0	20		FIM	0	SET UP SALE ADDRESS
	1	F4		F	4	OF MESSAGE "Adr I"
	2	51		JMS		LOAD NESACE INTO
		EI		TE	T	Tic Di A
		51	-	THE		Loop Eng Admit Kar and
-	4	c o			00.	LOUT POL NOF I TE TUDAD
		20		ENI	CK.L	ENTRY & ENTER DATA
-	0	AU		LD	D	KRET Adri LEAST SIG
		63		XCH	3	PUT IN R3
_	8	AC		LD	C	GET Adri MID
	4	82		XCH	2	PUT IN 82
	A	AE		LD	E	GET Adri MOST SIG
	В	84		XCH	4	Ditter DA
		51		THE	T	CODI LONG CLC O MIN
		Do		102		LUT LENSI SIG TIP
	4	00		L-41	JKI	OF AGE! TO KAM
	B	20		FIM	0	CODE FOR TEXT
	F	68		6	B	OF "2"
1	2	51		THE		CHANGE MARAY TO
d	-	01		AN	10	MAL ON
	+	51		AD	NO	ndr Z
-	6	51		JHS		LOOP FOR Adr-2 KEYBOARD
	i.	CO		ENT	ERL	ENTRY & ENTER DATA"
	4	28		FIM	8	SET UP Ad-2 RAM
	5	22		2	2	ADDRESS
_	6	29		SRC	9	
	7	AD		LD	D	IFATSIG Adr2 TO PAM
	8	EO		LIDM	-	The second second second
	0	10		NAIT	9	
		20	-	INC	7	
-	•	49		DKC		
		Ac		- Price	9	MA ALS - PAL
-	B	AC	_	LD	4	MID Adra TO RAM
	B	AC EO		LD NRM	4	MID Adra TO RAM
	B C	AC EO 20		LD NRM FIM	9 4	MID Ad-2 TO RAM
	B C) B	AC EO 20 E9		LD WRM FIM E	9 4 0 q	MID Adra TO RAM
	BC	AC EO 20 E9			9 6 9	MID Adra TO RAM
	B C B F Z	AC EO 20 E9 51		LD WRM FIM E JMS	000	MID Adr2 TO RAM GET CODE FOR TEXT OF "3" COMMENTE DISPLAY TO
1	B C B F B C	AC EO 20 E9 51 B6		LD WRM FIM E JMS	0 0 0 0 0	MID Adr2 TO RAM GET CODE FOR TEXF OF "3" CHANGE DISPLAY TO "Adr 3"
1	B C S F 3 0 1	AC EO 20 E9 51 B6 51		LD WRM FIM E JMS JMS	0 0 20	MID Ad-2 TO RAM GET CODE FOR TEXF OF "3" CHANGE DISPLAY TO "Ad-3" A STORAGE LOOP FOR Ad-3 KEYDOARD
1	B C B F 3 C F 3 C 1 2	AC EO 20 E9 51 B6 51 C0		LD WRM FIM E JMS JMS ENT	0 9 9 9 9 9	MID Adr2 TO RAM GET CODE FOR TEXT OF "3" CMMXGE DISPLAY TO "Adr 3" LOOP FOR Adr3 KEYDOARD ENTRIES & EMER DATA
1	B C B F 3 0 1 2 3	AC EO 20 E9 51 B6 51 Co 28		LD WRM FIN E JNS ENT FIN		MID Adr2 TO RAM GET CODE FOR TEX F OF "3" "Adr 3" LOOP FOR Adr3 REYDOARD ENTRIES & ENTRE DATA
1	B C S F 3 C 1 2 3 4	AC EO 20 E9 51 B6 51 Co 28 80		LD WRM FIM E JMS ADJ JMS ENT FIM 8	0 9 9 9 8 8 0	MID Adr2 TO RAM GET CODE FOR TEXF OF "3" CMANGE DISPLAY TO "Adr 3" LOOP FOR Adr3 KEYDOARD ENTRIES & EMER DATA
1	B C J B F S C 1 2 3 4 5	AC EO 20 E9 51 86 51 C0 28 80 29		LD WRM FIM E JMS JMS ENT FIM 8 SRC	9 0 9 10 8 8 0 9	MID Adr2 TO RAM GET CODE FOR TEXF OF "3" CMMXGE DISPLAY TO "Adr 3" LOOP FOR Adr3 KEYDOARD ENTRIES & COMER DATA SELECT CHAMP 4265
1	B C F S C 1 2 3 4 5 6	AC EO 20 E9 51 B6 51 Co 28 80 29 Fo		LD WRM FIM E JMS AD JMS ENT FIM SRC CLB	0 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	MID Ad-2 TO RAM GET CODE FOR TEX F OF "3" CMANSE DISPLAY TO "Ad-3" LOOP FOR AD-3 KEYDOARD ENTRIES & ENTRE DATA SELELT CHAMP 4265
1	B C S F S C S C S C S C S C S C S C S C S	AC EO 20 E9 51 Co 28 80 29 FO E5		LD WRM FIM E JMS ADI JMS ENT FIM 8 SRC CLB WRI		MID Adr2 TO RAM GET CODE FOR TEXF OF "3" CMANGE DISPLAY TO "Adr3" LOOP FOR Adr3 KEYDOARD ENTRIES & EMER DATA SELELT LHAMP 4265) BLANK
	B C S F S C S F S C S C S C S C S C S S C S S C S S S C S	AC EO 20 E9 51 86 51 Co 28 80 29 FO E5 E5		LD WRM FIM JHS FIM FIM SRC CLB WRI WP2		MID Adr2 TO RAM GET CODE FOR TEX F OF "3" COMMAGE DISPLAY TO "Adr 3" LOOP FOR AIRS REYDOARD ENTRIES REMTER DATA SELELT LHAMP 4265) BLANK
1	B C) B F F S C 1 2 2 3 4 5 5 6 7 8 8	AC EO 20 E9 51 B6 51 Co 28 29 FO E5 E6 Co		LD WRM FIM E JMS ADJ JMS ENT FIM SRC CLB WRI WR2		MID Adr 2 TO RAM GET CODE FOR TEX F OF "3" CMANGE DISPLAY TO "Adr 3" LOOP FOR AIRS KEYDOARD ENTRIES & EMER DATA SELECT CHAMP 4265) BLANK I DISPLAY
1	B C) B F F S C 1 2 2 3 4 5 5 6 7 7 8 8 9	AC EO 20 E9 51 B6 51 C28 80 29 FO E5 E6 51	More	LD WRM FIN E JNS ENT FIN 8 SRC CLB WRZ JNS		MID Adr2 TO RAM GET CODE FOR TEX F OF "3" CMANGE DISPLAY TO "Adr3 " LOOP FOR Adr3 KEYDOARD ENTRIES & EMER DATA SELET CHAMP 4265) BLANK I DISPLAY PROGRAM ONE PROM
1	B C S F S C 1 2 3 4 5 5 6 7 8 9 8 9 4	AC EO 20 E9 51 B6 51 C0 28 28 28 28 29 F0 E5 51 5A	MORE	LD WRM FIM STHS FIM SRC CLB WRI WRZ JMS WUNB	U Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q	MID Adr2 TO RAM GET CODE FOR TEX F OF "3" COMMAGE DISPLAY TO "Adr 3" LOOP FOR AIR3 KEYDOARD ENTRIES REMER DATA SELELT CHAMP 4265) BLANK) DISPLAY PEDGRAM ONE PROM LOCATION
1	B C F F C C C C C C C C C C C C C C C C	AC EO 20 20 51 86 51 Co 28 80 29 29 29 29 29 29 29 50 ES E6 61 5A 20	MORE	LD WRM FIM E JMS EVT FIM SRC CL8 WR1 WR2 JMS WUNB FIM		MID Adr2 TO RAM GET CODE FOR TEX F OF "3" CHANGE DISPLAY TO "Adr 3" LOOP FOR AIRS REYDOARD ENTRIES REMER DATA SELECT CHAMP 4265) BLANK J DISPLAY PROGRAM ONE PROM LOCATION SET UP ADDRES OF
1	B C S F 3 C 3 C 7 B C 3 A B C	AC EO 20 20 51 51 CO 28 29 FO E5 51 CO 28 29 FO E5 51 51 20 21 20 51 CO 28 29 20 20 51 20 20 20 20 20 20 20 20 20 20	MORE	LD WRM FIN E JMS ADI JMS ENT ENT FIN SRC CL8 WR2 JMS SRC CL8 WR2 JMS IMS SRC CL8	2 0 9 10 8 8 0 9 9 9 9 9	MID Adr2 TO RAM GET CODE FOR TEXF OF "3" "Adr 3" HOOP FOR AIRS REYDOARD ENTRIES REMER DATA SELET CHAMP 4265) BLANK) DISPLAT PEDGEAM ONE PROM LOCATION SET UP ADDRESS OF (N) DATA FOR MATCH
1	B C S S C C C C D	AC EO 20 20 51 51 Co 28 80 28 80 29 FO E5 E6 51 5A 20 10 22	MORE	LD WRM FIM E JMS ADJ JMS ENT FIM 8 SRC CLB WRI WRI WRZ JMS NONB FIN L MRZ	4 0 9 Jo FRL 8 0 9 9 9 9 9 9 2	MID Adr 2 TO RAM GET CODE FOR TEX F OF "3" COMMISSE DISPLAY TO "Adr 3" LOOP FOR AIR SKYDDARD ENTRIES & EMTRE DATA SELELT CHAMP 4265) BLANK J DISPLAY PROFRAM ONE PROM LOCATION SET UP ADDRES OF IN" DATA FOR MATCH SET UP ADDRES OF
1	B C S C S C S C C C C C S C C C S C C C S S C C C S S C C C S S C C C S S S C C C C S S S C C C C S S S C C C C C S S S S C C C S S S C C S S S C C S S S S C S	Ac EO 20 20 20 20 20 20 51 Co 28 29 29 29 ES ES ES 51 20 29 29 29 29 20 29 29 20 29 29 29 29 29 29 29 29 29 29	More	LD WRM FIN E JMS ADI JMS ENT FIN SRC CLB WR2 JMS UNB FIN CLB WR2 JMS 2	2 0 9 10 10 10 10 10 10 10 10 10 10	MID Adr2 TO RAM GET CODE FOR TEX F OF "3" CMMXGE DISPLAY TO "Adr 3" LOOP FOR Adr3 KEYDOARD ENTRIES & ENTR2 DATA SELECT CHAMP 4265) BLANK I DISPLAY PROGRAM ONE PROM LOCATION SET UP ADDRESS OF (N' DATA FOR MATCH SET UP ADDRESS OF
1	B C C B F C C C C C C C C C C C C C C C	Ac EO 20 20 20 51 51 51 CO 28 51 CO 28 29 FO ES E6 51 5A 20 10 22 20 E1 E4 E4 E4 E4 E4 E4 E4 E4 E4 E4	MORE	LD WRM FIN E JNS ADI JNS ENT FIN 8 SRC CL8 WRI WRI WRS WUNB FIN L FIN 2	4 0 9 30 58 8 0 9 9 9 9 9 9 9 0 0 2 0	MID Adr2 TO RAM GET CODE FOR TEXF OF "3" COMMAGE DISPLAY TO "Adr 3" LOOP FOR Adr3 KEYDOARD ENTRIES & ENTRE DATA SELELT CHAMP 4265) BLANK) DISPLAY PROGRAM ONE PROM LOCATION SET UP ADDRESS OF (N' DATA FOR MATCH SET UP ADDRESS OF OUT DATA FOR MATCH

PAGE	LINE	ROM	CODING	LABEL	OPERATION	OPERAND	001010000
1	4	AL	1		MOTON		COMMENTS
	1	20	1	1000	Find	-	C
		100	1		FIM	0	SET UP GASE ADDRESS
		P B	1	+	-	8	OF TEXT HESAGE FAIL
		11C		-	JNZ		STOP & DISPLAY "FAIL"
	4	DA			FIN	IS	IF AC NOT ZERO.
	5	20			FIM	0	SET UP ADDRESSOF Adr.
	E	12			1	2	FOR MATCH
_	Ī	22			FIM	2	CET UP ANDER OF Ade
	6	22			2	2	Col MATTH
	0	51			TMC	-	Caugage inter
	Í,	A		-	115		COMPANICE CURRENT HOP
	1	100			MA	ГСН	WITH Adr 2
1am		10			FIM	0	SET UP BASE ADDRESS
_	0	FC		-	F	C	OF TEXT HEARE donE
		14	-		JZ		STOP & DISPLAY
	E	DA			FIN	115	"donE"
	F	AO			580		SART PER-RANK O
1	5	32			157	-2	3
	1	52			DAS	T	Luxon ad-1
	1	62	-		INC	2	The second second second
1	5	51		DACE	THE	4	
6	33	0		THE .	JHS		NEW HOLT TO KAM
-	4	00	_		LAD	RI	1]
_	τ,	10			ISZ	D	1
	6	39			Hof	RE	INCREMENT Adr3
	7	66			INC	C	7
	9	41			TINK		I MP PACK GOD HELT BYT
		20	-	1	UUN	2.0	Din and Hole MEAL BTI
	-	34		-	MO	KE	PEND OF MAIN MOGT.
<u> </u>	2 4	28		WUNBTTE	EIH	8	SUBROUTINE PROGS BYTE
	В	00			0	0	
	C	29			SRC	9	SELECT PROGRAM MEMOR
	D	A4			LD	4	SNIPCE CHIP
-	B	EI			LIND		Loce sin .
-	P	22		-	WITE	-	1
		23			SKC	3	SEND OUT Adr
1	6 -	OE			RPM		READ FIRST NIBBLE
	1	F4			CHA		CONTRACT IT.
	2	BI			YCH	1	PUT IT IN PI
	3	OF			POM		OCAN CERAND NUPPLE
_	1	CA	-	-	644.0		KEND SELOND NIGDLE .
		104			CMH		COMMEMENT IT
	2	00			XCH	0	PUT IT IN RO
	6	28	_		FIH	8	1
	7	10			1	0	
	8	29			581	9	PUT IL DATA IL
	9	AI			1.0		OAM FOR US
	-	EA			1.101		ANT POK USE
-	-	ED			WEA	0	NITH MATCH
	B	64		+	INC	4	LATER.
	C	29			SRC	9	1
)	AO			LD	0	1
	2	EO			WRM		/
	F	Da			LDM	2	
1	30	En		1	Dell	-	COT PAUL PAULO
	T	20		1	DEL	0	HUNCT KAM BANK2
-	1	6.0		1	EIN	8	1
-	- 2	80		1	8	0	SERT 4265 No 2
-	3	29		+	SRC	9	/
	4	AL	_		LD	1	0
	9	E6			WR	2	L'IN DATA TO PORTS
	0	AO			10	0	1482
	1 ,	53			LUN2	2	1.~
-	-	67		1	WIK	3	5
_		וט		+ +	LDM	1	11
	- 9	FD			DEL		LOAD Adr3 TO PORTS
	A	AD			LD	D	1 W&X 4265 NO 1
	10	EA			INP O		17
	2 1			1	in the second se	1	11
	0	AC					14
	0	AC				-	1
	C D	AC			WRI	-	1
	D B	AC ES DE			WRI LOM	E	TURN ON PROGRAM

Fig. 9.7. PROMPT program listing

MK14-the only low-cost keyboard -addressable microcomputer! The new Science of Cambridge MK14 Microcomputer kit

The MK14 National Semiconductor Scamp based Microcomputer Kit gives you the power and performance of a professional keyboard-addressable unit – for less than half the normal price. It has a specification that makes it perfect for the engineer who needs to keep up to date with digital systems, or for use in school science departments. It's ideal for hobbyists and amateur electronics enthusiasts, too.

But the MK14 isn't just a training aid. It's been designed for practical performance, so you can use it as a working component of, even the heart of, larger electronic systems and equipment.

MK14 Specification

- * Hexadecimal keyboard
- ★ 8-digit, 7-segment LED display
- ★ 512 x 8 Prom, containing monitor program and interface instructions
- ★ 256 bytes of RAM
- ★ 4MHz crystal
- ★ 5V stabiliser
- ★ Single 6V power supply
- Space available for extra 256 byte RAM and 16 port 1/0
- Edge connector access to all data lines and 1/0 ports

Free Manual

Every MK14 Microcomputer kit includes a free Training Manual...It contains



operational instructions and examples for training applications, and numerous programs including mathroutines (square root, etc) digital alarm clock, single-step, music box, mastermind and moon landing games, self-replication, general purpose sequencing, etc.

Designed for fast, easy assembly Each 31-piece kit includes everything you

Each 31-piece kit includes everything you need to make a full-scale working microprocessor, from 14 chips, a 4-part keyboard, display interface components, to PCB, switch and fixings. Further software packages, including serial interface to TTY and cassette, are available, and are regularly supplemented.

The MK14 can be assembled by anyone with a fine-tip soldering iron and a few hours' spare time, using the illustrated step-by-step instructions provided.

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Phil Pittman, Wireless World, Nov. 1977.

The low-cost computing power of the microprocessor is already being used to replace other forms of digital, analogue, electro-mechanical, even purely mechanical forms of control systems.

TG

/AT. and p&p)

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Name -		_	
Address	(please	print)	

ddress (please print) ____

PE/5

Allow 21 days for delivery.



(box 24) and then enters a display loop using DDRV (box 25). From the foregoing you can see that the program always terminates with one of the two messages "fail" or "done" displayed, and that return to CHOMP must be carried out by use of the RESET button.

SUBROUTINE FLOWCHARTS

The flowchart of the ENTERL or "interruptible loop" subroutine is shown in Fig. 9.8, note the use of EIN and

DIN to control interrupts, and the way that the ENTER DATA flag is read using RDR and tested using JCN; the WRR before the BBL is used to reset the ENTER DATA flag. Fig. 9.9 shows the TEXT subroutine and its look-up tables. This subroutine is passed the base address of a text message table in register pair O,1 so that the FIN command can be used to fetch the message a byte at a time.

In PROMPT only four-character messages are per-

PROMPT

PAGE	LINE	ROM CODING	LABEL	OPERATION	OPERAND	MAN PRITS
1	8	OB		SRI		COMPACTO IN
	1	22		FIM	2	1
	1	19		1	9	DESCT DAAY
	3	24	1	EIM	4	Courses
	4	9A		a	4	CONTER.
1	8 5	72	MAIT	157	2	5
	E	85	14.111		-	1
-	T	73		157	2	+
_	8	85	-	I D L	T	STOME NOAN
	0	74	1	157	10	DELAT
		85	_	1101	-	11
	B	75		157	5	
-	c	85		h10.	+	
	,	DE		LDM	E	TURN OFF
	E	EO		LA HO and		DON DAM DIN SEE
-	0.0	74	NEIAU	15-7	4	TRUCKATI FUCSES.
	0	0E	L DE WIT	102		Down Roman
	1	25		157	AT S	CHELAY BEHOKE
	2	SE	1	DE1	AV	
_	2	D2		IDM	2	1
		FD	-	N.		(SOCT 12/5 147)
	4	29	-	Sac	a	AUEC1 4265 NO 2
		EC	1	PDO	-	K
	1 ,	ec	1	XCH	6	Don Dry had and
	8	50	-	RCH	2	LEEMS PROM OUT DATA
_	0			KDI		PUT IN KS, K4.
	4	54	+	XCH	4	2
	A	Vo	-	LIDM	0	
	D	PD	+	DCL	-	SELECTRAM BANK O
		28	-	FIM	8	CHIPO KEG-2
		20	-	2	0	11
	S S	29	-	SKC	9	1
-	F.	AS		LD	5	
1	A -	EO		WRM		
	1	69		INC	9	PUT OUT' DATA INTO
	2	29		SRC	9	RAM.
_	3	A4	2	LD	4	1
_	4	EO	1	WRM)
	5	CO		BBL	0	END OF WUN BYTE.
1	AE	FA	MATCH	STC		SUBROUTINE COMPARES 2 BYTES .
	1	DE		LDM	E	PRESET NIBBLE COUNTER .
_	8	85		XCH	5	
1	A 9	F3	LOOP2	CHC		
	A	21		SRC	+	
	B	E9		RDM		READ FIRST NIBBLE.
	c	23		SRC	3	
	,	E8		SBM		SURTRACT SECOND NIBBLE.
	8	61		INC	1	LINCREMENT RAM
	F	63		INC	3	ADDRESSES .
1	Bo	14		JZ		· · · · · · · · · · · · · · · · · · ·
_	1	83	-	SKI	P	
	2	CI		BBL	1	IF AC. NOTO BBLI
1	Bi	75	SKIP	152	S	NEXT PAIR OF
_	4	A9	1	LOOP	2	NIBBLES ?
	4	CO	-	BBL	0	IFACSTILO BBLO.
1	Bo	28	ADNO	Fin	8	SUBROUTINE MODIFIES DISPLAY
	7	OE		0	ε	SELECT RAN BANK O.
_	8	29		SRC	9	CHIP O, CHAR E.
	<u>\$</u>	Ao		LD	0	PUT FIRST NIBBLEIN
	A	EO		WRM		CHAR E.
	В	69		INC	9	NEXT CHAR
	C	29	1	SRC	9	
	D	AI		LD	1	PUT SECOND NIBBLE
	E	EO	T	WRM	-	IN CHAR F.
	F	co		BRI	0	END OF ADNO
	-		-			the state of the s

PAGE	LINE	ROM CODING	LABEL	OPERATION	OPERAND	1
1	6	N	EL HERD I	F		COMMENTS
	1	20	ENIERL	EIN	0	SURKOUTINE: KEY ENTRY LOOP
_	-	10		FIM	8	11
		40		4	0	Y WEAD SWITCH PLACES.
		29		SKC	9	
**	4	EA		RDR		2
	2	F6		RAR		PUTENTER DATA IN CY
	5	00	-	DIN		DISABLE INTERRUPTS .
	7	50		JMS	-	DISPLAY NEXT DIGIT.
	8	81		DDR	JV	1
_	9	12		JC		IF ENTER DATA NOT
		CO		ENTE	RL	PRESED THEN LOOP .
	B	28	1	FIM	8	NOW PRESED SO
	C	50		5	0	CLOAR SUNTH CLASS
	Γ,	29		SEC	q	CLEME SWITCH POINTS.
	B	E2		4100		
	F	0		Dei	0	END OF ENTRY
	00	20	LANGI	C.M		ENDOF ENTERL
		12	CAPURI	FILL	8	SUBKOWENE ANTI TO KAM.
	1	29	-	500	0	SEUCT CHAR BROAD
		AZ		SKC	4	CHIPO, KEG-1, CHARC 2.
		50		LU	5	1
	1 4	60		WKM	0	LEAST SIG TO KAM.
	5	57		INC	Ч	But a sta
	6	29		SRC	9	INEXT CHAR
	7	A2		LD	2	2
	8	EO		WRM	1	I HID TO RAM .
	4	CO		BBL	0	END OF LADEL.
1	DA	51	FINIS	JMS		SUPROUTINE : END OF PROG .
117	B	EI		TEXT	F	1000 TEXT TO DISPAY
1	DO	OA	LOOPX	SBO		Serie (KNI IL MIRCHI
	D	50		THS		DICOLAN NEWT DIGIT
	R	BI		DDD:	1	PISTERT MENT PIGTI .
				DUK	N	
	F	41		-T		0.0.0
_	F	41		JUN		LOOP INTIL RESET.
1	F	41		JUN	×	END OF FINIS.
1	F E	41	TEXT	JUN LOOT	2	LOOP WITH RESET. END OF FINIS. SUBBOUTINE: LOADS TEXT.
1	F E 1 2	41	TEXT	JUN LOOF FIM	2 2 8	LOOP WITH RESET. END OF FINIS. SUBBOUTINE: LOADS TEXT. SERT RAM BANK Q. O. RO, LE
1	F E 1 2 3	41	TEXT	JUN LOOF FIM O	2 2 8 2 2 8 2 2	LOOP WITL RESET. BUD OF FINIS. SUBBOTINE: LOADS TEXT. SORT RAM BANK G (0, 80, 18 PREFT RYTE COUNT.
	F E 1 2 3 4		TEXT	JUN LOOF FIM O LOM	X N B C S	LOOP WUTIL REFET. END OF FINIS. SUBBOUTINE: LOADS TEXT. SCIET RAM BANK GLOSED, CE RUBET RAM BANK GLOSED, CE
	F E 1 2 3 4 E 5		TEXT	JUN LOOF FIM O LOM XCH FIN	× ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	LOOP WITL RESET. END OF FINIS. SUBBOUTINE: LOADS TEXT. SCIET RAM BANK G.(0.90.18 PROBET BYTE COUNT. LOOK UP IN TEXT TABLE.
	F E 2 3 4 E 5 6		TEXT	JUN LOOT FIM O LOM XCH FIN INC	× 2 8 5 6	LOOP WITL RESET. END OF FINIS. SUBBOTINE: LOADS TEXT. SERT RAM BANK QUOD.02 REGET BYTE COUNT. LOOK UP IN TEXT TABLE. NEXT TABLE LINE
	F E 1 2 3 4 E 5 6 7		TEXT	JUN LOOS FIM O LOM XCH FIN INC SRC	× 2 00 0 5 6 1 3	LOOP WITL REFT. END OF FINIS. SUBBOTINE: LOADS TEXT. SERT RAM BANK Q.C. O.C. PREFT RAM BANK Q.C. O.C. PREFT RAM BANK Q.C. O.C. NEXT TABLE LINE RAM SEC
	F C 1 2 3 4 C 5 6 7 8		TEXT	JUN LOOT FIM O LOM XCH FIN INC SRC LD	× 2 00 0 5 6 1 3 7	LOOP WITL REFET. END OF FINIS. SUBBOUTINE: LOADS TEXT. SCIET RAM BANK GLOSED, CE PREFT RYTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SRC FIRST NUBBLE TO RAM
	F C 1 2 3 4 C 5 6 7 8 9		TEXT	JUN LOOS FIM O LOM XCH FIN INC SRC LD WRM	× 2 8 5 6 1 3 7	LOOP WITL REFT. END OF FINIS. SUBOTINE: LOADS TEXT. SUBT RAM BANK QUOD. OF REGET EVTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SEC FIRST NUBBLE TO RAM DURAY RUFFER.
	F C 1 2 3 4 C 5 6 7 8 9 8		TEXT	JUN LOOS FIM O LDM XCH FIN INC SRC LD WEM INC	× 2 8 6 1 3 7 3	LOOP WITL RESET. END OF FINIS. SUBBOTINE: LONDS TEXT. SERT RAM BANK QUOLO, SO REBET RAM BANK QUOLO, SO REBET RAM BANK QUOLO, SO NEXT TABLE LINE RAM SRC FIRST NIBBLE TO RAM DURAY RUFFER. NEXT FAM CHAR-
	F C 1 2 3 4 C 5 6 7 8 9 9 8 8 9 8 8 8 8		TEXT	JUN LOOS FIM O LOM XCH FIN INC SRC LO WRM INC SRC	X 2 8 0 5 6 1 3 7 3 M	LOOP WITL REFT. END OF FINIS. SUBDOTINE: WADS TEXT. SELET RAM BANK QUOLO, CE REGET RAM BANK QUOLO, CE REGET RAM BANK QUOLO NEXT TABLE LINE RAM SRC FREST NIBBLE TO RAM DURAY RUFFER. NEXT RAM CHAR-
	F C 1 2 3 4 C 5 6 7 7 8 9 9 8 8 9 8 8 9 8 0 7 8 8 9 9 8 8 8 9 9 8 8 8 8 8 9 9 8 8 8 8 9 8		TEXT	JUN LOOT FIM O LOM XCH FIN INC SRC LD WRM INC SRC LD UNC SRC LD	× 2 8 5 6 1 3 7 3 8 6	LOOP WUTIL RESET. END OF FINIS. SUBSOTING: LOADS TEXT. SCIET RAM BANK GLOBO GE REBET RYTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINF RAM SRC. FREST NIBBLE TO RAM DURAS RUFFER. NEXT RAM CHAR- SECOND NUBBLE TO
	F E 2 3 4 E 5 6 7 8 9 A B C 3		TEXT	JUN LOOS FIM O LOM XCH FIN INC SRC SRC LD WRM INC SEC LD WRM	2 8 5 6 1 3 7 3 3 6	LOOP WITL REFT. END OF FINIS. SUBDOTINE: LONDS TEXT. SERT RAMBANK QUOLO, OF REBET RAMBANK QUOLO, OF REBET RAMBANK QUOLO, OF NEXT TABLE LINE RAM SEC FIRST NUBBLE TO RAM DURAY BUFFER. NEXT RAM CHAR- SELOND NIBBLE TO RAM DIFFER.
	F U 1 2 3 4 U 1 2 3 4 4 5 6 7 8 9 4 B C 3 8 8 0 1 1 1 1 1 1 1 1 1 1 1 1 1		TEXT	JUN LOOT FIT O XCH FIN INC SEC LD WEM INC SEC LD WEM	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	LOOP WITL REFET. END OF FINIS. SUBBOTINE: LOADS TEXT. SERT RAM BANK Q.Q.20, 22 PRBET RYTE CONNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SRC FIRST NIBBLE TO RAM DURLAY RUFFER. NEXT RAM CHAR. SECOND NIBBLE TO RAM DISPLAY BUFFER. NEXT RAM CHAR.
	F U 1 2 3 4 5 6 7 7 7 7 8 9 8 8 8 8 8 8 7 8 7 7 8 9 8 8 8 8		TEXT	JUN LOOS FIM O LOM XCH FIN NC SRC LD WRM INC LD WRM INC	2 2 8 C 5 6 1 3 7 7 3 M 6 3 5	LOOP WUTIL RESET. END OF FINIS. SUBSOTING: LOADS TEXT. SCIET RAM BANK QUORUS REGET RYTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SRC. FIRST NIBBLE TO RAM DURAY RUFFER. NEXT RAM CHAR. SECOND NUBBLE TO RAM DISPLAY BUFFER. NEXT RAM CHAR.
	F U U 1 2 3 4 5 6 7 7 7 8 9 4 8 8 8 0 7 8 9 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8		LOOP 1	JUN LOOT FIM O LOM XCH FIN NIC SRC LO WRM INC INC INC INC	× 2 2 2 2 2 2 2 2 2 2 2 2 2 2	LOOP WITL REFET. END OF FINIS. SUBDOTINE: LOADS TEXT. SERT RAM BANK GLOBOLGE REGET RYTE COUNT. REGET RYTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SRC FIRST NIBBLE TO RAM DURLAY BUFFER. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RAM CHAR.
	P U U 1 2 3 4 4 5 6 7 6 9 ▲ B C 3 B C 3 B C 1 B C 1		TEXT	JUN LOOT FIN LOM LOM XCH FIN NC SRC LD WRM INC LD NC ISZ LOOT SRO	× 2 8 C 5 6 - 1 3 7 3 M 6 3 5 -	LOOP WITL REFET. END OF FINIS. SUBBOTINE: LOADS TEXT. SERT RAM BANK Q.Q.R.Q.R REBET RAM BANK Q.Q.R.Q.R REBET RAM BANK Q.Q.R.Q.R REST RAM BANK Q.C.R NEXT TABLE LINE RAM SRC FIRST NIBBLE TO RAM DIRLAY BUFFER. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RAM CHAR.
	P L 1 2 3 4 4 5 6 7 7 6 9 A B C 3 3 B C 7 B F 0 1 2 1			JUN LOOT FIM O UDM XCH FIN NC SRC UD NR UD NC SRC LD UD NR NC SRC LD SRO BBL	x 2 00 0 5 6 1 3 7 3 M 6 3 5 1 - 0	LOOP WITL REFT. END OF FINIS. SUBSOTING: LOADS TEXT. SCRET RAM BANK GLOOD, GLOB REBET RYTE CONNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SRC. FIRST NIBBLE TO RAM DURAY RUFFER. NEXT RAM CHAR. NEXT RAM CHAR. NEXT BYTE? END OF TEXT.
	P U 1 2 3 4 4 5 6 7 7 8 9 9 A B C 7 B F 1 1 2 3		LOOP	JUN LOOT FIM O LDM XCH FIN INC SRC LD MRM INC SRC LD MRM INC SRC SRC NC SRC SRC NC SRC SRC NC SRC NC SRC NC SRC SRC SRC SRC SRC SRC SRC SR	× 2 8 C 5 6 1 3 7 7 3 3 4 6 3 5 1 1 0 0	LOOP WITL RESET. END OF FINIS. SUBCOTING: LOADS TEXT. SUBET RAM BANK QUOLO. REGET RYTE COUNT. REGET RYTE COUNT. NEXT TABLE LING RAM SRC FIGST NIBBLE TO RAM DURAY BUFFER. NEXT RAM CHAR. NEXT BYTE? END OF TEXT.
	P U 1 2 3 4 4 5 6 7 7 8 9 A B 0 7 7 8 7 7 9 A B 0 7 7 1 2 7 1 2 7 1 2 7 1 2 7 1 2 7 1 2 7			JUN LOOT FIM LOM XCH FIN INC SRC LD WRM INC SRC LD WRM INC ISZ LOOT SB0 BBL NOC	× 2 8 C 5 6 1 3 7 3 M 6 3 5 1 - 0	LOOP WITLE REFET. END OF FINIS. SUBBOTINE: LONDS TEXT. SERT RAMBANK QUOLOUS REBET RAMBANK QUOLOUS REBET RAMBANK QUOLOUS REST RAMBANK QUOLOUS NEXT TABLE LINE RAM SRC FIRST NUBBLE TO RAM DURAY RUFFER. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RYTE? END OF TEXT. TEXT TABLE:
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	P U 1 2 3 3 4 5 6 7 7 6 7 7 7 8 8 0 9 2 3 9 7 7 8 7 7 9 7 7 9 7 7 9 7 8 9 7 1 1 2 3 1 2 3 1 2 3 1 2 3 1 2 3 1 2 3 1 2 3 1 2 3 1 2 3 1 3 4 1 3 4 1 3 4		LOOP 1	JUN LOOS FIM O LDM XCH FIN INC SRC LD LD NRM INC SRC LD NRM INC SRC LD NRM INC SRC LD NRM INC SRC LD R A	× 2 8 C 5 6 3 7 3 3 4 6 3 5 1 - 0 E C C	LOOP WUTIL RESET. END OF FINIS. SUBCOTINE LOADS TEXT. SUBET RAM BANK QUOLO LO REGET RYTE COUNT. LOOK UP INTEXT TABLE. NEXT TABLE LINE RAM SEC FIGST NUBBLE TO RAM NEXT RAM CHAR. NEXT BYTE? END OF TEXT. TEXT TABLE: R
	P U 1 2 3 3 4 5 6 7 7 6 9 9 9 A 8 6 7 7 7 8 8 6 7 7 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9		LOOP 1	JUN LOOT FIM LOM LOM XCH FIN INC SRC LD WRM INC SRC LD WRM INC ISZ BBL NOF BBL NOF BBL A	× 2 8 C 5 6 1 3 7 3 3 6 3 5 1 - 0 E C O 5	LOOP WITLE REF. END OF FINIS. SUBBOTINE: LONDS TEXT. SERT RAMBANK QUOLOGE REBET RAMBANK QUOLOGE REBET RAMBANK QUOLOGE REST RAMBANK QUOLOGE NEXT TABLE LINE RAM SRC FIGST NUBBLE TO RAM DURAY BUFFER. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RAM CHAR. NEXT RAME: DUD OF TEXT. TEXT TABLE: C
	P U 2 3 4 E 6 7 8 9 10 11 2 4 6 6 6 6 7		LOOP 1	JUN LOOT FIM O UDM XCH FIN NC SRC UD NC SRC LD UDM NC SRC UD NC SRC UD NC SRC SR	× 2 00 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	LOOP WUTIL RESET. END OF FINIS. SUBSOTING: LOADS TEXT. SCRET RAM BANK GLORO, GE REBET RYTE CONNT. LOOK UP INTEXT TABLE. NEXT TABLE LINF RAM SRC. FREST NIBBLE TO RAM DURAY RUFFER. NEXT RAM CHAR. NEXT BYTE? END OF TEXT. TEXT TABLE: I I I I I I I I I I I I I
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mitted to save program space, but any message of up to eight characters could be used in other applications, with appropriate table entries and a different preset for the byte counter, register 5. The messages themselves were worked out to give the most pleasing display within the restrictions of a seven segment format.

The MATCH subroutine is shown in Fig. 9.10, and here the ploy is to subtract one byte of data from the other and check if the result is zero. The result of the comparison is flagged to the main program via alternative BBL exits, BBL 0 means match, BBL 1 means no match. The addresses of the data to be compared are passed to MATCH in register pairs 0,1 and 2, 3 by the main program.

Note that source data is complemented using CMA

before programming to compensate for the 74L00 inwhich produces a delay of 540 milliseconds. For further details see page 2.17 of the MCS40 manual.

PROMPT LISTING

For PROMPT we have chosen to list the program code in the format introduced in Part 7 for the TONE program. This format differs from the cross assembler listing given in Part 6, and is used to demonstrate to all budding CHAMP programmers that hand-coding of long programs is perfectly feasible! No facilities other than CHAMP itself were used in the development of the **PROMPT** firmware.

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1A		400V	Ť	D5	60p
4A		200V	Ţ	2220	52p
4A 6A		200V		JZ20	70p 56p
6A		400V	Ť	0220	75p
6A		400V		266	80p
10A		200V	Ť	0220	87p
10A		400V	Ţ	0220	120p
Triac	s)	6000		5220	тчөр
6A	-	400V	T	0220	98p
8A 15A		600V		2220	135p
15A		400V	S	tud	220p
	. 1	TRAN	SIST	ORS	
AC127 AC128	18µ 18a	BC548 BC549	10p 10a	BRY56 DCP71	40p 120a
AC176	18µ	BCY70	15p	TIP41A	56p
AC187	20p	BCY71	15p	TIP42A	66p
AD149	70p	BD131	380	TIP2055	42#
AD161	40p	BD132	40p	TIS43	35p
AU162 AF279	40p 75n	BD133 BD137	48p 40a	2N2846 2N2905	50p
BC107	12p	BD138	40p	2N2926	12p
BC108	10p	BD139	42p	2N3053	28p
BC1080	12p	BF173	20	2N3054	52p 50p
BC1090	: 15p	BF181	30p	2N3442	130p
BC147 BC148	10p 10p	BF194 BF195	10p 10p	2N3702 2N3703	10p 10p
BC149	10p	BF196	10p	2N3704	10p
BC157	10p	BF197	12p	2N3705 ·	10p
BC159	10p	BFR39	24	2N3708	10p
BC182	12#	BFR79	28p	2N3710	10p
BC183 BC184	120	BFX29 BFX48	320	2N3819 2N3904	26p 15a
BC212	14p	BFX84	22p	2N3906	15p
BC213 BC214	14p	BFX88 BFY50	22p	2N6027	55p
BC441	32p	BFY51	18p	40673	65p
BC461	32p	BFY52	18p		
74 Serie	IS TIL	BR139	4ob		
7400	12p	7447	64p	7410,	37p
7401	14p 14p	7450	15p 14p	74121	36p 51m
7404	17p	7453	14p	74123	64p
7405	23p	7454	14p	74132	5 6 p
7406	14p	7472	29p	74150	173p
7410	14p	7473	299	74151	7 9 p
7413	62p	7474	23µ 51a	74154	730
7420	14p	7476	29p	74157	66p
7427 7430	36p 14n	7483 7485	91p 132n	74159	200p 126p
7432	28p	7486	40p	74174	110p
7437	36p	7490	46p	74179	1200
7440	зор 15р	7492	52p	74190	188p
7442	65p	7493	52p	74191	158p
7445	88p 88o	7495	73p 85e	74192	120p 120m
C-M	os	,		74367	120µ
4000	18p	4018	84p	4054	100p
4001	18p	4022	90p	4055	110p
4007	189	4023	64p	4030	90p 18p
4011	160	4027	48p	4081	18p
4012	48p 48e	4028	/6p 110e	451U 4511	132p 212p
4016	48µ	4047	78p	4528	124#
4017	84#	4049	48p	4588	256p
741	25p	ICAN 8	3302	Quad	comp
1200	55	5.40	. 7	TO din	-comp
digit	9) 40 drive	p. 556) 100 €1	p. *710	100
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7423	36p	74154	160p	4021	120p	LM377N	200	SN76013N		175p	AC125.6	200	80Y56	225p	TIP31C	68p	2N4871	60p	1N4148	4p	Plastic
7425	43p	74155	97p 97p	4022	140p	LM381N	1900	SN76018		2800	AC127/8	20p	BF167	25p	TIP32C	85p	2N5245	40p	1N5401/3	15p	3A 400V 85p
7427	40p	74157	97p	4024	90p	LM389N	180p	SN76023N		1750	AC176 AC187/8	20p	BF170 BF173	250	TIP33A	97p	2N5296	58p	1N5404/7	20p	6A 500V 107P
7430	18p	74159	250p	4025	23p	LM3911N MC1310P	1900	TAA621A		310p	AD149	600	BF178	30p	TIP34A	1240	2N5401 2N5457(8	40p		-	10A 400V 140p
7432	37p	74161	130p	4027	84p	MC1351P	110p	TAA661A TBA120		150p	AD161 AD162	450	BF180 1	350	TIP34C	160p	2N5459	40p	ZENERS		15A 400V 200p
7437	37p	74162	130p	4028	110p	MC1495L MC1496L	490p 112p	TBA641B		300p	AF114/5	30p	BF 184/5	24p	TIP35C	290p	2N0465 2N6107	45 p 70 p	2 77-33	'	15A 500V 225p
7438	37p	74164	120p	4030	67p	MC3340P	180p	TBA651 TBA800		225p	AF116//	30p	BF194	13p 11p	TIP36A	297p 360p	2N6027	60p	400m W	11p	40669 130p
7441	85p	74165	150p	4040	150p 97p	MC3360P	1200	TBA810		125p	AF139	40p	BF196	17p	TIP41A	70p	2N6254	140p	100	22p	BR100 36p
7442	75p	74167	320p	4043	100p	NE540L	225p	ZN414		140p	AF 239 BC 107/B	48p	BF 19/	490	TIP41C TIP42A	64p 76p	2N6292	70p			
7443	120p	74170	260p 750p	4045	150p	VOLTAGE P	CULATO	S. Fired			BC 108 B	10p	BF244B	40p	TIP42C	96p	3N140	97p	SCR TH	RIST	ORS
7445	108p	74173	190p	4049	64p	Plastic TO220-	-3 Terminals	1 Amo			BC109 BC109C	10p	8F257	34p	TIP2955 TIP3055	76p 60o	3N141 3N187	90p	1A 400V TI	05 05	85p
7440	750	74174	130p 97p	4054	1200	1 Amp • ve		57	7905	160p	BC147	90	BF258	390	T1S43	40p	40360	43p	3A 400V S	TUD	97p
7448	85p	74176	130p	4055	140p	5V 780	5 115p	12V	7912	160p	BC 148	11p	BF337	32p	2N698	25p 43p	40361 2	43p	16A 400V PI	astic	220p
7450	18p	74180	100p	4060	130p	8V 780	1150	24V	7924	160p	BC158 9	13p	BFR39	34p	2N706 B	22p	40411	3250	16A 600V PI	astic	270p
7453	18p	74181	3240	4068	30p	12V 781 15V 781	5 115p	Mart Dink	17 14	250	BC172	11p	BFA79	34p	21930	19p	40594	90 p	C106D 4A 4	00V Pla	stic 610
7460	180	74184	250p	4071	30p	18V 781	8 115p	suitable fo	r TO220	1.70	BC177	20p	BFR80 1	34p	2N1131 2	25p	40635	80p	MCR101 ,A	15V TC	192 35p
7470	38p	74185	190p	4072	300	100mA - vi	TO92			1000	BC179	20p	BFW10	900	2N1306 7	75p	40636	140p	2N4444 BA 6	00V Pla	stic 200p
7473	360	74190	140p	4078	30p	5V 78L	05 70p	100mA	- ve 79L05	80p	BC182/3	12p	BFX29/30	34p	2N1613	22p	40841	90p	2N 5060 0 8.	A 30V T A 100V 1	092 36p
7474	37p	74191	140p	4081	30p	12V 78L	12 70p	12V	79L12	80p	BC187	32p	8FX86 7	30 p	2N 1893	32p	40871	85p	2N5064 0 8	A 200V	1092 43p
7476	37p	74193	120p	4093	104p	15V 78L	15 70p	15V	79L15	800	BC212 BC213	14p	BFX88	300	2N2102 2N2160	80p		300			
7480	54p	74194	160p	4510	140p	LM309K TO	150p	MC1468 TRA625B	DIL	300p	BC214	16p	BFY51	22p	2N2219	22p		6810			7N425E
7482	90p	74196	130p	4516	130p	LM327N DIL	275p	7805K	TO3 .	150p	BC461 BC478	40p 32p	BFY52	22p	2N2222 2N2369	22p	128 ×	8 S1	TATIC F	RAM	64 20
7483	99p	74197	130p 270p	4518	140p	VARIABLE:					BCY70	20p	BRY39	48p	2N2484	320	24 pin	DIL	£4 · 32		14.20
7485	120p	74199	216p	4528	140p	723 DIL 7846720 DIL	45p	LM317 T1.430	TO220 TO92	325p 70p	BC124	24p 140p	MJE340	70p	2N2646 2N2904 A	32p					
7489	340p	74221	175p 150p	14433	5400			12450			B0131 2	65p	MJ481	175p	2N2905 A	22p	PLE	ASE	SEN	SA	FEOR
.7490	36p	74265	97p	14583	150p	OPTO-ELEC	TRONICS	TIL209	Red	140	BD135 6	54p	MJ491 MJ2501	250p	2N2906/A	250		NUD	CATA		
7492	540	74279	150p	Other		OCP71	136p	TIL211 TIL 32	Green	22p	BD140	60p	MJ2955	130p	2N2926RB	90	<u> </u>	JOR	CATA	LOC	JUE
7493	36p 90o	74283	220p	76107	175-	ORPSO	90p	0 210	Red	16p		INIO	LIOU		000		05 - D		5 N.a.		
7495	75p	74285	4750	75182	250p	ORP61 2N5777	90p	0 210	Green Amber	20 p 36 p	VAI	INC	LUSIVE	PH	ICES-	ADU	25p P	. OL P	—NO	otne	rextras
7496	90p 290p	74290	180p	75451	84p	2143711		Drivers					Court 1	Colle		dore		tod			
74100	140p	74298	220p	9312	1800	DISPLAYS 3015E Mir	itron 290	75491 0 75492	84p 96p		MAI	L	auvi., (Jone	ges of	uers	accep	leu			
74105	75p	74365	160p	9316	220p	FN0500 507 Rec	1 130	p 9368	200p		ORD	DER	TEC	HA	IOM	Δ1		TI	Te Te	: 01	204 4333
74107	360	74367	160p		-	DL704 Red DL707 Red	160 1/Green 160	P 9370 P 9374	200p		ONL	Y	IEC	nr		AI		- 1 1	Te	θx:	922800
74110	800	74393	245p	1 MHz		DL747 Red	/Green 250	P TIL312/5	120p			1	54 SAND	HUR	ST ROA	D. L(ONDON	NW9			
74111	75p	74490	250p	CLARCEL	14.00	1L321/2 Red	3 136	P TIL330 +	150p		1 .								-	_	

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