



MODERN PHASING TECHNIQUES GIVE MULTI-STRING SOUND

Also inside:

'AUTOMATIC'
ENLARGER TIMER

35 CARDIFF ROAD, WATFORD, HERTS., ENGLAND MAIL ORDER, CALLERS WELCOME, Tel. Watford 40588/9

ALL DEVICES BRAND NEW, FULL SPEC. AND FULLY GUARANTEED. ORDERS DESPATCHED BY RETURN OF POST. TERMS OF BUSINESS: CASH/CHEQUE/P.O.B OR BANKERS DRAFT WITH ORDER. GOVERNMENT AND EDUCATIONAL INSTITUTIONS' OFFICIAL ORDERS ACCEPTED. TRADE AND EXPORT INQUIRY WELCOME. PRP ADD 300° TO ALL ORDERS UNDER £10.00. OVERSEAS ORDERS POSTAGE AT COST. AIR/SURFACE

MINIMUM ORDER £2.00 PLEASE.

WAT Export orders no VAT. Applicable to U.K. Customers only. Unless stated otherwise, all prices are exclusive of VAT. Please add 8% to devices marked *. To the rest add 12}%.

We stock many more items. Sand S.A.E. for our free list. It pays to visit us. We are situated behind Watford Footbell Ground. Nearest Underground/BR Station; Watford High Street. Open Monday to Saturday 9.00 am - 6.00 pm. Ample Free Car Parking space available.

POLVESTER CAPACITORS: Axial lead type: (Values are in pF) 400V: 0.001: 0.0015: 0.0122: 0.0033: 8p; 0.0047: 0.0068: 0.01: 0.015: 0.018: 9p; 0.022: 0.0033: 10p; 0.047: 0.008: 41p; 0.1015: 0.015: 0.015: 0.018: 9p; 0.022: 0.033: 0.047: 0.008: 1.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0.015: 0

ELECTROLYTIC CAPACITORS: Axial lead type (Values are in μF)
63V 0.47 1.0. 1.5 2.2 2.5, 3.3. 4.7, 6.8 8. 10. 15. 22 9p; 47. 32. 50. 12p; 63. 100. 27p;
50V 1.0 7p; 50 1.00 220 25p; 470 50p; 1000. 2200 68p; 40V; 22. 33 9p; 100 12p; 330. 62p; 4700 64p; 35V: 10. 33 7p; 330. 470 32p; 1000 49p; 25V: 10. 22. 47 6p; 80. 100. 160
8p; 220. 250 13p; 470. 640 25p; 1000 27p; 1500 30p; 2200 41p; 3300 52p; 4700 54p; 160
10. 40. 47. 68. 7p; 100. 125. 8p; 470 16p; 1000. 1500 20p; 2200 34p; 100: 400 6p; 640 10p; 1000 14p.
TAG.END TYPE: 70V: 2000 98p; 4700 121p; 50V: 3000 75p; 40V: 4000 70p; 2500 65p; 25V: 4700 48p; 2000 37p; 40V: 2000 - 2000 95p; 325V: 200 - 100 - 50 - 100 190p.

TANTALUM	BEAD	CAPACITOR
		33 0 47 0 68
10 22µF 30	3 4 7 6	8 25V: 15 10
20V: 1.5 1	6V: 10	DuF 13p eac
47 100 40p.	10V: 22	2µF, 33, 47 6 V
47.68 100 3	W-68 1	00µF. 20p eacl

MYLAR FILM CAPACITORS	
100V - 0 001 0 002 0 005 0 01JF	5p
0 0 1 5 0 0 2 0 0 4 0 0 5 0 0 5 6 µF	6p
0 1. F 0 15 0 2 7p 50V . 0 47 pF	10p

CERAMI	CAPA	CITORS	501/	
0.5pF to 1: 15nF 22n 0.1p*	On€			3p eac 4p eac

10pF to 1nF 6p; 1 5nF to 47nF 10p.						
SILVER	MICA	(Values	173 p	oF1 3	3	4.7
68 10						
82 85 10						
250 300	330 36	60 390	600	B20	160	each

CERAMIC TRIMMER CA	
1000 1800 2000 2200	20p each
250 300 330 360 390 600	820 16p each
82 85 100 120 150 220	9p each

							30pF	
M	INIA	T	JRET	YPE	TR	ıM	MERS	
	5 6pl	F 3	1006	10	400	F		22
	256	£.,	16	co.	0	0_1		20

JACK PLUGS

CON	PF	RES	SION	TRI	MMERS
3 40	ηF	10	80pF	25	190pF
100	50	ODE	1250	ρF	

POTENTIOMETERS (AB or Carbon Track, 1W Log & 1W Li	
500Ω, 1K & 2K (lin only). Sing	
5KΩ 2MΩ single gang	2
5kΩ 2MΩ single gang D/P swif	ich 5
5K() 2M() dual gang stereo	7

OPTO ELECTRONICS* TIL209 Red TIL211 Grn 11L212 Yello

SLIDER POTENTIOMETERS .
0.25W log and linear values 60mm
5KΩ 500kΩ single gang
10KΩ-500KΩ dual gang
Self Stick Graduated Bezels

PRESET POTENTIOMETERS	
Vertical & Horizontal	
1W 50Ω 5MΩ Miniature	6
25W 100Ω—3-3MΩ Horiz	10
25W 200Ω- 4 7MΩ Veri	10

	5 κΩ 500 κΩ single yang 70p	OCP70 QRP12 2N5777 / Seg Displays
	PRESET POTENTIOMETERS Vertical & Horizontal 0.1W 50Ω 5MΩ Miniature 6p 0.1W 50Ω -3-3MΩ Horiz 10p 0.25W 100Ω-3-3MΩ Horiz 10p 0.25W 200Ω-4 7MΩ Veri 10p	TIL312 C An 3' TIL313 C Cth 3' TIL321 C A= 5 TIL322 C Cth 5 DL704 C Cth OL707 C A DL747 C A
1	RESISTORS - Erie make 5% Carbon Miniature High Stability Low noise	FND 357 LCD 3½ digit OWG 3902
	RANGE VAL 1 99 100 · 1W 2 2Ω 4 7M E24 1 5p 1p 1W 2 2Ω 10 M E12 2p 1.5p 1W 2 2Ω 10M E12 5p 4p 2's Metal Frim 10Ω·1MΩ 6p 4p	SWITCHES * TOGGLE 2A 250V SPST DPDT

iρ 3p	ng ENSEMBLE nents now avai		
	SOCKETS	S	
	 		-

not mixed values

2 5mm 3 5mm MONO STEREO	12p 15p 23p 31p	8p 10p 15p	8p 10p 15p 18p		tai p p	with break contacts 20p 24p	coup 11 12 18 22	
DIN 2 PIN Loudspeaker 3 4 5 Pin Audio		PLUGS	SOCKETS		In Lin	Push to f	SWITCHES * N Push to Make 15p ROCKER (white)	
CO-AXIAL	(TV)	14p		14p	14p	SP chang	geöver cen R: (black)	
PHONO assorted colours Metal Screened		9p 12p	8p (single fouble 3-way	15p	Lights w	R: Illumini hen on 3 Y: (ADJI 2p/2 6W	
BANANA	4mm 2mm	10p		12p	=	ROTAR	Y. Mains	

ine	SWITCHES * Miniature Non-Locking
р	Push to Make 15p Push to 8:eak 25p ROCKER (white) 10A 250V
Р	SP changeover centre off 28p ROCKER: (black) on/off 10A 250V 23p
p	ROCKER: Illuminated (white) Lights when on: 3A 240V, 52p
	ROTARY: (ADJUSTABLE STOP) 1 pole / 2-12 way 2p/2 6W 3p/2-4W 4p/2-3W 41p
-	ROTARY, Mains 250V AC 4 Amp 42p

DIL SOCKETS* (Low Profile - Texas) 8 pm 10p; 14 pm 12p; 16 pm 13p; 18 pm 20p; 20 pm 26p; 24 pm 30p; 28 pm 42p; 40 pm 58p. 10p 8p 9p VANDER 3mm 8p 9p DALO ETCH

SPECIAL OFFER

TV GAMES

Olympic Kit £20.80* 'Olympic'' Colour

£28.50* (p&p insured add 95p) EconoGame Kit £8.99* COLOUR ADAPTOR for

existing Black & White £8.85* Games BASIC AY-3-8600 B & W Kit includes PCB, Sound & Vision Modulators, Semiconductors, Resistors, Capacitors. Needs controls £13.98* only

(p&p insured add 48p) 'Joy Stick" £1.75* IC AY-3-8500 £4.50* IC AY-3-8550 £7.50* IC AY-3-8600



RHYTHM GENERATOR

Build this PE (Jan. '78) Easibuild Low cost Rhythm Generator. We are the sole suppliers of the complete Kit including the case, predrilled printed front panel and the printed Circuit Boards.

Complete Kit price incl. VAT £49.95 only. P & P £1.10 insured. Send S.A.E. for £9.00★ descriptive leaflet.

(TV Games & Rhythm Gen. Demonstration on at our shop)

SPECIAL ADDITIONAL DISCOUNTS ON OUR LOW PRICED TEXAS TTLS MOTOROLA CMOS. Due to massive purchases we are now able to offer the following discounts on published prices. Less 5% 10+ mixed Less 15% 75+ mixed Even more discounts

			% 25+ r			155 I D	% 100+		Even m			
	ll i	asc 109	% 50+ r	nived					on large			
3/9				IIIXOU	В	UY	NON	/!!!	SAVE£	££	£s	
,,,,	TTI	74*	7490	36	4009	58	4071	21	4520 108	4528	99 4534	788
				80	4010	58	4072	21	4521 268		165 4536	380
ERS	7400	14	7492	53	4011	19	4073	21	4522 199		85 4538	160
UE/	7401	14	7493	35	4012	18	4075	23		4531	165 4539	110
NAL	7402	16	7494	85	4013	55	4076	129	4527 152	4532	127 4585	101
IRY	7403	16	7495	70	4014	99	4077	40				101
RS	7404	20	7496	82	4015	93	4078	21		•		
: 45	7405	22	7497	262	4016	52	4081	22	LINEAR IC'S		YMC12021	
	7406	38	74100	125	4017	99	4082	21	702	75	MC1303L MC1304P	148
	7407	38	74105	62	4018	99	4085	74	709C 14 pin	35	MC1310P	185
prices	7408 7409	20	74107	33	4019	60	4086	73	710#	46	MC1312PQ	195
•	7410	20 15	74109	54	4020	102	4089	150	741C + 8 pin	22	MC1458P*	90
hind	7410	24	74111	70	4021	99	4093	85	747C	70	MC1496	101
y to	7412	23	74116	198 90	4022	90	4094	190	748C	36	MC14433L	1250
	7413	37	74120	115	4023	20	4095	105	753	150	MC1710CG	79
_	7414	74	74121	28	4024	76	4096	105	8038CC*	355	MC3401	69
	7416	35	74122	48	4025	19	4097	372	AY-1-0212	580	MC3360P	89
022.	7417	39	74123	70	4026	180	4098	110	AY-1+1313	660	MFC6040*	97
45p.	7420	16	74125	65	4027	55	4099	190	AY-1-5051	125	MK50253*	550
47p.	7421	33	74126	60	4028	90	4160	109	AY-1-6721/6	195	MK50362 *	550
т.р.	7422	24	74132	73	4029	108	4161	109	AY-3-8500 *	510	MM2101-2	210
-	7423	32	74136	73	4030	58	4162	109	AY-3-8550*	850		310
GH	7425	30	74141	72	4031	230	4163	109	AY-5-1224*	349		210
	7426	36	74143	314	4032	100	4174	110	AY 5-1230 *	490	NE555#	36
8p	7427	36	74144	314	4033	145	4175	99	CA3011 *	82	NE5560B *	90
σp	7428	38	74145	85	4034	196	4194	108	CA3018*	82	NE560*	325
	7430	18	74147	175	4035	120	4408	720	CA3020	170	NE561#	410
27	7432	32	74148	143	4036 4037	325	4409	720	CA3023	170	NE562 *	410
27p;	7433	40	74150	118	4037	100	4410	720	CA3028A*	95		185
160	7437	30	74153	75	4038	320	4411F	995	CA3035	140	NE566*	180
16V:	7438	33	74154	140	4040	105	4412V	1380	CA3036	180	NE567*	187
6p;	7440	17	74155	82	4041	86			CA3043	190	NE571	450
Op,	7441	74	74157	80	4042	81	4415F	795	CA3046	80	RAM2102-2★	210
65p;	7442	68	74159	225	4043	96	4415V	795	CA3048 CA3075	200 175	ROM2513 *	700
оор,	7443	115	74160	116	4044	95	4419	280	CA3075	80	SG3402 ±	255
_	7444	112	74162	116	4045	145	4422	545	CA3080E	190		125
	7445	94	74166	141	4046	130	4433	1225	CA3089E	210	SN76003N	240
.	7446	94	74167	198	4047	99	4435	825	CA3090AQ	390		240
	7447	82	74170	240	4048	58	4440	1275	CA3123E	200	SN76023N	140
13	7448	78	74172	625	4049	52	4450 4451	295 295	CA3130 +	94	SN76033N	230
24	7450	17	74173	175 116	4050	52	4451 4490F	695	CA3140	95	SN76115N	215
27	7451 7453	17 17	74177	146	4051	89	4490V	525	ICL7106Ev★	975	SN76227N	175
20	7454	17	74188	65	4052	89	4501	17	ICM7205 *	1150	TAA621AX1	228
17	7460	17	74190	140	4053	89	4502	120	LM300 H	170	TAA661A	155
1 21	7470	30	74194	140	4054	120	4503	69	LM301A	39	TAA700	353
40	7472	28	74195	95	4055	134	4506	51	LM308	140	TAA960	300
68	7473	32	74198	248	4056	134	4507	55	LM318	195	TBA120S	90
54	7474	32	75150	110	4057	2570	4508	298	LM324	98	TBA540Q	220
1	7475	42	75491	75	4059	480	4510	135	LM339	55	TBA550Q	355
125	7476	30	75492	80	4060	115	4511	168	LM348	395	TBA641BX11	250
125	7480	50				2380	4512	98	LM349N	120	TBA651	180
130	7481	97	CMC		4062	999	4513	206	LM380	98	TBA800	90
99	7482	82	4000	15	4063	110	4514	265	LM381	170	TBA810S	105
99	7483	95	4001	17	4066	58	4515	299	LM382 LM3900	125	T8A820	80
180	7484	95	4002	17	4067	380	4516	125	LM3909N	70	TBA9200	350 475
140	7485	110	4006	105	4068	22	4517	382	LM3911	125	TDA2020	320
. 40	7486	36	4007	18	4069BE	22	4518	102	M252AA *	750	ZN414	110
975	7489	320	400B	92	4070	32	4519	59	M253AA *	795	ZN424	130
	TD 4	NIC:	STO	DC								

TRANSISTORS

a pole on off 58p SUB MIN TOGGLE SP changer

SPST on off DPDT 6 tags DPDT Centre off

1A DPD**
1A DP c/over
1A DPD1

PUSH BUTTON Spring loaded SPST on/off SPDT c/over DPDT 6 Tag

Resist Pen -Spare Tip 75p*

COPPER
BOARDS **
Fibre Glass
Single Sided
6 x 6" 75p
6 x 12" 130p
SR 8 P
8 x 10 4" 70p

723C **45**p TBA**625B 95**p

TO3 Can = Ve
1A 5V 140p
1A 12V 145p
1A 15V 150p
LM323K 625p
1A = 5V 220p
PA = 12V 220p

Ve 99p 99p 99p 99p 91p 51p 51p 51p 51p

1A — 12V Plastic + 1A 5V 1A 12V 1A 15V 1A 18V 1A 24V

0 1A 5V 0 1A 6V 0 1A 8V 0 1A 12V 0 1A 15V

TO3 Can - Ve

1	AC117	35	BC:68C	12	BF197		0C45 *	35	ZTX503	19	2N3663	26
н	AC125 ★	19	801690	14	BF198	18	OC46 *	35	ZTX504	25	2N3702	10
1	AC126 ★	19	BC170	18	BF200	30	OC70 ★	30	ZTX531	28	2N3703	11
1	AC 127 *	18	BC171	1.1	BF224A	18	OC71 *	30	ZTX550	25	2N3704	10
1	AC128 *	18	BC172	10	BF256 *	50	OC72 *	30	2N526 *	58	2N3705	11
П	AC141 *	24	BC177 ◆	18	BF257 ★	29	OC77 *	76	2N696 *	35	2N3706	10
ш	AC141K *	38	BC178 *	16	BF258 ◆	29	OC79★	76	2N697 *	21	2N3707	10
ш	AC 142 *	24	BC179 ★	18	BF759 ★	30	OC81 *	28	2N698 •	39	2N3708	11
н	AC142K •	38	BC182	10	BF394	22	OC82 *	45	2N699 *	50	2N3709	11
ш	AC176 *	18	BC182L		BF594	40	OC820 •	35	2N706A +	19		
н	AC187 *	20	BC183	13	BF595	38	OC83 *	48	2N707 *	50	2N3710	16
L				10	BFR39	25	OC84 +	44	2N707#	19	2N3711	12
1	AC188 *	20	BC183L	12	BFR40	28	OC123 •	115			2N3715 +	250
Ł	ACY17	35	BC184	11	DEN40		OC139 +	140	2N914 #	32	2N3772 +	170
ı	ACY18 ±	40	BC184L	14	BFR79	28			2N916*	27	2N3773 •	288
1	ACY19 ≠	40	BC186	24	BFR80	28	OC140 ★	125	2N918*	30	2N3819	22
1	ACY20 ★	40	BC187★	28	BFX29★	28	00141+	157	2N920*	51	2N3820	38
1	ACY21	35	BC212	11	BFX81 ±	130	OC170 ±	40	2N930*	18	2N3823 #	65
1	ACY22 ±	40	BC2121	13	BEX84 *	24	OC171 +	40	2N961 *	61	2N3824 *	70
1	ACY28	40	BC213	11	8FX85 *	28	OC201 ±	125	2N1131 +	22	2N3866 *	90
П	ACY39	78	BC213L	14	BFX86 ◆	28	OC202 ◆	140	2N1132 •	22	2N3903	20
П	ACY40 *	48	BC214	14	BFX87 *	23	OC203 ★	150	2N1302 +	35	2N3904	18
	ACY44	39	BC214L	15	BFX88 *	26	OC204 ◆	150	2N1303 +	50	2N3905	18
	AD140	69	BC307B	20	BFY18	50	TIP29	43	2N1304#	50	2N3906	17
	AD149 +	70	BC328	15	BFY50 ★	17	TIP29A	44	2N1305 *	28	2N4037*	52
	AD161 ±	42	BC338 ★	15	BFY51 #	17	TIP29C	60	2N1306+	35	2N4058	17
	AD162 *	42	BC441 +	45	BFY52★	17	TIP30	52	2N1307 #	50	2N4058	17
	AF114 +	20	BC461 *	38	BFY71	47	TIP30A	52	2N1308 +	46	2N4061 *	120
	AF115 +		BC462 +		BSX20	18	TIP30B	64	2N1613+	20	2N4084 *	
1	AF115 *	20 20	BC547	45 12	BSX20+	18	*IP30C	70	2N1671 •	190	2N4236 * 21/4289	145
	AF117 ±		BC548	12	BSY65 ±	30	TIP31★	50	2N1671B+			20
	AF118 ±	22	BC549C	13	B\$Y95A ★	18	TIP31A ◆	50	2N1893 +	28	2N4859 2N5135	65
	AF121 *	55	BC557	15	BU105 +	140	TIP31B •	58	2N1986	60		. 42
	AF121 *	48 55	BC558	15	BU205 *	190	TIP31C*	66	2N2160+	105	2N5136 2N5138	42
	AF125 #		BCY30 *	57	BU208 *	225	TIP32 #	55	2N2217 *	48		20
	AF125# AF127#	35	BCY34 *	75	£5567	65	TIP32A #	60	2N2218A+		2N5179 ± 2N5180 ±	60
	AF127# AF139#	35 35	BCY39 *	78	MDB001 +	158	TIP328 +	75	2N2218A*		2N5180 *	60
	AF178 *			75	MJ400 *	90	TIP32C +	77	2N2220A			65
		70	BCY40 + BCY59 +	90	MJ491 ±	160	TIP33 *	85	2N222UA#		2N5305 +	40
	AF180 ±	70			MJ2955*	120	TIP338 *	100			2N5457	35
	AF186*	50	BCY70 *	15	MJE340+	45	TIP33C +	110	2N2222 * 2N2297	20	2N5458	36
	AF239 *	42	BCY7.1 *	20	MJE370*	80	71P34 +	95		45	2N5459	36
	ASY26 *	40	BCY72 ★	15	MJE371*	80	TIP34A #	95	2N2303 *	50	2N5485	38
	45Y27 *	45	BD115★	62	MJE520 +	65	TIP348 *	120	2N2368	25	2N5777	56
	BC107 *	9	B0121 *	78	MJE521*	74	TIP35+	219	2N2369 * 2N2476	15 125	2N6027	40
	BC1078 *	10	BD123	98	MJE2955+		TIP35A	225	2N2478	28	40311*	56
	BC108 *	9	BD124*	115	MJE3055*		TIP35C★	270	2N2483 *	30	40313*	125
	BC1088 *	12	BD131 +	38	MPF102	36	TIP36A*	280	2N2646#	48	40316*	95
	3C108C*	12	BD132 *	38	MPF103		TIP36C+	325	2N2040# 2N2784		40317★	52
	3C109 *	9	BD133*	43	MPF104	36 36	TIP41A*		2N2846	55 140	40326*	52
ш	BC109B * BC109C *	12	BD135	38	MPF105	36	TIP418*	66	2N2894		10327 €	62
		12	BD136	36	MPF105			73		30	40347 *	99
	3C113	17	BD137	36	MIFFIUE	50	TIP42A ◆	72	2N2904A *	22	40348 *	116
	BC114	20	BD138	50	MPF107	50	TIP42B#	82	2N2905A+	20	40360 *	43
	BC115	20	BD139	34	MPSA05	32	TIP2955*	65	2N2906 *	18	4D361 *	45
	3C116	20	6D140	36	MPSA06	32	TIP3055*	52	2N2907 *	20	40362 *	48
	BC117-	20	BD142	59	MPSA55	30	TIS43	36	2N2907A+	22	40411*	289
	3C118	20	BD145 *	198	MPSA56	30	TIS44	45	2N2926G	10	40412*	65
	BC119	28	BDY17 *	195	MPSA70	34	TIS46	45	2N29260	8	40576*	190
	3C134	20	BDY60★	110	MPSU02 *	58	TIS50	47	2N2926R	8	40594*	98
	3C135	20	BDY61 *	165	MPSU05*	50	TI\$74	47	2N2926Y	8	40603*	113
	3C136	18	8F115 *	22	MPSU06*	56	11591	24	2N3011+	30	40636★	165
	3C137	20	BF154 ★	25	MPSU52 ±	65	ZTX107	22	2N3053*	17	40673*	68
	3C140 *	35	BF156*	29	MPSU55 +	55	ZTX108	20	2N3054*	49		
	3C142 ±	28	BF173#	25	MPSU56*	60	ZTX109	25	2N3055 *	60	84-4-6	- 4
	3C143★	28	BF177*	24	MPU131 *	39	ZTX300	16	2N310B	60	Match	ea
	3C147	7	BF178*	25	OC25*	120	ZTX301	16	2N3121	40	Pair	
	C148	7	BF179 *	30	OC26★	170	ZTX302	25	2N3133	43		
	BC149	8	BF180*	30	OC28*	105	ZTX303	25	2N3135	33	10 p ex	tra.
	BC153	27	BF181 *	30	OC29+	160	ZTX304	24	2N3250	30		
	3C154	27	BF182 *	30	OC35 *	100	ZTX311	17	2N3252	36		
	IC157	10	BF183 *	30	OC36*	170	ZTX314	24	2N3302	35		
	G158	11	BF184 *	20	OC41*	48	ZTX341	20	2N3442*	142	CHOKES	
	IC159	11	BF194	10	OC42 +	40	ZTX500	17	2N3563	32	SC60	
	C160*	35	BF195	10	OC43 ±	65	ZTX501	15	2N3614*	169	100mH	90
E	C167A	11	BF 196	12	OC44*	35	ZTX502	19	2N3615*	135	, Ooma	50
	75					_		_				_

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TRANSFORMERS

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158 2000 54 80 OA 159 3000 79 05 OA 240 Cased Auto Transformers 240 Cable in & 115 USA 2 pin outlet VA E P & P Ref Ref R	156	1000	37 - 20	OA				
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HIGH VOLTAGE MAINS 150 LATING Prim 2001/220V or 4001/440V 230 Prim 2001/220V or 2001/240V 250 Ref						cable in &		pin outlet
HIGH VOLTAGE MAINS 150 LATING 200 9-92 1 45 655V Prim 200/220V or 400/440V 250 10-49 1 45 695V Va Ref £ PAP 500 15-73 1 64 53W Va Ref 50 243 5-89 1 32 750 18-55 1-76 83JW 350 247 14-11 1-84 1000 22-88 0A 84W 1000 250 35 55 0A 1500VA 26-02 0A 93W	*Please	specity 115	or 240V requ	ired				
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60 243 5-89 1-32 500 15 55 1-76 83W 350 247 14-11 1-84 750 18-55 1-76 83W 1000 250 35-65 QA 1500VA 26-02 QA 93W	Va			PAP				
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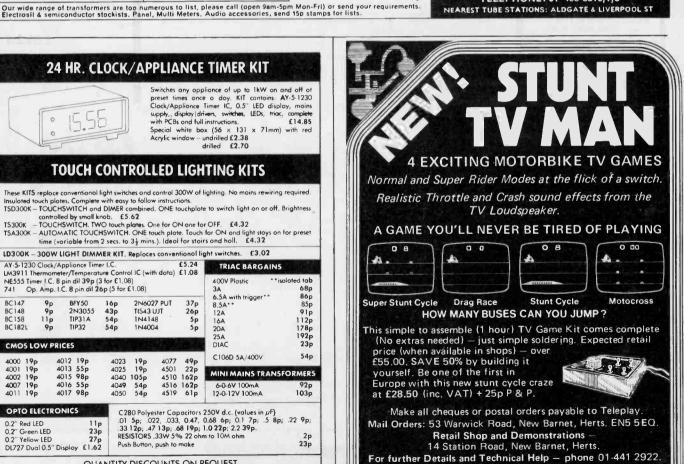
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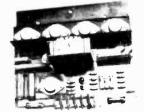
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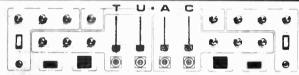
- Full wave control
- RCA 8A Triacs
- 1000W per channel
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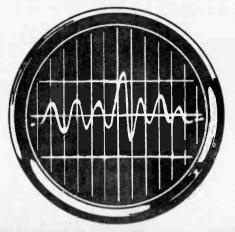
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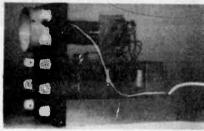
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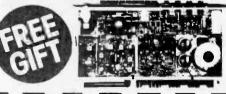
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Devices may be mixed to qualify for quantity price. Data is available for the above series of ICs in booklet form **price 35p**.

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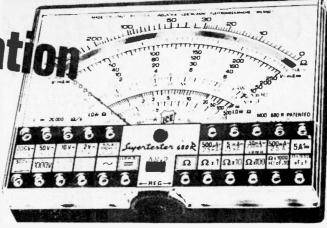


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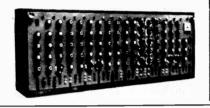
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Provides full manual control over attack, decay, sustain and release functions, and is for use with an existing voltage controlled amplifier

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This unit has its own voltage controlled amplifier and has full manual control over attack, decay, sustain and release functions Component set (incl. PCB)

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LIST—Send stamped addressed envelope with all U.K requests for free list giving fuller details of PCBs, kits and other components

OVERSEAS enquiries for list Europe send 20p other countries—send 40p



TRANSISTORS

AC128 AC176 BC107 BC108 BC109 BC147 BC148

BC149 BC149 BC157 BC158 BC159 BC182L BC184 BC187

BC204

BC209C BC212L

BC2121 BC213 BC478 BCY71 BD131

BD132

BFY50

BFY50 BFY51 BFY52 BSY95A MD8001 OC28 OC71 OC72 OC84 ORP12

ZTX107 ZTX108 ZTX501

ZTX501 ZTX503 ZTX531 2N706 2N914 2N1304

2N2905 2N2905A

2N2907

2N3053 2N3053 2N3054 2N3055 2N3702 2N3703 2N3704

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32p 63p 63p 205p 205p 205p 205p 205p 650p 195p 90p 150p 262p

KEYBOARDS AND CONTACTS
Kimber-Allen Keyboards as required for many published circuits, including the P.E. Joanna, P.E.
Minisonic, and P.E. Synthesiser. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C to C The keys are plastic, spring-loaded and mounted on a robust aluminium frame

mounted on a robust aluminium frame 3 Octave (37 notes) £39-75. Det (61 notes) £39-75. Contact Assemblies for use with above keyboards Single-pole change-over (type SP) as for P.E. Joanna and P.E. Minisonic. Two-pole normally-open make-break (type DP) as for P.E. Synthesiser. Special contact assembly (type 4PS) having 4 poles, 3 of which are normally-open make-break contacts and the fourth is a change-over contact—this special assembly enables THE SAME KEYBOARD to be used with the P.E. Synthesiser. P.E. Minisonic and the P.E. Joanna simultaneously thus avoiding the cost of more than one keyboard. See our list for other contacts.

Contact	Each	3 Octave Set	4 Octave Set	5 Octave Set
SP	24p	88.8 2	£11 ⋅ 76	£14-64
2P	27p	€ 9 · 99	£13·23	£16 · 47
4PS	53p	£19⋅61	€25 · 97	£32 · 33

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Power supply	£5⋅78
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A simple but effective sound-to-light controller capable of operating 3 lamps each of approximately 700 watts. Includes power supply, thyristors and by-pass switches.

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Multi-function circuits that, with the use of other external equipment, can serve as lie-detector, alphaphone, cardiophone etc.

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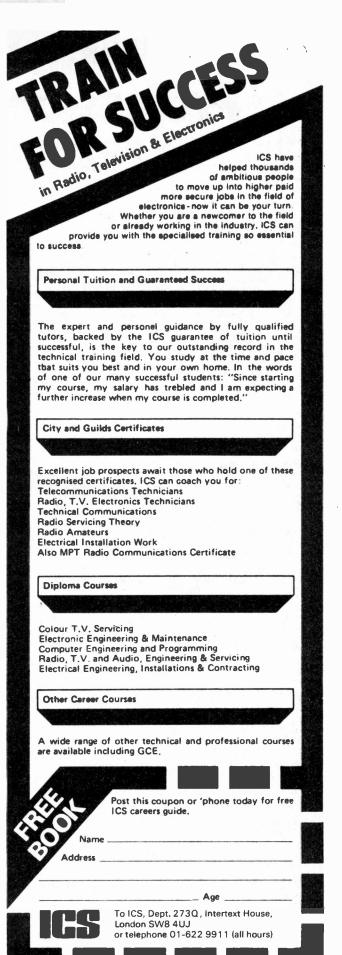
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Essential test equipment for the enterprising home constructor While stocks last
Set of resistor's, capacitors, semiconductors, potentiometers, makaswitches and PCB 19-63
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THE FUTURE

HERE do we go from here? The question is often put to us, sometimes by people who, one might feel, are in a far better position to provide a sensible answer than the staff of P.E.! Maybe they think we have a crystal ball, or that everyone on the frontiers of technology confides in us—alas not so.

Unfortunately many of the individuals beating new paths have neither the time nor the interest in publicity or keeping others informed. It is also often true that the ideas they are developing do not have any obvious impact on the public at large.

Well, where do we go from here? Our simple answer is to look at those items around us just crying out for some form of improved (electronic) control, or to look at the systems already developed which could stand expansion and improvement. Do not let the mind be restricted by restraints of the present technology; try to concentrate on how things could be improved.

We once asked a high ranking official of Tek, the company that make Tektronic equipment, where he saw possible uses for the microprocessor in the home. He simply said "everywhere!" Digging a little further it became clear that, to him, applications were so obvious that they should really need no explanation. He continued his reply by going over basic movements

during the day. Almost every operation could be computer controlled e.g. it is so much easier to get up if the bed is hard, your morning cuppa has been served, the lighting has been controlled to provide an artificial dawn and your shower is running—at the correct temperature of course.

Other obvious development areas are in the video field where Viewdata is on the way and video discs should soon be making a real appearance into everyday life.

PRINTING

There are a number of mechanical systems that are being or have been updated and sometimes even totally replaced by electronics. The print you are reading in this magazine has, since P.E. was first produced in 1964, been set by a linotype machine: A wonder of mechanical engineering with coded type transport systems; a compositors keyboard with the sensitivity of a modern electric typewriter; mechanical justification system to get the spaces in each line accurate and finally a bowl-full of hot metal to actually cast the line of type. Watching one of these machines in action has always been fascinating with their mass of levers and cams, travelling bits of type and the final production of an upside down, inside out" line of cast words.

The linotype machines producing P.E. will, within the next two months,

be replaced by a photocomposition system and the words you read will never have "seen" hot metal or "type" of the soon-to-be-outdated kind. The factory atmosphere of the typesetting department will give way to the clean air, clean floor atmosphere of a computer room with copy being entered—via a keyboard and eventually, maybe, via an optical reader straight off the typewritten page—into a v.d.u. for correction and then onto punched tape, to be later read and set by a photo-scanning system onto light sensitive paper.

CHANGES

So there is a pretty good example of "where do we go from here?" that has, or will have, improved the production of your magazine without any obvious changes as far as you are concerned.

One or two changes will however soon be obvious within these pages. The use of colour from our next issue onwards may not improve P.E. technically but it should help with presentation of such things as multiple curve graphs and double sided printed circuit boards.

So that is where we are going from here. We are not content to be the highest setting electronic hobbyist magazine in the U.K., we want to go on improving the quality and value of P.E.

Mike Kenward

FOITOPIA

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Copies of PE are available by post, inland or overseas, for £10.60 per 12 issues, from: Practical Electronics, Subscription Department, Oakfield House, Perrymount Road, Haywards Heath, West Sussex RH16 3DH.

Back Numbers and Binders

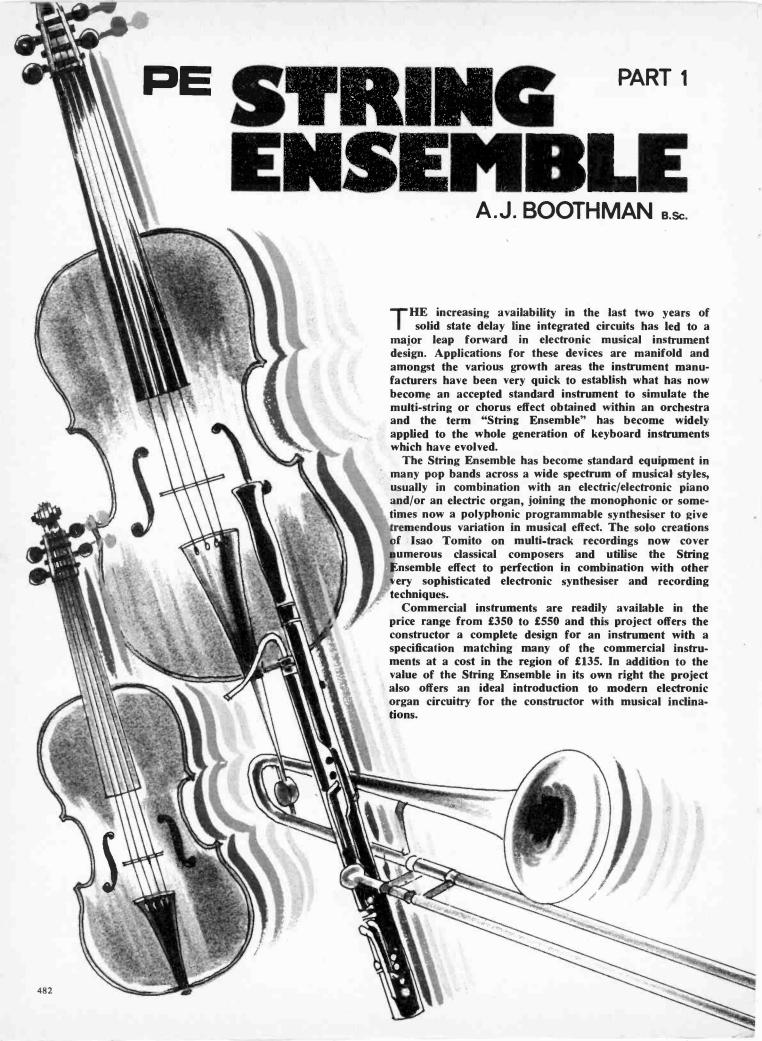
Copies of our June 1977 and subsequent issues are available from: Post Sales Department, IPC Magazines Ltd., Lavington House, 25 Lavington Street, London SEI 0PF, at 65p each including Inland/Overseas p & p.

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Cheques and postal orders should be made payable to IPC Magazines Limited.

Letters

Queries regarding articles published in PE should be addressed to the Editor, at the Editorial Offices, and a stamped, addressed envelope enclosed. We cannot undertake to answer questions regarding other items, nor to answer technical queries over the telephone.



SCOPE OF THE INSTRUMENT

The prime object of the instrument is to simulate the multiple source situation present in the string section of an orchestra, but a number of playing features have been introduced into the String Ensemble which add to its enjoyment and have practical advantages during a performance.

The split keyboard facility which operates on the bottom 16 notes commencing at E^b below middle C (See Fig. 1.1) allows the musician to select a register in the left hand which is either below or above the general compass of the right hand. The effect thus obtained of a moving string section in the right hand passing through a chord in the left hand is impressive. An inverse situation is the use of a single bass note in the left hand against moving chords in the right hand. Many combinations of this sort are possible giving effectively two manual capability.

A Pitch Transposition Control is available primarily for B^b and E^b instrumentalists who would like to use the Ensemble as a rest from playing saxophone or trumpet using their existing music pad, while the B transposition makes it easy to play with those determined guitarists who insist on playing everything in E major. For the home entertainer the apparent increase in the musical capability can bring forth admiration.

The alternative voices are not designed to achieve the same degree of simulation as the strings, but by using these voices in combination with the attack and sustain controls a wide range of sounds can be obtained from trumpet against strings, through piano accordion to the proverbial "Mighty Wurlitzer".

Due to the non-percussive nature of the String Ensemble it is safe to use with a normal hi-fi system although some care should be exercised in the use of heavy bass at full volume! Use with an existing organ speaker system is an alternative solution.

OVERALL SYSTEM

The block diagram shown in Fig. 1.1 contains the complete system and illustrates the inter-relationship of the various sub-assemblies within the system.

A single printed circuit board assembly contains regulated power supplies and a complete 96 pitch tone generator of which 85 pitches are used in the Ensemble. An oscillator running at approximately 2MHz, controlled by the transposer switch and Fine Tuning potentiometer, feeds into a 12 note master tone generator integrated circuit. Each M.T.G. output is followed by a seven stage divider giving a total of 96 available different frequency square waves, including the top octave.

Diode gating circuit boards are attached to the back of the keyboard with solder bands to anchor the contact wires which travel from the open circuit condition to a positive rail on depression of a key. The envelope available from the gates is controlled by attack and sustain sliders, and each keyer switches four octave related square waves, obtained from the tone generator, onto busbars at 16ft, 8ft, 4ft and 2ft pitch. The gating boards are arranged such that the lower 16 notes are transferred separately from the upper 33 notes, each section having for "footage" busbars as described feeding into the voice circuitry.

On the Voice Circuits Board the square waves are mixed and filtered to produce the required instrumental voices as controlled from the front panel. Balance, Expression Pedal and Master Level control are also connected to the Voice Board.

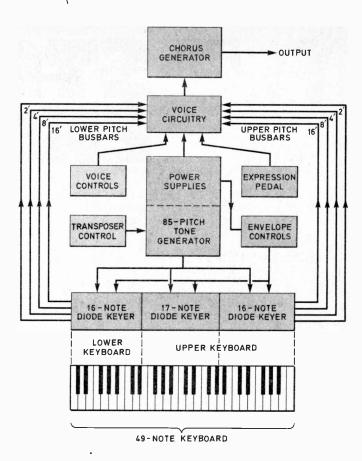


Fig. 1.1. Block diagram of complete system

The description to this stage follows one of the most popular methods employed in electronic organ design, and is easily adaptable to the conventional two manual type of instrument based on square wave tone generation. The circuitry used throughout is of CMOS type and the result is a very economic and easily constructed system.

CHORUS GENERATION

The fundamental difference between the String Ensemble and a conventional electronic organ comes from the chorus generation technique coupled with suitable voice circuitry.

Chorus generation will be covered in depth later in the series, but for readers not familiar with the term in this context, it can be defined as the creation of an apparent multiplicity of sound sources from a single generator, each source producing on average the same note. Within a string section the score will dictate that a group of instruments play the same note but due to changing variations in the phase relationship of each sound the ear detects the fact that more than one instrument is playing.



SPECIFICATION ...

MUSICAL COMPASS

Four Octaves C to C-49 Notes Keyboard Split-16 Notes Lower Section/33 Notes Upper Section Strings available at 16ft and 8ft Transposable Pitches C-B-Bb-Eb

FREQUENCY COMPASS (Concert Pitch)

Fundamental Range (16ft) 60Hz to 1kHz approx Fundamental Range (8ft) 120Hz to 2kHz approx Even Harmonic Generation up to 8·2kHz Master Oscillator Frequency 2MHz approx

NOMINAL OUTPUT LEVELS

High Level IV Low Level 100mV

MAINS INPUT

240 Volts, 10 Watts

SIZE AND WEIGHT

Dimensions $33\frac{1}{2}$ in $\times 12\frac{3}{4}$ in $\times 5$ in Weight 201b approx

CONTROLS

Power Indicator (l.e.d.) Transposition Switch Fine Tuning Upper Voice Sliders String I (16ft) String II (8ft) Woodwind (16f1) Brass (16ft) Upper Level Balance Lower Voice Push Buttons Couple Strings String 1 (16ft) String II (8f1) String III (4ft) Master Level Expression Pedal Envelope Sliders Attack Rate Sustain Length

REAR PANEL TERMINATIONS

Mains Supply Socket and Switch Mains Fuse Pedal Socket High and Low Level Output Sockets

COMPONENTS ...

POWER SUPPLY/TONE GENERATOR

Resistors

R1 1.8kΩ R2 3.9kΩ R3 470Ω R4 1.5kΩ All $\frac{1}{4}$ W 5% carbon

Capacitors

C1-C2 1000 \(\mu \) F elect. 25V (2 off) C3-C4 10 \(\mu \) F ceramic (2 off) C5 4.7 \(\mu \) F elect. 16V

Potentiometers

VR1-VR4 1k Ω presets (100 mW sub miniature) VR5 500 Ω linear

Semiconductors

D1-D9 1N4002 D10-D12 1N4148 D13 TIL209 IC1 LM341-15 + ve regulator IC2 LM320-15 — ve regulator

IC3 4069 IC4 AY-1-0212 IC5-6 4069 (2 off) IC7-18 4024 (12 off)

Miscellaneous

FS1 315mA slow blow fuse and holder. S1 Mains on-off switch. S2 4-way rotary switch. T1 Mains transformer with two secondaries each 15V 10VA. SK1 Mains input socket. 15 off 14 lead d.i.l. sockets. 1 off 16 lead d.i.l. socket. 114 off terminal pins. 1 printed circuit board.

The changing phase difference is caused by many factors associated with physical variations in the instruments, for example string tension, mass and length, body resonance and bridge design, bow characteristics, in addition to the effects introduced by the instrumentalist through bowing technique and small changes from absolute pitch. The controlled addition of vibrato introduces a further major variation in phase relationships, which are very noticeable, particularly when it is realised that the ear is extremely sensitive to small changes in phase relationships between two sound waves.

In the String Ensemble the effective length of electronic delay lives are controlled in a continually changing manner such that the phase relationship between similarly pitched notes coming out of the lives is continuously changing thus simulating a multiple source.

ENSEMBLE LAYOUT

All the circuitry of the string ensemble is laid out on printed circuit boards which are mounted either on the underside of the keyboard or flat on the chipboard base panel. The simplicity of the concept can be seen in the photograph. Three p.c.b.s contain all the diode gating circuits and contact wires which are pressed onto the keyswitch rest when a note is played. The transformer, P.S.U. Tone Generator, Chorus and Voice p.c.b.s are mounted on the base. An earthed screen covers the chorus and voice circuitry to prevent pick-up from the tone generator harness. All controls are fixed to a front panel and input/output sockets are mounted within apertures in the rear panel.

BUILDING SEQUENCE

The cabinet has been designed to give a convenient construction sequence for the whole project. The base panel of the cabinet is cut to 32in $\times 11$ in $\times \frac{1}{2}$ in chipboard

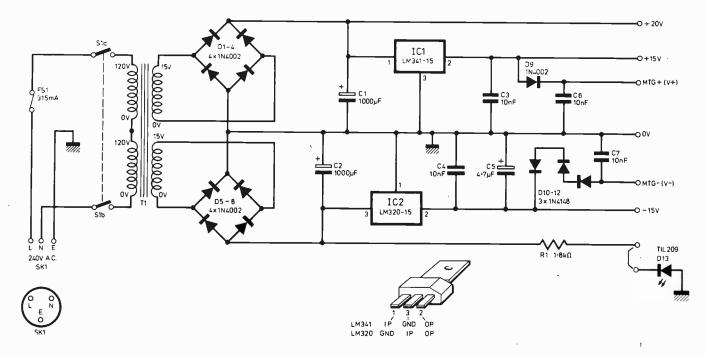


Fig. 1.2. Circuit of power supply

and used as the test bed throughout the project. Veneered sections of the cabinet may be assembled towards the end of the sequence to avoid damage to the surfaces.

The first sub-assembly described is the P.S.U./Tone Generator consisting of the transformer and one printed circuit board. These units can be fixed to the base panel, interwired and tested. The keyboard is then mounted onto the base panel from a timber key-bar supporting the rear of the keyboard via hinges. The Diode Gating p.c.b.s will be described, followed by the method of fixing to the keyboard and setting up the keyswitch action. After interwiring of the diode gate inputs to the tone generator, square wave tests may be carried out from the keyer output busbars. The Chorus printed circuit board will be described and may be initially tested using the square waves available at that stage. Finally, the Voice p.c.b. is constructed, and after interwiring to the front panel controls, and construction of rear ad side panels, the instrument is complete.

POWER SUPPLY

The circuit of the power supply is shown in Fig. 1.2, and consists of a transformer with two 15 volt secondary windings, followed by two bridge rectifiers, which give an efficient running condition for the transformer, producing unregulated supplies of approximately plus and minus 20 volts. The positive rail provides the supply to the keyswitch busbar via the attack potentiometer, whilst the negative rail supplies the l.e.d. front panel power indicator (D13) via R1.

After capacitive smoothing integrated circuit voltage regulators produce plus and minus 15 volts which are used to supply the Voice and Chorus boards. Diodes D9-D12 reduce the supply levels to conform with the requirements of the AY-1-0212 master tone generator, taking into account the tolerance spread which can be obtained from

the 15 volt regulators and the voltage supply envelope given in the AY-1-0212 data sheet. In the prototype instrument +14.8 and -15.0 volts were obtained from the regulators giving +14.0 volts and -12.5 volts at the AY-1-0212. This is equivalent of the General Instruments Microelectronics data sheet definition of $V_{\rm DD} = -14$ volts and $V_{\rm GG} = 26.5$ volts which is the best condition for use at its highest operating frequency as required in the String Ensemble.

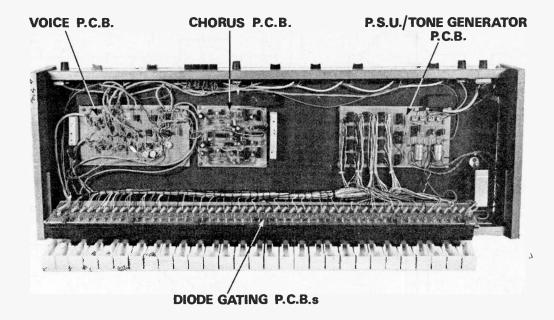
The integrated circuit dividers are also supplied from the voltage derived after D9 and were operating at 14-0 volts in the prototype. Whilst the mains current taken by the power supply is only 40mA, the surge at switch-on created by the inductance in the transformer can be many times greater and it is therefore recommended that a slow blow fuse be used; the 315mA version being a convenient standard type which gives ample protection to the instrument.

TONE GENERATOR SYSTEM

Frequency generation is centred on the use of the G.I. integrated circuit type AY-1-0212. The remainder of the tone generation circuitry is entirely dependent on cmos integrated logic circuits producing a system which is very economic, easy to construct and reliable in operation.

A single integrated circuit is used to produce the starting frequency of approximately 2MHz. Many application notes produced by cmos manufacturers give the simple oscillator shown in Fig. 1.3 which consists of two inverting gates, which in themselves are high gain amplifiers.

Gates connected this way are inherently unstable such that if one considers the input to Inverter 1 to be low, its output and hence the input to Inverter 2 to be high, and the output to Inverter 2 to be low, then capacitor C will charge through resistor R until the voltage at point (A) rises sufficiently to change the state of Inverter 1 such



Internal layout of String Ensemble

that its output becomes low. Inverter 2 then also changes its state to become high at the output. The low state of the output of Inverter 1 then provides a low impedance to ground for C to discharge through R thus reversing the cycle.

The CMOS oscillator shown in Fig. 1.4 has considerable advantages over that previously described, the first of which is that it must oscillate. Some polyphonic instruments have been manufactured which when switched on do not always operate due to the fact that the oscillator does not start. Usually this can be cured by switching off and on again quickly but it can be disconcerting to the non-electronically minded musician.

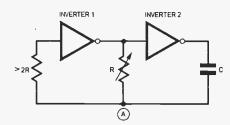


Fig. 1.3. Two inverter CMOS oscillator

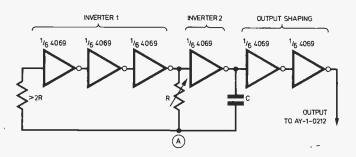


Fig. 1.4. Multi-inverter CMOS oscillator

In Fig. 1.4 the first inverter comprises three gates, and it is a fact that any odd number of gates connected from the final output back to the input will oscillate at a frequency determined by the total delay through the chain obtained by running the propagation delay time of each gate. The three gate circuit in the String Ensemble has a natural oscillating frequency of approximately 10MHz.

Inverter 2 consists of one gate and, by the process described for the simple oscillator, the C and R now slow down and determine the frequency of oscillation.

Two extra gates (inverters) finally shape the driving signal to a good square wave swinging over the full power supply range and not degrading as the frequency is changed.

MASTER TONE GENERATOR

The very clean driving wave form produced by the last two gates in the multi-inverter oscillator allow the AY-1-0212 to be used reliably over its full operating frequency range, and although the G.I.M. specification gives a 1-5MHz maximum for the standard device; out of twenty or so samples tried all worked in excess of 2MHz, many over 3-5MHz.

The slightly more expensive AY-1-0212A is guaranteed to work up to 2.5MHz and this could be used instead of the standard device.

Since its initial introduction the specifications associated with the AY-1-0212 have varied, particularly in respect of operating voltage. As described earlier the power supply has been designed to meet the latest recommendations, particularly for high frequency operation, but it should be noted that circuit descriptions in the String Ensemble adopt the convention of $V_{\rm DD} = \text{Ground}$, $V_{\rm SS}$ is positive, and $V_{\rm GG}$ is negative for the AY-1-0212.

TONE GENERATOR CIRCUIT

The complete Tone Generator circuit is shown in Fig. 1.5 and is capable of producing 96 tones of which 85 are used. A single 4069 cmos integrated circuit, IC3, provides the

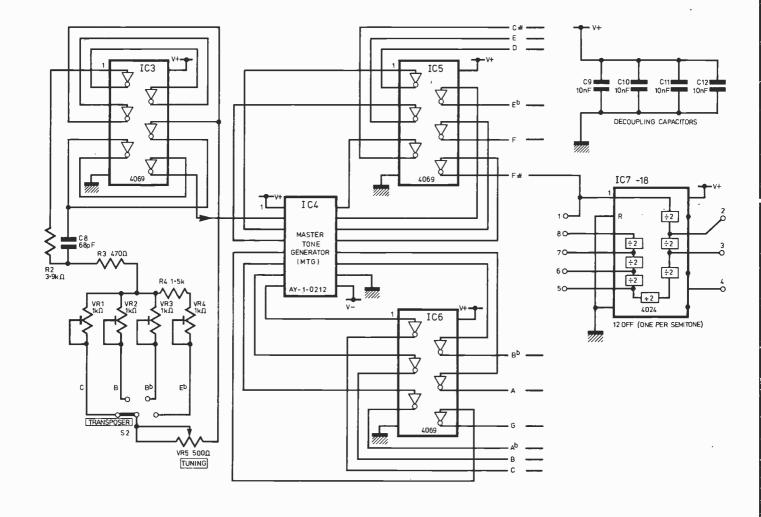


Fig. 1.5. Circuit of 96 Tone Generator

six inverters required for the oscillator, the frequency determining network consisting of C8 and the resistive combination of R3 in series with VR5 and VR1, VR2, VR3 or R4 plus VR4.

The alternative resistor combinations are switched by S2. Following IC4, two hex inverters, IC5 and IC6, are used as buffers to ensure reliable operation of the 4024 seven stage dividers.

Twelve dividers are required, one for each semitone produced by the master tone generator.

The mains input socket, fuse holder and switch feeding the P.S.U./Tone Generator p.c.b. are mounted on a subpanel at the rear of the cabinet, details of which will be given later.

The transformer T1 is mounted on the base panel which it is suggested is used for all construction work as the project proceeds.

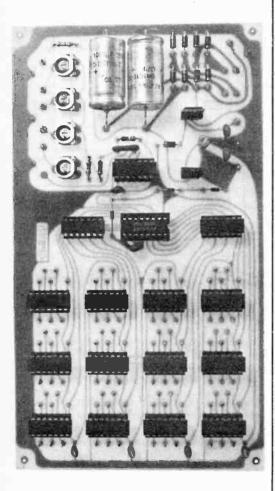
All other power supply and tone generator components are mounted on a single printed circuit board, the etching and drilling details of which are given in Fig. 1.6 with the component assembly details in Fig. 1.7.

To assemble the board the terminal pins should be fitted, followed by resistors, diodes, i.c. sockets, preset potentiometers, small capacitors and finally the large capacitors C1 and C2 and the voltage regulators IC1 and IC2. Sockets have been recommended for all dual in line integrated circuits on this board, partly due to the relative cost of the i.c.s and for easy fault tracing, and also to minimise handling of the i.c.s.

HANDLING PRECAUTIONS

The AY-1-0212 and cmos integrated circuits are susceptible to damage by static electricity. All contain internal protection networks designed in by the manufacturers, and after handling considerable quantities of cmos the author is convinced that he has never damaged a device through static even though handling of the devices has been careless. Nevertheless it is wise to take the precaution of minimising device handling, and carry out the advice of touching an earthed lead before proceeding to insert the integrated circuits into their sockets. Damage will occur if the devices are reversed.





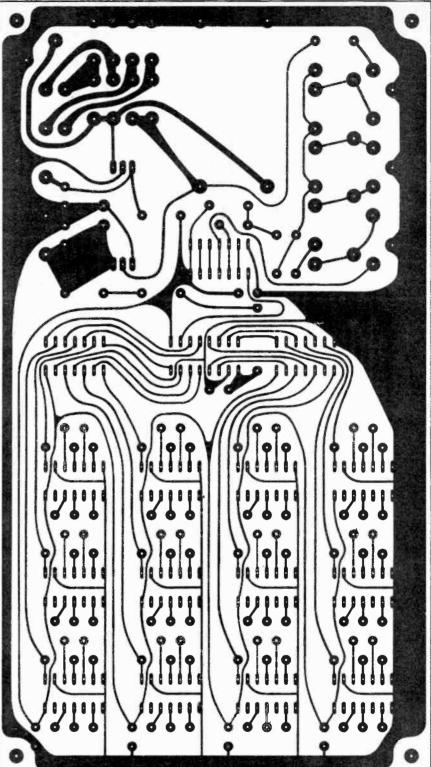


Fig. 1.6. Printed circuit layout of P.S.U./Tone Generator p.c.b.



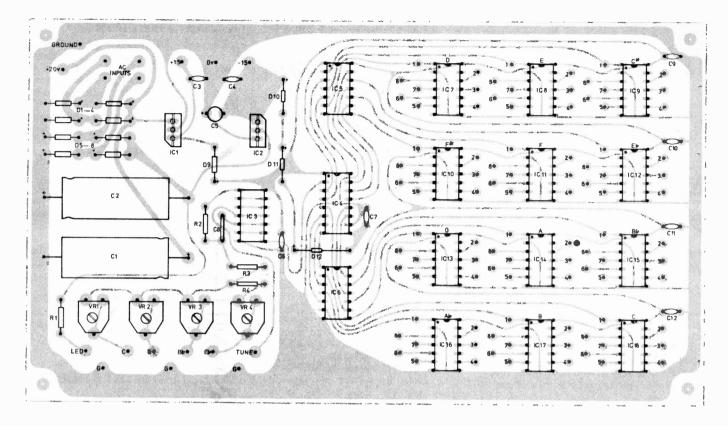


Fig. 1.7. Component layout of P.S.U./Tone Generator p.c.b.

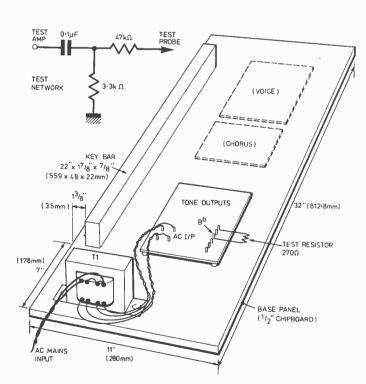


Fig. 1.8. Mounting of P.S.U./Tone Generator components on base panel for initial test

The danger of incorrect insertion could arise on the voltage regulators IC1 and IC2. A clearly identified p.c.b. is recommended to avoid this, but for those constructors who may not be using p.c.b.s your attention is drawn to Fig. 3 and to the point that IC1 and IC2 pin connections are different.

INTERWIRING AND TESTING

Wiring at this stage is limited to connecting the T1 secondaries to the printed circuit board AC input pins as shown in Fig. 1.8. The sketch also shows how the base panel can be prepared by fitting the key bar, and after mounting T1 and the P.S.U./Tone Generator printed circuit board a sub-assembly test can be performed.

To simulate the Transposer and Tuning controls a test resistor of 270 ohms should be soldered between the pins marked "Bb" and "Tune". On connecting the mains, signals should be present at all the output pins grouped around each of the twelve 4024 integrated circuits, and the frequency should be variable by adjusting VR3.

To check the operation the probe network shown in Fig. 1.8 could be used which reduces the signal to approximately 300mV to feed a test amplifier. It is important to note that the 47 kilohm resistor is necessary in order not to overload the dividers. The 14 volt peak-to-peak voltage available from these is far too high for the average amplifier without the 3.3 kilohm attenuation resistor shown.

This test should be carried out very carefully since shorting the output pins to each other or ground could cause damage to the divider integrated circuits.

Next Month: Keyboard, keyswitch and diode gating assemblies.

MARKET PLACE

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

WIRE WRAPPING

Any constructor building the P.E. CHAMP system will be pleased to learn that Verospeed can supply Kynar insulated wire. The extremely tough but thin insulation of this wire is part of the requirement for wire wrapping applications. Although no wrapping is necessary for CHAMP, Kynar wire is excellent for high density wiring, even with soldered connections.

An extensive range of wire wrap and unwrap tools are also illustrated in the Verospeed catalogue, including battery driven, hand operated, and mains types.

The Kynar wire (0.254mm dia and rated at 3.5A) is available in 500m reels, and in the following colours: Red, Green, Yellow, White, Blue, Black and Natural, each of which are currently priced at £11.85.

Tubes of pre-stripped lengths are available, in steps ranging from 25mm through to 254mm with corresponding prices of £2.56 and £5.98. These packs consist of 500 wires, and the specified lengths refer to the insulated portion, there being about an extra 25mm stripped wire at each end.

Further details can be found in the new Verospeed catalogue, and we are informed that the company is well equipped to supply small quantities without surcharge, and all items in the catalogue are subject to only 8 per cent VAT.

Verospeed, 10 Barton Park Industrial Estate, Eastleigh, Hants, SO5 5RR.

RADIO CONTROLLED GARAGE DOOR OPERATOR

A radio controlled garage door opener is now being introduced to electrical, do-it-yourself and car accessory retailers throughout the U.K. by the Haos Company Limited. It is designed to be either radio or key operated and will fit most up-and-over garage doors.

In response to a signal it has the facility to open the garage door, turn on the light and then close and securely

lock the door. After $1\frac{1}{2}$ mins it will automatically turn off the light, having allowed sufficient time for the driver to get out of the car and leave the garage.

The radio control works on the longwave frequency and is not subjected to interference from other electrical equipment; nor does it impair radio or television in the area.

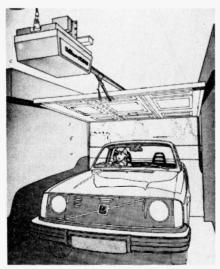
The compact transmitter unit runs on a 9V battery and has a transmitting range of 40 feet. Each radio controlled unit is individually coded to ensure that only the transmitter with the corresponding code can set the motor in motion. Any number of additional transmitters with the same frequency can be purchased as an optional extra.

The system includes a push button switch that can be placed within the home, garage or suitable outbuilding to enable operation of the system without the use of the transmitter. In situations where there is no secondary entry into the garage an outside key release is available, allowing the garage door to be opened manually from the outside in the event of a power failure.

If the door is obstructed during the closing cycle, the motor automatically reverses and the door returns to the fully open position. If an obstruction occurs within 50mm of the ground—or in the fully closed position—the clutch falls into neutral stopping the door until the closing cycle is completed and the motor shuts off.

The system is available in two sizes, deluxe and standard, depending on the size of the door to be lifted, the deluxe model also has a built-in light. The cost ranges from £150 to £250 depending on the type of unit required. As a licence is required before operating the appliance the Haos Company obtain the first licence on behalf of the purchaser.

For further information contact The Haos Company Limited, Built in Centre, 32 Letchworth Drive, Bromley, Kent.



Radio controlled garage door operator from the Haos Company

CABLE STRIPPER

The new AB MK 02 cable stripper which has been developed by A.B. Engineering Co. is a very useful tool, as it has an additional facility which allows electrical power cable insulation to be slit longitudinally.

It is suitable for all sizes of round cable from 4.5mm to 28.5mm dia and it has an adjustable cutting blade which can be set by turning the knurled screw to match the precise thickness of the insulation to be stripped.

The cable is retained by a spring loaded gripping clamp, rotation of the tool around the cable cuts cleanly through the insulation. The cutting blade is then turned through 90 degrees by depressing a knob on the side of the tool, this allows the insulation to be cut along the length of the cable and simply peeled away from the core.

Further details of the range of cable strippers may be obtained from A.B. Engineering Co, Apem Works, St Albans Road, Watford, WD2 4AN.

DECADE RESISTANCE UNIT

A very useful 7 decade resistance unit covering the range 1 ohm to 11 Megohms in 1 ohm increments has just been produced by Electronic Services & Products.

It is called the R-Decade 111 and the unit, which fits into one hand, could solve the frustrating problem of being unable to find the right value resistor.

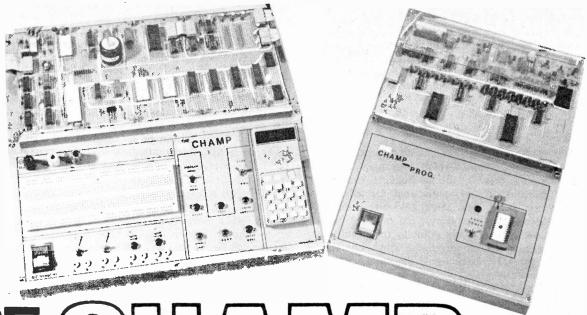
A third terminal allows the unit to be used as an accurate potential divider or as an attenuator, variable from 0 to 140dB.

The R-Decade 111 employs 0.5W 1 per cent high stability metal film resistors and uses a b.c.d. switching technique.

Further details can be obtained from E.S.P. Unit 2, Middle March, Long March Industrial Estate, Daventry, Northants.



Decade resistance unit from E.S.P.



PE GAMP R.W. COLES B.CULLEN

PART SEVEN

WITH the construction of CHAMP and its keyboard behind us, and with a 4702A containing the CHOMP program plugged into the Chip Zero socket, we can now move on and put the system to work for its living.

This month we will be covering the operation of the system, and showing how it can be used for the development of software and hardware, which can later be used in a separate "SON OF CHAMP" dedicated system, or be used as an extension of CHAMP itself.

LOADING A PROGRAM

When CHAMP is turned on, with the keyboard connected, a display of 000 200 should appear. The 200 tells us that the CHOMP address counter points to the first available RAM location, Chip 2 Location 00, and that program loading can now begin. Program instructions or data are entered one byte at a time, by pressing the two appropriate hexadecimal keys in succession. As each key is pressed the digits appear in the proper position on the left of the display, replacing the zeros which existed to start with. If an error is made on entry, the incorrect digits can be replaced with zeros or overwritten simply by pressing extra keys until the display is satisfactory.

If all is well, pressing the ENTER DATA key will enter the single byte into RAM location 200 and the CHOMP counter will be incremented to show 201, the next location in sequence. A complete program can be entered in this step by step fashion quite rapidly, each pair of hexadecimal digits being entered into the next available location by means of the ENTER DATA key.

If you wish to enter subroutines or data tables starting at some address other than 200H, the ENTER ADDRESS key can be used to reset the CHOMP address counter. A CHAMP address is twelve bits long, and so three

hexadecimal keys are pressed before using the ENTER ADDRESS key. Any address in the range 200H to 3FFH can be entered in this way, in preparation for program entry, and in fact any address from 000H to 3FFH can be entered ready for an examination of its contents using the DUMP key. This means that a PROM based user program in the Chip One socket, or even CHOMP itself, can be examined if required.

Operation of the DUMP key will display on the two lefthand digits the hexadecimal content of the memory location indicated by the three right-hand display digits. Note that it displays the byte whose address was indicated before depression of the DUMP key, and that DUMP, like ENTER DATA, automatically increments the CHOMP address counter so that whole programs can be quickly examined by rapid operation of this key.

After using the DUMP key once, the two left-hand digits display the byte resident at the next lowest address to that indicated on the three right-hand digits.

RUNNING A PROGRAM

When a program has been entered and is considered satisfactory, it may be allowed to run by changing the MODE switch to RUN MODE. Operation of this switch causes CHOMP to carry out a JUN to location 200H where the first instruction of any user program should be situated.

CHOMP assumes that every program starts at 200H and this may be inconvenient, particularly if a number of small programs are co-resident in the CHAMP RAM area. To ensure that entering RUN MODE starts the required program, regardless of its position in memory, a JUN can be entered into locations 200H and 201H. To leave this option open it is best to begin any program which

starts at 200H with a couple of NOP instructions, thereby leaving room for the JUN should it ever be required.

Once a user program has been started, CHOMP takes no further part in the proceedings until PROGRAM MODE is re-selected and the RESET key is depressed. User programs can of course use any of the CHOMP subroutines such as DDRV or CLRF, without prejudice.

PROGRAM DE-BUGGING

When a program is tried out for the first time, it is normal for it to contain "bugs" which prevent it from operating correctly. To help in the de-bugging process CHAMP can be set to STOP and instructions carried out one at a time using the SINGLE SHOT key. For simple programs which control lamps or relays, for example, the use of the SINGLE SHOT can quickly point to the problem area, but for more complicated programs which contain several JCN, JMS, or JUN instructions, the SINGLE SHOT capability alone is not enough.

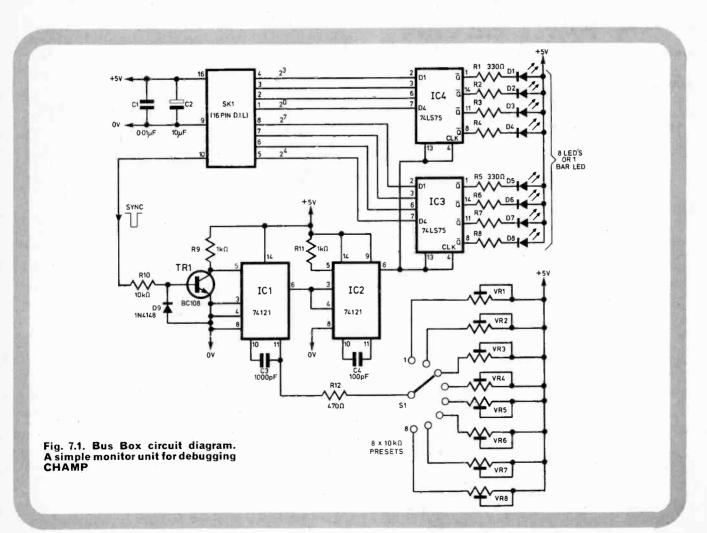
In these circumstances it is an advantage to have a knowledge of the 4040 data bus contents, or the 4289 data and address bus contents so that the operation of the program can be closely studied, and the changes after each instruction execution, monitored.

Monitoring the data and address buses cannot be achieved with software of course, and requires the use

of a hardware device normally called a "Bus Analyser" which samples the buses at an appropriate moment and latches their content for display on l.e.d.s or lamps.

A very simple bus analyser which we have called "BUS BOX" has been designed for use with CHAMP, and the circuit for this unit is shown in Fig. 7.1. The "BUS BOX" can be plugged into sockets 2, 4 or 6 on CHAMP to observe the 4040 main data bus, the program memory data bus, or the program memory demultiplexed address bus, respectively. BUS BOX will display up to eight bits in binary form, although a hexadecimal format could easily be achieved with the use of appropriate decoders and seven bar displays if required.

The principle of operation is quite simple: The 4040 SYNC pulse is used to start a variable delay formed by a 74121 monostable circuit. When the delay expires, a second 74121 monostable generates a strobe pulse to load up to eight bits from the CHAMP buses into 7475 quad latches whose Q outputs drive l.e.d. lamps. When the BUS BOX is connected to the four bit 4040 bus (SK2) any of the eight time periods from A1 to X3 (see 4040 manual, Fig. 1-2) can be monitored and the bus contents displayed on four of the eight l.e.d.s. By this means all twelve bits of the current address, eight bits of the fetched instruction, four bits of the current accumulator value, and the eight bit SRC address can all be monitored in sequence by selecting an appropriate delay with the first 74121 monostable.



Notice that the data on the 4040 bus is inverted by TR2 to TR5 on the CHAMP main board so that the required positive logic I = l.e.d. on is realised. Being able to monitor the main data bus is very useful when sorting out elusive bugs, but looking at the data four bits at a time can be tedious if all you need to do is to follow the address flow of a program, or to monitor each instruction byte as it is fetched. To simplify this task the BUS BOX can also be connected directly to the 4289 data and address buses (SK4, SK6) where all eight l.e.d.s are used simultaneously, and in this case the delay is set so that it monitors the M2 bus time slot when both address and data information are available on their respective buses.

SIMPLE

The Bus Box circuit presented here is of the most basic type possible, and suffers from several disadvantages as a result. This was a deliberate policy so that construction costs could be kept to an absolute minimum; but anyone who feels that the rather crude method of time slot selection (which really requires an oscilloscope for initial set-up) or the primitive binary display, are not good enough can of course design something better. A counter, reset by the SYNC pulse can be used with a decoder for time slot selection, and combined hexadecimal latch, decoder, display chips can be used to present bus content.

All kinds of other embellishments can be added to produce a very powerful de-bugging tool, but if low cost is high on your list of priorities the Bus Box makes a good starting point.

WRITING PROGRAMS FOR CHAMP

Program writing for a simple four-bit chip like the 4040 can be carried out quite successfully at the machine code level, and there is no need for an extensive knowledge of computer science or of any high level languages such as Fortran. If you are already knowledgeable about such things, the simplicity of the 4040 might strike you as a disadvantage, but if you are basically a "hardware person" you will soon feel at home thanks to an intimate contact with the registers, gates and flip-flops which you will control via your programs. The creation of programs is of course a skill which must be learned gradually, by trial and error really, and the most important tip that we can give is to start with something simple, so that the almost inevitable "bugs" can easily be unravelled.

To start you on your way we have put together a simple program which involves both hardware and software design, and which serves as a useful springboard for a greater range of more sophisticated projects. As we describe the creation of this program we will introduce a number of useful programming tips and aids.

SAMPLE PROGRAM

The program we have written is called "TONE", and its sole purpose is to generate an audio tone of about 1kHz in an external speaker whenever the CHAMP TEST button is depressed.

The first step in program writing is of course the flow chart, and Fig. 7.2 illustrates this first stage in the design of TONE.

Box 1 indicates that we want to do nothing but idle while waiting for TEST to be pressed, and here we have a simple wait loop which will use the JCN conditional branch instruction to monitor the state of the TEST input.

Box 2 is entered when TEST is pressed and here we want to generate a delay of about one millisecond to set the frequency of the generated tone. The best way to generate delays of this sort is to set up a counter chain using ISZ instructions with 4040 registers acting as the counters; it also seems reasonable (though not essential) to make this a separate subroutine. After the delay, we can activate the output pulse which will drive the speaker, and one of the 4265 output lines which is subject to the bit set/reset command, WRM, seems a good choice to receive this output signal.

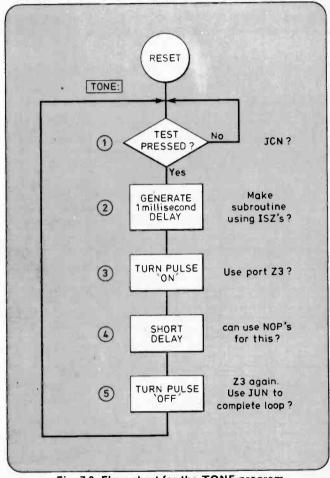


Fig. 7.2. Flow chart for the TONE program

In this simple program we are not after a 1:1 mark space ratio for the tone signal and so we can set the output pulse width by means of a very simple delay formed from NOP instructions (Box 4) before turning the pulse off again (Box 5). Having generated a single pulse we must of course loop back to see whether TEST is still depressed, and we can achieve this by means of a JUN.

Note that after drawing the basic boxes and lines required, we have added notes on the way we may want to code the program when we eventually reach that stage.

HARDWARE

During the flow-charting stage, port Z3 was proposed as a suitable interface to the external hardware, in this case, the speaker. A glance at the 4265 data sheet (page 5-41, 4040 Handbook) shows that any of the Z outputs can sink 16mA in the low state, whereas their high

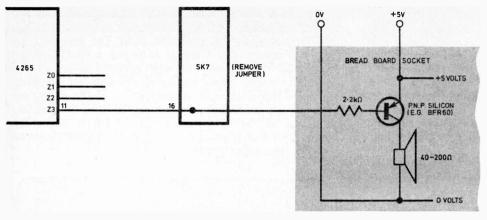


Fig. 7.3. Hardware required by TONE program. The breadboard mounted on the CHAMP facia is provided for such supporting hardware.

TITLE			TONE		1K3 NO	te	DATE 26 - 1/- 77 PAGE No. 1 OF 1
HEX			BIN MNEMONIC			1,100,100,1	
	LINE	ROM	CODING	LABEL	OPERATION	OPERAND	COMMENTS
2	0 0	00		TONE:	NOP	4	1
_	1	00			NOP		
	2	11			JNT		(Wait for TEST)
	3	00				WE -	107 11. 11. 12.
	4	52			JMS		(Delay subroutine)
	5	18			DN	EK	
	6	28			FIM	8	(Address 4265
WU.	7	80			8	0	I/O chip)
	8	29			SAC	9	1,000,7
	9	DE			LDM	Ė	
	A	ΕO	STEE HYD	THE STATE OF	WRM	NECES IN	(Pube "ON")
		00	N. Bart		NOP		1
	C				NOP	The state of	
-	D	00			NOP		
	E	00			NOP		
	F				NOP		
2	1 0		A TOTAL		LDM	F	
_	1	EO			WRM		(Pulse " OFF")
- 20	2				JUN		(Continue while
	3					NE -	TEST Pressed)
	4	XX					
	5	XX					
	6						
	7	XX					
2	1 8	22		ONEK:	FIM	2	(Preset counters)
	9	DD			D	2 D	,
2	1 A	72		LOOP:	15%		(821 microsecond
_	В	1A			100	P -	delay)
	C	73			ISZ	3	1
	D				LO		
	E				BBL	0	(Branch back)
	F	XX					213.01
				MOTEC	(1) Dent	77	for TONE outpu
					(2) Keyboo	ard disconi	nected at Jumper
				Contraction of			
			18-4/1	1000 12	(3) XX	= don't d	care

Fig. 7.4. TONE program instructions laid out on a standard program sheet

state sourcing ability is probably poor. This fact suggests the use of a p.n.p transistor stage to drive the speaker, and so the final hardware circuit is as shown in Fig. 7.3. Notice that these few external components can be assembled on the breadboard socket, and that the SK7-SK8 jumper is removed to gain access to Z3 on pin 16.

CODING THE PROGRAM

With the flow-charting and hardware design out of the way it is now possible to turn the program outline into a set of ready-to-load 4040 instructions, and our attempt at this is shown in Fig. 7.4. To make life a little easier we have designed our own 4040 program sheets which we duplicate and make up into pads, each sheet having room for 32 separate instructions. If you can get sheets like this duplicated then it is a good idea to copy our design, although an exercise book with a few lines ruled on it would serve just as well.

Each line on the sheet corresponds to a single address in program memory, hexadecimal address information being entered in the first two columns as required. The second two columns are for entry of hexadecimal instruction codes (and the binary equivalent if required) but these columns are filled out last of all. Column 5 is used to hold any address label or name that may be applied to any particular location, and columns 6 and 7 are used to write out the mnemonic form of the instructions as the program is developed.

Column 8 allows the insertion of plain-English comments to explain the action of the program; a necessary addition as you will soon appreciate when trying to unravel programs which you may have written some weeks previously, without useful comments!

The first address of TONE is 200H, the start of program RAM, and the first four lines of the program represent Box 1 of the flow chart. The two NOPs are not essential but we inserted them to allow a JUN to be entered when running programs elsewhere in the RAM address range, as discussed earlier.

SUBROUTINE

The one millisecond delay is coded as a subroutine which we have called ONEK. The actual location of the subroutine is unimportant but we chose address 218H, to allow some room between it and the end of the main TONE program, in case TONE "grew" after de-bugging. Reference to page 2–18 of the 4040 manual shows that a one millisecond delay can be achieved with two four bit counters, given the standard 5-185MHZ clock frequency normal with 4040 systems, including CHAMP.

Since the total period of the pulse stream is determined not only by ONEK but also the time the pulse is ON and the time taken to execute other instructions in the loop, a value of 821 microseconds is actually used, and the "fine tuning" of this delay is achieved by loading the two count registers with hexadecimal data before counting begins. This register, preset to DDH, is performed by means of the FIM instruction at the start of ONEK.

After the JMS ONEK instruction in the main program, comes another FIM which loads register-pair 8 with the SRC address value of the 4265 (actually 80H). This is then sent out by the SRC 9 in line 208H to select the 4265 ready for output.

The pulse is turned on by setting Z3 to the low output state using LDME WRM, a sequence which can be best understood by reference to page 5-39 of the 4040 manual. After the 54 microsecond delay produced by the five NOPs, LDMF, WRM is used to return Z3 to its high state, followed by a JUN back to TONE. When all the mnemonics and comments had been entered, we coded the program by looking up the hexadecimal equivalents in the manual and entered these along with hexadecimal addresses (in place of the labels) into column 3 of the program sheet.

REGISTER MAP

The subroutine ONEK required Index registers for use as counters, and the main program used an Index register pair as a source for SRC addresses, but you may be wondering just why we used the registers that we did use.

TONE is a very simple program which uses only two register pairs out of the total of twelve available, and so we *could* have used any of the Index registers with equal success. When writing larger programs this is often not the case; CHOMP and PROMPT use every available

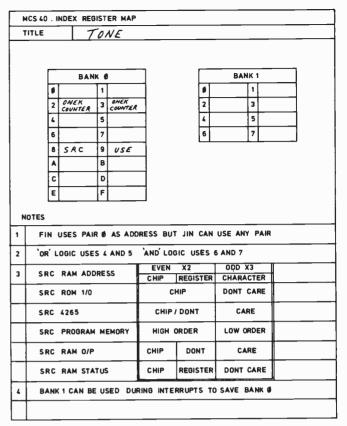


Fig. 7.5. Index register map. A standard sheet such as the one shown would serve as a method of recording the deployment of each register, and this one has been entered up for TONE

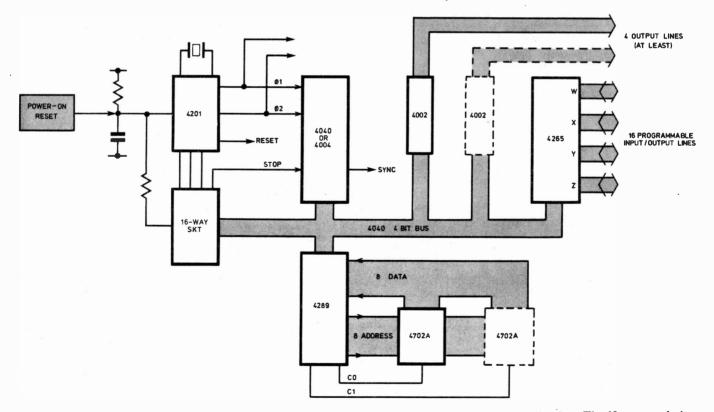


Fig. 7.6. Possible "Son of CHAMP" layout for a 4040 or 4004 based system for dedicated application. The 16-way socket is for bus analyser testing and a manual control unit. The 4002s and 4702As should be socket mounted so that only the required chips are used

COMPONENTS . . .

BUS BOX

Resistors

 $\begin{array}{lll} 8 \text{ off } 330\Omega & \text{R1-R8} \\ 2 \text{ off } 1k\Omega & \text{R9, R11} \\ 1 \text{ off } 470\Omega & \text{R12} \\ 1 \text{ off } 10k\Omega & \text{R10} \\ \text{All } \frac{1}{8}W \text{ 5\% carbon} \end{array}$

Potentiometers

8 off 10k Ω presets VR1-VR8

Capacitors

1 off 0·01µF C1 1 off 10µF elect C2 1 off 1000pF C3 1 off 100pF C4

Semiconductors

8 off discrete l.e.d.s, or one bar l.e.d. array D1-D8 1 off 1N4148 D9 1 off BC108 TR1

Integrated circuits

2 off 74121 IC1, IC2 2 off 74LS75 IC3, IC4

Miscellaneous

1 off 16-way d.i.l. socket SK1
1 off single pole 8-way rotary switch Stripboard, cabinet e.t.c.

register for example, and use some of them for several different jobs. This means that keeping track of Index register usage is very important. To help with this aspect of programming we have put together another duplicated sheet which we call an Index Register Map, and Fig. 7.5 shows how this looks for TONE.

Of course TONE is a very simple program and not much use as it stands, but we feel that its basic principles can be incorporated in such projects as Stylophone type instruments, musical doorbells and a host of others.

CHAMP PROGRAMMING SERVICE

Readers who have no PROM programming facilities may have their own 4702A or 1702A PROM programmed by post with the following:

(a) CHOMP £5.35 (b) Reader's own software £10.35

(c) Reader's own software re-programmed with up to 16 corrections to original program £3.35

All prices include postage and packing.

Programs, or corrections to programs, *must* be supplied as a *clear* list of two-digit hexadecimal code with hex' address information alongside. Also, PROMS should be sent well packed, and protected with conductive foam.

CHOMP software will be tested on a CHAMP system, otherwise programs are committed to PROM at reader's own risk,

This service is provided by, and payment should be made to:

C. C. CONSULTANTS.

Dept P.E., 3 Gainsborough Drive, Worle, Westonsuper-Mare, Avon.

Do not send PROMS to P.E.

DEDICATED SYSTEM

After developing hardware and software for, say, a musical doorbell, you will need to produce a small dedicated hardware system into which you can plug the PROMs programmed by CHAMP-PROG.

Figure 7.6 shows a possible layout for a small 4040 or 4004 based system which could be put to a multitude of different uses, and which would fit onto a six inch square circuit board.

NEXT MONTH: CHAMP-PROG.

NEWS BRIEFS

SOUND 78 INTERNATIONAL

This exhibition has been organised by the Association of Sound and Communications Engineers (formerly the Association of Public Address Engineers), and is to be held at the Cunard International Hotel, Shortlands (near Hammersmith flyover), London W6.

The Sound 78 International Exhibition will be held over the period from March 14–16 inclusive, and on display will be some of the most sophisticated and up to date sound and communications equipment in the world. It will be possible to view amplifiers, microphones, automatic announcement equipment, alarm systems, background music systems, sports event timing equipment, loud-speakers, hotel and hospital communication systems, discotheque equipment, intercoms and paging equipment, mixers, studio recording and audio visual equipment.

There will also be experts present who can discuss the design, installation and function of most of the equipment on display

The exhibition is to be open each day from 10.00 to 18.00 (17.00 on the last day), and admission will be absolutely free to anyone having a professional or business interest.

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If anything goes wrong, we'll replace damaged components free. We want you to enjoy building the kit, and to end up with a valuable, useful, powerful calculator.

Contents.

Acrylic/ABS case and display window parts. Two-part stitched strap and spring bar clips. PCB. Special direct-drive chip (no interface chip required). Display.

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Each of the 34 components is contained in a plastic box; and neatly shrink-wrapped, accompanied by full instructions for assembling and using the calculator. All components are fully guaranteed.

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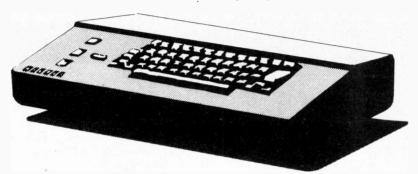
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AO Page

CATALOGUE

TO P.E. READERS ONLY

TO P.E. READERS ONLY

TO P.E. READERS CONSTRUCTORS

TO P.E. READERS CONSTRUCTORS

TO P.E. READERS CONSTRUCTORS

TO P.E. READERS ONLI
This new spring '78 catalogue, which
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data.

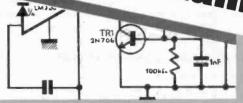
A contest of speed and dexterity between two players of devoid of further challenge.

A contest of speed and almost any age. The game is limited by the devoid of further challenge.

WINDLE NUDGE Game

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DESIGN
IDEAS

We present a bumper bundle of readers' design ideas for your edification. A large number of ingenious circuits will be shown together with a brief description of each.



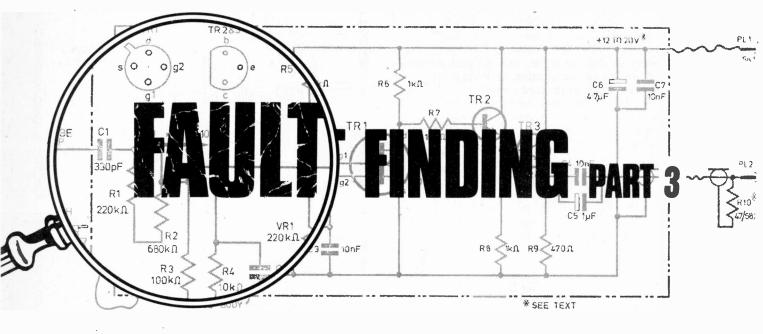
Electronic KEYSWITCH

Employing CMOS logic, this unit presents an unusual approach to the construction of an electronic lock.

PRACTICAL

ELECTRONICS

OUR APRIL ISSUE WILL BE ON SALE FRIDAY, 10 MARCH, 1978



G. LOVEDAY

Fault finding on triac and thyristor circuits

THYRISTORS and triacs are semiconductor devices that are increasingly being used to replace conventional mechanical switches and relays, mainly because they offer faster switching speeds, high reliability and the ability to smoothly control the power dissipated in a load. They find many diverse applications such as lamp dimmers, sound-to-light units, power supplies, motor speed control etc., so an understanding of their operation, use, and fault diagnosis is important for anyone interested in electronics.

OPERATION AND CONSTRUCTION

Just like any other component it is important to appreciate how thyristors or triacs work before attempting to diagnose faults in typical circuits containing them, so a small amount of theory follows.

A typical thyristor structure is shown in Fig. 3.1a. It consists of a four layer p-n-p-n silicon sandwich, just like two rectifiers connected in series. The symbol is of a rectifier with an additional terminal called the gate (Fig. 3.1b).

It is the gate that enables the action of the rectifier to be controlled. As for an ordinary rectifier, when the anode is negative with respect to the cathode the device is reverse biased and no current flows. If the anode is made positive with respect to the cathode the device will still not conduct (provided that the forward breakover voltage is not exceeded) and it is said to be forward blocking (Fig. 3.1c).

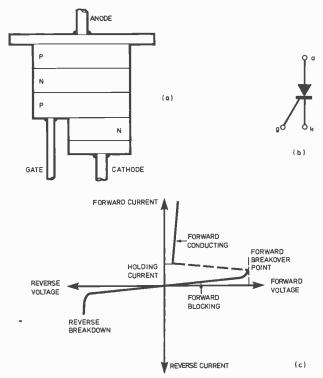


Fig. 3.1(a). Thyristor structure (b) symbol (c) characteristic

The thyristor can be triggered into a forward conducting state by applying a short pulse of relatively low power at the gate. Once switched on it will pass large values of current, limited only by the external load, with only a small voltage dropped between anode and cathode. Only a few milliwatts of gate power are required to switch hundreds of watts in the anode circuit and it remains conducting even if the gate signal is removed.

It can only be turned off by reducing the anode current to just below the holding value, the specified minimum current that will ensure conduction. It is typically a few per cent of the maximum forward current.

In a.c. circuits the thyristor turns off every time the supply voltage passes through zero, but in d.c. circuits special techniques must be used to reduce the anode current and achieve turn off.

THE TRIAC

The triac is similar to two thyristors connected in reverse parallel (Fig. 3.2a) but with a common gate connection. This means that the device can pass or block current in both directions. It is triggered into conduction in either direction by positive or negative gate signals. The symbol and operational characteristics are shown in Figs. 3.2b and c.

Both devices find their main application in power controllers. With an a.c. supply, power dissipation in the load can be made greater or smaller by controlling the time during the mains cycle at which the trigger pulse is applied to the gate. Triacs are used in full wave a.c. power control circuits in preference to two thyristors, because simpler heat sink and economical trigger circuits can be used.

FAILURES-THEIR CAUSES AND SYMPTOMS

As with most other electronic components, thyristors and triacs fail largely for thermal reasons. High temperatures in the relatively small volume, or a high rate of temperature cycling causes the device to slowly deteriorate and this ultimately leads to failure.

They can also be destroyed like fuses if the maximum ratings are exceeded, so don't expect them to withstand large overload surges. Make sure that an adequate heat sink is used.

Failure can also be caused by the rate of change of the anode current. At the instant of triggering, the gate current and also the anode current is constrained to flow in a small area. If the rate of rise of anode current exceeds a critical value the heat generated in this small area may be too large and the thyristor will fail. Normally the inductance of the load circuit limits the rate of rise to a safe value.

The faults that do occur in a thyristor are:

Anode to cathode — No current flow from anode to open circuit cathode.

Anode to cathode — Thyristor conducts in both forshort circuit ward and reverse directions. Measured voltage between anode and cathode will be zero.

Gate to cathode — Thyristor off and cannot be open circuit triggered into conduction.

Measured gate signal will be high.

Gate to cathode — Thyristor off and cannot be triggered into conduction.

Measured gate signal will be

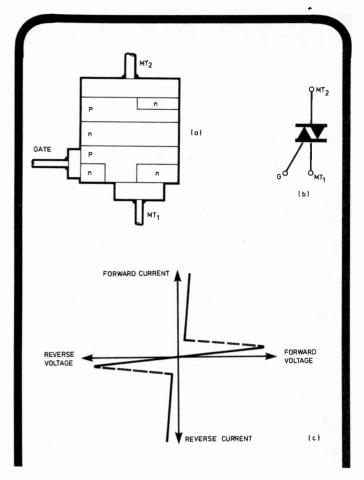


Fig. 3.2(a). Triac structure (b) symbol (c) characteristic

These are complete failures, but remember that partial failures such as poor gate sensitivity and low forward breakcover voltage can also occur.

With some circuits it is possible to test the thyristor or triac while it remains in circuit. When switched on the voltage between anode and cathode should be approximately 1V and the voltage between gate and cathode about 0.7V.

With the power switched off you can measure for short circuit anode to cathode or for open or short gate to cathode with an ohmmeter. The gate cathode of a thyristor has similar characteristics to a diode. A low resistance (typically a few hundred ohms) should be indicated with the gate +ve with respect to cathode and a high resistance (greater than $100 \mathrm{k}\Omega$) with the gate -ve with respect to cathode. But remember that other components in parallel with the gate circuit will affect the readings. If in doubt unsolder and lift the gate lead before making the measurement. Now let's move on to some fault diagnosis in typical circuits.

TRIAC LAMP DIMMER

A common circuit for a lamp dimmer is shown in Fig. 3.3 using a R\$134 triac and a phase shifting network of R1, VR1 and C2. In fact VR1 and C2 act as a variable potential divider and variable phase shift network. This feeds an attenuated and phase shifted signal to a slave network R2, C3. When the voltage across C3 exceeds about 35 volts the diac D1 triggers to partially discharge C3 into the triac gate. This then conducts and power is applied to the lamp.

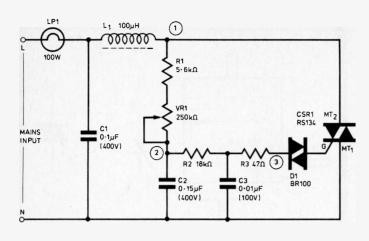


Fig. 3.3. Triac lamp dimmer circuit

The purpose of the slave network is to prevent any large change of voltage occurring across C2 when the diac triggers.

The conduction angle of the triac can be controlled up to nearly 170 degrees. This is when VR1 is set to near maximum value. Under these conditions very little power would be applied to the lamp and it would run at low brightness. Note that the triac switches off when the mains voltage goes through zero and is pulsed on in both the negative and positive half cycles of the mains.

L1 and C1 are filter components to prevent switching spikes being fed back from the triac into the mains supply.

FAULT CONDITIONS

Having looked at the way the unit works, let's consider some possible faults. Suppose that on switch on the lamp burned at maximum brightness and no control could be achieved with VRI. Without making any measurements we can see that the fault can be caused by only two components, either a short circuited triac (MT₂ to MT₁) or possibly CI short. Lift one lead, say, MT₂, to determine which component is at fault. No other component fault could give these symptoms.

On the other hand if the lamp fails to light at all and assuming that we know the lamp is o.k. which components could cause this? In this case we are looking for an open circuit in components such L1, R1, VR1, R2, R3, D1, and open junctions on the triac; either open gate to MT_1 or open circuit MT_{1-2} to MT_1 .

Another cause could be C2 or C3 short also. Measurements with a multirange meter (set to 250V a.c.) at the test points have to be made to narrow down the fault to one component. Suppose we obtain the following readings from the test points with respect to the neutral line.

Test point	1	2	3
A.c. voltage	235V	56V	43V

Lamp will not light.

These indicate that components L1, R1, RV1 C2, R2 C3 and R3 are o.k. and that the fault can only be an open circuit diac or an open circuit gate connection on the triac.

Which component fault would give the following symptoms?

(Answers are given at the end)

(a)	Test point	1	2	3
	A.c. voltage	235V	0V	0V

Lamp will not light.

(b)	Test point	1	2	3
	A.c. voltage	235V	53V	32V

Lamp will not light.

(c)	Test point	1	2	3
	A.c. voltage	0V	0V	0V

Lamp will not light.

(d)	Test point	l	2	3
	A.c. voltage	235V	36V	32V

Lamp very dim. No light increase can be obtained by varying VR1.

Finally what would be the symptoms for these conditions? (e) R2 open circuit.

- (f) C2 open circuit.
- (1) C2 open circuit.

THYRISTOR LAMP FLASHER

This circuit is of a lamp flasher unit, the flashing of a lamp commencing if the ambient light falls below a selected level.

At first glance the circuit may look a little complicated, but if it is split up into sections the operation is more easily understood. A light dependent resistor (ORPI2) is used to sense the ambient light level and the changes in resistance are detected by TR1 (Fig. 3.4). With sufficient light the l.d.r. has a fairly low resistance and TR1 is therefore forward biased with a low collector voltage so that D1 is reversed biased. C1 cannot charge and no pulses occur at R5.

If the light level falls, the resistance of the l.d.r. rises and TR1 turns off. D1 then conducts and C1 charges via R3. When the voltage across C1 exceeds the trigger point of TR3 the u.j.t. conducts and rapidly discharges C1 through R5 to give a positive pulse on b_1 . This pulse is fed through C2, R6 to trigger on CSR2 and so the bulb lights. The anode voltage of this falls to about 1V and forward biases TR2. C1 is now charged via R3 and TR2 and the u.j.t. triggers again to give another pulse. CSR2 is already on so the pulse switches on CSR1 causing its anode voltage to fall sharply. C3 couples this negative step to CSR2 anode which reverse biases it and therefore turns it off, and the lamp goes out. Note that R7 is $10k\Omega$, a value that maintains the current through CSR1 below the holding current value so that it turns off automatically.

While the light level is low the lamp will flash at a rate determined primarily by R3 and C1 about 2 flashes per second.

Now we can consider some fault conditions.

FAULT CONDITIONS

Suppose we had the following fault conditions. With bright light falling on the l.d.r. as soon as the unit is

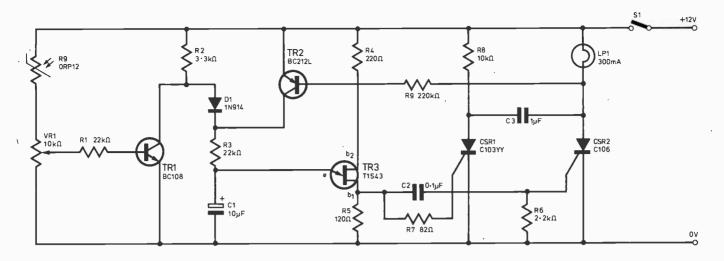


Fig. 3.4. Thyristor lamp flasher circuit

switched on the lamp lights and remains on without flashing. Before reaching for the meter we should study these symptoms because they are the guide to the faulty component. Unless we have a number of simultaneous faults the failure can only be caused by one component. You've probably worked out already that it is an anode to cathode short on CSR2.

If on the other hand we had the symptoms that the lamp would light when the l.d.r. was obscured but then remained on without flashing, we have the possibility of a failure in CSR1 and its associated components, R7, R8 and C3. An open circuit in any one of these would give these symptoms.

You can see that with this type of unit fault diagnosis is helped a lot by the visual indication given by the lamp. It's quite a simple matter to check bulb operation too by just shorting the anode to cathode of CSR2. Also a quick check can be made on each thyristor by momentarily connecting a $2.2k\Omega$ resistor from gate to +12V.

Can you work out the symptoms for the following?

- (1) C3 short circuit.
- (2) TR1 base emitter short.
- (3) CSR2 gate to cathode short.

POINTS ARISING

P.E. CHAMP (December 1977)

The use of wiring pens on the CHAMP board should be restricted to small signal connections and not supply lines. Pen wire may have maximum current ratings of as little as 30mA, thus introducing resistance which can cause poor localised supply regulation due to switching transients.

CAR BURGLAR ALARM (December 1977)

It seems that constructors are experiencing considerable difficulty in procuring the capacitors C1 and C4 ($150\mu F/15V$ tantalum). They are, however, available from the Radio Resistor Co. Ltd., of Hitchin, Herts, part number SD-15-157K.

- (4) TR2 collector base short.
- (5) CSR1 anode to cathode short.

ANSWERS

Lamp dimmer faults:

- (a) R1 or VR1 open circuit or possibly C2 short.
- (b) Gate to MT, on triac short.
- (c) L1 open circuit.
- (d) VR1 wiper open circuit.
- (e) With R2 open Lamp not on

Test Point	1	2	3
A.C.	235V	75V	0V

(f) With C2 open the control over the lamp's brightness will become very limited. The lamp will burn at high brightness with VR1 at minimum but will only reduce slightly in intensity with VR1 at maximum.

Thyristor circuit:

- (1) C3 short circuit.
 - Assuming power is applied while ambient light is high then the lamp will be off. When the light level falls the lamp will be lit and will remain on.
- (2) TR1 base-emitter short.

 Lamp will flash on and off irrespective of ambient light conditions.
- (3) CSR2 gate to cathode short. Lamp will not light at all.
- (4) TR2 collector base short.
 - C1 will slowly charge via R9. So the lamp will flash at a very low rate even when ambient light conditions are high.
- (5) CSR1 anode to cathode short.
 As soon as light level falls lamps will come on and remain on.

Next month: Fault finding i.c. circuits



This circuit is a very useful addition to any model car racing layout and has several advantages over the mechanical lap counter.

The mechanical counter is prone to either jamming or not working at all, whereas this digital method of recording the number of laps completed is not only reliable but has the added advantage that it can be cheaply constructed; the only two critical components are IC1 and IC2.

The number of laps can be preset so that after the winning car has crossed the finishing line the power to the track is automatically out off.

THE CIRCUIT

The circuit diagram is shown in Fig. 1 and consists of an SN7490 binary counter and an SN7448 b.c.d. to 7 segment display decoder. The pulses for the 7490 counter are generated by a Light Dependent Resistor (R1) and lamp arrangement either side of the track. The lamp is set to shine directly onto the l.d.r., and whenever a car passes between the lamp and the l.d.r. a pulse is sent to the counter. On receiving a pulse, the counter will count one, and on each subsequent lap it will add one more up to a maximum of nine, after which it will reset to zero.

When the automatic power shut-off is used, the preset number of laps can be set to either one, two, four or eight by means of S2. If S2 is set to position 5, the base of TR1 is connected to ground permanently so that the circuit will not turn off the power but just count the laps, resetting each time at nine.

When S2 is set to either one, two, four or eight laps it connects one of the four outputs from the 7490 to the base of TR1. If, for example, output C of the counter is connected to the base of TR1 it will count up to four and then the output C of 7490 will go high, turning on TR1 and consequently the relay, which in turn interrupts the power supplied to the track via its normally closed contacts (RLA1).

When the reset button is depressed the counter is automatically reset to zero.

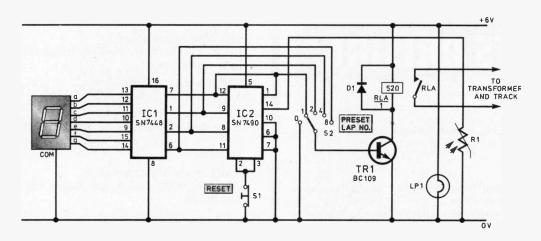


Fig. 1. Circuit diagram of the Digital Lap Counter

CONSTRUCTION

In the prototype the relay was mounted on the Veroboard, and a hole drilled through the relay case to take a fixing screw. The relay was then used to hold the Veroboard in position on the front panel; brackets could, however, be used. The i.c.'s were soldered directly onto the board but holders could be used if preferred. The Veroboard layout shown in Fig. 2 is slightly more expanded than that used in the prototype, and this is to make the wiring between the i.c.'s less congested. Hence the position of S2 must be altered to allow for the longer board.

The seven segment display used was of a sub-miniature type, but as this type does not now seem readily available,

one of the more common ones is specified. A suitable fixing arrangement is shown in Fig. 3 for this seven segment display. The Veroboard should be assembled first, the relay quoted can be directly mounted to the board without the need for a relay holder.

ALTERNATIVE HOUSING

If the unit is to be mounted in a case, the front panel should be drilled and cut so that the two switches and seven segment display can be mounted. R1 was fitted on two spare terminals of switch S2. It may be necessary to cover R1 with a piece of plastics tube in order to protect it from ambient light; a hole must be drilled in the side of the case for R1.

D₁

TR₁

COMPONE

Semiconductors IN4001

BC109

SN7448

SN7490

IC2

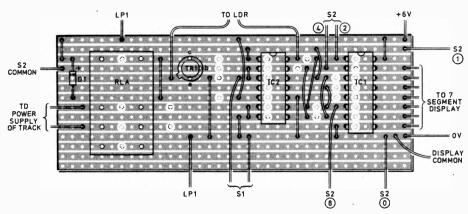
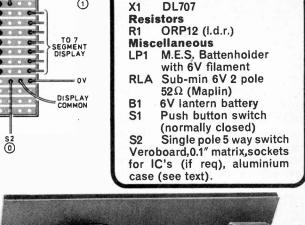


Fig. 2. Component layout and wiring of the lap counter



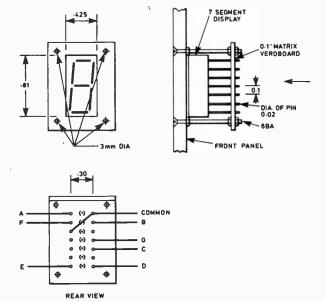
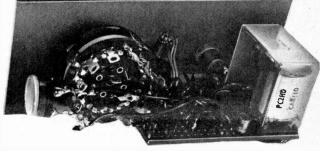


Fig. 3. Fixing arrangement for the seven segment display

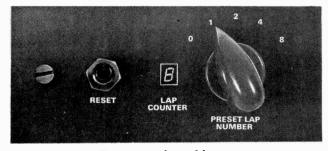
INSTALLATION

For the normal dual lane layout two complete circuits will be required.

The R1/lamp arrangement should be approximately one car's length behind the start/finish line because the completed lap is not recorded until the car actually passes completely through it.



Rear view of the chassis

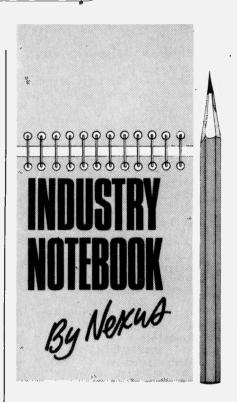


Front panel marking

POWER SUPPLY

The circuit can be supplied using a 6V lantern battery but the current drain is quite high (approximately 100mA) according to the type of relay used.

If preferred a power supply unit could be constructed using one of the many designs that have been previously published.



NEW BROOM

A prime minister's or a president's first hundred days in office are crucial. This is the period when the leader establishes style and example. There is a parallel in large commercial enterprises and Sir William Barlow, chairman of the Post Office, has just completed his first hundred days as leader of Britain's largest business.

Sir William arrived on the scene with a lot of old-fashioned virtues. So out-moded that they appeared almost revolutionary. Concepts like improved customer service and keeping prices down. He said: "Let us look at what we can give, not take away." He has views on aggressive marketing, on expansion of the services. He wants to run the Post Office for the benefit and convenience of the customer rather than for the Post Office, though it, too, will gain in the end because value for money always expands trade. In fact, quite like old times.

We must hope, too, that Sir William will devote some of his time to procurement policies and avoid such shambles as last year's mass cut-backs in orders to the telecommunications manufacturing industry which caused such chaos and real distress, the repercussions of which are still being felt today.

Sir William Barlow deserves every success.

STILL GOING WEST

The livelier European enterprises are still looking to the United States as the best market for expansion and have recognised that the quickest way

in is through acquisition. Latest British buy in the USA is Carterphone of Dallas, Texas, acquired by Cable & Wireless for £9·3 million. Carterphone rents, leases, sells and services data communications terminals through 40 branches throughout the nation.

Apart from the USA being the biggest individual market for electronics equipment in the world, the most significant business reason for investing there is political stability. As a C & W official points out, "—— political risks are low." The disadvantage is that the USA is also the most competitive market in the world so you have to be smarter and work harder for every dollar earned. But with a market twice the size of the whole of Europe, even quite a small penetration can mean very big business by UK or European standards

AVIONICS BOOST

British avionics companies have had a strong injection of orders for updates and new equipment. Nimrod maritime reconnaissance aircraft are now being progressively withdrawn from service for installation of improved sensor, navigational and tactical systems. A new tactical computer on the aircraft will process information from sonobuovs more than 50 times faster than on existing equipment. The new computerassisted Searchwater radar can spot even smaller targets at greater range, and a new inertial navigation system will improve precision. Communications improvements include teleprinters with on-line encryption. The update programme is intended to meet all envisaged submarine threats through to the 1990s.

A parallel production line is being established for converting some of the Nimrods to the airborne early warning role, itself a multi-million pound programme. More than 100 avionic units are being developed for the communications system alone and the programme is providing 2,000 jobs for the main contractor (Marconi) and the subcontractors.

Plessey and Marconi also have huge contracts running for updating over 30 types of RAF aircraft with v.h.f. and u.h.f. radio equipment. The programme is said to be worth £10 million and involves building and fitting 2,000 sets of equipment. The technical requirement is to double the number of radio channels available from 360 to 720. This is achieved by reducing the channel spacing from 50kHz to 25kHz.

Ferranti have got the go-ahead for development of a new horizon gyro for the air defence variant of the Tornado (MRCA). Unusual feature is pitch and roll pick-offs feeding signals to the avionics systems.

Fly-by-wire, i.e. electrical control of aircraft which superseded push-pull

rods and mechanical cables and pulleys is now, in turn, being superseded by fly-by-light using optical fibres. Marconi-Elliott has five systems on trial in the US Boeing YC-14 STOL transport aircraft and expect orders for 300 more sets this year with a possible long-term sale of 3,000 sets. Optical fibre links have definitely arrived after years of experimentation and learning how to make the fibres and, equally important, how to connect them together. A trial installation over 100 yards long in the warship HMS Tiger has given over 6,500 hours service with no degradation in performance. American confidence in such systems is such that a fibreoptic data link between computers in the Chevenne Mountain command post of the North American Air Defence System has been in operation since 1975, again with no failures reported.

HITACHI . . .

The Japanese company Hitachi has shelved but not cancelled plans for setting up a TV manufacturing plant in the UK. The news got a mixed reception, relief from UK manufacturers and frustration from those in favour of increased capital investment in the UK from whatever source.

... AND MULLARD

Meantime, Mullard is planning to invest another £4.5 million in the Southampton semiconductor plant to meet production demand for the upsurge in business expected from Teletext, Viewdata, digital tuning of TVs and TV games. The range of i.c.s and modules is based on technology drawn from the whole of the Philips Group including the US Signetics, based in Silicon Valley. Mullard currently claims 33 per cent of a UK market estimated at £12 million. By 1982 the market is expected to grow to £30 million with Mullard targeted on £18 million.

INDUSTRY NEWS

The bloom has faded from the medical scanner business. It was too much to hope that EMI's world lead could last forever. Now the going is much tougher, especially in the USA where domestic competition has increased. AB Electronics and Chloride have both been hit by strikes, now resolved but at great cost.

There are 108 distributors of electronics components in the UK in a recently published list. But according to some sources there are actually over 170 operating. Strange when you think that only a few years ago it was confidently predicted that the number, then approaching 100, would drop to three very large broad line distributors and half a dozen specialists.

Explained

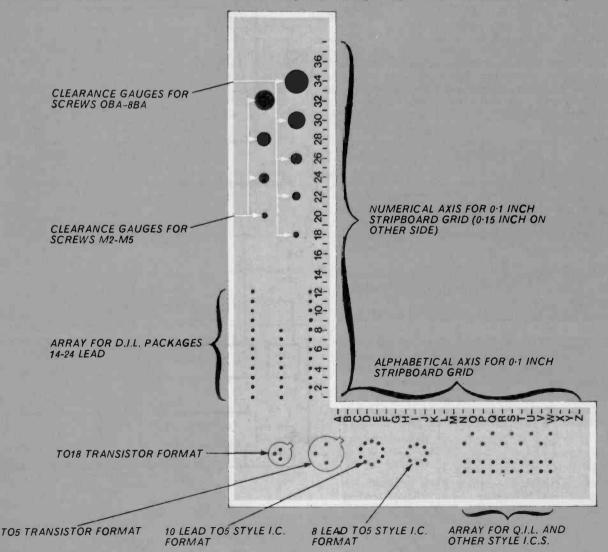
The Matrix Marker is a useful aid to The Matrix Marker is a useful aid to the electronics constructor, and is marked on both sides to enable grid referencing of both 0·1 and 0·15 inch stripboard. By placing the marker against the corner of your stripboard, component and track cut positions can be identified instantly.

The illustration below shows how the

Matrix Marker can be used for checking clearance drill and screw sizes for Metric and BA range screws. There are two ruled edges for inches and centimetres, and hole arrays to suit most i.c.s. and transistors.

The first hole array is for up to 24 lead d.i.l. i.c.s, and the second array is for the quad in-line packages and

less usual ones often associated with audio i.c.s. Positions for 8 and 10 lead TO5 linear i.c.s, and TO5 and TO18 transistors are also included. These holes can be used for marking out copper clad board when making a p.c.b., or simply for aligning device pins prior to insertion. See below for Matrix Marker layout.



A full charge everytime with no danger of overcharging BATTERY

NiCad CHARGER

E.A.PARR B.Sc. C.Eng.

NICAD battery chargers usually fall into one of two types. The first is the constant current charger, which charges the battery at a constant current for an indefinite period. The user has to note the state of charge of the battery by measuring the short circuit current and relating this to the charge required; note the start time and be back in time to take the battery off charge. In theory this works, in practice it does not. The author once maintained mobile radios on a steelworks and the battery chargers worked on this scheme. The number of batteries ruined by overcharging was surprising.

The second type of charger is the charge to a voltage type. In this type of charger the battery is charged until its terminal voltage rises to a set level. If the voltage is correctly set the battery cannot be ruined by overcharging. Unfortunately, the charge curve of a Nicad battery is very flat, see Fig. 1, and if the trip voltage is only slightly out you can end up with a 50 per cent charged battery or a battery ruined by overcharging. In addition, the trip voltage required will not be the same for all batteries, even those of the same nominal type.

The tendency is for people using this type of charger to set the trip voltage on the low side (for safety) and put up with partly charged batteries.

This article describes a charger which gives a fully charged battery every time, with no danger of overcharging. It uses the Ferranti ZN1034 timer to define the charging time, which in conjunction with a constant current, defines the charge put into the battery. To accept a battery in any state of charge, the battery is discharged before charging commences. This is perfectly alright since completely discharging a Nicad battery does no harm. The circuit will stand reversed battery or short circuited output.

TIMER CHIP

The Ferranti ZN1034 is a timer chip designed for long time applications. It consists of an oscillator feeding a twelve stage binary counter (Fig. 2). The control logic

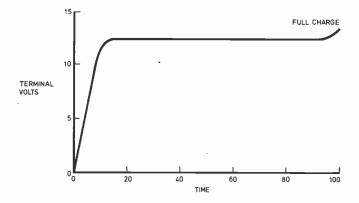


Fig. 1. Nicad battery charge curve at constant current

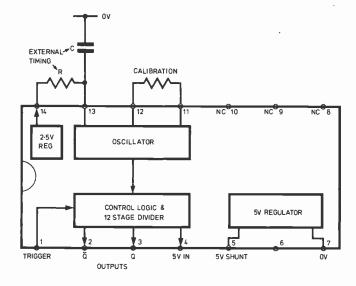


Fig. 2. Block diagram of ZN1034

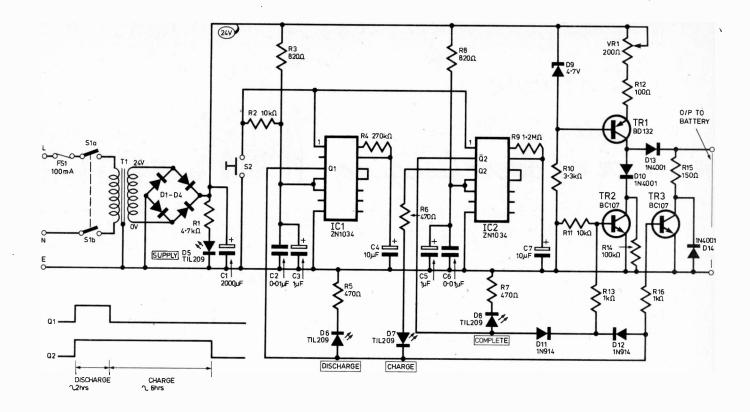


Fig. 3. Circuit of Nicad charger

times out after 4095 counts, giving a long period monostable with reasonable value components. The timed period is given by:

T = KRC seconds

where K is a constant determined by an external resistor connected between pins 11 and 12. With pins 11 and 12 linked, K=2730. With an external resistor connected between them, K increases up to K=7500 for a resistance of 200 kilohms.

The ZN1034 has an internal 5V shunt regulator, allowing it to run off any voltage supply, with a suitable dropper resistor. True and complement outputs are available on pins 3 and 2 respectively and the device is triggered by 0V to pin 1.

CIRCUIT DESCRIPTION

The basic charger circuit diagram is shown on Fig. 3. IC1 controls the battery discharge, IC2 the battery charge. Both use their internal 5V regulators; R3 and R8 being the dropper resistors, decoupled by C3 and C2, C6 and C5.

IC1 is set for about 2 hours by R4, C4 and IC2 for about 8 hours by R9, C7. Both timers start together by S2, giving 2 hours discharge and 6 hours charge, a total of 8 hours.

Transistor TR1, Zener D9 and resistors R12, VR1 form a simple constant current source. This current can go to the battery, via D13, or be shunted to 0V by transistor TR2. TR3 is turned on by Q1 (IC1) or Q2 (IC2) via D11. The current is thus shunted during the discharge time and after the charging is complete.

TR3 discharges the battery by R15. This is simply turned on by Q1 output, hence the battery is discharged for the 2 hour period of IC1.

The charge period is thus the difference between the

periods of IC1 and IC2, nominally 6 hours. The charge/discharge cycle is summarised on the timing diagram in Fig. 3.

The operation is displayed by three l.e.d.s. D6 is driven direct off Q1 and hence indicates "Discharge". D7 is connected from Q2 to Q1 outputs and indicates "Charge". D8 is driven off Q2 and indicates "Cycle Complete".

The power supply is a simple unregulated 24V supply. The two i.c.s provide their own 5V rail from their internal regulators as described previously.

Resistor R14 and diodes D10, D14 provide protection against a reversed battery.

CONSTRUCTION AND USE

The prototype was built on Veroboard with the layout shown in Fig 4. Transistor TR1 has to dissipate about 1.5W in a reversed battery condition and should be mounted on a heat sink or onto the case. The unit was made to charge one battery, and fitted comfortably into an 8in × 5in box.

It is suggested that the circuit is first tested with R4 set at 5.6 kilohms and R9 at 6.8 kilohms to give three minutes discharge and one minute charge. Eight hours is a long time to wait to see if it all works.

A link somewhere in the battery leads where the charge/discharge currents can be monitored is also very useful.

Since the capacitors used have a tolerance of ± 20 per cent, it follows that the timed periods are not going to be exactly two hours and eight hours. There are two options, the aesthetically pleasing and the practical. In both cases it is essential that the capacity of the battery is known. The batteries used in the prototype were 225 mA/hr. In both cases, set up without the battery in, observing the l.e.d.s.

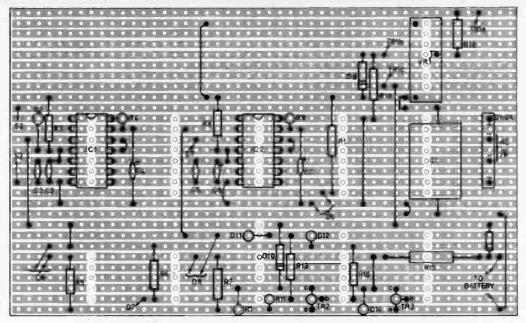


Fig. 4. Veroboard component layout and wiring diagram

The aesthetic first. The discharge and charge periods are made two hours and six hours exactly by calibration trim pots. The links between pins 11 and 12 on each i.c. are replaced by 200 kilohm trim pots, and R4 reduced to 180 kilohms and R9 to 820 kilohms.

Adjust the trim pots to set IC1 to two hours and IC2 to eight hours. If a 'scope with a high impedance probe is available, the oscillator period can be measured on pin 13 of each i.c. The total period is found by multiplying the period by 4095. The current is then set to the capacity divided by six.

The practical method is recommended, however. The discharge/charge times are measured, and the charging current changed to suit.

The prototype was found to have a discharge time of about 1.8 hours and a total time of about 8.6 hours. This gives a charge time of 6.8 hours. The charge current required, therefore, is capacity/6.8 (225/6.8 = 33 mA) and the current set by VR1 accordingly.

In both cases the calibration should be checked if C4 or C7 is changed.

The eight hour cycle was chosen to give overnight charging. By doubling C4 and C7 a sixteen hour cycle can be made. The lower charging current would probably be better for battery life.

The sixteen hour cycle is suitable for equipment like mobile radios being used on a three shift system. The batteries spend two shifts on charge and one in use.

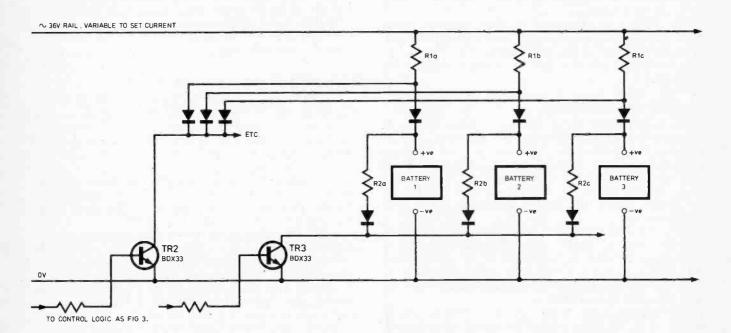


Fig. 5. Charging method for several batteries

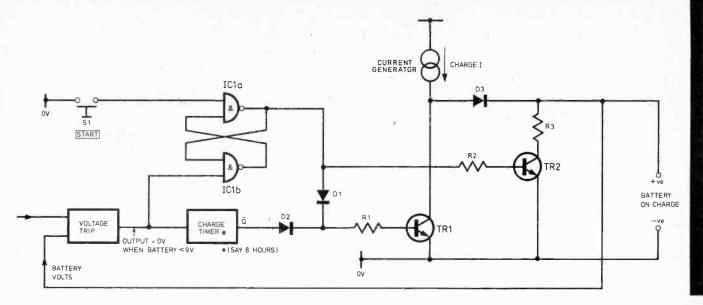


Fig. 6. Schematic for fully automatic charger

COMPONENTS . . .

TO SALVEY AND STATE STATE	THE RESIDENCE THE PROPERTY OF THE PARTY OF T
	3 off) 1W
Potentiometer VR1 200Ω line	2.5
	aı
Capacitors C1 2000μF elect C2 0.01μF cera C3 1μF tantalu C4 10μF tantalu C5 1μF tantalu C6 0.01μF cera C7 10μF tantal	nmic m 6V um 6V m 6V nmic
Semiconductors C1- C2	ZN1034 (2 off) BD132 BC107 (2 off) 1A 50V Bridge Rectifier TL 209 I.e.d. (4 off) 4·7V Zener 200mW 1N4001 (3 off) 1N914 (2 off)
Switches S1 Double-po S2 Press swit	le mains on/off switch
Transformer T1 240V pri; 2	0V sec at 0.5A

VARIATIONS

The prototype was built to meet the author's requirement of a one battery overnight charge, with a battery of unknown charge state.

The control logic can be extended to charge several batteries at the same time by changing TR2 and TR3 as in Fig. 5.

For several batteries it is costly to provide a true constant current source for each, and a reasonable compromise would be to take the supply rail up to about 36V and utilise a single resistor (R1a-c).

This would approximate a constant current source over the voltage range and the battery would vary (12–14V).

With a 36V rail R3 and R8 should be increased to 1.8 kilohms with a 1W rating.

If it is certain that the batteries are already discharged, IC1 can be omitted, along with D12, R13, TR3, R15, etc.

D7 should be connected to 0V. Note that the battery charges for the whole period of IC2 now, and the period or the charging current should be adjusted to suit.

AUTOMATE

One further development that could be made is to automate the battery discharge along the lines of Fig. 6. The start button sets the memory IC1 which turns on the discharge transistor TR3, discharging the battery in the usual manner.

The battery terminal volts are measured by a voltage trip circuit. When the voltage goes below, say, 9V, the memory is reset and a charge timer, IC2, started to charge the now flat battery for the correct time.

This sets the discharge period correct for the battery, and shortens the whole cycle for the average, nearly discharged, battery.



NOMOGRAPHS for integrated circuit timers

E.A. PARR B.Sc.

THE integrated circuit timers that have become available in recent years have proved to be amongst the most useful devices for both the professional and the amateur electronics engineers. Unfortunately the choice of components for a desired result necessitates juggling with kilohms, megohms, microfarads and picofarads. It is very easy to end up with a period or frequency that is out by a factor of ten or a hundred.

CAPACITANCE 0 001µF (1nF, 1000pF) % [% □ PERIOD RESISTANCE 1kA 10µS (10nF, 10000pF) 2بر100 1mS 10 k 0-1_µF 10mS+ 100 k 100mS - 10 S 10ہ∈ 1MA 100 S 王 1000S 100 µF

Fig. 1. Nomograph for the 555 monostable.

The nomographs in this article were drawn up to make a selection of component values for all the common timers a quick and foolproof process.

555 MONOSTABLE

The nomographs for the 555 monostable are shown in Fig. 1. A line drawn between resistance, capacitance and

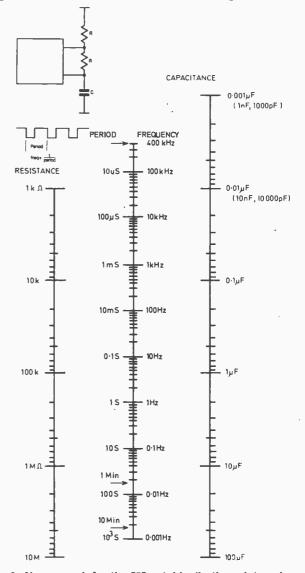


Fig. 2. Nomograph for the 555 astable (both resistors have value R).

time will give the third when two are known.

For example, suppose we have a $1\mu F$ capacitor and we want a one second period. The nomograph gives $800k\Omega$, so the nearest preferred value of $820k\Omega$ is chosen.

555 ASTABLE

There are three possible ways of using the 555 as an astable, making both timing resistors equal (giving 2:1 mark space), coupling pin 3 (output) to pins 2 and 6 with a timing resistor to give equal mark space or choosing different values for the two timing resistors to give a definite waveform. All are covered by the four 555 astable nomographs.

The simple case of both resistors equal is covered by Fig. 2. As can be seen, a typical result would be $50k\Omega$ and 0.1μ F giving 100Hz.

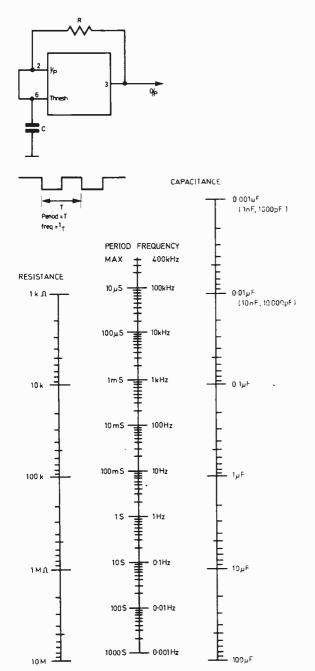


Fig. 3. Nomograph for equal mark/space 555 astable.

The equal mark space arrangement is shown by Fig. 3, a typical result being 1 kHz; approximately given by $4.7 \text{k}\Omega$ and $0.15 \mu\text{F}$.

To assemble a desired waveform use Figs. 4 and 5. The general equations for an astable are:

Output high
$$T_a = 0.7(R_a + R_b) \times C$$

Output low
$$T_b = 0.7R_b \times C$$

Total period =
$$T_a + T_b = 0.7(R_a + 2R_b) \times C$$

A nomograph for R_b and C is given in Fig. 4 to select the output low time (which must be done first).

Given R_b and C, $(R_a + 2R_b)$ is now found from Fig. 5 for the desired period or frequency. Knowing R_b , R_a is easily found.

To demonstrate this, suppose we want a 0·1 second pulse occurring every second, then:

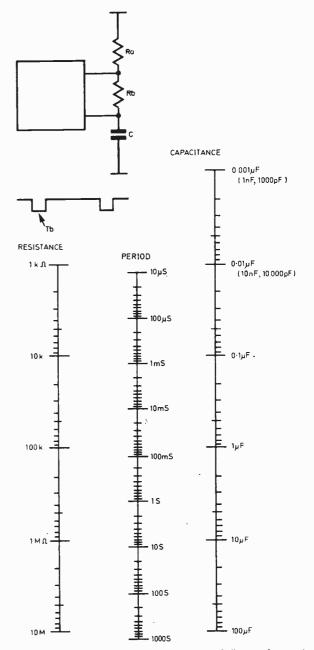


Fig. 4. 555 astable. Plot for selection of \mathcal{R}_b to give output low time. Use in conjunction with Fig. 5 to give desired waveform.

 $T_a = 0.9S$

 $T_{\rm b} = 0.18$

T = 1S

frequency = 1Hz

First we select R_b and C using Fig. 4 to give 0.1 second. Typical values would be $33k\Omega$ and 4.7μ F (to the nearest preferred values).

Now we use Fig. 5. We have C ($4.7\mu\mathrm{F}$ from above) and the frequency is 1Hz. The nomograph gives $(R_{\mathrm{a}}+2R_{\mathrm{b}})$ to be $280\mathrm{k}\Omega$, hence R_{a} is $220\mathrm{k}\Omega$ to the nearest preferred value. The values desired are thus $R_{\mathrm{a}}=230\mathrm{k}\Omega$, $R_{\mathrm{b}}=33\mathrm{k}\Omega$, $C=4.7\mu\mathrm{F}$.

ZN1034 TIMER

The ZN1034 is a very useful device for applications in

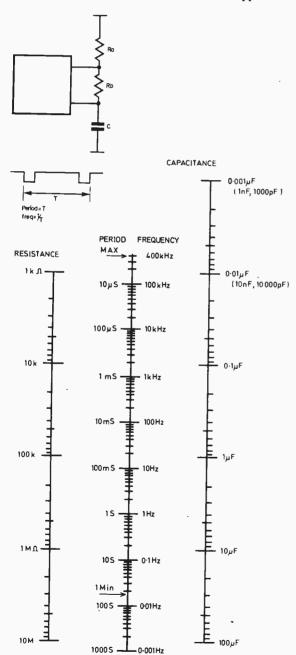


Fig. 5. Nomograph for 555 astable, plotted for $R_{\mathrm{a}}+2R_{\mathrm{b}}$.

long delay circuits. It has been described elsewhere, but for people not familiar with it, it consists of an oscillator and a 12 bit binary counter. This allows very long delays to be obtained with reasonable value components.

The device has the facility for trimming the period. This is done by connecting a resistor between pins 11 and 12. With pins 11 and 12 linked, the calibration resistor is $100k\Omega$. With a $200k\Omega$ resistor between pins 11 and 12 the calibration resistor is $300k\Omega$.

The timed period given by:

$$T = K \times R \times C$$

Where K is a multiplier dependent on the calibration resistor. For a calibration resistor of $100k\Omega$, the multiplier is 2,700. For a calibration resistor of $300k\Omega$, the multiplier is 7,500.

Two nomographs are shown: Fig. 6 has a multiplier of 2,700 and Fig. 7 has a multiplier of 7,500. Between

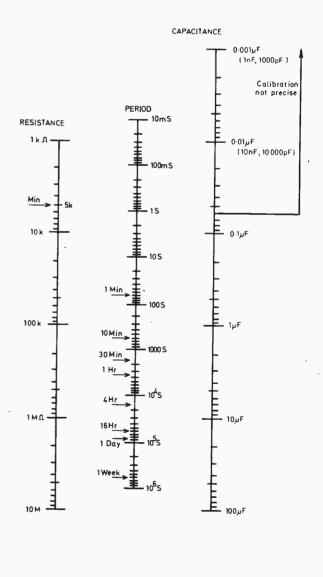


Fig. 6. Nomograph for ZN1034 timer. Pins 11 and 12 linked (internal calibration 100k Ω). Multiplier K = 2,700.

them the two nomographs show the limits attainable with a $250 \mathrm{k}\Omega$ potentiometer between pins 11 and 12. Note the small value components to give a specific value compared with the 555 monostable.

If it is desired to get a specific value choose a time 2/3 of the desired time and use Fig. 6. The desired time can then be trimmed with the $250 \text{k}\Omega$ pot between pins 11 and 12.

For example, assume we want one minute. We choose values for 40 seconds on Fig. 6. A typical choice would be $1\mu F$ and $15k\Omega$. Fig. 7 confirms the maximum attainable with this combination is 100 seconds. The desired value of 60 seconds is around the middle of the range and can be accurately set with the $250k\Omega$ pot.

Note that the calibration is not precise for values of C below $0.068\mu\mathrm{F}$. Times up to 20 per cent away from the calculated value may be expected.

TTL MONOSTABLES

The nomographs Figs. 8 and 9 cover the 74121, 74122, 74123 and 96000 series monostables. The operation of these is similar to the 555 monostable.

In general TTL monostables are best suited to times below one second, above this it is probably easier to use the 555. For very short periods the stray capacitance will probably increase the calculated time.

GENERAL OBSERVATIONS

The accuracy of capacitors, particularly electrolytics, is not good, so do not be surprised if there is a difference between calculated and observed values. The nomographs have an accuracy of about 10 per cent which is better than most electrolytics.

Electrolytics also have a characteristic of exhibiting a

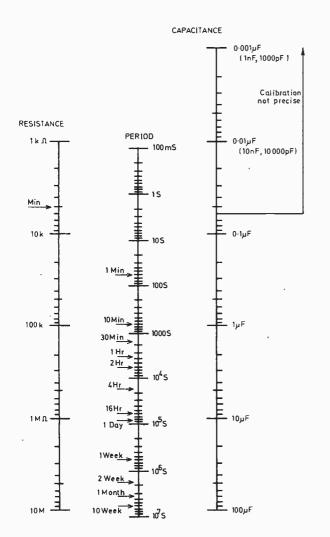


Fig. 7. Nomograph for ZN1034 timer with 200k Ω between pins 11 and 12 (calibration 300k Ω). Multiplier K = 7,500.

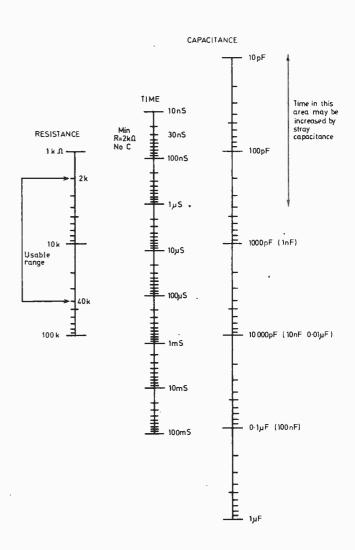


Fig. 8. Nomograph for 74121 monostable. N.B. Capacitors up to $10\mu F$ allowed. For $1\mu F$ to $10\mu F$ use 0.1 to $1\mu F$ part of capacitor scale and multiply time by ten. For times above 100mS, divide time by ten, and multiply capacitance found by ten, e.g. 250mS: 25mS gives $0.8\mu F$ with $39k\Omega$, therefore $8\mu F$ with $39k\Omega$ will give 250mS.

low value until about 10 per cent of their working voltage. If a 100V electrolytic was used in a 12V circuit, the observed times could be different from the calculated times by a factor of four. For best results the circuit volts should be, only just below the working voltage of the timing capacitor.

For use in an electrically noisy environment, it is always preferable to use a large C and small R rather than vice versa. This is not, however, the cheapest way to give a specific time. The cheapest way is to use a small C and large R. For small values of C, however, remember that stray capacitance will increase the observed period.

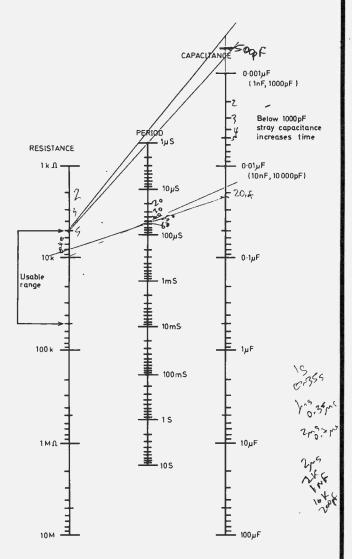


Fig. 9. Nomograph for 74122/3 monostables. Use also for 9600 monostables.

NEWS BRIEFS

Interference Between Systems

As a result of the increase in electronic systems of all types in recent years, there is a greater possibility of interference between systems if proper precautions are not taken. An example of what can occur in the absence of such precautions was a ship's officer who could tune his radio telephone to a certain frequency, press the transmit button only to find that it provoked a sudden change in the ship's course. This potentially hazardous case highlights the problems of non-compatability between separate but adjacent electronic systems, concerned respectively with communications and control.

The problems of preventing or controlling the generation of interference signals and the reduction of the sensitivity of equipment to interference between and within systems is the main theme of the conference on Electromagnetic Compatability which is being organised by the Institute of Electronics and Radio Engineers at the University of Surrey, Guildford, in April.

Spring Hi Fi Show

THE 1978 International Spring High Fidelity Exhibition is to be staged at the Cunard International Hotel. Hammersmith, London. This decision was the result of a policy change by the Heathrow Hotel Management to limit their operations to classic hotel functions rather than large exhibitions.

The previous four Spring Exhibitions were held at the Heathrow Hotel, but because the 1977 Autumn Show logistics worked successfully at the Cunard, it was considered possible to introduce smoothly the 1978 Spring Exhibition into the same venue.

The Exhibition will run from May 2 to May 6 (10 am to 8 pm), with the first three days open to trade only.

Microprocessor Courses

A MICROPROCESSOR consultancy and training company, Bleesdale Computer Systems Ltd, regularly present two microprocessor courses. Both courses deal with the Motorola M6800 microprocessor. One is a five day introductory course costing over £200 and the other a ten day workshop course.

The introductory course assumes that the participants have little or no knowledge of computers and starting with the basic principles, the operation of a complete 6800 system is dealt with. There is also an introduction to the basic concepts associated with the programming of computers. This includes the aspect of defining the solutions to problems in the format of a flowchart. The rules regarding the design and development of good quality software are discussed. Course members are involved in the designing and writing of software solutions to set problems.

The ten day course is designed for people who have a basic knowledge of microprocessors and ideally who have attended the introductory five day course.

The objective of the course is to learn how to design and produce good quality, highly reliable microprocessor based systems. The participants have ample opportunity to get practical experience using Motorola development systems. They are also expected to design and build the software for one of several set problems, or design and produce the software for one of their own projects.



FRANK W. HYDE

TWO PIONEER SPACECRAFT TO VENUS

Two spacecraft will be launched to Venus this year. They are the Orbitor and a Multiprobe vehicle. The Orbitor will be launched in May and will be inserted in the planned orbit in December. The second vehicle will be launched in August and timed so that the probes enter the Venusian atmosphere five days after the arrival of the Orbitor. The probes will be launched from the Multiprobe vehicle 20 days before the rendezvous date. Each will have a different target point on the surface of Venus. There are four probes, one large and the others small.

The target areas are scattered so that one small probe will enter on the dark side of the planet high up to polar latitude and another just below the equator, also on the dark side. The large probe will land on the equator and the third small probe will land in the middle latitude below the equator, both on the sunlit side. The carrier vehicle, or 'bus', will enter the atmosphere of the planet at a low elevation, making measurements of the atmosphere by means of a spectrometer, before it burns up at an estimated height from the surface of 125 kilometres.

THE ORBITOR

The Orbitor mission is designed to cover the whole globe of the planet and map the atmosphere by remote sensing and radio occultation. The upper levels of the atmosphere will be directly measured as will the ionosphere and its reaction with the solar wind. The Orbitor will also study the planetary surface by remote sensing and radar mapping techniques. It is hoped that there will be

useful information on the surface craters and also perhaps an estimate of the global shape of the planet.

The Orbitor will be inserted in a highly inclined elliptical orbit. At its lowest point this orbit would bring the spacecraft 200 kilometres above the mid latitude. This would mean that the observations could go on for a period exceeding the Venusian day which is 247 Earth days. The spacecraft is about 250 centimetres in diameter and weighs 567 kilogrammes. It carries a payload of 43 kilogrammes. The launch trajectory will take the Orbitor more than 180 degrees round the solar system in eight months. With the apogee at a point some 60 thousand kilometres, perigee is at 200 kilometres. The spacecraft will make most of its measurements when at the perigee point. The useful time for data transmission will be about 1 hour each day.

THE MULTIPROBES

This spacecraft consists of the basic bus unit which has been modified to carry two scientific instruments and four probes. The whole assembly will be spin stabilised for the interplanetary flight and powered from solar cells. All trajectory movements and corrections will be made by the bus, as well as targeting the probes when they are launched. The four probes will be launched toward the planet 20 days before the target date.

The large probe and the small probes are each complete systems that provide the necessary subsystems to carry the instruments to the target. During this time communications links will be direct from the probes to the Earth based stations.

THE LARGE PROBE

The large probe is 145 centimetres in diameter and weighs about 291 kilogrammes. The data transmission will be at the rate of 256 bits per second. The instrument load is of the order of 28 kilogrammes. Like the small probes the front end is a heat shield of carbon phenol; it also serves to stabilise the vehicle aerodynamically when descending through the atmosphere of Venus with the parachute system.

The other probe subsystems, that is the data handling equipment, power, thermal protection, communications, are enclosed in the pressure vessel. The scientific payload is also inside the pressure vessel.

THE SMALL PROBES

Each of the three small probes are identical. They are 71 centimetres in diameter and weigh 86 kilogrammes. There are for each probe scientific instruments weighing 2.7 kilogrammes. Transmission of data is at the rate of 16 to 64 bits per second. The pressure vessel seals the instruments against the hostile atmosphere of Venus. Each probe has a conical shield of heat resistant

material at the forward end. No parachutes are used with these probes. The time of descent after entering the atmosphere is estimated to be about 70 minutes. During this period measurements will be made to give details of the winds and circulation patterns. The probes will not survive the impact. An instrument called the Nephelometer, developed by the Ames Research Centre, will measure the extent of the clouds, their density and altitude. A net flux radiometer will be included in the instruments in order to investigate the change of heat energy between the Sun and the atmosphere.

RADAR AND RADIO SYSTEMS

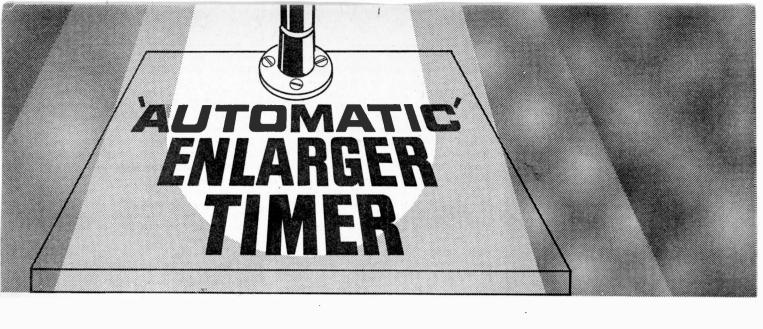
An interesting point about the communications on these vehicles is the fact that the communications equipment can be employed as a scientific instrument. This is accomplished by using the radio signals to assess the alterations in performance caused by the planet Venus and its atmosphere. As the radio signal passes through the atmosphere, the signal is changed and these changes can be measured. The type of change can reveal the characteristics of the atmosphere. The radio system will therefore be used to determine composition and density of the atmosphere, the cloud locations, atmospheric turbulence and wind velocities.

These radio experiments will be carried out with the Orbitor and the probe vehicle as well as the probes themselves. All the entry vehicles will be involved using the S-band equipment. Each entry vehicle transmits directly to the Earth-based stations in the Deep Space Network. The Orbitor will use the S-band telemetry and a specially designed X-band beacon system.

THE MISSION PHILOSOPHY

The study of weather patterns on other planets can help to solve some of the vital problems that exist regarding the weather patterns of the Earth which at the moment are of great concern. For example, no data is available to help decide the freakishness of the tornado and hurricane paths which occur. Study of the meteorology of Venus will be a great opportunity to solve such problems for the Earth. Many factors are involved on the Earth such as the mixing of the Oceanic and Continental air masses. Partial cloud cover too is involved. Many things are difficult in the study of the atmosphere of the Earth such as the axial tilt, and the rapid rotation. Venus is easier to study in depth. It has an atmosphere which contains 95 per cent carbon dioxide, very slow rotation, very little tilt of the axis and no oceans.

Many fundamental questions can be answered by the study of the vital conditions on slow and fast rotating planets. There will also be answers with regard to the different evolutionary paths that each planet has taken.



This unit was designed as a result of the need for a simple to operate "automatic" darkroom timer for use on all black and white enlargements. With the sensor in place under a diffuser and the enlarger set up, it is only necessary to make one simple adjustment for "automatic" timing of exposures.

CIRCUIT

The power supply (see Fig. 1) consists of a 6-0-6V transformer which, along with D1, D2, D3 and C1, C2 give a $\pm 8V$ d.c. unstabilised output. The -8V is needed for IC1 only. All the power supply components are mounted on a separate circuit board. The main board can then be used with batteries, without modification.

The principle of the sensor (R1, R6, R7 and VR1) is simply a potential divider with a manually variable element, VR1a. When the light dependent resistor is

exposed to light from the enlarger, its resistance alters, thus also altering the voltage on pin 3 of IC1, VR1a serves to "balance" the voltage at this point to within useful limits.

Integrated circuit IC1 is a voltage follower which is included to impedance match between the balance circuit and the lamp driver. If the follower is not included the base current drawn by TR1 loads down the lower half of the balance circuits.

Lamp driver, TR1, is driven by IC1 and responds to the voltage change created by VR1a and R7: When the circuit is balanced, the voltage at TR1 base is approximately 650mV and the transistor does not conduct, hence the display lamp, LP1, is extinguished. Conversely, when the circuit is out of balance, the voltage on TR1 base rises, the transistor conducts and LP1 lights to display an out of balance condition. This method of indication was used for its simplicity and to avoid the cost of a meter.

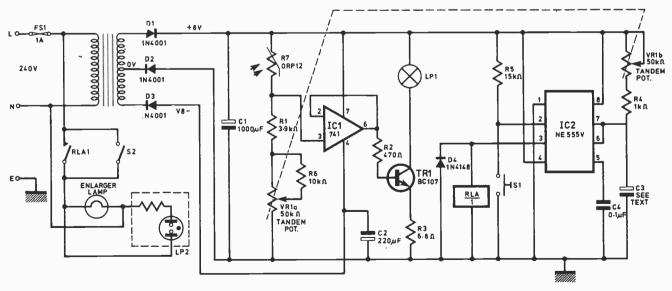


Fig. 1. Power supply/timer circuit

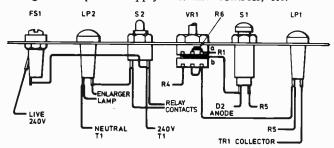
TIMER

The timer is based on the well proven circuit built around the versatile 555 i.c. Potentiometer VR1b acts as the variable element in the timer circuit and C3 the timing capacitor. Pushbutton S1 initiates the timer.

The output of IC2 drives a relay used to switch the enlarger lamp. Switch S2 is supplied to give the manual control required for balancing; LP2 is used to indicate enlarger lamp "live" condition for setting up or in case of enlarger lamp failure.

Layout and construction, etc. are not critical and the user can build the unit according to his/her requirements. The sensor can be made of anything that will house the ORP12 in a horizontal position under the diffuser.

Layout and wiring of the components mounted on the circuit board is shown in Fig. 2 whilst Fig. 3 shows wiring of the power supply unit and controls, etc.



Front panel wiring

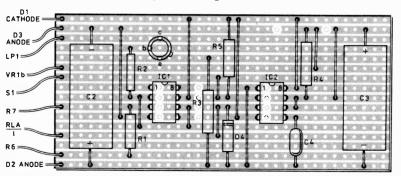


Fig. 2. Main circuit board

SETTING UP

The initial setting up procedure is very simple; one needs a multimeter and a "known" negative.

Assuming that the "known" negative's exposure time is, say, 5 seconds, all that is required is to expose the negative to the sensor in the usual way (explained later) and balance the unit, then switch off (plug out) the timer and measure the resistance (between track and wiper) of VR1b; then, using the formula:

$$C = \frac{t}{1.1R}$$
 (where $C = \text{Farads}$)

Calculate the value of the timing capacitor C3. So, if the resistance was found to be 25 kilohms then:

$$C3 = \frac{5 \text{ secs}}{1.1 \times 25,000\Omega}$$

$$C3 = 181.8 \mu F$$

In this case, obviously some trouble has to be taken to select a component as close to this as possible, and, if an a.c. null voltmeter is not available then it requires trial and error methods of soldering in nearest preferred values, not moving the setting of VR1, depressing S1, and timing the "on" period of LP2, this being repeated until 5 seconds is achieved.

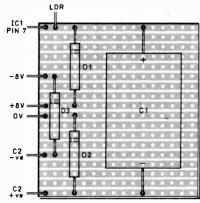


Fig. 3. Power supply board

Resistors

R1 3.9kΩ

R₂ 470Ω

R3 6.8Ω

R4 $1k\Omega$

R5 15k Ω

 $10k\Omega$

ORP12 light dependent resistor

Capacitors

C1 1,000µF elect. 25V

C2 220µF elect, 25V

C3 see text (tantalum if possible)

C4 0.1µF

Semiconductors

D1-D3 1N4001 (3 off)

D4 1N4148

BC107

TR₁ IC1 741 op. amp.

IC₂ 555 timer

COMPONENTS.

Miscellaneous

FS1 1 amp fuse and panel mounting holder

T1 6-0-6V at 1A, 240V primary mains transformer

VR1 50k Ω lin. ganged tandem potentiometer (VR1a and VR1b)

RLA1 6V 400 Ω relay with normally open contacts capable of switching the enlarger lamp current

LP1 6V 0.36W lilliput lamp

LP2 main neon with built-in resistor

S1 miniature push to make, release to break pushbutton

S2 single pole on/off toggle switch

Veroboard, connecting wire, case, mounting for R7, Perspex for diffuser, etc.

USE

Before use, two simple things must be arranged; they are, a method of easily switching off the darkroom safelight as this upsets the unit during measurement (the safelight can be used as normal during exposure, etc. but must be off during the measurement procedure) and a light dispersing filter made of finely sanded perspex sheet; this makes the light from the negative into a neutral grey and saves errors in judgement if looking for such an area on the negative.

So, having done this the procedure is as follows:

- 1. Switch S2 to turn on enlarger lamp.
- 2. Place and focus negative.
- Swing diffuser into place (remove filter to fit diffuser).
- Stand sensor roughly centrally on baseplate illuminated area.
- 5. Balance, using VR1, until display lamp LP1 is just out. (LP1 is fitted "naked" in a grommet so that in the dark even the dullest glow of the filament can be seen.) One should try and keep the balance roughly in the centre (or above) of

- the potentiometer span so that very short exposures of, say, 0.5 or 1 second are not produced; often this is achieved by "stopping down" on the enlarger lens
- Remove the sensor from the baseplate, cover it (use a small plastic cup—this prevents the safelight "lighting" LP1 during exposure) and swing the diffuser out of place.
- 7. Switch off the enlarger lamp with S2 and turn on the safelight.
- 8. Place the paper in the frame and depress \$1; the lamp will light for the required time and go out automatically. The print can now be removed and processed in the usual way.

This may seem complex but it becomes automatic after a few sessions.

The instrument is very hardy and needs no special attention other than allowing the sensor a few seconds to settle after exposure to bright light. No on/off switch is provided because the instrument will be required all the time the other darkroom equipment is used, and therefore, can be plugged in with other units and controlled by one master switch.

RECTION FROM OUR POSTBAG

Readers requiring a reply to any letter must include a stamped addressed envelope. We regret that we cannot answer any technical queries on the telephone.

Tribute

Sir—As one of your "founder readers" and, in more recent years, a contributor may I say how sorry I am to hear that Fred Bennett has relinquished his post as Editor.

I would like to pay tribute to the way Fred has dedicated himself to the amateur electronics cause over the last dozen years and congratulate him on his expertise and far sightedness. I am privileged to have had the opportunity of working for him and to have shared private discussions with him. Above all things I think that Fred, through the pages of PE, inspired the amateur to get away from the "junk box" approach to amateur construction and over the years has elevated the standard of the magazine from "Camm's comic" to a periodical valued by amateur and professional alike.

He wasn't right all the time! I remember one occasion in 1967 when he expressed the sentiment that ICs would

"never catch on with the amateur". One only has to analyse the content of the magazine from that year onwards to see that he was quick to make up for this slanderous statement and bring the excitement and challenge of professional electronics into the dining room and kitchen

I would like to thank him, on behalf of all readers and contributors, for all he has done for our hobby and in particular for all the help, advice and encouragement he has given to me personally. It is good to know that he is continuing with your sister magazine (Everyday Electronics). You had better "watch it" because, knowing Fred, he'll be out to steal your readership! With all his energy channelled in that direction your loss is going to be the gain of all young people keen to get started on one of the most exciting, challenging and satisfying hobbies.

M. Hughes, Biggin Hill. Kent.

Production method

Sir—Having tried various different methods for the production of printed circuit boards I have finally settled upon the following.

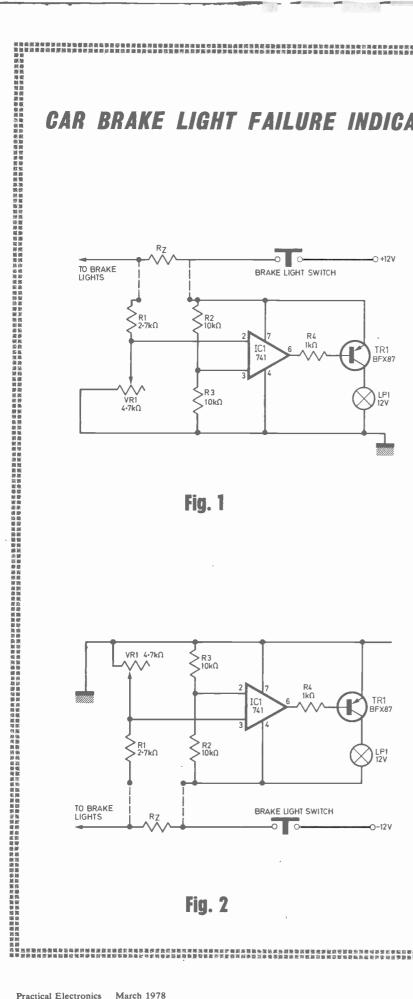
A piece of Shire Seal or similar clear self-adhesive plastic is cut slightly larger than the circuit, which is then traced on to the plastic film with a ballpoint, felt-tipped or ink pen (I use a Rotring 0.2mm drafting pen). Do not attempt to copy the circuit exactly, rather aim to include curves, component pads, etc., within straight lines, leaving as much copper as possible in the finished board. The backing is removed and the plastic film stuck carefully down on to the cleaned copper surface of the p.c.b., making sure that no air bubbles are trapped. The traced design is now cut around with a very sharp knife (e.g. a scalpel or craft knife with a scalpel type blade) and the plastic peeled off in the areas to be etched.

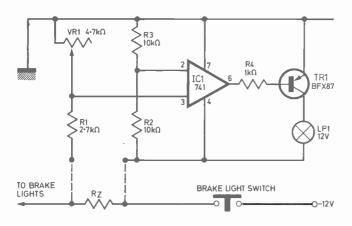
A wipe with a cloth slightly dampened with white spirit (to remove any adhesive) and straight into the etching solution, the plastic film protecting the copper areas which are to remain. When the board is etched peel off the plastic film, wipe with white spirit, then scrub the board with soap and water to remove any residual etching solution.

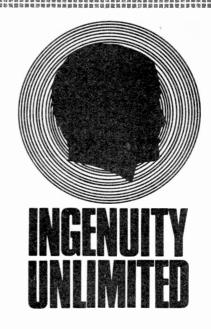
Although this system sounds time consuming it has proved itself admirably on some very complex boards. The greatest advantage, however, is the ability to produce professional looking switch plates, or keyboards for mini electronic organs, since it is possible to produce sharp edged "keys" separated by extremely small gaps.

Roger D Knight, Sheffield.

CAR BRAKE LIGHT FAILURE INDICATOR







韄篗椺縏麬篗竤鍦篗瘱鍦贕鯣

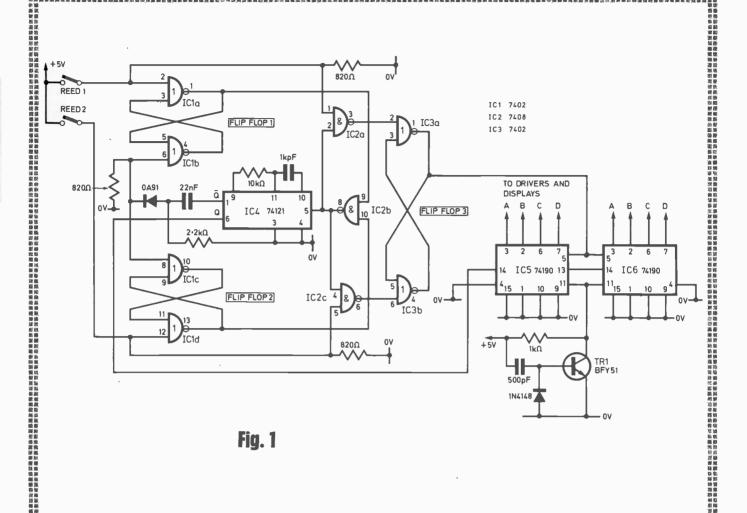
"HESE circuits (Figs. 1/2) were originally designed to monitor two 21W brake lights, but by changing the value of Rz, it could no doubt be used to monitor a wide variety of bulbs. Fig. 1 is for negative earth and Fig. 2 for positive earth vehicles. The monitoring of the brake lights depends on the potential difference across the resistor Rz. With a total brake light power of 42W, and Rz at 0.22Ω , the p.d. is 0.77V which will drop to 0.385V with one bulb extinguished.

VR1 is set to compensate for the 0.77 voltage drop and when the p.d. across Rz drops (i.e. one or both bulbs are extinguished) a higher voltage appears at the non-inverting input for positive earth, and at the inverting input for negative earth, which amplified by the op. amp, drives the base of TR1 and turns on the monitor bulb.

A light emitting diode may be used instead of a bulb and transistor, however, R4 may need to be changed. Rz should be chosen to give a suitable voltage drop which will not greatly affect the brightness of the bulb being monitored.

Rz is connected directly to the brake light switch, and flying leads are taken from both sides of Rz to the appropriate positions on the circuit board, so no heavy current flows through the circuit board.

> N. R. Garman, Newhaven, Sussex.



UP-DOWN COUNTER

This circuit was originally designed for counting turns on a hand operated coil winding machine, and automatically subtracting any removed turns. The circuit is triggered by a magnet sequentially passing over two reeds, the order determining the direction of count. A clock pulse will only be delivered after both reeds have been energised, eliminating any miscount due to contact bounce. The circuit shown in Fig. 1 works as

Energising the reeds momentarily will set flip-flops 1 and 2. IC2b detects that both flip-flops have been set, which fires the monostable IC4. The \overline{Q} output (pin 1) resets the flipflops, making them ready to accept further input pulses, and the Q output (pin 6) clocks the counter.

Only one reed is energised at any time, so only the output from the last reed to be activated will coincide with the output from IC2b.

This is detected by either IC2a or IC2c, 'setting flip-flop 3 output either high or low. This is fed to the up-down inputs of the counter thus determining the direction of the count. The counters are connected for asynchronous operation and further stages may be cascaded by taking the ripple through output to the next stage.

TR1 and associated components set the counter to zero when first switched on.

> Philip R. Landau, Southend-on-Sea, Essex.

A selection of readers' original circuit ideas. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought. Why not submit your idea? Any idea published will be awarded payment

according to its merits.
Articles submitted for publication should conform to the usual prac-tices of this journal, e.g. with regard to abbreviations and circuit symbols. Diagrams should be on separate sheets, not inserted in the text.

Each idea submitted must be accompanied by a declaration to the effect that it is the original work of the undersigned, and that it has not been accepted for publication elsewhere.

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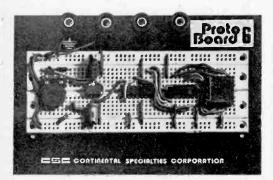
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TRANSFORMERS -6-0.6v 100mA, 9-0.9v 75mA, 12-0-12v 50 mA 75p eoch. 12-0-12v 100mA 95p. 12v 500mA 95p. 35v 2A AND 2.5v 2A TOROID 62.75 + 35p P & P. 18v 10mp RECTIFIED 61.95 + 35p P & P. 25v 2 omp 61.75p + 35p P & P. 0.12-15-20-24-30v 1 omp 63.25 + 35p P & P (2 omp VERSION 64.45 ** 35p P & P). 30-0-30v 1A 63.00 ** 35p P & P. 25v 25v 2A 63.95 ** 35p P & P. 100 volt LINE TRANSFORMER 15 worts MAX -0-8-150 61.80 ** 35p P & P. 100 volt LINE TRANSFORMER 15 worts MAX -0-8-150 61.80 ** 35p P & P. 100 volt LINE TRANSFORMER 15 worts MAX -0-8-150 61.80 ** 35p P & P. 100 volt LINE TRANSFORMER 15 worts MAX -0-8-150 61.80 ** 35p P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE TRANSFORMER 15 worts MAX -0-8-150 61.80 ** 35p P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SECOND 100 F) 80 ** 350 P & P. 100 volt LINE (SE 18 TRIAC XENON PULSE TRANSFORMER 30p. 6MH 3 omp CHOKES 30p. SWITCHES—MIN. TOGGLE, SPST 12 x 6 x 9mm 54p. D.PDT

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Semiconductor UPDATE...

FEATURING: TL071 MC14412 UDN-6118A/6128A

R.W. Coles

BIFET UPGRADE

Until recently, if you needed an op-amp with a very high input impedance you were forced to use either a discrete f.e.t. input stage with a conventional op-amp i.c., or to use an i.c. which contained an op-amp chip and an f.e.t. chip already wired together, such as the NE536. Both solutions were expensive and involved compromises on other parameters such as input offset voltage, and this meant that high input impedance was something we learned to do without unless it was absolutely essential.

All this changed with the introduction of BIFET technology which for the first time allowed high quality matched junction f.e.t.s to be integrated on the same chip with other conventional bi-polar op-amp components. Since its introduction the BIFET process has mushroomed in popularity, and today there are several BIFET op-amps, with assorted characteristics, to choose from, all at knock down prices. One example which caught my eye because of its immediate practical applications, is the **TL071** from Texas Instruments.

The TL071 is optimised for use in hi-fi amplifiers, and has the advantage that it is a plug-in replacement for the 741 types which have been widely used in the past. Using the TL071 to replace 741's in past, present or future designs, will improve system noise figures, and gives the advantage of a very high input impedance where this is useful.

Texas have proved their point about plug-in replaceability by upgrading the performance of their own popular "Texan" amplifier with three TL071 devices in place of the original 741's, a substitution which required no other component changes, and which costs very little.

For newer designs Texas offer the TL072 dual and the TL074 and TL075 quads, all of which have identical performance characteristics to the TL071 but with higher circuit density.

The TL071 family are available in 8 or 14 pin plastic dual in line packages, and operate from standard op-amp supply rails. For the hi-fi buff, the typical noise

figure of the TL071 family is only 18 nanovolts per root Hertz, and when you consider that a 100 kilohm resistor can introduce 40 nanovolts per root Hertz, you can see that these chips are pretty quiet.

CMOS MODEM

When you want to send binary data to a distant terminal you have to resort either to radio or to telephone type lines. In either case sending your data in the form of an interrupted voltage or current is not such a good idea because such a simple communication link would have no protection against the inevitable build up of noise signals.

To prevent the corruption of transmitted binary data a more sophisticated scheme must be used, so that 0s and 1s on the line can be filtered out from all the other rubbish at the receive end.

One common way to improve matters is to employ Frequency Shift Keying (F.S.K.) where the link is modulated with two different audio tones; one for a logic 1 signal and another for a logic 0 signal. This system has the advantage that it is compatible with existing speech communication links such as Post Office telephone lines, regardless of whether the links employ wire, multichannel cables, microwave links, or satellites.

A further advantage of F.S.K. is that since audio frequency tones are used to transmit the data, it is not strictly necessary to make electrical connections to telephone lines at all, signals can be coupled acoustically by placing a speaker/microphone combination in intimate contact with a standard telephone handset.

Commercial equipment of this type is available and all you need to couple your microprocessor system to any remote location are a couple of boxes called "Modems", which will both send and receive F.S.K. data. Preset Modems are rather costly and are stuffed full of i.c.s, filters and discrete components, but this is all set to change with the introduction of the Motorola MC14412 "Universal-

Low-Speed-Modem" chip.

The MC14412 is a CMOS 16 pin dil package, and yet it contains all the digital and analogue circuitry required to transmit serial binary data as a sine wave F.S.K. signal, and to turn similar received signals back into logic levels.

A typical use for this component is as an interface between the U.A.R.T. output of a microprocessor system and a wide variety of communication links, including acoustic couplers used with standard P.O. telephones. Data rates of up to 600 bits per second can be achieved, and transmitted carrier frequencies can be set to conform to either the U.S. standard or the European CCITT standard.

Under logic control this device will also send a special 100Hz tone to disable the line echo suppressors often used with long distance telephone systems so that they cannot corrupt the transmitted data.

The number of extra components required to complete a working system is small, the timing of the MC14412 being set by means of a 1MHz crystal.

FLUORESCENT DISPLAY DRIVER

If you like the distinctive green characters produced by those fluorescent displays often found on pocket calculators, you may be interested to hear that Sprague have just introduced a couple of chips to take care of all the display driving functions for this type of display. The two devices coded **UDN-6118A** and **UDN-6128A** can each act as either digit or segment drivers in a multiplexed display scheme. The UDN-6118A can be driven from TTL or 5 volt CMOS whereas the UDN-6128A is intended for use with PMOS or CMOS circuits operating from 6 to 12 volt supplies.

The new devices are housed in 18 pin plastic packages, and will operate from supplies of 25 to 85 volts. There are some very practical fluorescent display panels now available, and their continued use in calculators means that they have certainly not been made obsolete by l.e.d.s so if you do like them, you can now consider using them for one of your own pet projects!



PATENTS BEVIEW...

Copies of Patents can be obtained from :
the Patent Office Sales, St. Mary Cray, Orpington, Kent
Price 95p each

FILLER ARIVER SP 1 487 176

There is currently much controversy in the hi-fi world over the relative merits of "linear phase" or "minimum phase" loudspeakers. In a linear phase loudspeaker, there is at least an approximation to phase coherence over the frequency range covered, so that low notes and high notes are reproduced in phase.

One of the first patents on linear phase systems to appear is BP 1 487 176 from Bang & Olufsen of Denmark. The patent covers the 'filler driver' technique that is now an integral part of B & O loudspeakers.

Conventionally, a loudspeaker contains at least two transducers, with an electronic crossover routing high frequency signals to one transducer (the tweeter) and low frequency signals to the other (the woofer). If the crossover action is gradual, involving first order pass filters only, each transducer must operate well outside its normal frequency range without distortion or break-up. This is difficult to achieve. If the crossover filters are of higher order and cut off more sharply, there is a far greater signal disturbance unless the transducer transfer functions are corrected by meticulous and expensive design.

The B & O proposal, which demonstrations have shown to work well, is that a third and extra transducer unit (operating in the range between the woofer and the tweeter) should be employed to fill the acoustic gap left between the other two, and provide an overall transfer function which is constant.

Fig. 1 shows how a gap centring on crossover frequency \mathbf{f}_0 is left between the curves a and b, representing the characteristics of the high and low pass filters of a crossover circuit. Curve c represents the transfer function of the auxiliary or filler driver, which fills in the gap.

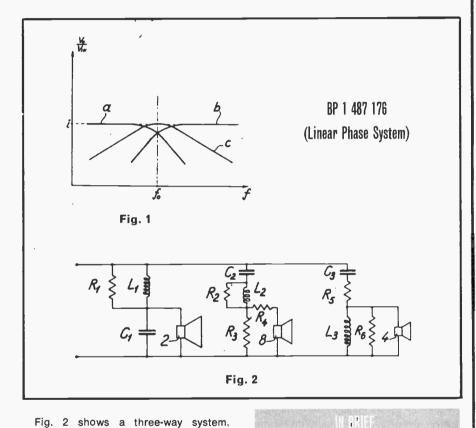


Fig. 2 shows a three-way system, using three transducers or drivers of 4 ohms each and a crossover frequency of 2kHz. Woofer 2, tweeter 4, and filler driver 8 are associated with simple pass filters based on components for which representative values are given in the patent. It is suggested that adoption of the invention enables relatively inexpensive transducer units to be used, with minimal loss of quality, because the acoustic correction provided by the auxiliary driver throws far less demand on the design and performance of the woofer and tweeter. For a three-way system (woofer, tweeter and midrange unit) filler drivers are used between each acoustically adjacent

BP 1 487 360—Ito-Patent AG. Method of automatically orientating and controlling a vehicle. A self-positioning vehicle, for instance a highly sophisticated invalid carriage, which uses electro-optical distance sensing for long distances, and electro-acoustic or electromagnetic sensing for short distances. Computer evaluation of the long and short distance sensing enables the vehicle to position itself in a room and avoid stairs, etc.

BP 1 485 682—General Electric Co. Audio amplifier. Details of an i.e. to provide audio amplification with reduced idling current, achieved by a novel combination of feedback loops.

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By designing and manufacturing in our own Essex factory and selling direct to YOU the customer, we believe we have produced just about the best value ever in mixer/control equipment. You can buy the Disco 2 Unit assembled, tested and ready to connect up and use at once, or build your own unit using Stirring Sound Basic Modules. Either way you stand to save – and look at the advantages you get – sensibly arranged controls (on the built unit), proper DJ/PA facilities and RELIABILITY.

- INPUTS Left deck, right deck, mic. and aux.
- INPUT IMPEDANCE 47K ohms
- POWER SOURCE 220-240V. A.C. Mains
- CONTROLS Mains on-off, master volume, base $\pm 15 \mathrm{db}$, treble $\pm 15 \mathrm{db}$, L and R mixing, L and R motor switches,

selector switch for P.F.L. (Pre-Fade Listening), headphone volume, mic, vol., aux. vol., LED indicators on main and decks on/off switches.

- HEADPHONE AMPLIFIER Powerful 2 watts into 8 ohms;
- TERMINATIONS Five 1" jack sockets 2 input, 2 output, headphones.
- SIZE $23\frac{1}{4}'' \times 3\frac{3}{4}''' \times 2\frac{1}{2}'''$ max. depth to rear (plus separate power unit). Panel in matt black with controls sensibly grouped for easy handling.

Suggested Stirling Sound power amps with heat sinks and power supply units - 140PH £18. 160PH £22. 1100PH £26.75.

Kit of basic modules less power pack, pots, 5 jack sockets, and 3 mains switches, but with front

READY BUILT OR D.I.Y. MODULES

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SOUND-LIGHT UNITS SSTL 3/250B - 3 channels 250w. each £22 95 SSTL 3/1000B - 3 channels, 1000w. each

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In strongly made metal cases, complete POWER AMP 40 – 40w. r.m.s./4 ohms, 2 ch. mixer £43.00

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Disco 25 - 25w. r.m.s. in cabinet, 20' lead £23	95
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Disco. 100 100w rms in cabinet 20' lead €55	95

Complete Disco with Disco 2 console and Ampower 50. ditto with Disco 2 console & two Ampower 50s ...
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ditto with Disco 2 console & two Ampower 100s ... £210.00 £175.00 £250.00

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For constructors wishing to build systems to their own requirements. As their description implies, these modules will require control knobs, pots in some cases, etc. Each module is supplied assembled on its own PCB

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£2.75 £3.75 Four channel mixer stage

SS.DTM

SST13/250

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SS.103-3	Stereo version of above, 2 I.C.s	£5.00
SS.105	5 watts R.M.S. into 3Ω using 13-5V.	
	Sensitivity - 30mV. THD - 0.3%.	
	3½" x 2" x 1"	£3.95
SS.110	10 watts R.M.S. into 4Ω using 24V.	20.00
33.110		
	Sensitivity + 60mV, THD - 0.3%.	
	3¼" x 2" x 1"	£4.65
SS.120	20 watts R.M.S. into 4Ω using 34V.	
	Sensitivity - 80mV, THD - 0.3%.	
	3\frac{1}{2}" x 2"x 1"	£5.15
SS.125	25 watts R.M.S. into 8Ω using 50V.	
	Sensitivity - 140mV. Distortion -	
	Less than 0.05% into 8Ω S/N	
	better than 70dB.	£7.25
SS.140	40 watts R.M.S. into 4Ω using 45V.	
	Sensitivity - 300mV. Distortion	
	typically 0.1%. 5"x 3\frac{1}{2}" x 1\frac{1}{2}"	£6.50
S.160	64 watts R.M.S. into 4Ω using 50V.	
	Sensitivity - 350mV. Distortion	
		00.50
00 4400	typically 0.1%. 5" x 3\frac{1}{2}" x 1\frac{1}{2}"	£8.50
SS.1100	100 watts R.M.S. into 4Ω usIng	
	70V/2A. Input sensitivity – 500mV.	
	Distortion at half-power, typically	
	O 19/ Eff to 2.18 to 1.18	

POWER SUPPLIES

Every Stirling Sound Power Unit is tested and guaranteed under working conditions before despatch. All units except SS.312 include a stabilised low voltage take-off point (13-15V) for pre-amp, tone control, radio tuner, etc. Outputs quoted are minimal unloaded ratings.

\$\$.312 \$\$.318 \$\$.324 \$\$.334 \$\$.345 \$\$.350 \$\$.360	12V/1A 18V/1A 24V/1A 34V/2A 45V/2A 50V/2A 60V/2A	£6.60 £6.95 £7.65 £8.75 £10.75 £11.75
SS.370	70V/2A	£14.75

SS.310/50 Stabilised power supply unit with variable output from 10V to 50V/2A. Short circuit protected£17.75

SS.300 Power stabilising unit variable from 10 to 50V/8A for adding to unstabilised supply units£5.50

CONTROL/PRE-AMPS.

UNIT ONE

Combined stereo pre-amp & active tone control unit 50mV in for 200mV out 10 16V operation Bass ±15dB; Treble ±15dB; Balance control; Volume control. Ceramic P.U., radio or tape inputs. WITH FREE CONTROL PANEL FASCIA£9.00

UNIT TWO

Controls as UNIT ONE but for magnetic cartridge input. 5mV in for 200mV out. R.I.A.A. corrected. WITH FREE CONTROL PANEL FASCIA.....£12.43

CONTROL PANEL FASCIA For above

Basic active stereo tone control module to provide ±15dB on bass at 30Hz and on treble at 10KHz£3.00

SS.101

Stereo pre-amp suitable for ceramics, tape, radio, etc. £2.75

Stereo pre-amp for mag. pick-ups.....£4.45

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AF117	0 - 25	BF173 BF177	0 - 39	OC25 OC26	0.90	2N1303 2N1304	0 - 37	7460 7470	0 · 20 0 · 35
AF139 AF186	0 - 40 1 - 50	BF178 BF179	0 - 45	OC28	2.00	2N1305	0 - 45	7472	0.36
AF239	0 - 45	BF179 BF180	0 - 48	OC28 OC29 OC35	2.00	2N1306	0 - 50	7473	0 - 36
AFZ11 AFZ12	2·75 2·75	BF181	0 - 45	0035	1 · 50 1 · 50	2N1307 2N1308	0 - 50	7474 7475	0 - 40
ASY26	0 - 45	DESAGO	0 - 45	OC36 OC41	0 - 50	2N1309	0.60	7476	0 - 42
ASY27 ASZ15	0·50 1·25	BF183 BF184 BF185	0 - 45	OC42 OC43	0 · 50 1 · 50	2N1613 2N1671	0 · 33	7480 ~ 7482	0 - 60
ASZ16	1 - 25	BF185	0-37	OCAA	0 - 50	2N1893	0.33	7483	1 - 00
ASZ17	1 - 25	*BF194	0·12 0·11	OC45 OC71 OC72	0 - 50	2N2147 2N2148	1 · 40 1 · 65	7484 7486	1-00
ASZ20 ASZ21	0 - 75 1 - 50	*BF195 *BF196	0 - 13	OC72	0 · 45 0 · 45	2N2218	0.33	7490	0.52
AU113	1.70	L*RF197	0 · 14 0 · 32	OC73	1-00 0-75	2N2219	0 - 42	7491AN	0.85
AUY10 BA145	1 · 70 0 · 15	BF200 *BF224 *BF244	0 - 20	OC72 OC73 OC74 OC75 OC76 OC77 OC81	0-60	2N2220 2N2221	0·35 0·22	7492 7493	0.60
BA148	0 - 15	*BF244	0 - 35	OC76	0·50 1·20	2N2222	0 · 25	7494	0 - 60
BA154 BA155	0 · 10 0 · 12	BF257 BF258	0 · 37 0 · 42	OC77 OC81	0.75	2N2223 2N2368	2 · 75 0 · 17	7495 7496	0 · 80 0 · 90
BA156	0-13	BF259	0 - 45	CC81Z	1.00	2N2369A	0 - 21	7497	3-67
BAW62	0.05	*BF336 *BF337	0·50 0·53	OC812 OC82 OC83 OC84 OC122 OC123 OC139 OC140 OC141	0.75	2N2484 2N2646	0 - 21	74100 74107	1 · 75 0 · 45
BAX13 BAX16	0.07	*BF338	0 - 55	OC84	0 - 60	2N2904	0.35	74109	0-86
BC107 BC108 BC109	0-12	BFS21 BFS28	2 · 27 1 · 38	OC122	1 · 50 1 · 55	2N2905	0 · 35 0 · 25	74110	0 - 57
BC108 BC109	0.12	*BFS61 *BFS98	0 - 25	OC139	2 - 25	2N2906 2N2907	0-21	74111 74116	0 · 86 1 · 89
*BC113	0 - 15	*BF\$98 BFW10	0 - 25	OC140	1-95	*2N2924	0 - 15	74118	0.95
*BC114 *BC115	0 · 18 0 · 19	BFW10	0-90		2·25 0·75	*2N2925 *2N2926	0·17 0·13	74119 74120	2 · 00 1 · 10
*BC116	0 - 19	BFW11 BFX84	0 - 38	OC171 OC200 OC201	0.75	2N3053	0 - 25	74121	0 - 45
*BC116 *BC117 *BC118 *BC125	0 · 22 0 · 15	BFX85 BFX87	0·41 0·35	OC200	1-00 1-50	2N3054 2N3055	0 - 50 0 - 65	74122 74123	0 - 60 1 - 00
*BC125	0-18	I BFX88	0 - 32	OC202 OC203 OC204	1 - 25	2N3440	0-60	74125	0-80
*BC126 *BC135	0 · 25	BFY50 BFY51	0 · 28 0 · 26	OC203	1-25	2N3441	0 - 60 1 - 20	74126	0 - 80
*BC135 *BC136	0 - 15 0 - 19	BEY52	0 - 25	1 OC205	1 · 25 1 · 75	2N3442 2N3525	0.90	74128 74132	0.60
*BC137 *BC147	0 - 16	BFY64	0 - 30	OC206 OC207 OCP71 ORP12	1 - 75	2N3614	1 - 20	74136	0.58
*BC147 *BC148	0 - 10 0 - 10	BFY90 BSX19	1 · 32 0 · 34	OC207	1 · 25 1 · 25	*2N3702 *2N3703	0 - 15 0 - 15	74141 74142	0 - 85 3 - 00
*BC149 *BC157	0.13	BSX20	0 - 34	ORP12	0.83	*2N3704	0.15	74143	3 - 00
*BC157	0 - 12	BSX21 BT106	0 · 32 1 · 25	*R2008B *R2009	2 · 25 2 · 25	*2N3705 *2N3706	0 · 15	74144 74145	3-00 1-00
*BC158 *BC159	0-11 0-13	BTY79/400R	3 - 19	*R2010B	2 - 25	*2N3707	0 18	74147	2 - 45
*BC159 *BC167	0.13	*BU205 *BU206	2 - 25	T1C44	0 - 36	*2N3708	0 - 14	74148	2.00
*BC170 *BC171	0-16 0-14	*BU208	2 - 50	T1C226D T1L209	1 · 30 0 · 25	*2N3709 *2N3710	0 · 15 0 · 14	74150 74151	1 · 75 0 · 90
*BC172	0 - 13	BY100 BY126	0-45	I *T1P29A	0 - 50	*2N3711 2N3771	0.15	74154	2-00
*BC173 BC177	0 · 15 0 · 19	BY127	0 - 15	*T1P30A T1P31A	0 - 62	2N3772	1-60 1-70	74155 74156	0 - 90
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BC179	0 · 20 0 · 11	Series BZY88	0 - 13	T1P33A T1P34A	1 · 00 1 · 20	*2N3819 *2N3820	0 - 36 0 - 46	74159 74170	2-50 2-60
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*BC308 *BC327 *BC328	0 · 22 0 · 18	MUESMO	0 - 58	*ZTX108 *ZTX109	0·10 0·12	*2N4126 *2N4286			1 · 25 1 · 25
*BC337 *BC338	0 - 19	MJE370	0 - 65	*ZTX300	0 - 12	*2N4288	0·25 0·25		1 · 10
*BC338 BCY30	0 - 18	MJE370 MJE371 MJE520 MJE521	0-81 0-65	*ZTX301 *ZTX302	0 · 13 0 · 17	*2N4289 *2N5457	0 · 25 0 · 35	74196 74197	1 · 20 1 · 00
BCY30 BCY31	1-00	MJE521	0.75	*ZTX303	0 - 17	*2N5458	0 - 35	74198	2 · 25
BCY32	1-00	MJE521 MJE2955 MJE3055	1 · 25 0 · 75	*7TV211	0 · 19 0 · 12	*2N5459 3N125	0 · 35 1 · 75	74199	2 · 25 1 · 75
BCY33 BCY34	0-90	I *MPF102	0 - 30		0 - 12	3N125 3N141	0-85		1.75
BCY39	3.00	*MPF103	0 - 30	*ZTX500	0.13	1			ckat
BCY40 BCY42	1 · 25 0 · 30	*MPE105	0 - 30	*ZTX501 *ZTX502	0 · 14 0 · 16	CIRCUITS		Plugs in so —low profil	e
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HY5

Preamplifier

The HY5 is a mono hybrid amplifier ideally suited for all applications. All common input functions (mag Cartridge, tuner, etc.) are catered for internally, the desired function is achieved either by a multi-way switch or direct connection to the appropiate pins. The internal volume and tone circuits merely require connecting to external potentiometers (not included). The HY5 is compatible with all I.L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is supplied with each pre-amplifier.

FEATURES: complete pre-amplifier in single pack multi-function equalisation, low noise, low distortion, high overload, two simply combined for stereo

distortion, high overload, two simply combined for stereo APPLICATIONS: hift, mixers, disco, guitar and organ, public address SPECIFICATIONS: hift, mixers, disco, guitar and organ, public address SPECIFICATION: Inputs-magnetic pick-up 3mV, ceramic pick-up 30mV, tuner 100mV, microphone 10mV, auxiliary 3-100mV, input impedance 47k final tikHz. Outputs-tape 100mV; main output 500mV R.M.S. Active Tone Controls-treble ± 12dB at 10kHz, bass ± 12dB at 10kHz, bass t-12dB at 10kHz, signal/noise ratio 68dB. Overload—38dB on magnetic pick-up. Supply Voltage— ± 16-50V

Price £5:22 + 65p VAT. P. & P. free

HY5 mounting board B.1. 48p + 6p VAT. P. & P. free



The HY30 is an exciting New kit from I.L.P. It features a virtually indestructible I.C. with short circuit and thermal protection. The kit consists of: I.C., heatsink, P.C. board, 4 resistors, 6 capacitors, mounting kit, together with easy to follow construction and operating instructions. This amplifier is ideally suited to the beginner in audio who wishes to use the most up to date technology available.

FEATURES: complete kit, low distortion, short, open and thermal protection, easy to build APPLICATIONS: updating audio equipment, guitar practice amplifier, test amplifier, audio oscillator SPECIFICATION: Output Power—15W R M S into 8Ω Distortion—0 1% at 15W Input Sensitivity—500mV Frequency Response—10Hz-16kHz −3dB

Price £5-22 + 65p VAT. P. & P. free

HY50 25W into 8Ω The HY50 leads I.L.P. s total integration approach to power amplifier design. The amplifier features an integral heatsink together with the simplicity of no external components. During the past three years the amplifier has been refined to the extent that it must be one of the most reliable and robust High Fidelity modules in the World. FEATURES: low distortion integral heatsink only five connections 7 amp output transistors no external components

external components
APPLICATIONS: medium power ht-fi systems, low power disco, guitar amplifier.
SPECIFICATION: Input Sensitivity—500mV Output Power—25W R.M S. into 80 Load Impedance—
4-160 Distortion—0 04% at 25W at 1kHz Signal Noise Ratio—75dB. Frequency Response—10Hz45kHz - 3dB Supply Voltage—±25V Size—105 × 50 × 25mm

Price £6-82 + 85p VAT. P. & P. free



60W into 8Ω

The HY120 is the baby of I.L.P. s new high power range, designed to meet the most exacting requirements including load line and thermal protection this amplifier sets a new standard in modular design

FEATURES: very low distortion, integral heatsink load line protection, thermal protection five connections, no external components

APPLICATIONS: hi-ft high quality disco, public address monitor amplifier, guitar and organ

SPECIFICATION: Input Sensitivity—500mV Output Power -60W R M.S. into 80 Load Impedance—4-160. Distortion—0 04% at 60W at 1kHz Signal Noise Ratio—90dB Frequency Response—10Hz-45kHz -3dB Supply Voltage—±35V Size—114 × 50 × 85mm

Price £15.84 + £1.27 VAT. P. & P. free



120W into 8Ω

The HY200 (now improved to give an output of 120 watts) has been designed to stand the most rugged conditions such as disco or group while still retaining true hi-fi performance

FEATURES: thermal shutdown very low distortion load line protection integral heatsink no external

components

APPLICATIONS; hi-fi disco. monitor, power slave, industrial, public address

SPECIFICATION: Input Sensitivity—500mV. Output Power—120W R M.S. into 80. Load Impedance—4–160. Distortion—0-05% at 100W at 1kHz. Signal Noise Ratio—96dB. Frequency Response—10Hz-45kHz –3dB. Supply Voltage—±45V. Size—114 × 50 × 85mm

Price £23-32 + £1-87 VAT. P. & P. free



The HY400 is I.L.P is Big Daddy of the range producing 240W into $4\Omega^{\dagger}$ It has been designed for high power disco or public address applications. If the amplifier is to be used at continuous high power levels a cooling fan is recommended. The amplifier includes all the qualities of the rest of the family to lead the market as a true high power hi-fidelity power module.

FEATURES: thermal shutdown very low distortion, load line protection, no external components APPLICATIONS: public address, disco, power slave industrial SPECIFICATION: Output Power—240W R.M.S. into 40 Load Impedance—4-160 Distortion—0.1%

at 240W at 1kHz Signal Noise Ratio—94dB. Frequency Response—10Hz-45kHz – 3dB. Supply Voltage

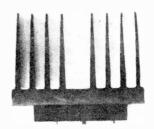
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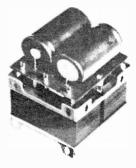
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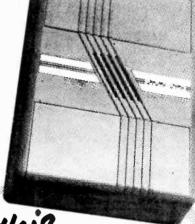
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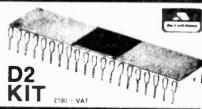
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	BC179B 0-19	BF173 0 · 20	OC72 0-45	2N3704 0:13°	1/53 0-10* 47/35 0-12*	100, 200, 470, 560, 1000, 1500, 2200, 3000, 4700, 10000	709 (TO99) 6-35	7401 20p
	BC182B 0-12°	BF178 0 · 24	OC74 0-45	2N3705 0-14*	1/75 0-10" 50/10 0-10"	47000pf; 1MFD 10V. All at	709 (8 PIN DIL) 0 48	7402 18p
	BC182L 0-11"	BF179 0 25	O C81 0 60	2N3707 0-12°	2-2/25 0-10 50/15 0-10	\$p* each, 1MFD 63V 8p*.	741 (8 PIN DIL) 0-28	7403 18p
	BC183B 0-18"	BF163 0 34	OC82 0.70	2N3708 0-12*	2-2/63 0-10" 100/18 0-06"	·	AY-5-1224 3-75	7404 23p
	BC183L 0-10°	BF184 0 25	ORP12 0-60	2N3709 0·14*	2-5/64 0-10° 100/25 0-10° 4-7/16 0-08° 100/35 0-11°	RESISTORS 2p° each	AY-3-8500 T.V. 6-90	7407 48p 7408 24p
AC153 0-35 E	BC184B 0-12*	BF185 0 28 BF194 0 10*	TIP29A 6-47*	2N3710 0 11° 2N3711 0 11°	4·7/16 0·98* 100/35 0·11* 4·7/63 0·10* 100/50 0·15*	One-third WATT E12 (5%)	AY-3-8550 9.00° AY-3-8600 12.50°	7410 18p
	BC184L 0-11* BC186 0-25	BF194 0·10* BF195 0·10*	TIP31A 0-57	2N3819E 0 25°	5/10 0 10 100/35 0 15	1 ohm-10m ohm.		7411 24p
			TIP32A 0-67	2N3820 0-45°	5/16 0 11 220/16 0 15	PRESET MIN. VERT.	CA 3130 0-87*	7412 25p
	BC187 0-26 BC204A 0-16*	BF196 0-12* BF197 0-12*	TIP33A 0-94	2N3823E 0 25°	6-8/25 0-10* 220/25 0-16*	SUB MIN V & H.	LM 301 AN 0-55 LM 308 1-48	7413 38p
	BC204B 0-16*	BF199 0-15*	TIP34A 1-13	2N4036 0-40	6-8/40 6-10° 220/63 0-25°	100ohm, 220ohm, 470ohm,	LM 309K 2:00	7414 72p
	BC209B 0-13*	BF200 0-38	TIP41A 0-67	2N4058 0·16°	8/70 0 10 250/12 0 12	IK, 2K2, 4K7, 10K, 20K, 50K,	LM 324 2:05	7416 36p
	BC212A 0-13*	BFX29 0-26	TIP42A 0-80	2N4059 0·10°	10/16 0 00 250/50 0 16*	100K, 250K, 470K, 1M, 2M2.	LM 380/SL60745 1 - 29*	7417 36p
	BC212L 0-15°	BFX30 0.25	TIP2955 0-97	2N4061 0-12°	10/25 0.09 250/64 0.20	äp* each.	LM 38IN 2-00*	7420 18p
	BC213B 0-12*	BFX40 0-28	TIP3055 0:60	2N4124 0 · 20°	10/35 0 10 330/16 0 15	THYRISTORS	LM 555 0-49	7421 26 p 7427 32 p
	BC213L 0-14"	BFX84 0 · 22	TIS43 0-45	2N4126 0-30°	10/64 0 10" 470/6V3 0 10" 10/250 0 18" 470/10 0 12"	50V 1A 0.25	LM 723 0-50	7427 32 p 7428 50 p
	BC214 0·15"	BFX88 0.22	ZTX109 0 14"	2N5298 0:50 2N5457 0:50	10/250 0·18° 470/10 0·12° 15/40 0·10° 470/16 0·18°	100V 1A 8:38 TAG 1 100	LM 3900N 0-69	7430 18p
	BC214L 0-17"	BFY50 0.25	ZTX300 0.13°	2N5458 0-48°	15/400 0-35° 470/25 0-20°	200V 1A 0-60 TAG 1200	MC 1310/CA1310E	7432 28p
	BC237A 0-16*	BFY51 0 25	ZTX301,0-13°	2N5459 0-48°	16/10 0-10° 680/25 0-25°	600V 1A 0-80 TAG 1 600	2.55*	7437 42p
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	BC267A 0·17 BC268B 0·17	BY126 0 16	ZTX530 0 23°	40673 0-85	22/16 8-16" 1500/25 8-35"	ZENERS (400mw) BZX 83	MC 1350P 0-75*	7444 1:00p
	BC269 0-17	BY127 0-16	1N914 0-05		25/25 0 11° 2200/6V3 0 · 30°	3V, 3V3, 5V1, 5V6, 7V5, 9V1,	NE 555 0-49 SK 1122 T.V.	7446 1-80p
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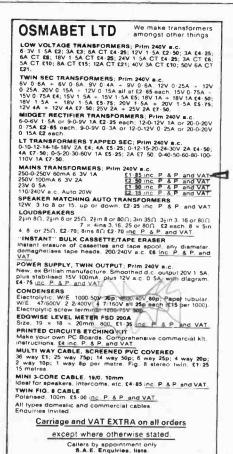
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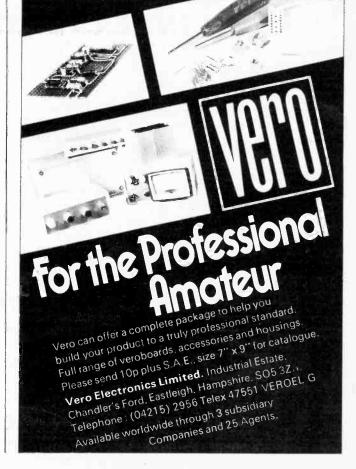
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7409	22p	74128	82p	4009	67p	AY-3-8500 775			450p	LOW PROFI	LEDII		MPSU56 OC28	140p	2N3820 2N3823	70p		_ 4A 100V 90
7410	18p 26p	74132	81p	4011	21p	CA3028A 112 CA3046 85			450p	SOCKETS B			OC35	140p	2N3866	97p	OA202 10	
7412	250	74136	81p 85p	4012	23p 55p	CA3048 250	NE565		140p 200p				OC71	32p	2N3903 4 2N3905 6	22p 22p		6A 100V 108
7413	40p	74142	30 0p	4014	90p	CA3053 75			180p	8 pin 12p	22 pin	36p 40p	R2008B R2010B	225p 225p	2N4058	190		" 6A 400V 120
7414	85p	74145	95p	4015	90p	CA3065 200 CA3080E 97			432p	14 pin 13p 16 pin 14p	24 pin 28 pin	48p	TIP29A	50p	2N4060	190		7 7 10 10 10 10 10 10 10 10 10 10 10 10 10
7417	40p	74147	205p	4016	54p 120p	CA3089E 250	SG3402N		275p	18 pin 30p	40 pin	60p	TIP29C	62p	2N4123 4	22p		P 1
7420	18p	74148	160p	4017	110p	CA3096AQ 425			54p 275p			_	TIP30A	60p	2N4125 6 2N4401 3	22p 34p		P
7421	43p	74151	61p	4019	57p	ICL8038CC 400 LM339N 175			280n	TRANSISTO	RS		TIP31A	56p	2N4427	970	1N4005/7 8	PITRIACS
7423	28p 36p	74153	81p	4020	140p	LM377N 200	SN76013N		175p		BDY56	225p	TIP31C	68p	2N4871	60p	1N4148 4	Plastic
7425	33p	74154 74155	160p 97p	4021 4022	120p	LM380N 112	SN76013ND		160p	AC125/6 20		24p 25p	TIP32A TIP32C	830	2N5179	75p	1N5401/3 15	P 3A 400V 85
7426	430	74156	97p	4023	23p	LM381N 190 LM389N 180			280p	AC127/8 20 AC176 20		25p	TIP33A	85p 97p	2N5245 2N5296	40p	1N5404 7 20	p 6A 400V 107
7427 7428	40p	74157	97p	4024	90p	LM3911N 150			160p	AC187/8 20	BF173	27p	TIP33C	120p	2N5401	62p		6A 500V 120
7430	18p	74159	250p 90p	4025 4026	23p	MC1310P 190	TAA621A		310p	AD149 60		30p	TIP34A	1240	2N5457 B	40p	TENEDE	10A 500V 160
7432	37p	74161	130p	4027	84p	MC1351P 110			150p	AD161 45 AD162 48		35p	TIP34C TIP35A	160p	2N5459 2N5485	40p	ZENERS	15A 400V 200
7433 7437	43p	74162	130p	4028	110p	MC1495L 490 MC1496L 112	1 -0.0.00		300p	AF114/5 30	BF184 5	240	TIP35C	290p	2N6107	700	2 7V-33V	15A 500V 225 40430 130
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7444	120p	74170	750p	4047	120p	VOLTAGE REGULATO	BS-Fixed			BC108/B 10		40p	TIP42C TIP2955	96p 75p	3N140	97p	1A 50V TO5	45¢
7445	108p	74173	190p	4049	64p	Plastic TO220—3 Terminals	1 Amp	VA		BC109 10 BC109C 11		34p	TIP3055	600	3N141 3N187	90p 200p	1A 400V TO5	850
7446	108p	74174	130p	4050 4054	58p	1 Amp - ve	5V	7905	160p	BC147 9	9F258	390	71S43	40p	40360	43p	3A 400V STUE	
7448	75p 85o	74176	97p 130p	4055	140p	5V 7805 115p	12V	7912	160p	BC148 8 BC157 11		48p 32p	2N697 2N698	25p 43p	40361 2	430	16A 400V Plast	
7450	18p	74177	100p	4056	145p	6V 7806 115p 8V 7808 115p	15V 24V	7915 7924	160p	BC158 9 13		340	2N706 8	220	40409 10	75p	16A 600V Plast	C 270p
7451 7453	18p	74180	160p	4060 4068	130p 30p	12V 7812 115p	/• V	1927		BC169C 15	BFR40.1	34p	2N918	43p	40594	90p	BT106 1A 700V C106D 4A 400V	
7454	18p	74181	324p	4069	30p	15V 7815 115p	Heat Sini	17 W	25p	BC172 11 BC177 20		34p 34p	2N930 2N1131 2	19p 25p	40595	97p	MCR101 +A 15	
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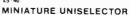


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Published approximately on the 15th of each month by IPC Magazines Ltd.. Westover House, West Quay Road, Poole, Dorset BH15 1JG. Printed in England by Chapel River Press, Andover, Hants. Sole Agents for Australia and New Zealand—Gordon & Gotch (Asia) Ltd.: South Africa—Central News Agency Ltd.

Subscriptions INLAND and OVERSEAS £10.60 payable to IPC Services, Oakfield House, Perrymount Road, Haywards Heath, Sussex.

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