PRACTICAL

# H-ETRONICE <br> ロCTロBER 1977 <br> 40p 



Alsa in this issuc...

## 

U.K. RETURN OF POST MAIL ORDER SERVICE also WORLDWIDE EXPORT SERVICE


This kit is suitable for record players, tape play back, guitars, electronic instruments or small P.A. systems. Two versions are
availeble. The mono kit uses 13 semiconductors. The stereo kit available. The mono kit uses 13 semiconductors. The stereo kit
uses 22 semiconductors. Both kits have printed front panei and volume. bass and treble controls. Spec. 10W output into 8 ohrns, 7 W into 15 ohms. Response $20 \mathrm{c} / \mathrm{s}$ to $30 \mathrm{kc} / \mathrm{s}$, input $100 \mathrm{M} . \mathrm{V}$. high imp. Size $9 \frac{1}{2} \times 3 \times 2 \mathrm{in}$. AC mains operated
Mono $£ 11 \cdot 25$

 ELAC 10 inch $£ 4 \cdot 50$ Ribbed cone. Large ceramic magne
$50-16.000 \mathrm{c} / \mathrm{s}$. Bass resonance $55 \mathrm{c} / \mathrm{s}$. $50-16.000 \mathrm{c} / \mathrm{s}$. Bass reson
10W. 15 ohm impedance.
ELAC $9 \times 5$ in HI-FI 23.45 SPEAKER TYPE 59RM Post 35p This famous unit now available 10w, 8 ohm.

ELAC HI-FI SPEAKER 8 in TWIN CONE Dual cone plastic roll surround. Large ceramic magnet. $50-16.000 \mathrm{c} / \mathrm{s}$. Bass resonance $40 \mathrm{c} / \mathrm{s}$. 8 ohm impedance. $\mathbf{1 5}$ watts. RMS. $\mathbf{5 5}$ Posi 35p


GOODMAN'S COMPACT 12in BASS WOOFER
Standard 12 in diameter fixing with cut sides $\mathrm{r} . \mathrm{m} . \mathrm{s}$. 4 ohm impedance. Bass resonance: 30 c.p.s. Frequency response: $30-8.000$ c.p.s.
$£ 10.95$ each. Post $£ \dagger-00$.

PERIOD LOUOSPEAKER CABINETS Two styles available, Regency and Queen Anne. Size approximately $34 \times 19 \times 16 \mathrm{~m}$. These cabinens are slighty solld from 10 each. Callers only

BARGAIN 3+3W STEREO AMPLTFIER. 10 Transisto Push-Pull Ready bultt with volume. treble and bass $\mathbf{E 1 0 . 9 5}$
controls. 240 V operated.

E.M.I. $13 \frac{1}{2} \times 8 \mathrm{in}$ SPEAKER SALE!
State 3 or 8 ohm.


As illustrated. $\mathbf{~ 7 ~} \cdot 95$ Posi 45p
15W model $£ 10 \cdot 50$
20W model $\quad$ £11.50
TEAK VENEER HI-FI SPEAKER CABINETS
MODEL "A". $20 \times 13 \times 12 \mathrm{in}$. For 12 in . dia. or
10 in . speaker. Illustrated. $\mathrm{C14.50} \mathrm{Post}$
MODEL "B'. BOOKSHELF
For $93 \times$ Bin. or
Bin. speaker.
E8. $50 \begin{gathered}\text { Post } \\ \text { c1 }\end{gathered}$
R.C.S. BOOKSHELF SPEAKERS

Size $14 \times 9 \times 6 i \mathrm{in}$. approx. Response 50 to 14,000 DS 6 watt rms 8 ohms. £16 pair $\begin{gathered}\text { Post } 30\end{gathered}$


ACOUSTIC WADDING 18 in . wide, 20p ft.

## KUBA-KOPENHAGEN STEREO <br> mex

TUNER-AMPLIFIER CHASSIS AM-FM $5+5$ WATY
This Continental 4band radiogram chassis uses first class quality components throughout. Features: Large facia panel with 7 push
buttons for medium, long, short, VHF-FM AFC buttons for medium, long, short. VHF-FM, AFC, phono. Mains
on-off. 4 rotary controls. tuning, volume, tone, balance. Facia size $17 \times 44$ in. Chassis size $17 \times 4 \frac{4}{2} \times 5 \frac{1}{2}$ ine. DiN-connector socket for tape record/playback. loudspeakers, phono pick-up. external FM-AM aerials. Automatic stereo beacon light. Built-in ferrite rod aerial for medium/longwave.
£33.50 Post $£ 1.50$

$\Sigma 15$ Post $£ 1$-00
$30-14,500 \mathrm{c} / \mathrm{s}$. 12 in double cone, wooter and tweeter cone together with a BAKER ceramic magnet assembly having a flux density of 14.000 gauss and a total flux
of 145,000 Maxwells. Bass resonance $40 \mathrm{c} / \mathrm{s}$. Rated 25 W .
NOTE: 4 or 8 or 16 ohms available.

Module kit, $30-17,000 \mathrm{c} / \mathrm{s}$ with tweeter crossover, baffle, $19 \times 12$ in.
instructions. As illustrated.
Please state 4 or 8 or


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ALL TRANSISTOR AMPLIFIER CHASSIS
inputs. 4 outputs separate volume treble and bass controls. Ideal disco or slave amplifier chassis.
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$18.000 \mathrm{c} / \mathrm{s}$. 25W 8 ohm.
E.M.
Sin. mid
E.M.I. Sin. mid range 25 W £4.95.
E.M.I. $13 \times 8 \mathrm{in}$. 25 W Bass Unit $£ 10.50$
R.C.S. 100 WATT VALVE AMPLIFIER CHASSIS


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board and assembly instructions.
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Useful response
8 or 16 ohms mo
16.500 gauss
$20-17.000 \mathrm{c} / \mathrm{s}$

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Flux Density
Useful response
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$12 \times 12 \mathrm{in} .5 \mathrm{p}, 16 \times 10 \mathrm{in}, 750$. ALUMINIUM ANGLE BRACKET
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Our November issue will be on sale Friday, 7 October, 1977
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From NATIONAL SEMICONDUCTOR
functions. As Timeband but without alte
$5+3$ Time/Date facility. DAC5 series (left)
DACS WS Chrome on Strap DAC5 WB Chrome Dracele
DAC5 YB G. P. Bracele
OAB5 Series (Right)
DAB5 WB All stainles DABS YB Heavy G.p.

## $\mathbf{2 2 2} .90$ $\mathbf{2 5} .90$ $\mathbf{2 4 . 5 0}$ $\mathbf{2 2 5} \cdot 90$ $\mathbf{2 4} .90$ $\mathbf{2 8} .50$ 33.50 39.50



W/T 6F


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K004 Mylar capacitors，min 100 V type． 10 each all values from 1000 pF to $10,000 \mathrm{pF}$ ．Total 130 for $\mathbf{~} 4-45$ K005 Polystyrene capacitors， 10
each value from 10 pF to $10,000 \mathrm{pF}$ ， each value from 10pF to $10,000 \mathrm{pF}$ ，
E 12 series $5 \% 160 \mathrm{~V}$ ．Total 370 for E12． 30 K006 Tantalum bead capacitors．
10 each of the following： $0.1,0.15$ ， $0.22,0.33,0.47,0.68,1,2 \cdot 2,3 \cdot 3$ ， $\begin{array}{llll}0 \cdot 22, & 0.33, & 0.47, & 0.68,1,2 \cdot 2,3 \cdot 3, \\ 4.7,6 \cdot 8, & \text { all } 35 \mathrm{~V}, & 10 / 25 \quad 15 / 16 & 22 / 16\end{array}$ 33／10 47／6 100／3．Total 170 tants for ع14－20
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E .72


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Sparkrite Mk. 2 is a high performance, high quality capacitive discharge, electronic ignition system in kit form. Tried, tested, proven, reliable and complete. It can be assembled in two or three hours and fitted in $15 / 30$ mins.
Because of the superb design of the Sparkrite circuit it completely eliminates problems of the contact breaker. There is no misfire due to contact breaker bounce which is eliminated electronically by a pulse suppression circuit which prevents the unit firing if the points bounce open at high R.P.M. Contact breaker burn is eliminated by reducing the current to about $1 / 50$ th of the norm. It will perform equally well with new, old, of even badly pitted points and is not dependent upon the dwell time of the contact breakers for recharging the system. Spark rite incorporates a short circuit protected inverter which eliminates the problems of SCR lock on and, therefore, eliminates the possibility of blowing the transistors or the SCR. (Most capacitive discharge ignitions are not completely foolproof in this respect). All kits fit vehicles with coil/distributor ignition up to 8 cylinders.
THE KIT COMPRISES EVERYTHING NEEDED
Ready drilled pressed steel case coated in matt black epoxy resin, ready drilled base and heat-sink, top quality 5 year guaranteed transformer and components, cables, coil connectors, printed circuit board, nuts bolts, silicon grease, full instructions to make the kit negative or positive earth, and 10 page installation instructions.

## OPTIONAL EXTRAS

Electronic/conventional ignition switch
Gives instant changeover from "Sparkrite" ignition to conventional ignition for performance comparisons, static timing etc., and will also switch the ignition off completely as a security device, includes switch connectors, mounting bracket and instructions. Cables excluded. Also available RPM limiting control for dashboard mounting (fitted in case on ready built unit).

CALLERS WELCOME. For Crypton tuning and fitting service 'phone (0922) 33008
PRICES INCLUDE VAT, POST AND PACKING. Improve performance \& economy NOW
NOTE-Vehicles with current impulse tachometers (Smiths code on dial RV will require a tachometer pulse slave unit. Price $£ 3 \cdot 35$ inc. VAT, post \& packing

ELECTRONICS DESIGN Associates, B2 Bath St, Walsall, WSI 3DE
Quick installation Ho engine modification required
Electronics Design Associates, Dept. PE10 82 Bath Street, Walsall, WS 1 3DE. Phone: (0922) 33652

## Name

Address


## KITS FOR SYNTHESISERS, SOUND EFFECTS



COMPONENTS SETS include all necessary resistors, capacitors, semiconductors, potentiometers and transformers. Hardware such as cases, sockets. knobs, etc. are not included but most of these may be bought separately. Fuller details of kits. PCBs and parts are shown in our lists.
circuit and layout diagrams are supplied free with all PCBs designed by Phonosonics. PHOTOCOPIES of the P.E. texts for most of the kits are available-prices in our lists.

## PHONOSONICS

MAIL ORDER SUPPLIERS OF QUALITY PRINTED CIRCUIT BOARDS, KITS AND COMPONENTS TO A WORLD-WIDE MARKET.

## P.E. MINISONIC MK, 2 SYNTHESISER

A portable mains-operated Miniature Sound Synthesiser with keyboard circuits. Although having slightly fewe facilities than the large P.E. Syntheslser the function offered by this design give it great scope and versatility. Consists of 2 log VCOs, VCF, 2 envelope shapers, 2 voltage controlled amps. keyboard hold and control circuits. HF oscillator and detector, ring modulator, noise generator output amp and mixer, power supply.

Set of basic component kits
from $\begin{array}{r}\text { 864. } 25 \\ 59.71\end{array}$
P.E. SYNTHESISER (P.E. Feb. 73 to Feb. 74)

The well acclaimed and highly versatile large-scale mains-operated Sound Synthesiser complete with keyboard circuits. Other circuits in our lists may be used with the Synthesiser to good advantage, notably. P.E. Minisonic Phasing Unit, Wind and Rain, Rhythm Generator, Sound Bender, Voltage Controlled Filter. Guitar Effects Pedal and Overdrive, Fuzz, Tremolo and Wah-Wah units.
The Maln Synthesiser: PSU. 2 linear VCOs. 2 ramp generators, 2 input amps, sample hold, noise generator shaper voltage controlled amp. Full details in lists
Set of besic component kits. Full details in lisis. Set of printed circuit boards
883.03
$\mathbf{1 1} 1.45$

The Synthesiser Keyboard Circults (can be used without the Main Synthesiser to make an independent musical instrument): 2 logarithmic VCOs, divider, 2 hold circuits, modulation amps, mixer, 2 envelope shapers and additiona PSU. Full details in our lists Set of basic component kits
$£ 48.18$
$\varepsilon 7.66$
GUITAR EFFECTS PEDAL (P.E. July 75)
Modulates the attack, decay and filter characteristics of an avitar but from any of an source, producing 8 different switchable effects that can be further modified by manual controls. Possibly the mosi interesting of all the low-priced sound effects units in our range. Circuit does not duplicate effects from the Guitar Overdrive Unit
Component set with special foot operated switches $\mathbf{~} 7$-59 Alternative component set with panel mounting switches
Printed circuit board
54.96
51.43

SOUND BENDER (P.E. May 74)
A multi-purpose sound controller, the functions of which include envelope shaper, tremolo. voice-operated fader uency-doubler
Component set for above functions (excl. SWs)
Printed circuit board
Optional extra-additional Audio Modulator, the use of which, in conjunction with the above component set, can produce "jungle-drum" rhythms
Component set (incl. PCB)

PHASING UNIT (P.E. Sept. 73)
A simple but effective manually controlled unit for introducing the "phasing" sound into live or recorded music.

Component set (incl. PCB)
C2. 8
PHASING CONTROL UNIT (P.E. Oct. 74)
For use with the above Phasing Unit to automatically control the rate of phasing

Component set (incl. PCB)
SOPHISTICATED PHMSING AND VIBRATO UNIT
A slightly modified version of the circuit published in
A slektor'. December 1976, and includes manual and automatic control over the rate of phasing and vibrato. Component set Printed circuit board
\& 17.69
$£ 2.33$
WAH-WAH UNIT (P.E. Apr. 76)
The Wah-Wah effect produced by this unit can be controlled manually or by the integral automatic controller.
Component set (incl. PCB)
\&. 3.55
AUTOWAH UNIT (P.E. Mar. 77)
Automatically produces Wah-pedal and Swell-pedal sounds
Component set PCB specia
Comperial foot switches
1-27

## POST AND HANDLING

U.K. orders-under $£ 15$ add 25p plus VAT, over $£ 15$ add 50 p plus VAT. Keyboards $\{1 \cdot 50$ plus VAT
Optional Insurance for compensation against loss or damage in post, add 35p in addition to above post and handling.
Elre, C.I., B.F.P.O., and other countries are subject to Export postage rates.
P.E. JOANNA (P.E. May/Sept. 75)

A five-octave electronic piano that has switchable alternative voicing of Honky-Tonk piano, ordinary piano harpsichord or a mixture of any of the three, together with facilities ncluding fast and slow tremolo, loud and soft pedal switching, and sustain pedal switching. The powar amplifier ypically delivers 24 watts into 8 ohms. The PCBs have been edesigned by ourselves making improved use of the space available.
Main power supply, tone generator, 61 envelope shapers, oicing and pre-amp circuits.
Set of basic component kits for above
Set of printed circuit boards for above
ower amplifier
$£ 75.29$
$\mathbf{8} 20.35$
Printed circult board for power amp

## RHYTHM GENERATOR (P.E. Mar./Apr. 774)

Programmable for 64.000 rhythm patterns from 8 effects circuits (high and low, bongos, bass and snare drums, long and short brushes, blocks and soft cymball, and with ariable time signatures and rhythm rates. Really fascinating and useful.
rempo, timing and logic circuits
PCB for above circuits (double-sided)
Component set for all 8 effects circuits
PCB for all 8 effects
Simple mixer (our design) incl. PCB
(incl. PGB) with external volume controls
Power supply for $T, T$ and $L$, and effects
(incl. PCB)
(See our list for Power Supplies for Mixers)
$\mathbf{E 1 2 . 7 0}$
$\mathbf{E} 3.33$

HYYTHM GENERATOR-NEW CONTROL UNIT
Using an M252 Rhythm Generator integrated circuit this using an M252 Rhythm Generator integrated circuit this and Logic control. It provides 15 different and readily selectable rhythm patterns such as Waltz. Tango, March. Foxtrot, etc.
Component set (incl. PCB but excl. sw's) $\quad$ 12.50
Power supply (incl. PCB)
12.90
812.00

SEE OTHER PAGE FOR KEYBOARDS, AND
OUR LISTS FOR OTHER COMPONENTS AND ACCESSORIES STOCKED

## WIND AND RAIN UNIT

A manually controlled unit for producing the above-named ounds
Component set (incl. PCB)

## GUITAR OVERDRIVE UNIT (P.E. Aug. 76)

Sophisticated, versatile Fuzz unit. including variable and witchable controls affecting se fuzz qualty whinst relaing and decay, and also providing filtering. Does not duplicate the effects from the Guitar Effects Pedal and can be used with it and with other electronic instruments.
Component set using dual slider pot
Component set using dual rotary pot
Printed circuit board

## FUZZ UNIT

Simple Fuzz unit based upon P.E. 'Sound Design' circuit.
Component set (incl. PCB)
TREMOLO UNIT
Based upon P.E. ''Sound Design'" circuit.
Component set (incl. PCB)
£3-84

## TREBLE BOOST UNIT (P.E. Apr. 76)

Gives a much shriler quality to audio signals fed through it.
The depth of boost is manually adjustable.
Component set (incl. PCB
£2.40

## P.E. TUNING FORK (P.E. Nov. 75)

Produces 84 switch-selected frequency-accurate tones. A
LED monitor clearly displaya all beat note adjustments. Ideal for tuning acoustic and electronic musical instruments alike.

Main component set (incl. PCB)
£15. 59

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## P.E. SYnCHRONOME (P.E. Mar. 76)

An accented-beat electronic metronome, providing duple triple and quadruple times with full control over the bea rate. Can also be used as a simple drum-beat rhythm Component set (incl loudspeaker)
Printed circuit board $\quad \$ 11.62$

VOLTAGE CONTROLLED FILTER (P.E. Oct. 74)
An independently designed VCF that can be used with the P.E. Synthesiser.

Printed circuit board

ENVELOPE SHAPER WITHOUT VCA (P.E. Ocl. 75)
Provides tull manual control over attack, decay, sustain and release functions, and is for use with an existing voltage confolied amplit
Component set (incl. PCB)

ENVELOPE SHAPER WITH VCA (P.E. Apr. 76)
This unit has its own voltage controlled amplifier and has full manual control over attack. decay, sustain and releas
Component set (incl. PCB)
E5. 68
TRANSIENT GENERATOR (P.E. Apr. 77)
An envelope shaper, without VCA, having the usual attack, decay, sustain and release functions, and in addition it also provides a Repeat Elect enabling a synthesiser to be programmed to imitate such instruments as a mandolin or
Comp
Printed circuit
84.52
51.82

## WAVEFORM CONVERTER

Slightly modified from a circuit published in a German adition of "Elektor'. Converts a saw-tooth waveform into four different waveforms: sine-wave, mark-space saw-tooth regular triangle form, and squarewave with an externally Component set (incl. PCB but excl. sw's)

VOLTAGE CONTROLLED FILTER (P.E. Dec. 74)
Part of the P.E. Minisonic now released as an independent Cit use with other synthesisers. Component set (incl. PCB) (Order as Kit 65-1)
£8.22

RING MODULATOR (P.E. Jan. 75)
Part of the P.E. Minisonic now released as an independen
kit for use with other synthesisers.
Component set (incl. PCB) (Order as Kit 59-1)
£5.50

NOISE GENERATOR (P.E. Jan. 75)
Part of the P.E. Minisonic now released as an independent it for use with other synthesisers.
Component set (incl. PCB) (Ord

SOPHISTICATED POWER SUPPLIES
A wide range of highly stabilised low noise power supply kits is available-details in our lists.

MICROPHONE PRE-AMP (P.E. Apr. 77)
Component set (incl PCB)
E3. 78
VOICE OPERATED FADER (P.E. Dec. 73) For automatically reducing music volume during Component set (incl. PCB)

ع3.97

DYNAMIC RANGE LIMITER (P.E. Apr. 77)
Automatically controls sound output to within a preset
Component set (incl. PCB)
ع4.58

## EXPORT ORDERS are welcome, though we advise that

 a current copy of our list should be obtained betore ordering as it also shows Export postage rates. All payments must be cash-with-order, in Sterling and preferably by International Money Order or through an English Bank. To obtain list send 40 p.
## AND OTHER PROJECTS

PHOTOGRAPHS in this advertisement show two of our units containing some of the P.E. projects built from our kits and PCBs. The cases were built by ourselves and are not for sale, though a small selection of other cases is available.

LIST-Send stamped addressed envelope with all U.K. requests for free other components.

OVERSEAS enquirles for list: Europe
send 20 p : other countries-send 40 p .


## KEYBOARDS AND CONTACTS

Kimber-Allen Keyboards as required for many published circuits, including the P.E. Joanna, P.E. Minlsonic, and P.E. Synthesiser. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are $C$ to $C$. The keys are plastic. spring-loaded and mounted on a robust aluminium frame.
3 Octave ( 37 notes) £25.50. 4 Oct ( 49 notes) £32-25. 5 Oct ( 61 notes) 839.75.
Contact Assemblles for use with above keyboards: Single-pole change-over (type SP) as for P.E. Joanna and P.E. Minisonic. Two-pole normally-open make-break (type DP) as for P.E. Synthesiser. Special contact assembly (type 4PS) having 4 poles. 3 of which are normally-open
make-break contacts and the fourth is a change-over contact-this special assembly enables THE make-break contacts and the fourth is a change-over contaci-this special assembly enables $14 E$
SAME KEYBOARD to be used with the P.E. Synthesiser, P.E. Minisonic and the P.E. Joanna simultaneously thus avoiding the cost of more than one keyboard. See our list for other contacts.

| Contact | Each | 3 Octave Set | 4 Octave Set | 5 Octave Set |
| :---: | :---: | :---: | :---: | :---: |
| SP | $24 p$ | $£ 8.88$ | $£ 11.76$ | $£ 14 \cdot 64$ |
| 2P | $27 p$ | $£ 9.99$ | $£ 13.23$ | $£ 11.47$ |
| 4PS | $53 p$ | $£ 19.61$ | $£ 25.97$ | $£ 32.33$ |

PRINTED CIRCUIT BOARDS for use with the above contacts and thus eliminating most of the inter-wiring required, are available Details in our lists.

## MORE NEW KITS!

## ELECTRONIC ORGAN

Soctave electronic organ with 5 basic voices that can be used individually or together. 5 pitches ( $2 \mathrm{ft}, 4 \mathrm{ft}$. $8 \mathrm{ft}, 16 \mathrm{ft}$, 32tt), varlable attack, tremolo, vibrato, phasing. and
variable sustain. Details in our list.

## ORGAN CONVERSION KIT

Converts the P.E. Joanna electronic piano to also provide most of the facilities offered by the above electronic organ.
Basic
£12.34
SYNTHESISER TUNING INDICATOR (P.E. July 77) A simple 4 octave frequency comparator for use with the full

Component and PCB (but excl sw.)
GUITAR FREQUENCY DOUBLER (P.E. Aug. 77)
A modified and extended version of the circuit published Details in list.

TAPE NOISE LIMITER
Very effective circuit for reducing the hiss found in most tape recordings. All kits include PCBs
standard tolerance set of components
Superior tolerance set of components £2.96
ع3. 76
Regulated power supply (will drive 2 sets) $\mathbf{2 3 . 7 6}$

SOUND-TO-LIGHT (P.E. Aurora) (P.E. Apr.-Aug. 71)
Four channels each responding to a different sound frequency and controlling its own light. Can be used with most audio systems and lamp intensities.
Basic component set (excl. thyristors)
Printed circuit board for above
Power supply
515.92
53.90

PCB for power supply
3-CHANNEL SOUND-TO-LIGHT (P.E. Apr, 76)
A simple but effective sound-to-light controller capable of operating 3 lamps each of approximately 700 watts. Includes power supply, thyristors, and by-pass switches.
Component set (inct. PCB)
£11. 95
DISCOSTROBE (P.E. Nov. 76)
4-channel light-show controller giving a choice of om, or full strobe mode of operation. Basic component sel

BIOLOGICAL AMPLIFIER (P.E. Jan /Feb. 73)
Multi-function circuits that, with the use of other external cquipment. can serve as lie-detector. alphaphone, cardiophone etc. Pre-Amp Module Component set (incl. PCB) Basic Output Circulte-combined component set
with PCBs. for alphaphone, cardionhone, with PCBs. for alphaphone, cardiophone,
frequency meter and visual feed-back lampdriver frequency meter and visual feed-back lampdriver
circuits
Audlo Amplifier Module Type PC7
SEMI CONDUCTOR TESTER (P.E. Oct. 73)
Essential test equipment for the enterprisiing home onstructor. While stocks last.
Set of resistors, capacitors, semiconductors. polemior (500, makaswitches and PCB
Panel meter ( $500 \mu \mathrm{~A}$ )
prices are correct at time of press.
E. \& O. E. DELIVERY SUENECT TO AVAILABILITY

| TRANSISTORS |  |
| :---: | :---: |
| AC128 | 26p |
| AC176 | 26p |
| BC107 | 14p |
| BC108 | 14p |
| BC109 | 14p |
| BC 147 | 12p |
| BC148 | 12p |
| BC149 | 12p |
| BC157 | 13p |
| BC158 | 13p |
| BC159 | 13p |
| BC182L | 12p |
| BC184 | 12p |
| BC187 | 25p |
| BC204 | 14p |
| BC209C | 14p |
| BC212L | 15p |
| BC213 | 15p |
| BC478 | 29p |
| BCY71 | 22p |
| BD131 | 44p |
| BD132 | 54p |
| BFY50 | 22p |
| BFY51 | 22p |
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| OC84 | 25p |
| ORP12 | 70p |
| ZTX107 | 12p |
| ZTX108 | ${ }^{9 p}$ |
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| ZTX503 | 15p |
| ZTX531 | 23p |
| 2N706 | 13p |
| 2NS14 | 22p |
| 2N1304 | 22p |
| 2N2219 | 27p |
| 2N2905 | 35 p |
| 2N2905A | 36p |
| 2N2907 | 22p |
| 2N3053 | 18 p |
| 2N3054 | 66p |
| 2N3055 | 48p |
| 2N3702 | 12p |
| 2N3703 | 12p |
| 2N3704 | 12P |
| 2N3819 | 35p |
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| Power handung guide |  |
| :---: | :---: |
| $\underset{\substack{\text { Sy atem } \\ \text { Impoctence }}}{ }$ | Capsecty |
| 20 hms | 312 watts |
| 4 Ohms | 156 wath |
| 0 ohms | 78 watts |
| 16 onms | 39 wals |


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## VOLUME 14 No. 2 OCTOBER 1977

## FULL MEASURE

| T is an exceptional hobby where most of the tools and instruments needed for its practice can be made in the norinal pursuance of that activity. This ideal state of affairs, representing a big move towards total do-it-yourself, is enjoyed by the electronics enthusiast. From time immemorial the designing and building of test gear has been an essential part of this hobby. One type of instrument invariably excluded on grounds of impracricability has been that indispensable workhorse the multimeter. This was the one vital instrument one had to buy.
But eventually the moving-coil instrument lost its monopoly of this area of electrical measurement, with the coming of entirely electronic methods for measuring and displaying those prime quantities: volts, amperes, and ohms. The digital multimeter has now become just another project for the constructor, so extending his area of self-sufficiency even further.
With specially designed i.c.s now available the constructional work is reduced to the minimum and, also of great importance, the performance of the finished instrument can be vouched for.
This month's cover features a Digital Multimeter having useful and valuable features. This multimeter is fully described in our pages and we consider this an excellent example of the kind of highclass instrument the constructor can build for himself today.

Seen against the broader background, this particular project typifies much that is happening in electronics at this time. The constructor's general indebtedness to the i.c. industry is one of the facts of life. What a boon these custom-designed devices have proved to him. And yet this certainly is no one-way traffic. Many original ideas germinating in fertile minds outside the industry find their way back into commercial areas, since they enhance and extend the usefulness of a particular i.c. A very happy situation benefiting, in the long run, all concerned.

## FUN AND GAMES

What is the explanation for the popularity of electronic games? Are they one form of escapism from economic blues? There again it might, rather unkindly, be suggested that we are suffiering from a plethora of these amusements, simple and harmless in their performance though they be. Certainly the attraction of some electronic games wanes quite quickly. The television game is a particular victim for it is reckoned (by some who should know) to have an active life of only about four weeks before the attraction wears off and the equipment is consigned to the cupboard under the stairs.

Clearly, in the long term, monochrome ping-pong for two cannot hold a luminescent spot to the all-family attractions of The XYY Man or The Black and White Minstrel Show. Still, technologically speaking, we can count on enterprising designers to continue producing more involved and appealing(?) games for our amusement. It is one very useful and convenient way for i.c. designers to exercise their talents.
Games have a particularly significant place in the field of minicomputers. The personal minicomputer, far more than the television set, has a definite need for "invented" tasks to keep it in full-time employment. How else to justify its existence after one has run through one's personal accounts and income tax records? There is an awful lot of capacity awaiting programming. So why not play games-highly intellectual ones pre-ferably-?
Our new feature Micro-Bus will include examples of programs for gamesfor the very good reason that ". . . the techniques of programming are much the same whether one is writing a gameplaying program or a factory process control program .. ." Micro-Bus will present other useful information and ideas concerning microprocessors and minicomputers, all having a distinctly practical bearing on the application side of this fast opening-up area of electronics.
F. E. BENNETT,

Editor.

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Queries regarding articles published in PE should be addressed to the Editor, at the Editorial Offices, and a stamped, addressed envelope enclosed. We cannot undertake to answer questions regarding other items, nor to answer technical queries over the telephone.
-


# Multimeter <br> P. BIRNIE 

THE construction of a digital voltmeter (DVM) has in the past involved the use of many components, both linear and digital, and with further circuitry to perform other functions, the digital multimeter (DMM) has required still more discrete and integrated devices. This has been a deterrent to the wouldbe constructor, even with the help of recent digital i.c.s which perform the system control of analogue to digital conversion ( A to D ).

The Siliconix LDI30 has brought nearer, the day of the "one chip DVM", because with only the assistance of a BCD to 7 segment/decode driver i.c. and a few other passive components, a $0-999 \mathrm{mV}$ DVM can be built.

The LDI30, comprising both linear and digital circuitry, could offer an accuracy of $\pm 1$ count when used in this fashion, in addition to automatic polarity with an output for polarity sign indication, and an input impedance of higher than $10^{9} \Omega$, provide an underrange/overrange output which may be used for auto-ranging, and would operate from $\pm 5$ volt unregulated supplies, consuming only 25 mW (the LDI 30 itself).

The DMM featured in this article is such a digital voltmeter, with a few refinements such as an input attenuator network to allow other voltage ranges to be measured, and converters to enable a.c., resistance, and current to be measured also.

## LO130 OPERATION

This device has improved upon the Siliconix dual LD110/ LD111 A to D system employing the "Quantised Feedback" principle, by incorporating the two functions in a single i.c.

Very basically, quantised feedback is a system whereby the voltage to be measured is integrated to ramp up or down (depending on polarity), and then fed to a voltage comparitor before going to the digital controller. The comparitor reference level is designated "analogue ground", and if its output is positive, set charge packets of opposite sense are delivered to the integrator input until the ramp returns to zero. Consequently the comparitor will then go negative. If the comparitor was initially negative, the reverse happens; but in
either case the digital controller counts the number of ct arge packets required by the feedback path to zero the integrator.
The reason for the name "Quantised Feedback" now becomes apparent. There are of course many refinemencs to this system, and advantages too, and these shall be explamed.
Figure 1 shows a block diagram of the LDI30, and this can be split into two areas: the logic zone (right), and the analogue zone (left). The logic zone controls the switching into circuit of the various amplifier elements, and also generates the information for interfacing with the three digit multiplexed display. An independent output (pin 5) is multiplexed in conjunction with the digit drive lines, to provide negative sign, overrange, and underrange indication.
If the control logic is the brain of this device, then by the same analogy the analogue section is the limbs; in this case clutching measuring beakers! It should be remembered that everything happens to the beat of pulses generated by the internal oscillator, and squared through a flip-flop. This pulse train is then used to motivate the control logic and is hereafter referred to as the "clock".
During conversion from A to D, which takes 3072 clock cycles, there are two pericds: Auto-Zero (AZ), and Measure (M), each occupying 1024 and 2048 clock cycles respectively.

The AZ period is best described first, as the purpose of this is to null offsets within the LD130 linear stages before going. ahead with measurement, and as will be seen later, to provide a negative reference voltage to enable inputs of either polarity to be converted

## THE A-Z PERIOD

At the start of the Auto-Zero period the non-inverting input of the Input Buffer is grounded, and a few clock cycles later the non-inverting input of the Auto-Zero Buffer is routed to internal resistor RD. This links the AZ Buffer and integrator together as a closed loop second order system, at which time the up/down (U/D) switch pulses at 50 per cent duty cycle. That is, up to Vref for four cycles and down to analogue ground for four cycles.

It should be noted that the latter, and all other operations work on clock pulse groups of eight, which are called octets. At the outset of the 50 per cent U/D signal, the AZ Buffer, which is monitoring the integrator output, will be theoretically at zero volts; therefore the integrator will only be working on an input switching at 50 per cent duty cycle between ground and Vref (which is 2 volts).

A stepped negative going ramp will be generated at the output of the integrator, which will be repeated at the AZ Buffer output. This, you will see from the diagram, is back at the integrator input! Now that the ramp is under-way; due to the positive reference voltage across $R_{A}$, and the increasing negative voltage now across Rc, the integrator will begin to produce a gradually more triangular waveform until equilibrium is reached, with the integrator output being a true triangle wave, about a mean negative voltage.

At this point the relationship between the values of RA and Rc should be considered. Since the value of these two resistors is the same, the integrator output will only hold steady when the current in each is equal and opposite, since this will give an average centre voltage of zero.
Simple figures will show that this is when the AZ Buffer voltage is exactly $-\frac{1}{2}$ Vref.

Let the integrator current during U/D high, be $I_{1}$, and during U/D low be $I_{2}$.
The AZ Buffer output voltage (to be established) $=\mathrm{V}_{\mathrm{AZ}}$.
The integrator ceases ramping when $\mathrm{I}_{1}+\mathrm{I}_{2}=0$. (1)

$$
\begin{align*}
& \mathrm{I}_{1}=\frac{\mathrm{Vref}}{\mathrm{RA}_{\mathrm{A}}}+\frac{\mathrm{VAZ}^{\mathrm{RC}}}{}  \tag{2}\\
& \mathrm{I}_{2}=\frac{\mathrm{VAZ}}{\mathrm{RC}} \tag{3}
\end{align*}
$$

Using equation (1), ramping ceases when:
or

$$
\begin{gathered}
\frac{\mathrm{Vref}}{\mathrm{RA}}+\frac{\mathrm{VAZ}_{\mathrm{A}}}{\mathrm{RC}}+\frac{\mathrm{VAZ}_{\mathrm{A}}}{\mathrm{Rc}}=0 \\
\frac{\mathrm{Vref}}{\mathrm{RA}}=-2 \frac{\mathrm{VAZ}}{\mathrm{RC}}
\end{gathered}
$$

Since $R_{A}=R c, V_{A Z}=-\frac{1}{2} V r e f . \quad V r e f=2 V$, therefore $V_{A Z}=$ -1V.

This negative voltage is stored on capacitor CAZ, whilst during the last few cycles of the $A Z$ period, the integrator is returned to zero. The a to D converter will now be ready for the measure ( M ) period.

## THE M PERIOD

Now that internal offsets have been taken care of, and a balanced negative reference voltage ( $\mathrm{V}_{A Z}$ ) has been prepared, the Input Buffer will be switched to monitor Vin, and the loop via the $A Z$ Buffer is broken.

The integrator will now begin to ramp (rate and polarity depending on Vin), and the comparitor will switch accordingly. Charge packets will now be fed back to the integrator input, by control of the U/D switch. This is done in response to the comparitor output, and follows an elementary set of rules.

For a "high" comparitor, the U/D voltage will be $u p$ for 1 clock cycle, and down for 7 (Duty cycle A).

For a "low" comparitor, the U/D voltage will be up for 7 clock cycles and down for 1 (Duty cycle B).

The comparitor is sampled only during the clock cycle preceding each octet. An up/down BCD counter increments by one count for each U/D "up" charge packet, and decrements


Fig. 1. Internal block diagram of the LD130 DVM chip


Fig. 2. Circuit diagram of the DMM
for each U/D "down" charge packet, consequently registering a net count of six for each duty cycle. The input polarity is detected by sensing which duty cycle is being employed.

## COUNT CORRECTING OVERRIDE

For the most part, the counting is done in groups of six, but an exception to this rule has to be made as a final stage in measurement. Within the first 32 clock cycles of the following AZ period, a little time is stolen to fine tune the counter to the nearest individual count. During this period, known as the "override interval", individual charge packets are fed back to balance the integrator, thus improving the accuracy of the system to $\pm 1$ count.

## BASIC DVM CIRCUIT

The diagram of Fig. 2 shows the complete circuit of the DMM, which without the Input Range Selector section, leaves what is basically the DVM part of the multimeter.

The components which form the voltage reference for IC1 can be seen connected to pin 2 (R2, VR1, and CR1). The BCD coded output from ICl is decoded into seven segment drive using a CD4511, cmos decoder driver with output source capability of 25 mA continuous.

Because the IC2 outputs are not current limited, seven $68 \Omega$ resistors are used as current limiters.

The seven segment displays have their segment anodes in common, in the conventional multiplexed display manner, and the cathodes are driven by three pairs of transistors connected
in "super-alpha" configuration (TR2--TR7). Using BC182L transistors in this way, allows a very small drive current to control the relatively large peak currents required by the displays. This is important to prevent the LD130 from having to source several milliamps current.
A single point source l.e.d., driven from TR1 which is controlled by the "negative sign" output of the LD130 (pin 5), illuminates when the input applied to the DVM is negative.

The input to ICl goes via a $\mathrm{IM} \Omega$ resistor (R32), which provides protection against over-voltage, but because the input impedance of IC 1 at $\mathrm{VIN}_{\text {IN }}$ is $1,000 \mathrm{M} \Omega$, this series resistor has virtually no effect on readings.

A capacitor of 1 nF connected between the input pin and ground prevents any noise spikes picked up along the p.c.b. from adversely affecting the readout. In addition, several smoothing capacitors are used (C5, C6, C18 and C19), and these are absolutely essential for correct operation, as are D1 and C7.

## INPUT RANGE SELECTOR

The complete input range selection circuitry is shown in Fig. 3. Switches are shown in the non-select (out) position, and the use of press button keys for all ten switches is assumed, although conversion to rotary switches should be fairly straightforward.

The five range selection switches (S6-S10) are mechanically interlocked such that only one can be operated at any given time. The other five switches are partially interlocked.


Fig. 3. Input Range Selector

The on/OFF and $A C / D C$ switches are push-to-select, push-tocancel, and do not interact with any other switches.

The ohms, amps and volts switches are interlocked such that only one function can be selected at any one time.

A word of warning is needed here, about connection of inputs with the wrong range selected, or illegal button combinations. As with an analogue meter, such actions may cause serious damage to the input circuitry, although the design carefully protects the LDI30 itself against applications of up to 1 kV on any range, leaving only a few resistors and an operational amplifier to suffer the consequences.

During normal operation, with S5a pressed, the input voltage "sees" R20 to R27 in series, about $10 \mathrm{M} \Omega$ in all, and dependent on the range selected, a proportion of the input voltage appears at point X . The a.c. to d.c. converter is always operational, and if the AC/DC switch is pushed, then S3a will select the rectified signal rather than the voltage at point X . The pole of S3a is now fed to Vin of the LD130.

On a current input, with S4a operated, the current passes through some or all of resistors R24-R27, depending on the range selected. Point X is now at the voltage generated across the selected shunt resistors, and switch S3a selects either this voltage, or the output of the converter. Again, the pole of S3a now goes to VIN of the LD130.

## OHMS CONVERTER

The measurement of resistance relies on the use of the programmable current generator of Fig. 4. At the input terminal, 'a constant current is produced which is dependent on the value of resistor selected by the range selection switches. This current passes through the positive input terminal of the meter, the unknown resistor, and the negative input terminal to ground. The voltage developed across the resistance by the known current is measured by the DVM section of the instrument.

The range selector switch contacts activate the display decimal point at the correct position for the particular range selected.

## DC TO DC CONVERTER

The system requires a negative supply of about 5 volts, and rather than provide a separate supply for the LD130 and two operational amplifiers, the simple d.c. to d.c. converter of Fig. 5 is used.
Transistors TR8 and TR9 form a free running oscillator with the collector of TR9 having a 1 mH inductor rather than the usual load resistor. The back e.m.f. from the switch-off current in Ll is coupled by C 16 to be shunted by D7. Diode D8 conducts when C16 is pushed negative by induced e.m.f. from L1. This latter conduction is negative with respect to ground, and C17 is charged with this voltage, but regulated to around 5 volts by the Zener diode which is fed back in such a way as to damp down oscillation when it conducts.


Fig. 4. Ohms Converter


Fig. 5. DC-DC Converter


## AC TO DC CONVERTER

This conversion is performed using a straightforward precision rectifier with smoothed output (Fig. 6). The a.c. signal is applied to the input of the CA3130 cmos operational amplifier, and amplified by a factor of 5 . The output, when rectified, is a d.c. voltage of the same value as the r.m.s. voltage of the input waveform. The scale factor is set during calibration by preset VR3.

It should be noted that only sinewave inputs will give a true reading with this circuit.

## LAYOUT PHILOSOPHY

The development of any system involving both digital, and sensitive linear circuitry, brings to light problems related to power supply ripple, and noise pick-up at the input wiring.

The pulse currents caused by the multiplexed display (up to 210 mA ), passing along a p.c.b. track with a resistance of, say, $20 \mathrm{~m} \Omega$, can cause over 4 mV of noise to appear. It would be disastrous if the input earth line shared a current path such as this.

It is for this reason that the LD130 has separate earth lines internally for its analogue and digital sections, and which emerge at different pins. With isolated earths it can therefore be arranged on a p.c.b. for the noisy digital earth to return directly to the supply source, and the input signal earthing to do the same.

The positive supply is provided with $6,600 \mu \mathrm{~F}$ of reservoir capacitance to "soak up" much of the peak current requirements of the displays. As will be seen, the p.c.b. layout has two links which are used to connect Vin and analogue ground to the LD130 to keep crosstalk to a minimum.

## ASSEMBLY OF CIRCUIT BOARDS

The Main p.c.b. (Fig. 8)'is single-sided, and should be of glass-fibre at least one millimetre thick so that it will firmly support the switches without flexing, which might split the tracks. Short mounting spacers may be néeded for this p.c.b.

Assembly should be commenced by making up the two switch frames, taking care that the interlock functions operate as desired (so that two conflicting ranges cannot be operated simultaneously). Next, mount them on the p.c.b. as shown in Fig. 8. No screws are used for this, as the many soldered leads will be sufficient. The wire links are then soldered in position, all except the one marked " $A$ ". The components can then be mounted, leaving the i.c.s until last, after ensuring that the soldering iron is earthed. The usual precautions for cmos devices must be taken.

Taking the Display p.c.b. (Fig. 7); again insert the links first, followed by all other components. The seven segment displays are orientated using their decimal points. The polarity of the sign indicator l.e.d. can be checked with a meter, and this component should be stood-off from the board so as to make it sit close to the display window. This board is now put aside while a check on the Main p.c.b. is carried out.

## CONVERTER CHECK

The correct operation of the DC-DC Converter on the Main p.c.b. can be checked by connecting a 6 volt battery to the supply and operating the on/Off switch. A multimeter connected across C17 should now read approximately 5 volts negative. If this test is satisfactory, switch off the power and connect link " A ". Otherwise check the component positions and soldering.

The two sets of holes provided for tying down the inductor core are for a "U" shape copper wire strap, and not a continuous loop of wire, since this would cause an effective shorted turn by destroying the flux path in the vicinity.

## FINAL ASSEMBLY

An aluminium bracket should be cut out as showṇ in Fig. 9 and fixed to the Main p.c.b. using two M3 screws. Next the 2 mm input sockets are bolted in position, with the positive socket on the left. Two leads connect these sockets to the Main p.c.b., and should be connected up. The Display p.c.b. is now mounted on the bracket using M3 nuts to space them apart.

## COMPONENTS . . .

Resistors

| R1 | $820 \Omega$ | R22 | 90k $\Omega 1 \%$ hi-stab $\frac{1}{2} W^{*}$ |
| :---: | :---: | :---: | :---: |
| R2 | $5.6 \mathrm{k} \Omega \frac{1}{2} \mathrm{~W}$ m.o. | R23 | $9 \mathrm{k} \Omega 1 \%$ hi-stab $\frac{1}{2} \mathrm{~W}^{*}$ |
| R3. R4, | $10 \mathrm{k} \Omega$ (2 off) | R24 | 900S $1 \%$ hi-stab $\frac{1}{2} \mathrm{~W}^{*}$ |
| R5-11 | $68 \Omega$ (7 off) | R25 | $90 \Omega 1 \%$ hi-stab $\frac{1}{2} W^{*}$ |
| R12 | $1 \mathrm{M} \Omega$ | R26 | $9 \Omega 1 \%$ hi-stab $\frac{1}{2} \mathrm{~W}^{*}$ |
| R13 | 470kS | R27 | $1 \Omega 1 \%$ high-stab, 1.5 W * |
| R14, R15 | 10 kS (2 off) | R28 | $3 \cdot 3 \mathrm{k} \Omega$ |
| R16 | $4.3 \mathrm{k} \Omega$ | R29 | $4 \cdot 7 \mathrm{kS}$ |
| R17 | $100 \mathrm{k} \Omega$ | R30 | $47 \mathrm{k} \Omega$ |
| R18 | 3.3 k ת | R31, | R32 1MS (2 off) |
| R19 | 330 , |  |  |
| R20 | $9 \mathrm{M} \Omega 1 \%$ hi-sta | $\frac{1}{2} W^{*}$ |  |
| R21 | 900k $\Omega 1 \%$ hi-st | $b \frac{1}{2} W$ |  |

All $\frac{1}{4}$ W 5\% unless otherwise stated
*See Constructor's Note

## Potentiometers

VR1 2'2k 2 hor cermet preset
VR2 $1 \mathrm{k} \Omega$ hor cermet preset
VR3 $2 \cdot 2 k \Omega$ hor cermet preset

## Capacitors

C1
C2, C16
C3, C4, C10, C14
C5, C6
C7, C18, C19
C8, C9
C11-C13
C15
C17
C20
$0.033 \mu \mathrm{~F}$ polyester
$0.1 \mu \mathrm{~F}$ polyester (2 off)
$0.001 \mu \mathrm{~F}$ polystyrene ( 4 off)
$3300 \mu \mathrm{~F} 6.3 \mathrm{~V}$ electrolytic (2 off)
$47 \mu \mathrm{~F} 6.3 \mathrm{~V}$ tantalum bead ( 3 off)
$47 \mu \mathrm{~F}$ tantalum bead ( 2 off)
$4.7 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum bead (3 off) 470pF polystyrene
$470 \mu \mathrm{~F} 6.3 \mathrm{~V}$ electrolytic
100 pF polystyrene

Inductors
L1 35 turns of 30 s.w.g. enamelled copper wire, on Mullard FX3312 ferrite toroid

## Semiconductors

| IC1 | LD130 Siliconix |
| :--- | :--- |
| IC2 | CD4511 |
| IC3, IC4 | CA3130T (2 off) |
| CR1, CR2 | E501 Siliconix current regulator (2 off) |
| TR1, TR8 | BC212L (2 off) |
| TR2-TR7, TR9 | BC182L (7 off) |
| X1-X3 | DL704 (3 off) |
| D1-D5, D7, D8 | 1N914 (7 off) |
| D6 | 6.2 V Zener 300 mW |
| D9 | TIL209 red |

## Switches

S1, S3-S5, S10 2 pole c/o push button (5 off) RS type
S2, S6-S9
4 pole c/o (5 off) RS type

## Miscellaneous

Mounting frames to take six switches each (2 off)
Square plastic buttons (10 off)
Input sockets 2 mm (1 red and 1 black)
Polystyrene case $188 \times 110 \times 60 \mathrm{~mm}$ ( 1 off)
HP7 batteries ( 40 off) and suitable holder with press sfud connector to fit
Main p.c.b. and display p.c.b.
8 mm M3 panhead screws for main p.c.b. mounting, and 3 mm solderable metal spacers for same (4 off each) Aluminium for display mounting bracket
6 mm M3 panhead screws (2 off)
12 mm M3 countersunk screws (2 off).


Fig. 7. Display p.c.b. This is mounted on the input socket bracket

M3 nuts (8 off)
Red display filter $60 \times 20 \mathrm{~mm}$ (1 off)
Dry letter transfers and lacquer.
Stick-on cabinet feet
Two 2 mm plugs, some flexible wire, and a couple of probe clips will be required for the test leads. Integrated circuit holders.

## CONSTRUCTOR'S NOTE

The Siliconix LD130 and the two E501 current regulators are available from Semiconductor Specialists (UK) Ltd, Premier House, Fairfield Road, Yiewsley, West Drayton, Middlesex. The combined cost of the three items is approximately $£ 11 \cdot 00$.
The interlocking switches and associated accessories are available from Doram Electronics, and also the Mullard FX 3312 toroid, 30 s.w.g. wire, and polystyrene case. The switches must be the RS type, which are available via Doram by special order.
*The resistors R20 to R27 are shown in the components list as their ideal values. Such a range of close tolerance high-stab resistors may be difficult to obtain through amateur component suppliers with no expensive minimum order charge. However, Maplin Electronic Supplies do a sufficiently good range of $\frac{1}{2} \mathrm{~W} 1 \%$ resistars to provide the following values: R21-910k $\Omega, \mathrm{R} 22-91 \mathrm{k} \Omega, \mathrm{R} 23-9 \cdot 1 \mathrm{k} \Omega$, R24-910 $\Omega$, and R25-91 $\Omega$.
Accuracy will not be significantly affected by going to the E24 range resistors in multiples of $9 \cdot 1$, or by the extreme upper and lower value resistors reverting to $2 \%$ tolerance.
Resistor R27 may be the Maplin $1 \Omega 3 \mathrm{~W}$ wirewound resistor at $5 \%$ tolerance, if nothing closer can be found.


Fig. 8. Main p.c.b. Mounting spacers may be necessary, and may be soldered to the copper cladding



Fig. 9. Dimensions of the aluminium input socket bracket

The segment drive resistors can next be soldered in position between the two p.c.b.s, and all other flying leads, including the decimal point flying leads from S 8 and S 9 . The display window and push button slot should now be cut out to the dimensions shown in the photograph.

The battery holder can be glued to the lid of the box, or if a removable type is used, it can be held in place by a plate and two screws.

## TESTING AND CALIBRATION

With a 6 volt supply and the unit switched on, the display should read all zeros when the $A C / D C$ switch is in the $D C$ position (out), and the voltage function is selected. Due to small amounts of crosstalk, the display may occasionally indicate 001 , but this is of little significance, as it represents an error of only 0.1 per cent. Should a greater reading than this appear, of a value up to 004 or thereabouts, switch off the power and carefully clean any flux away from the pins of the LD130 using switch cleaner or cellulose paint thinner. This should reduce stray signals to a minimum thereby correcting the display reading.


The voltage reference for the LD1 30 must now be set, using a known voltage source and adjusting VR1 for the correct display reading. Any source of supply, from a battery to a power supply will suffice, as long as it is known to the accuracy desired from the DMM after calibration. The ideal voltage from which to calibrate is about 0.9 V because this range setting will bypass the range selector resistors, and hence you would be calibrating the LD130 DVM chip directly.

Certain combinations of E501 and LD130 may not allow VR1 to give sufficient voltage range to allow the reference to be set, and in this case R2 should be changed until the correct setting can be achieved.
Calibration of the AC-DC Converter is carried out by feeding a known sinewave voltage to the instrument, and adjusting VR3 for a correct reading. The response of the circuit to changes in VR3 is fairly slow, and a few seconds should be left after each alteration to this preset before noting the display.

The Ohms Converter can be set up by connecting a close tolerance resistor to the instrument, selecting ohms and the appropriate range, and then adjusting VR2 for the correct display reading.

Note that with ohms selected and no resistor at the input of the instrument, the overrange indication (flashing display) will be active.

## OPERATION

Care is required when using this meter to ensure correct function selection lest the instrument be damaged. This really applies to the function switches for oHMs, AMPS and volts, as the range selection switches will, if wrongly set, cause either no display or the flashing overrange indication.

The AC-DC switch position has no effect on the operation of the ohms circuitry, although this switch must be in the correct position for current and voltage inputs.

In use, the instrument should be used on the most sensitive range obtainable without overrange indication.

## ACKNOWLEDGEMENT

The design of this DMM, is in part based on circuits suggested in the Siliconix LSI Design Catalogue.


FRANK W. HYDE

## THE VOYAGERS

The two Voyager vehicles which will make their way to Jupiter and Saturn are of the Mariner class. Included in the programme for the Jupiter and Saturn encounter are detailed surveys of their satellites or moons. Each of the Voyagers could fly past four planets and a dozen satellites during the estimated 12 year period of the mission.
From the control centre at the Jet Propulsion Laboratory it will be possible to direct one of the vehicles to Uranus after the Saturn encounter. It could be that the Voyager will go on to Neptune for an encounter in 1989.
Some of the broad outlines of this mission have already been noted in a recent Spacewatch. Now further details are available as the launch date arrives. When you read this article the first of the vehicles will be on its way for the "launch window", open for 30 days, opened on August 20.
The spacecraft Voyager 2 will be the first to be sent on its way. It will follow a trajectory which will allow the Voyager I to overtake it and make its encounter some four months ahead of its companion. By the time of the Saturn encounter Voyager 1 will be nine months ahead of Voyager 2.
The trajectories are very carefully planned and are subject to special considerations, particularly those concerned with safety, since the spacecraft will pass through an intense area of radiation when in the vicinity of Jupiter. There are also the possible hazards in the ring area of Saturn. The particle size in the rings themselves and the environment near them is not known though many theories thrive.

Cost and complexity are rivals in these matters since there will be low Sun-Earth-spacecraft angles and this will affect telemetry, command performance and data return. The scientific importance that arises from variations of trajectory in order to do special tasks, as for example taking a look, a close look, at the Saturnian satellite Titan. This is known to have an atmosphere and may well be a priority for close observation.

Also, now that the possibility of a ring system around Uranus has been established, this becomes a must for investigation at close quarters.

## LAUNCH SYSTEM

Because the Titan/Centaur launch vehicle cannot accelerate the payload of 800 kg ( $1,760 \mathrm{lbs}$ ) to the energy level required for a ballistic trajectory to Jupiter, an additional upper stage is required. This expendable module will be attached to the bottom of the mission module. This will be the first time that such a module has been used on a planetary spacecraft.

This stage is ignited about 15 seconds after the separation from the Cemtall. The basic vehicle is a module which weighs 24.5 kg ( 541 lb ), it is a ten sided framework with ten electronic compartments and has a spherical tank mounted in the centre of the framework. This tank contains the Hydrazine fuel for the thrusters which maintain attitude of the spacecraft.

As the mission is away from the Sun, solar panels for the power supply would not be a suitable system to use. The panels would have to be very large and would have the effect of reducing the effective payload. In place of these panels, isotope thermoelectric units will be used. There are three of these units grouped together on a boom which holds them away from the main body, thus ensuring the least interference with the experimental equipment. The output of the generators is some 430 watts at launch falling to about 380 watts after the Saturn encounter.

On these spacecraft there is a much larger high gain antenna, 3.66 metres in diameter, than has been used before. Communications with the spacecraft will be in the S-band for "up" links, and for the "down" links the X and S -bands will be used. The X-band horn is set in the centre of the main reflector. The S-band feed horns are mounted back to back on the structure of the antenna. One of the special features of this mission is that every 50 million miles these spacecraft will slowly rotate on their axes and optical measurements will be taken in all directions thus making direct calibrations of the instruments.

## MISSION PROGRAMME

The spacecraft will commence activity shortly after launch and will observe the Earth and the Moon. The actual imaging
of Jupiter will begin on December 1978 when Vovager / will be within 80 days of its encounter position.

Some hours before the closest approach of the spacecraft to Jupiter, it will pass the satellite Amalthea. This satellite is very close to Jupiter and will be within 290,000 miles of the spacecraft. By March 1979 the fly past of the Voyager $/$ will be at a distance of 174,000 miles and at this time the spacecraft will observe the Galilean satellites.
The distance of lo will be about 15,000 miles, Europa will be at a distance of about 465,000 miles and Ganymede and Callisto at a distance of 80,000 miles. When Voyager 2 is passing lo it will fly through the region of intense magnetic and plasma activity known as the "flux tube".
The Voyager $I$ will cease imaging Jupiter in April 1979 at the time when Voyager 2 begins its task. At this time Voyager 2 will also observe four of the satellites.
On July 10 the spacecraft will pass at a distance of 397,000 miles from Jupiter. This distance from the planet has been selected to avoid danage to the spacecraft by the intense radiation in that area. The imaging stage will be terminated in August 1979.

One year later in August 1980 Voyager 1 will be imaging Saturn. The spacecraft will then be some 62 million miles from the ringed planet. The picture taking will go on until January 1981.
When Voyager $l$ passes Saturn it is estimated that it will be 2,200 miles from the large satellite Titan. This satellite is larger than the Earth and is known to have an atmosphere, making it a focus of special interest for scientists. The other satellites Rhea, Tethys, and Enceladus will be scanned before the spacecraft passes Saturn and its rings.
As this situation will block out the Sun and Earth as seen from the spacecraft, the spacecraft will make occultation measurements. Voyager 1 will then pass the southern hemisphere of Saturn at about 80,000 miles.
At this time it will be possible for the controller to decide whether Voyager 2 should be retargeted to Uranus instead of Saturn. The decision need not be made until the spacecraft is within four months of the Saturn encounter. It is a decision which will depend on the possible damage that may have occurred on Volager I in passing the rings of Saturn. The decision will also depend on the "health" of Voyager 2 particularly as to whether there is enough attitude control gas available.

The best trajectory would be for a flyby of Saturn at 62,000 miles which would mean that Voyager 2 passed the visible edge of the outer ring by 23,000 miles. If it is possible to approach Uranus it will be in an ideal position to study the profile of its magnetosphere and any plasma cloud that may exist.

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## MICRO-EUS

## Compiled by DJD

This is the first of a new regular feature covering all aspects of microprocessors and minicomputers. Appearing every two months, Micro-Bus will present ideas, applications, and programs for the most popular microprocessors; ones that you are unlikely to find in the manufacturers' data books. The most original ideas will probably come from readers working on their own microcomputer systems, and payment will be made for any contribution featured here. This is also the place to air your views, in general, on this new technology, so let's be hearing from you!

## DIGITAL WAVEFORMS

OIE interesting area where micros are being used is in the synthesis of electronic music. The mpu can actually generate any arbitrary waveform. The levels at regular intervals along the wave are coded into 8 -bit binary numbers and stored in memory; the program outputs these numbers to a digital-to-analogue converter which converts the 8 -bit number to a voltage level proportional to that number.

The D/A converter can be connected to the mPU bus by an 8 -bit latch, and one way of forming a latch is from two 74157 quad 2 -input data selectors as shown in the circuit diagram, Fig. 1. These devices are already present in the SC/MP kit with keyboard, seven of the latches being used to drive the segment lines of the display. If the eighth unused data selector is connected to DB7 as shown, the eight outputs can also serve to drive the D/A since the display is blank when a program is being executed. The latches can be addressed as any location with AD9 different from AD10; e.g. X'0400 (X'= Hexadecimal).


Fig. 1. Circuit showing a latch formed from a pair of 74157 quad 2-input data selectors

A program to test the circuit is given in Fig. 2, and this outputs the 36 values stored at X'OF50 repeatedly to the D/A to give a digital approximation to a sinewave. The values in locations X'OFF7
to X'OFFC are loaded into the pointer registers by the monitor program; in systems without such a monitor, code to load these registers will have to be appended to the program.

## TITLE SINE-WAUE

; FOR SC/MP INTROKIT OR KEYEOARD KIT

| $=$ BF 30 |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 9 F 30 | CADB | OUTPUT: | LDI | BEGIN-LAST | ; FROGRAM START |
| 0F32 | 01 | NEXT: | XAE |  | : LOAD E WITH -37 |
| 0 F 33 | C180 |  | LD | -128(1) | : LOAD FROM P1+E |
| 0F35 | CAbb |  | ST | (2) | : STORE TO DAC |
| 9F37 | 40 |  | LDE |  |  |
| dF38 | F401 |  | ADI | 1 | : FOR NEXT FOINT |
| OF3A | 9894 |  | JZ | OUTPUT | 'LAST PDINT? |
| OF3C | 9694 |  | JMP | NEXT | , NO |
| ; LOCATIONS TO SET POINTERS . $=$ OFF7 7 |  |  |  |  |  |
| BFF7 | BF2F |  | . DBYTE | OUTPUT-1 | : FC FOR INTROKIT |
| 0FF9 | OF74 |  | . DEYTE | LAST | ;P1 -) ENO |
| AFFB | B4BF |  | . DEYTE | 048F | ; P 2 -) DAC |
| ; POINTS FOR SINE-WAVE . $=0 \mathrm{~F} 50$ |  |  |  |  |  |
| 0F50 | 日 | EEGIN: | . BYTE |  |  |
| QF59 | FF |  | . BYTE |  |  |
| 0F62 | 80 |  | . BYTE | 080, 06A, 054, 040, 02E, 01E, 011, 608, 002 |  |
| 6F6E | 00 |  | $\begin{aligned} & \text { EYTE } \\ & \text { = LAST } \end{aligned}$ | 000, 002,808 | 91E, 62E,040,054, 06A |
|  | 0000 |  | END |  |  |

Fig. 2. Program for SC/MP to give a digital approximation to a sine wave


Fig. 3. Oscilloscope traces of sine wave produced from program in Fig. 2

The resulting waveform is shown in the oscilloscope traces, Fig. 3. The measured first-harmonic distortion was about 0.3 per cent, which is what you would expect since 8 bits gives you 256 levels-the maximum deviation from a true sine-wave is $1 / 256$.

Although a sine-wave was chosen for this demonstration, any waveform can be generated with its accuracy determined by the number of sample points and the number of bits, and the frequency can be made continuously variable by feeding a variable-frequency oscillator to the interrupt input SENSE-A and modifying the program to output the data on interrupts.
To take the idea a step further an electronic organ or synthesiser could contain "templates" in read-only memory for the waveforms of notes of different instruments. These would then be used by the MPU to generate the different voices of the instrument.

## THE EURO-MICRO

The Ferranti $F 100-\mathrm{L}$ is a unique microprocessor in two respects. Firstly it has been developed and designed entirely in Europe. Secondly it uses the bipolar CDi technology rather than the more usual nmos giving it a speed of around 10 MHz , at least twice that of other mpus, and a typical instruction time of $4 \mu \mathrm{sec}$.
It can address up to 32 K 16 -bit words, and it communicates with the memory and peripherals by a 16 -bit bus and 5 control lines. The decoding into address and data lines is performed by special interface sets which will cater for a variety of possible requirements.

The instruction set has a pleasing orderliness about it and all the instructions can use the four addressing modes: direct, immediate indirect, immediate data and pointer indirect. With direct addressing any location within the first 2 K of memory can be specified in a-single-word instruction; alternatively immediate indirect addressing allows one to specify the full 15 -bit address in the next word.

Immediate data addressing supplies the operand in the second word. Finally pointer indirect addressing takes the contents of a pointer to be the required address. There are no pointer registers on the CPU chip; instead any of locations 1 to 255 can be specified as pointers, with the option of auto-increment or decrement. Thus up to 255 separate stacks can be maintained in memory.

As well as the usual instructions to add/ subtract memory into the accumulator there are add/subtract accumulator into memory instructions, making it possible to replace: load X , add Y , store to Y , by the shorter: load X, add to Y.

Although there are no multiply or divide instructions, an additional singlechip unit, the F101-L, will provide these functions with execution times of less than $15 \mu \mathrm{sec}$.

The arithmetic and logical functions act on the accumulator and memory, but the shifts and a variety of bit-test, set, and clear instructions operate on any of the

#  <br> cows: $0 \mathrm{NA}^{2} \mathrm{NXD}=1$ cow 

Fig. 4. The eight consecutive locations for Bulls and Cows game
accumulator, condition register, or memory. A single instruction provides a jump if a defined bit in a word is set (or clear) and then clears (or sets) it; this would be invaluable for controlling lockouts in timesharing or multi-processor systems.

The prospective computer-builder reading this may be disheartened to learn that production devices will not become available until October, when the prices will be $£ 55 \cdot 00$ for the processor and $£ 25.40$ for the interface set ( 1 off). However, you can already set about designing your system with the help of the Hardware and System Manual, available for $£ 7.50$.

## BULLS AND COWS GAME

The techniques of programming are much the same whether one is writing a game-playing program or a factory process-control program, and whereas the first is of almost universal interest, the second would probably only inspire a very restricted brand of programmer. For this reason game programs will frequently


Fig. 5. Program for "Bulls and Cows"
be featured in Micro-Bus while at the same time admitting that microprocessors have inore serious applications.

One absorbing game which lends itself to being programmed is the traditional pencil-and-paper guessing game variously referred to as "Bulls and Cows" or "Moo", and recently made popular as "Mastermind". For those not already familiar with it , a brief description of one form of the game follows:

The two players first each think of a code consisting of four digits, each digit being one of the eight octal digits $0-7$. The players then take turns in trying to determine the other player's code by making a guess of a number. The guesser is told the number of "bulls", i.e. digits correct and in the right position (bullseyes), and the number of "cows", i.e. digits correct but in the wrong position. This information guides the player towards deducing the code. For example, if the code is " 3455 ", to the guess " 4653 " the reply is 1 bull, 2 cows. The first player to guess the other's code wins.

A program has been written for the 6800 in which the human player and the microprocessor alternately try to guess each other's code. It centres around the two subroutines which count the numbers of bulls and cows between two numbers, and listings for these in assembler are given below, Fig. 5.

The two numbers to be compared, referred to as KEY and TRY in the program, are stored unpacked (one octal digit per word) in the eight consecutive locations starting at 0000 ; see Fig. 4. Subroutine BULLS is called first and makes four comparisons between corresponding pairs of digits, returning with the number of matches in the B accumulator ( 2 bulls in the case shown). It complements matching digits so that they will not be counted as cows. Subroutine COWS then compares each digit in TRY not complemented with the four digits in KEY again returning with the number of matches in $\mathbf{B}(1$ cow in this case). XREG is a double-byte variable used to store the $X$ register.

The machine's strategy is at every stage of the game to make a guess chosen at random from the class of numbers consistent with the replies made to its previous guesses. The program usually wins unless its human opponent uses careful logical thinking, and the result is a highly entertaining game; readers with microcomputer systems are urged to program it. Alternatively the program "Bulls and Cows" is available from Practical ElecTronics in a form suitable for running on a Motorola D2 kit and using $\frac{1}{2} \mathrm{~K}$ of memory. A listing and Kansas-City format cassette containing the program will be supplied on receipt of $£ 2.50$.

The next Micro-Bus will be in the December issue, and we hope to feature circuits and programs submitted by readers. If you have any interesting and original contribution, send it to: Micro-Bus, Practical Electronics, Fleet way House, Farringdon Street, London, EC4.

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Last month we examined the CHAMP "family" concept of combining a microprocessor unit (CHAMP itself), PROM programmer (CHAMP-PROG), and a PROM eraser (CHAMP-UV) to produce a self-sufficient and capable development system.

This month we shall start to look in detail at the circuitry on the CHAMP microprocessor board, and at the 4040 MPU chip around which the system is constructed.

## MAIN BOARD

The main objective of the CHAMP design was to produce a complete microprocessor system at the lowest possible cost, and in keeping with this objective no expensive plug-in cards and edge connectors are used at all. Major circuitry is mounted on a single piece of 0.1 in matrix Veroboard which, in fact, is a much more convenient packaging solution than more expensive plug-in cards anyway!

Connections to the board are made via 16 -way d.i.l. plugs and sockets, and in the basic system, only two of these are occupied with the others available for system expansion and debugging purposes. Power is coupled to the board via three hardwired leads terminated in wander plugs. These can be plugged into sockets on the power supply itself, or, when the board is in its vertical position, into sockets adjacent to the breadboard.

## CONSTRUCTOR'S NOTE

A kit comprising the main i.c.s for CHAMP:

| 4040 | 4289 | 2 off $5101-8$ |
| ---: | :---: | :--- |
| 4201 A | 1 off $4702 A$ | Plus |
| 4265 | 3205 | 4040 XTAL |
| 1 off $4002-1$ |  |  | 1 off 4002-1

is available from Rapid Recall Ltd., Dep. N, 9 Betterton Street, Drury Lane, London WC2H 9BS at the special price of $£ 49.68$ including post, packing and VAT

The board can be removed rapidly when necessary by simply uncoupling the connectors mentioned above and then sliding it sideways out of the self adhesive card guides in which it rests. Under the board, on the plinth, is another card guide which can be used to support the board in the vertical position with all connections remade, on those occasions where access to both sides of the board is required.

The board itself measures $305 \times 159 \mathrm{~mm}$ and carries 25 integrated circuits including PMOS, сMOS and TTL devices.

## 4040 MPU CHIP

At the heart of the board is of course the 4040 microprocessor chip. The 4040 is a development of the Intel 4004, which had the honour of being the first microprocessor ever produced. As mentioned last month, CHAMP is downwards compatible with the 4004 chips, allowing the development of very low cost dedicated systems when the more sophisticated features of the 4040 are not required.

The 4004 chip is housed in a 16 -pin package whereas of course the 4040 uses the larger 24 -pin version; both chips are made using the well tried pmos technology and need 15 volt supplies. In CHAMP, supplies of --5 V and -10 V are used so that interface to TTL and cmos can be simply achieved without recourse to level translation.

The 4040 and 4004 are "four-bit" microprocessors, which means that their arithmetic units operate on "words" of four binary bits, and that transfer of data within a 4040 or 4004 system is carried out four bits at a time. This does not of course mean that arithmetic resolution is limited to four bits: any arithmetic resolution can be achieved by simply cascading four-bit operations. "Natural" 4040 arithmetic resolutions are in fact 64 bits binary or 16 digits decimal, as we shall see when we consider the arrangement of data memory.


Fig. 2.1. System timing and data bus contents for the 4040

## COMPONENTS . . .

| CHAMP BOARD |  |  |
| :---: | :---: | :---: |
| Resistors |  |  |
| 4 off $47 \Omega$ | R1-3, R40 |  |
| 2 off $100 \Omega$ | R33, 59 |  |
| 8 off $150 \Omega$ | R51-58 |  |
| 1 off $270 \Omega$ | R32 |  |
| 17 off $1 \mathrm{k} \Omega$ | R6, R8-13, R15, 17, 26, | , 31, 34, R35-38, R60 |
| 13 off $5 \cdot 1 \mathrm{k} \Omega$ | R14, R18-25, R45-48 |  |
| 9 off $10 \mathrm{k} \Omega$ | R4, 7, 16, 39, R41-44, R | R49 |
| 4 off $12 k \Omega$ | R27-30 |  |
| 1 off $47 \mathrm{k} \Omega$ | R50 |  |
| 1 off $1 \mathrm{M} \Omega$ | R5 |  |
| All 2\% $\frac{1}{1}$ W carbon film |  |  |
| Capacitors |  |  |
| 2 off 33pF | Sub-min ceramic C | 4, 5 |
| 7 off 10nF | Ceramic disc 18V C | 6-11, C14 |
| 1 off $0.1 \mu \mathrm{~F}$ | Ceramic disc 18V C |  |
| 3 off $0.22 \mu \mathrm{~F}$ | Polyester C1 | 1, 2, 13 |
| 1 off $1 \mu \mathrm{~F}$ | Tantalum bead 35V C3 |  |
| 1 off $4.7 \mu \mathrm{~F}$ | Tantalum bead 35 V C1 |  |
| Transistors |  |  |
| 5 off BC108 | TR1-5 |  |
| Diodes |  |  |
| 1 off BYZ88C | 3 V 9 Zener, 3.9 V 400 mW | D13 |
| 14 off OA47 | D1-12, D19-20 |  |
| 6 off 1N4148 | D14-18, D21 |  |
| Integrated Circuits |  |  |
| 1 off 3205 | IC17 |  |
| 2 off 4002 | IC4, 5 |  |
| 1 off 4040 | 1 C 2 |  |
| 1 off 4201 | 1 C 1 |  |
| 1 off 4265 | IC6 |  |
| 1 off 4289 | 1 C 15 |  |
| 2 off 4702A | IC18, 19 |  |
| 4 off 5101 | IC20-23 |  |
| 3 off 74L00* | IC3, 10, 24 |  |
| 1 off 74L02* | IC25 |  |
| 3 off 74L74* | IC11-13 |  |
| 1 off 74123 | IC9 |  |
| 1 off 74125 | 1 C 14 |  |
| 1 off 74157 | 1 C 16 |  |
| 2 off 75491 | 1C7, 8 |  |
| *See Text |  |  |
| Miscellaneous |  |  |
| B1 Nickel Cadmium stack, 4.8 V 225 mAh |  |  |
| XL1 Crystal $5 \cdot 185 \mathrm{MHz}$ |  |  |
| 8 off 16-pin d.i.l. low profile sockets, SK1-8 |  |  |
| 500 off Soldercon sockets |  |  |
| Veroboard VB124 $179 \times 454 \mathrm{~mm}$ |  |  |

## 4040 INSTRUCTIONS

Although the 4040 is a "four-bit" device, its instruction set is based on an eight-bit word length which means that program memory (which is separate to data memory) is organised as consecutive locations each containing eight bits. A popular name for an eight-bit word is "byte", and a fourbit word is often called a "nibble" (for obvious reasons!). From now on we will be using these terms when appropriate.

The 4040 has a total of 60 separate instructions, some of which are 16 bits long and require two consecutive bytes in program memory.

For dedicated applications, program memory would normally consist of roms or Proms, but because CHAMP is a development system, an area of RAM program memory is also provided, for user programs, and this makes it important for us to differentiate between program and data ram which are of course used for different purposes. The 4040 uses a 12-bit address counter which allows up to 4096 bytes of program memory to be directly addressed, although only 1024 locations are actually used in the CHAMP system as it stands, 512 bytes being assigned to Prom and 512 bytes to ram. The CHOMP system firmware occupies 256 bytes only; when the PROMPT programmer firmware is added however, the full 512 bytes of PROM are utilised.

## USER'S MANUAL

It is important that any intending CHAMP constructor should obtain a copy of the "Intel MCS-40 User's Manual" preferably of the March 1976 or later edition. This is provided free when a chip set is purchased, and is a mine of information on 4040 operation, programming, and interfacing, and contains data sheets on systems components like rams and PROMS.
The description of 4040 operation provided here is necessarily limited by space considerations, and most CHAMP users will soon find themselves wanting to know more! The User's Manual provides all the answers to technical questions and includes many programming and applications examples to whet one's appetite!

## 4040 OPERATION

The 4040 uses the dynamic mode of operation which means that it must be continuously clocked to ensure proper data retention. The necessary 2-phase clock is best provided by the Intel 4201 clock generator which is produced especially for
this purpose, since in addition to containing the clock circuitry this device provides the power-on reset logic and the single step logic which forms an essential part of any development system. The basic clock frequency is determined with the aid of a crystal, and is normally set, as in CHAMP, to $5 \cdot 185 \mathrm{MHz}$ to give the data sheet instruction cycle time of 10.8 microseconds. The basic clock frequency is divided in the 4201 to give two 740 kHz nonoverlapping pulse trains which are used to drive the MPU chip clock inputs.

Inside the 4040 this clock frequency is further divided into "instruction cycles" which each consist of eight clock periods. The instruction cycle is really the shortest interval which can be isolated in an operational system. When the single shot mode is used it initiates either one or two of these instruction cycles depending on whether a one- or two-byte instruction is involved. The 4040 signals the start of a new instruction cycle with a pulse output on its SYNC pin, and this signal is wired to all the other devices which interface directly with the 4040 bus so that they can keep in step with processor timing.

The 4040 uses a four-bit data bus ( $\mathrm{D}_{0} \mathrm{D}_{1} \mathrm{D}_{2} \mathrm{D}_{3}$ ) to communicate with its associated flock of program memory, data memory, and input/output ports. In fact this so called data bus is really a combined data and address bus, since there is 。 no separate address bus as in most other microprocessors.

Now, if you have followed me so far, you may be wondering how on earth the 4040 manages, during the execution of a single instruction, to send out 12 -bit addresses, retrieve 8 -bit instructions, and shift 4 -bit data nibbles around when all it has to do it with is a single four-bit bus! The answer, of course, is provided by time multiplexing, and now we can begin to see why one instruction cycle consists of eight clock cycles.

## DATA BUS CONTENTS

Immediately after the 4040 sync pulse, the low order four bits of a program memory address are sent out on the data bus followed one clock cycle later by the middle four bits, and then the high order four bits, after this back to the 4040 come the first four bits of the instruction, followed by the second four bits. This leaves three clock cycles out of the eight for the execution phase of the instruction, when the accumulator contents and data ram addresses are able to use the bus as required by the particular instruction which was fetched.

The use of a time multiplexed bus of this type drastically reduces the number of interconnections required (at least 20 wires would be required by a non multiplexed bus) but it does impose a time penalty. It is our contention that for home built systems this is a trade-off worth making, after all, even with a 10.8 microsecond instruction cycle, 92,592 single-byte instructions can be carried out in one second! System timing and data bus contents are summed up in Fig. 2.1.

## ADDRESSING DATA MEMORY

The data memory used with a 4040 system is of a special type, organised in a unique way. The chips used are coded 4002 and they contain, in a 16 -pin package, four ram registers and a four-bit output port. Each register consists of 20 separately addressable locations of four bits, subdivided into 16 main memory locations and 4 status characters (Fig. 2.2).


Fig. 2.2. Organisation of 4040 system data memory

This memory organisation was originally intended for the convenient storage of 16 -digit binary coded decimal floating point numbers, the status characters being intended for storage of the mantissa sign, two-digit exponent, and exponent sign. Despite this design intention, the 4002 structure is quite suitable for all other likely uses and can readily be used for the storage of binary arithmetic operands, status flags, counters, and what-you-will. The status characters are directly addressable within a register and are therefore useful as "overspill" registers to take the load off the internal 4040 register array when space is limited.

Addressing a particular 4002 location is achieved with the aid of an instruction called SRC (Send Register Control) which causes the eight-bit address of a ram location to be sent out on the 4040 data bus in two consecutive nibbles. The 4002 contains all the necessary demultiplexing circuitry to unscramble and latch this address.

## ADDRESSING PROGRAM MEMORY

The CHAMP system uses standard 4702A EPROM chips and $5101256 \times 4$ RAM chips to form the program array, but these chips have no internal facilities for demultiplexing the 4040 bus. To provide the necessary multiplexing and demultiplexing functions, another member of the 4040 family, the 4289 memory interface chip, is ised. The 4289 "unscrambles" the 4040 bus to give tweive parallel address outputs, and also "scrambles" the eight-bit instruction words from the program memory so that they can be sent back to the mpu chip over the bus.
The combination of 4289 and standard memory components is therefore equivalent to the 4308 mask-programmed roms which do contain 4040 bus interface logic but which are of course unsuitable for use with a development system because they cannot be reprogrammed. The 4308 parts also contain a number of input/output ports which can be accessed using the $R D R$ and $W R R$ instructions, after selection with an appropriate $S R C$. To duplicate this function the 4289 provides a four-bit bidirectional $\mathrm{I} / \mathrm{o}$ bus which interfaces with up to 16 input and 16 output ports built with TTL or cmos logic.

## INPUT/OUTPUT

In the CHAMP system this "rom $1 / 0$ " facility is used only by the CHOMP firmware for control functions and for writing programs into program Ram. CHAMP users would normally concern themselves only with the data ram based i/o provided by the 4265 programmable general purpose $1 / \mathrm{O}$ chip.


Fig. 2.3. Circuit diagram of the CHAMP board


Bird's-eye view of the CHAMP board

The 4265 is a powerful addition to the 4040 family which can live at the end of the 4040 bus and yet provide 16 input/ output lines which may be configured using software into any one of 14 separate operating modes. The 4040 system can directly address up to four 4265 chips, and one is provided on the CHAMP main board. If CHAMP-PROG is added, then a further two 4265s come with it, their ability to talk directly to the 4040 bus being demonstrated by the fact that only a single 16 -way flat cable is needed to pass all programming data and power supplies between the two units!

The 4265s occupy address space normally used by 4002 data ram chips, and are in fact addressed and accessed in the same way, using the same instructions. The mode of operation for each 4265 is programmed during system initialisation by means of the WMP instruction, subsequent data transfers being made by use of the $W R 0$ to $W R 3, R D 0$ to RD3, $W R M, R D M, A D M$ or $S B M$ instructions.
The CHAMP "on board" 4265 is put into mode 9 during initialisation and used as the keyboard/display interface during program load and debug. When a user program is run, however, the same 4265 can be reprogrammed to a different mode, with connections to user circuitry made via the 16 -way d.i.l. socket provided for this purpose. Needless to say, this is a very useful and powerful facility! The 4265 chips even have a mode which allows them to be used as a data memory interface for use with standard memory chips like the 2111 . This is very useful where a lot of data ram is required because a 4265 and four 2111 chips provide 1024 four-bit nibbles in a much more compact form than the 16 4002 chips otherwise required. It is only fair to point out, however, that most 4040 applications do not need that much data Ram!

## INTERRUPTS

The 4040 has a single-level interrupt which can be extended externally to any number of lines. CHAMP uses the interrupt facility for keyboard entries, although user programs can reallocate the interrupt to another source or sources as required; multiple interrupts being resolved by using an input port to "poll" all possible sources.

The 4040 has an internal seven-level hardware address register stack which is used to save the current address value when an interrupt occurs. This stack is also used for saving subroutine return addresses.

## PUTTING THE PIECES TOGETHER

In Fig. 2.3 we show the overall circuit of the CHAMP board and you should now be able to pick out the main. system components like the 4201 clock generator, the 4002 data RAM, the 4289 program memory interface, the 4702 A and 5101 program memory chips, the 4265 programmable $1 / 0$ and of course the 4040 MPU chip itself. You will also see that scattered among these major systems components there are a number of TTL gates and flip-flops and of course a variety of discrete components, which together form an essential part of the CHAMP microprocessor circuit. Next month we shall be examining the operation of this circuitry in detail, but meanwhile a word about interfacing is necessary.

## TTL COMPATIBLE

CHAMP brings together on one board pmos system chips, CMOS memory chips and TTL gates and flip-flops, all of which differ in their interface requirements. Most 4040 system parts have a variety of options available via their supply pins so that their output drive levels may be programmed to be compatible with all the logic families likely to be encountered.
In CHAMP, tTL interfacing has been chosen since this is practical and uncomplicated and is also suitable for use with 5 volt cmos. For complete details of the interface considerations involved, refer to chapter three of the user handbook where the $4040,4289,4265$, and the 4002 are dealt with.
In general it is best to use low power TTL in an MCS40 system since it is both sufficiently fast and easy to drive, although certain 4289 and 4265 outputs are capable of driving standard TTL loads if necessary. In CHAMP, low power TTL is recommended, although it is only essential in the IC3 position.

NEXT MONTH: circuit description

# Semiconductor UPDATilkeo FEATURING : sc/MP-II MC4000 B-Series R.W. Coles 

## REVAMPED SC/MP

The SC/MP microprocessor chip from National is a good compromise between price and performance, and has now become very popular with hobbyists both here and in the USA.

It's not just the basic price of an MPU chip which determines the overall cost of a working system of course, the numbers and types of any necessary supporting i.c.s will usually be more important, and it is on this count that the SC/MP chip beats the more sophisticated opposition represented by the Intel 8080 A and the Motorola 6800.

The SC/MP has an on-chip clock oscillator which will run with just an RC timing network if required, and this alone can save the significant cost of a crystal and the clock generator chip (or chips) often required. It also has CMOS or TTL compatible outputs too, and the use of memory mapped input/output removes the need for the more capable but rather expensive, programmable interface chips, allowing a functional system to be built with just an MPU chip and a handful of standard Iogic.

To improve this image of capable economy, the SC/MP needs a low cost plastics package and the advantage of operation from readily available five volt logic power supplies, but with the PMOS process technology this has not been possible. Both these assets could have
been gained by switching to NMOS technology but until now this switch has been avoided because PMOS has been cheaper and more readily produced.

Now National have relented and introduced SC/MP-II which does live in a plastics package and does run from a single 5 V supply. SC/MP-II retains all the original SC/MP-1 fəatures and an identical instruction set, but now an advanced silicon gate $n$-channel ion implant process is used for chip manufacture.

In most cases the new chip can be directly substituted for the old with just a change of chip supply voltage, and 1 believe that a low cost conversion set is to be offered to the many users of the popular Introkit.-Well done National!

## BE BUFFERED

You may have decided that 4000 series CMOS logic, with its wide supply voltage range, low power operation, and high noise immunity is the best thing since sliced bread. Or, you may be a TTL man! No matter what your feelings about CMOS, you should take a new look at this popular but sometimes controversial family now that $B$-series devices are becoming freely available.

The B in B-series CMOS stands for "buffered" because each ouptut is isolated from its associated inputs by a separate buffer stage so that output drive
is independent of the number of driven inputs. This is a big improvement over standard 4000 series devices, which have a poor output drive capability which can sometimes only be improved by paralleling gate inputs!

The B-series CMOS will drive without compromise a couple of low power TTL loads or a single low power Schottky load over the full temperature range. A big improvement, but that's not all. The Bseries devices have been improved in other respects, and now feature improved noise margins, reliable high voltage operation, improved static charge protection and a guaranteed fanout to over 50 fellow CMOS inputs.

Motorola already offer about 100 suffix B types with such favourites as the 14011, the 14016 and 14013 now freely available in this new style as the 14011B, 14016B and the 14013B. Certain B series devices are complete redesigns of existing 4000 series parts and these offer performance advantages in other areas.

Take the MC14538B dual monostable for example. This is a plug-in replacement for the older MC14528 dual mono with improvements like high precision pulse timing, reduced temperature dependence, and a new timing equation. This device should bring CMOS mono performance claser to that of the excellent workhorse, the TTL 74123, and put an end to the difficulties sometimes encountered with the 14528.

## Pollits baishlic

## P.E. MINISONIC 2

('Sound Design'"-A P.E. Publication)
On the component layout drawing. Fig. 21, page 24, the diode D5/2 in the Envelope Shaper should have its polarity reversed.

Readers intending to make their own printed circuit boards for this project can obtain a clean copy of the track layout master from the Editorial Office, free of charge. Please send a large stamped addressed envelope.

## LINEAR OHMMETER (September 1977)

In Fig. 3. page 47, VR2 wiper should connect to IC3 pin 4. In the second paragraph under the side heading "Setting Up", $0.6 \mu \mathrm{~A}$ should read 0.6 mA .

## FREQUENCY COUNTER/TIMER (September 1977)

Referring to Fig. 8, the unmarked pad on pin 11 of 1 Cl 17 should be numbered: 37. In Fig. 4, capacitor C6 goes to ground and not $+5 V$.

Every guitarist must be familiar with the sound of a sustain unit. It enables the length of a note to be greatly extended-indefinitely if necessary with acoustic feedback to help. A sustain unit for an electric guitar works by maintaining a constant output level as the actual output signal from the guitar dies away.

## ALTERNATIVES

The simplest way to achieve sustain is to use a high gain amplifier and then clip the output in a similar way to a fuzz unit-The clipped output is then filtered to remove harsh sounding high order harmonics and give a more musical sound (Fig. 1).

The second, and more sophisticated method is to automatically increase the gain of a v.c.a. (voltage controlled amplifier) as the output signal from the guitar dies away (Fig. 2).
Using this method preserves the original sound of the guitar without distortion. It also enables full chords to be played unlike the first method-which causes such severe intermodulation distortion that care has to be taken to avoid playing even two notes simultaneously.

The circuit described here uses the second method with all its advantages. Obviously the circuit cannot amplify a signal which is infinitely small and the maximum gain has to be a compromise between a long sustain and excessive noise and hum pickup as the gain increases to maximum at very low signal levels.

## FULL ATTACK

To maintain the guitar's natural attack the initial transient is allowed to pass through the unit without compression, but the circuit then maintains a virtually constant output level down to 0.5 mV input, when the output dies away.

## INVESTIGATIONS

There are two problems in designing a v.c.a. type sustain unit-firstly to design a v.c.a. system with a sufficiently fast response to follow the envelope of the output waveform from a guitar without causing any significant waveform distortion, and secondly thehieve an acceptable noise performance.


Fig. 1. Clipping type sustain unit


Fig. 2. Block layout for producing a non-distorting sustain unit using a voltage controlled amplifier

Various methods of controlling the amplifier gain were tried out in several experimental prototypes, using f.e.t.s and biased diodes, but all suffered from either poor transient response or unacceptable distortion. Eventually the answer was found in a combination of an l.e.d. and a cadmium sulphide photoresistor (I.d.r.). As the current through the l.e.d. is increased its brightness increases-causing the resistance of the photoresistor to fall and hence reducing the gain of the amplifier. This arrangement also has other



Fig. 3. Output response for different input levels

useful characteristics. The photoresistor responds rapidly at high signal levels but much more slowly at low levelsthis effectively gives the system a variable time constant and reduces distortion at low levels, as the note is dying away. Also as the negative feedback provided by the photoresistor is reduced so the bandwidth of the amplifier is reduced too. This helps to reduce noise and hum at low levels and at the same time it gives increased emphasis to the higher harmonics in the guitar output to compensate for the falling harmonic content in the note as it dies away.

Low noise is an essential requirement in a sustain unit as the gain can increase by 500 times from the start to the end of a note. This design uses a specially selected ZTX384 type transistor in the first stage, operated at a low collector current to obtain as good a noise performance as posisible.

## CIRCUIT DESCRIPTION

In Fig. 5 TRI and TR4 form a two stage amplifier with an open loop gain of around 1000 . Negative feedback is provided by the l.d.r. to control the gain and d.c. feedback to stabilize the operating point by R8. This circuit arrangement was chosen because it has very good stability and will operate over a wide range of battery voltage-from 10 V for a fresh battery down to 6 V for a worn out one. The average current' drain is only about 2 mA , in the interests of a long battery life.

Normally there would be a resistor from the collector of TR4 to the negative rail, but in this design the resistor has been replaced with a constant current source comprising TR2 and TR3. This increases the gain of the amplifier and enables a very large output voltage swing to be obtained. The circuit will give an output of at least 2 volts r.m.s. without distortion, which enables the high amplitude transients at the start of a note to pass through the amplifier with minimum distortion. Commercial sustain units often use two batteries to achieve similar performance.

Fig. 4. Frequency response of sustain unit showing how bandwidth changes with input


Fig. 5. Circuit diagram of sustain unit

## CUITMR SUSTAMIN



Fig. 6. Printed circuit board (full size)


Fig. 7. Component layout and mounting details for D1 and R12 shown above

## COMPONENTS ...

## Resistors

| Resistors |  |  |  |
| :--- | :--- | :--- | :--- |
| R1 | $22 k \Omega$ | R7 | $3.3 k \Omega \Omega$ |
| R2 | $220 k \Omega$ | R8 | $22 k \Omega$ |
| R3 | $27 k \Omega$ | R9 | $27 \mathrm{k} \Omega$ |
| R4 | $390 \Omega$ | R10 | $220 k \Omega$ |
| R5 | $150 k \Omega$ | R11 | $390 \Omega$ |
| R6 | $22 k \Omega$ |  |  |

All resistors $\frac{1}{3}$ Watt 5 per cent carbon film

## Potentiometer

VR1 $100 \mathrm{k} \Omega$ single gang log law potentiometer

## Capacitors

C1 220 pF 63 V polystyrene
C2 $\quad 0.1 \mu \mathrm{~F} 250 \mathrm{~V}$ Mullard C280 polyester
C3 $22 \mu \mathrm{~F} 25 \mathrm{~V}$ electrolytic or tantalum
C4 $22 \mu \mathrm{~F} 25 \mathrm{~V}$ electrolytic or tantalum
C5 10 pF ceramic or polystyrene
C6 $0.015 \mu \mathrm{~F} 250 \mathrm{~V}$ Mullard C280 polyester
C7 $0.1 \mu \mathrm{~F} 250 \mathrm{~V}$ Mullard C280 polyester
C8 $150 \mu \mathrm{~F} 16 \mathrm{~V}$ electrolytic

## Semiconductors

| R12 | RPY58A Mullard |
| :--- | :--- |
| D1 | XC5053R Xciton |
| TR1 | ZTX384W |
| TR2 | ZTX108 Ferranti |
| TR3 | ZTX108 Ferranti |
| TR4 | BC415P Ferranti |
| TR5 | ZTX108 Ferranti |
| TR6 | BC415P Ferranti |

All semiconductors can be obtained from Davian Electronics

## Miscellaneous

JK1 Jack socket, front contact normally open rear contact normally closed
JK2 Standard jack socket, non switching
S1 Arrow D.P.D.T. push to make/push to break footswitch (Davian)
Case ITT Diecast box type 46R.CS00.043.A00
Printed circuit-Davian Electronics
Control knob, PP3 battery, battery clip, connecting wire, rubber feet, small piece of foam rubber


TR 5 and TR6 operate as a half wave rectifier, the compound Darlington arrangement providing a very high input impedance so as not to load the output of the amplifier. The operation is as follows.

When a note is played this passes through the amplifier and turns on TR5 and TR6. This causes the l.e.d. D1 to light up, which reduces the resistance of the I.d.r. and hence the gain of the amplifier to provide an output signal just sufficient to keep TR5 and TR6 turned on. All this occurs in the first few cycles of the note.

As the input signal dies away the signal at the base of TR5 tries to fall. This reduces the current through the l.e.d., increasing the gain of the amplifier to maintain constant output. When the input falls below about 0.5 mV , DI is extinguished and the gain of the amplifier cannot be increased any further. The output then falls with the input.

## MECHANICAL CONSTRUCTION

The unit is constructed in an ITT diecast box, which provides an enclosure rugged enough to be stood on, dropped or generally kicked around.

After drilling the case should be cleaned thoroughly and sprayed with paint. Gold was used on the prototype and gives a very attractive finish. The unit can then be lettered with Letraset or some similar product and finished off with a thin coat of protective clear lacquer.


To prevent the unit sliding around on the floor it is a good idea to fix two small rubber feet to the rear of the case lid. This also tips the box forward at a convenient angle for foot operation. Finally glue a small piece of foam rubber inside the lid to hold the battery in place.

## ELECTRICAL CONSTRUCTION

Most of the components are mounted on a small printed circuit board which fits into the slots in the box. There is not a great deal of room to spare and miniature components should be used.

The printed circuit board pattern and layout are shown in Fig. 6 and should be largely self explanatory. The only point to note is that R12 is mounted flat on the printed circuit board with the active face pointing upwards (the opposite side to that which the wires are connected to). The I.e.d. is then mounted facing down towards the l.d.r. with the leads bent double. Take care when bending the l.e.d. leads as they tend to be rather brittle. The I.d.r. should be soldered in place as quickly as possible with a clean iron. It is rather sensitive to heat and the wires tend to fall off if it is overheated! The anode of the l.e.d. is the shorter of its two leads and this should be connected to the positive rail.

Mount the two jack sockets, the output potentiometer and the footswitch in the diecast box. Note that the push button switch must have a push to make/push to break action. The tags on the footswitch must be bent sideways so as they lie flat-otherwise they will short out to the lid of the box.

The negative rail of the circuit is earthed to the diecast box by means of a wire soldered to the case of the output potentiometer. One of the metal tab. securing the cover of the pot can be bent up and used as a solder tag if desired.

Miniature screened cable must be used for the input and output and the leads to the footswitch as any stray coupling between input and output can cause the circuit to oscillate. Note that the input jack socket JK1 has a front contact (nearest the nut) which is normally open. The battery negative is wired to this contact (see photo) which makes when the input jack is inserted to switch, the unit on.

## USING THE SUSTAIN UNIT

For best results the unit should be operated with as much input as possible, therefore the volume control on the guitar should be set at or near maximum. The output control on the sustain unit should be set to give the same output level as with the unit switched out.

It is possible with the high gain involved for magnetic or mechanical feedback to occur between non-humbucking pickups and the loudspeakers and care must be taken to avoid this by reducing the volume control on the guitar or by moving further away from the loudspeakers. On the other hand acoustic feedback can be used advantageously to sustain the note for as long as desired, but a certain amount of playing technique is involved as the sound system may have a peak at some frequency other than the note being played, and this may cause other strings to be excited if they are not damped.

One final point. The unit will not work if any external light is allowed to fall on the l.d.r. The diecast box is excellent in this respect since it has a lip around the lid which provides a very effective light seal. Needless to say, a makeshift box which is not light proof, or failure to use a box at all, will prevent the circuit from giving any noticeable sustain effect.

## IBA

## EMEAINERIIIG



Independent Broadcasting Authority is responsithe for the desion, construction and operation of the large network of tansmitting stations throughout the UK for Independent Television and for the new Mreperdent Local Radio.

SOME aspects of the Independent Broadcasting Authority's current engineering developments were revealed during a series of open days held at the IBA Engineering Centre, Crawley Court, near Winchester, in July.

Crawley Court is a purpose-built Broadcast Engineering and Administration Headquarters situated in idyllic surroundings in a well-wooded park. This modern building has an elegance not always synonymous with precast concrete structures; it well simulates the quiet atmosphere of a venerable college, yet it has the practical advantages of a well-planned functional layout within.

## RESEARCH AND DEVELOPMENT

Of the 400 persons employed here about 40 are research engineers who, with an additional 50 -strong technical services team, work in the Experimental and Development Department's Laboratory. This is divided into three sections: Automation and Control, Radio Frequency, and Video/Colour. Examples of important work in progress in all these sections were shown and demonstrated to visitors. A summary account of some of these activities is given below.

## ORAGLE JUNIOR

A service with information generated by computer was devised by IBA engineers and first demonstrated in 1973.

This first "Oracle" system allowed up to 50 pages with text in only one colour. The present system is more attractive and flexible, with up to 800 pages, colour and other features. including a digital clock accurate to 20 msec
(The generic title "Teletext" is now often used, $b: a t$ the name Oracle remains for the ITV service.)

Current development includes work on a small Oracle system based on a microprocessor, for origination and demonstration purposes. It operates at 64 kilobauds and provides up to 60 pages. A semiconductor memory is used. The keyboard (part of a VDU) provides insert/delete facilities. Apart
from its use in broadcasting organisations, it was suggested that this small Oracle system has commercial possibilities for hotels, holiday camps, etc., allowing local information to be inserted as an alternative to nationally transmitted information.

## MEASUREMENT OF DATA SIGNALS

Satisfactory Oracle (Teletext) reception requires the receiver to be able to distinguish between 1 and 0 , despite multi-path propagation distortion or "ghosting". The measurement of data signals requires different methods to those employed for normal transmis. sions. Teletext decoder manufacturers have little means of checking the performance of receivers their instruments will be used with, and to solve this problem a signal generatór has been developed as a design aid for decoder makers.

This signal generator provides pulse and bar test pattern, and facilities for carrying out the different methods for measuring Teletext signals. It is understood that a standard for such measurements will be produced in the future.

## StEERABLE ADAPTIVE AERIAL

Receivers used as part of a broadcast link are likely to suffer from co-channel interference (CCI), resulting in severe patterning from other stations, if a conventional aerial is used.

The provision of colour TV to the Channel Islands proved to be particularly difficult since reception from the nearest mainland station in Devon was subjected to considerable co-channel interference from other UK and European transmitters.

After initial investigations an experimental 8-element Adaptive Aerial System was built at Crawley Court. The final system built during the beginning

This 2W u.h.f. transposer has been designed by IBA devalopment engimeers as part of a feasibility study into fow-power, low-cost stations for small communities. Maintenance is on a pre-aligned module basis to minimise the reed for test equipment in remote locations

of 1977 at Alderney, Cl , incorporates a $16 \times 4$ dipole array.

The Adaptive Aerial is essentially a phased array in which the amplitude and phase of the outputs of the individual elements are adjusted before combining in such a way as to produce an aerial pattern which has nulls in the directions of the interfering sources.

It is expected that simpler versions of this aerial system will be used for rebroadcast reception at other sites in the UK where the increasing number of transmitters will require aerial systems with the ability to null CCl by up to 50 dB .

## ADAPTIVE FILTER

Another method for overcoming $\mathrm{CCl}_{1}$ is the Adaptive Filter. This is a comb filter with adjustable notches which can track the frequencies of the CCl beats. This device is seen as complementary to the Adaptive Aerial, in improving the technical quality of signals.

## LOW-COST U.H.F. TRANSPOSER

To bring u.h.f. television to small viewer areas. IBA engineers have de-


Experimental digital vision mixer developed by the IBA. Input and output signals are coded in linear PCM, at a sampling rate of $2 \mathrm{f}_{\mathrm{sc}}$. The mixer offers additive mixing and split-screen wipe facilities
requirements, and they consider there is much in favour of a system combining features of Matrix H and 45 J .

Engineers are able to listen to various types of programme material encoded in QS, SQ, UMX, Matrix H and 45J.


These off-air pictures show the effect of SABRE on CCI which is only 20dB below the wanted signal
signed a low-cost 2 watt transposer. This receives a broadcast and retransmits the programme on another channel. The transposer is small and portable, of suitcase construction and designed with ease of on-site servicing in mind.

## Independent local radio

Since the advent of Independent Radio in 1972. IBA has had an interest in sound radio. More recently, Crawley Court has become involved in a study of "surround sound" or quadraphony. The establishment of standards for broadcasting is important, for those adopted are likely to stand for 20-40 years.

The current investigations are concerned with both subjective and technical problems of this subject and IBA engineers have now come to the conclusion that a " $2 \frac{1}{2}$-channel" type of system best meets these two difficult

## the all-digital studio of the future

As part of a long-term study of digital techniques for television the IBA has developed in experimental form the major component parts needed for an all-digital television studio. All major studio vision operations, excluding picture origination, are carried out using digital video signal processing, including digital vision coding and decoding, vision mixing and switching, video tape recording and the generation of a colour-bar test signal.

The development of an operational digital studio depends on overcoming the problems of recording digital signals on magnetic tape without requiring extremely high-speed tape transport.

The experimental digital recording system developed by the IBA applied to a standard broadcast analogue machine is capable of producing excellent halfwidth colour pictures with a tape consumption half that of conventional

Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

## PRINTING CALCULATORS

Into the very competitive market of small calculators, Hewlett-Packard Ltd. has just launched its first pocket-sized printing calculator. The new HP-10 is a general purpose, pre-programmed "adding machine type" instrument with all proven functions, plus a printer for "hard copy" readout.
Intended for executives and professionals in their business and personal applications, the HP-10 features a 10 digit l.e.d. display, a quiet thermal printer, keys to allow the user to add, subtract or recall data from memory, a data storage accumulator, and automatic decimal point positioning for pounds and pence calculations. This calculator weighs 12 ounces, measures $3.4 \times 6.5 \times 1.6$ in and costs $£ 125$.

Three other new calculators for specialist applications have also been announced by Hewlett-Packard.

Full financial evaluation capabilities with mathematical and statistical functions are embodied in the HP-92 Investor, a portable printing model designed for professional investment analysts, stockbrokers, bankers and other finance executives. The HP-92 has a powerful set of pre-programmed functions to suit these applications. Price $£ 475$.

A new scientific pocket calculator, the HP-29C, is intended for engineers. scientists, technicians and students. Fully merged programming allows as many as


The first pocket printout calculator, type HP-10, from Hewlett-Packard
four keystrokes to be combined into a single step of memory. Programming is further simplified by the 30 storage registers. CMOS memory chips allow programmes and data to be retained in memory for long periods after switch off. Price $£ 149$.

A handheld printing version, the HP-19C, will be available shortly, at approximately $£ 260$.

## ORGAN KIT CATALOGUE

An exciting new catalogue featuring musical instruments, effects units and amplification systems from Wersi Electronic has just arrived. Wersi, a well-known company in Europe, predominantly specialise in electronic organ kits for the do-it-yourselfer. Conceived eight years ago, Wersi has grown considerably. One can only assume that this is as a result of the technical excellence and quality of the products, this is certainly reflected in the detailed, full colour, 104, page catalogue.

The fact that the eight organs available -from smallest spinet to the largest console-have a professional specification would indicate that they are not cheap. For example, at the low end the fully assembled and tested Wersi Orion W1T retails at $£ 4,594$ with VAT to add! At the top end the three manual Galaxis W4 is a heady $£ 18,239$. However, since the range is available in kit form these figures can be dramatically reduced- $£ 1,913$ for the Orion WIT and $£ 5,957$ for the Galaxis.

The organisation of the Wersi kit concept is based on the constructor's requirement to play either entertainment music or more ambitious works. Usually, these ambits are regulated by keyboard size, either 49 or 61 notes.
As an example, to assemble a full biown entertainment organ such as the Orion WIT, a dozen kit packs would be required. These packs are made up of the p.c.b.s, components, wiring harnesses and other piece parts to make up the various organ sub-assemblies, each being labelled for contents.

To mastermind the assembly of a pack an illustrated manual is included with step-by-step instructions and check list so that project realisation can be achieved with maximum continuity.

To defer the cost of a full specification instrument, it is possible to make up a basic organ from any one of the Wersi range and then add to it-money permitting.

To achieve this, kit packs are organised in so-called "option levels" of purchase. Option Level I for the Orion WIT would cost you $£ 906$, consist of 5 kit packs and provide you with an 8 octave multiple wave master generator and dividers, manuals, electronic keyer circuits, drawbars, special effects and cabinet. From this you have a playable instrument. The second option of 4 kit packs would cost another $£ 426$ but would provide pedals, fixed stops, rotating and string choir sounds and other effects.

The final option of 3 kit packs provides piano voices, a 24 rhythms unit, autoaccompaniment and registration memories. This for an extra $£ 581$, but now the instrument is truly complete.

Other instruments, besides organs, in the Wersi catalogue include rhythm units, electronic pianos, string orchestras, PA systems, mixers and rotating sound systems.

Handling Wersi kit sales is Aura Sounds, Dept. P.E., Copthorne Bank, Crawley, West Sussex. Anyone wishing to hear or view instruments or equipment should first ring Mr Griffiths (0342-713338) for an appointment.

The catalogue price is $£ 2$ which is redeemable on purchases.
Another new Catalogue/Order Form worth investigating is the one just released by a new company known as Ace Mailtronix Ltd.

This company, specialising in mail order, lists approximately 500 of the more popular components that the home constructor might require at one time or another. The catalogue is layed out more in the form of an order sheet/s with spaces for inserting quantities required and component costings. This means that the purchaser simply indicates his/her requirements, adds up totals, attaches the relevant sum and posts the order.
It is claimed that delivery is ex-stock, that is to say every order is despatched on the day it arrives at their office. Prices are guaranteed and fixed throughout a preset period of validity of an order form, the form being updated and automatically replaced with each order received.
All products are guaranteed for twelve months from date of purchase since they are all hona fide new products. Items carried include transistors, diodes, i.c.s, resistors, capacitors, switches, lamps, plugs and sockets and technical books.

Copies of the catalogue/order form are available from Ace Mailtronix Ltd., Dept. P.E., Tootal Street, Wakefield, West Yorkshire, WF1 5JR.

## MPU LECTURES ON RECORD

In the May ' 77 issue we reported the success of a microprocessor forum jointly organised by National Semiconductor (UK) Ltd., A. Marshall (London) Ltd., and ourselves. In fact the venture was so oversubscribed a second forum had to be staged.

Fortunately, for anyone who might have missed this baptism in microprocessor technology, the edited lectures have been committed to tape by a team called Specialist Productions. The result is in effect an audio-visual "teach yourself" course as the C60 and C90 cassettes are backed by a 72 page booklet which is keyed to the lectures.

The course components are available from A. Marshall (London) Ltd. (see advertisement in this issue) price $£ 9.95$ which includes VAT and postage.


An 8 -page supplement describing a range of selected integrated circuits, having some particular emphasis in consumer applications. Areas of application include Electronic Musical Instruments, Motor Vehicles, Domestic Control Systems, TV Games, etc. A certain source of inspiration for experimenters and constructors alike

## TUNE-PRIGGRAMMABLE SEQUENCER...

## Digital beaction timer



Displays digitally a subject's reaction time on a scale of 1-9. The time scaling is arbitrary

This unit will enable a synthesiser, e.g. PE MINISONIC, to automatically play a pre-programmed tune consisting of up to 32 pitches in a 128 -note-long sequence from a single RAM. The note length and the rhythmic pattern can be made variable.

PRACTICAL
GLECTRONICS


## 

THis month will see the description of the control circuits and an introduction to the scoring logic, where we deal with the formation of the results for the number of the players' coloured pegs correct for both colour and position (the "P" results).

## THE ENTRY COUNTER

Each single deduction comprises four entries which are counted by the "entry counter" within the machine. A shift register, type SN 7496 N (IC20), is used to perform this function producing four output signals, K, L, M and N .

The 7496 is a five bit shift register, although for this counter only the first four bits are required. The circuit diagram is shown in Fig. 3.1

The depression of one of the clear buttons, $S 7$ or $S 8$, will clear the register, setting all outputs to logical zero and making the serial input, pin 9, logical 1 via the Nor gate IC12c. When the first colour is entered into the machine the $Z$ signal clocks the counter and the first output, K, goes high and the serial input low. The second entry simply steps the logical 1 from position K to position L and so on.

* Mastermind is the registered trade mark of Invicta Plastics Ltd


Fig. 3.1. Control logic circuitry

On the fourth entry output N is high and will remain as such until the next deduction is commenced, the first entry of which will set K high again. The counter is therefore a simple ring counter, so called because a single 1 circulates around through all outputs in succession.

## COMPARISONS COUNTER

The main function of the comparisons counter is to provide the signals $C_{1}-C_{4}$. These signals are generated each time an entry is made so that the code on lines RST is compared with each X code in turn. The results of these comparisons appear on the E lines and it is the function of the scoring logic to interpret these and produce from them the correct scores for display to the player.

In addition to these four signals, the counter generates $\mathrm{C}_{0}$, the pre-clear pulse, and $\mathrm{C}_{5}$ which performs computational duties in the scoring logic.

A shift register is used, the six bits being obtained from one half of a type SN 7474 N (IC16b) dual flip-flop $\left(\mathrm{C}_{0}\right)$, and an SN7496N five bit shift register, IC17 ( $\left.\mathrm{C}_{1}-\mathrm{C}_{5}\right)$. The whole counter is enabled by the Z signal, connected to its clear inputs, pin 13 of IC16b and pin 16 of IC17. Note that all the flip-flops are cleared by logical zeros.

When an entry is made the Z signal is generated and is used to clock flip flop IC16a sending its output, pin 5, to logical 1. This output is connected to pin 12, of IC16b, the first stage of the register, whose output, pin 9, will go high when it is clocked by the master clock, forming the signal $\mathrm{C}_{0}$. However, $\mathrm{C}_{0}$ will promptly clear IC16a, via IC11b, so that pin 12 of IC16b is from then on 0 . A single string of 1 s therefore appears on the outputs $C_{0}-C_{5}$, as shown in the timing diagram of Fig. 3.2.

## SERVICE SIGNALS

The various service signals required are produced by 1C19 and 27 . $\overline{\mathrm{KC}}_{0}$, IC19 pin 3, is the pre-clear signal, serving to clear the scoring logic of the results of a previous


Fig. 3.2. Timing diagram for the comparisons counter


Main board assembly order


Fig. 3.3. Illustrating the clock phasing


Fig. 3.4. The "P" flip flops and associated logic
deduction, thus obviating the need to manually clear the logic after each deduction. This is combined with the external clear signal, RSG, to form:

$$
\overline{\mathrm{R}}_{\mathrm{L} 2}=\overline{\mathrm{KC} C_{0}+\mathrm{RSG}}
$$

which is the equation for the unconditional reset of the logic.
A single $S N 7400 \mathrm{~N}$ NAND gate has a maximum permitted fan-out of ten, but since $R_{L 2}$ will be applied to more than this number of inputs two 7400 gates are used in parallel to provide a fan-out capacity of twenty inputs.

## INTRODUCTION TO THE SCORING LOGIC

As hinted earlier, the function of the scoring logic is to interpret the results of the comparisons, appearing on the E lines, in relation to the timing signals produced by the entry and comparison counters. We now consider how the " $p$ " results are derived from this information.

If the first entry made by a player is equal to the first code, X 1 , within the generator, then this particular entry is obviously correct for both colour and position. Similarly if the player's second entry is equal to the second code, X 2 , then this too is correct for colour and position, and so on.

Since $C_{1}$ from the comparisons counter calls the first $X$ code, X1, for comparison, and since the player's first entry is recognised by the fact that signal K from the entry counter will be high, then a flip flop clocked during $\mathrm{C}_{1}$ and presented with data KE will set if the entry is correct for position and colour. A second flip flop clocked during $\mathrm{C}_{2}$ and presented with input LE will record the second " $P$ " result, and similarly for the third and fourth entries.

## THE "P" FLIP FLOPS

These four flip flops are called the "P'" flip flops and are 'shown in Fig. 3.4 as IC22 and 23.

The " $K$ " inputs to these are not required, and are therefore grounded. The equations for the " J " inputs are $\mathrm{J}_{\mathrm{P}_{1}}=\mathrm{KE}$, $\mathrm{J}_{\mathrm{P} 2}=\mathrm{LE}, \mathrm{J}_{\mathrm{P} 3}=\mathrm{ME}$ and $\mathrm{J}_{\mathrm{P} 4}=\mathrm{NE}$, these functions being implemented using the and gate of IC21.

An example illustrating the action of these flip flops is shown in Table 3.1.

## CLOCK PHASING

Flip flop $P_{1}$ is not clocked directly by $C_{1}$, nor is $P_{2}$ by $C_{2}$, etc. The reasons for this stem from the existence of time delays in the gates of the data selector and the comparator, which if not considered would lead to errors in the functioning of the system. The signal $C_{1} \bar{C}$ is therefore used to clock $P_{1}$, being related to $C_{1}$ as shown in Fig. 3.2. Fig. 3.3 is an example showing just how this derived or "phased" signal overcomes the time delay problem. As shown in the diagram $\mathrm{E}=1$ during $C_{2}$, so that $P_{2}$ would set if $L$ were present. If $C_{2}$ had been used for the clocking, then the $E=0$ overlapping from the $C_{1}$ interval would be responsible for setting $P_{2}$ to 0 , producing an error in operation. The phased clock ensures that the data input to the flip flop is absolutely stable for the entire duration of the clocking period ( $\mathrm{C}_{2} \overline{\mathrm{C}}$ in this case).

Notice that when no entry has been made $E=1$. This is of little consequence, however, since E will remain high until an untrue comparison is made.

IC18 produces the phased clocking signals, this being an SN7408 and package.

## "P" INDICATOR LINE

Consider the combination of X codes and first entry shown in Table 3.2. We see that entry one is " $P$ " correct. However,

$P_{1}, P_{3}$ and $P_{4}$ are set indicating that three colours are correct for position
it is also correct for colour and incorrect for position ("' $I$ " correct) with two of the remaining X codes, X 2 and X 3 . The correct score for this entry is just one black key peg, so that the comparisons with X2 and X3 must be disregarded. A strobe signal, to be called Pl , is therefore formed to indicate to subsequent logic that the first entry is " P " correct, and as such the comparisons made between it and X2 and X3 may be disregarded. Clearly the function $\mathrm{PI}=\mathrm{K} \mathrm{P}_{1}$ will serve here for the first entry, so that for all four entries the function is

$$
\mathrm{PI}=K \mathrm{P}_{1}+L \mathrm{P}_{2}+M \mathrm{P}_{3}+N P_{4} .
$$

IC30 produce the inverse of this, $\overline{\mathrm{PI}}$, shown in Fig. 3.4.

## RESULT ADDERS

The number of " $P$ " flip flops which are set indicates the " $p$ " result and this number is formed by an adder. The circuit is shown in Fig. 3.4, and ICs 24 and 25 take the four "P" outputs and produce from them two individual two bit sums,

| Table 3.2 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Entries |  | X Codes |  |  |
|  | $\begin{gathered} \text { Red } \\ \times 1 \end{gathered}$ | $\begin{aligned} & \text { Red } \\ & \text { X2 } \end{aligned}$ | $\begin{aligned} & \text { Red } \\ & \times 3 \end{aligned}$ | $\begin{gathered} \text { Blue } \\ X_{4} \end{gathered}$ |
| Red | $\begin{array}{r} \mathrm{K}=1 \\ \mathrm{C}_{1}=1 \\ \mathrm{E}=1 \\ \mathrm{P}_{1}=1 \end{array}$ | $\begin{aligned} \mathrm{K} & =1 \\ \mathrm{C}_{2} & =1 \\ \mathrm{E} & =1 \\ \mathrm{PI} & =1 \end{aligned}$ | $\begin{array}{r} \mathrm{K}=1 \\ \mathrm{C}_{3}=1 \\ \mathrm{E}=1 \\ \mathrm{PI}=1 \end{array}$ | $\begin{aligned} \mathrm{K} & =1 \\ \mathrm{C}_{4} & =1 \\ \mathrm{E} & =0 \\ \mathrm{PI} & =1 \end{aligned}$ |
| Green | $\begin{aligned} \mathrm{L} & =1 \\ \mathrm{C}_{1} & =1 \\ \mathrm{E} & =0 \\ \mathrm{PI} & =0 \end{aligned}$ | $\begin{aligned} \mathrm{L} & =1 \\ \mathrm{C}_{2} & =1 \\ \mathrm{E} & =0 \\ \mathrm{P}_{2} & =0 \end{aligned}$ | $\begin{aligned} \mathrm{L} & =1 \\ \mathrm{C}_{3} & =1 \\ E & =0 \\ \mathrm{PI} & =0 \end{aligned}$ | $\begin{aligned} \mathrm{L} & =1 \\ \mathrm{C}_{4} & =1 \\ \mathrm{E} & =0 \\ \mathrm{PI} & =0 \end{aligned}$ |
| $P_{1}$ is set, so that the score at this stage is one black key peg |  |  |  |  |



Fig. 3.5. Prototype components layout for the middle third of the main board (section $\mathbf{B}$ in photograph). To assemble this constructors should work from the circuit diagrams
which are subsequently added together by IC26, a type SN7482N adder package.

The mode of operation is as follows. The and gate whose output is pin 11 of IC24 and the exclusive or gate whose output is pin 6 of IC25 together form one of the two bit adders, producing the binary sum $\mathrm{P}_{1} \mathrm{QP}_{2}$. IC24, pin 8 , and IC25, pin 3, produce the second two bit sum $\mathbf{P}_{3}+\mathbf{P}_{4}$. These sums are then added together by IC26, producing the three bit sum $P_{1}+P_{2}+P_{3}+P_{4}$. An identical adder serves for the " 1 " results; this is also shown in Fig. 3.4.

## CONSTRUCTION

The main board positions of the i.c.s are shown in Fig. 3.5, and with reference to the circuit diagrams of Figs. 3.1 and 3.4 the wiring is very straightforward and is carried out using single cored wire on the blank side of the board.

The timing, reset and equality signals are connected to a large number of i.c.s, some of which have yet to be described, It is for this reason that the most important of these are wired to a signal bus, organised as in Fig. 3.5. The connections to the i.c.s from this bus may then be made using wires of a recognisable colour particular to each different signal.

The six outputs from the "P" and "I" adders are taken to six Veropins, as shown in Fig. 3.5, ready for connection to the display board later.

## TEST SCHEDULE

The timing circuits are to be tested first as these are required in order to test the rest of this month's construction.

## (a) The Entry Counter

This is readily checked as follows. Press either $\mathbf{S 7}$ or S8 and using a logic probe or a voltineter (d.c.) verify that all outputs of IC20 are at logical 0 . Then using the colour entry buttons, check that the counter operates. (Note that a logical 0 will be in the region of 0.5 V and a logical 1 approximately 3.5 V .)

## (b) The Comparisons Coumter

An almost static test may be performed on this by slowing down the master clock, IC2, to a frequency below 1 Hz . This is achieved by placing a large electrolytic capacitor (approximately $4,700 \mu \mathrm{~F}$ ) temporarily in parallel with $\mathrm{C}_{3}$ of IC2. This clock will now be slow enough to enable the counter to be monitored using a d.c. voltmeter and Fig. 3.2 verified.

With the master clock operating so slowly it will be necessary to hold an entry button depressed for about ten seconds in order to give all the signals from this counter a chance to appear.

Verify that $\overline{\mathrm{R}}_{\mathrm{L}_{2}}$ goes low whenever a first entry or one of $\mathbf{S 7}$ or $\mathbf{S 8}$ is activated.

## (c) The Flip Flops

Still keeping the clock running slowly, monitor and record the four X codes in ICs 3-6. Now, after clearing with $\mathbf{S} 8$, enter the first of these codes using the colour entry buttons, holding the button down until $P_{1}$ sets. Enter the three remaining codes, after which all flip flops should be set. The output of the "P" adder, 1C26, should be $100(4)$ on pins 10,12 and 1 respectively, indicating that the entries were all correct for position.

Variation may be introduced by entering four colours, only two of which are correct for position, so that IC26 gives 010, etc.

In all these cases the PI signal, IC30 pin 8, should go low, whenever an entry correct for colour and position is made, following the setting of the respective " P " flip flop.

If these tests are successful restore the master clock to full speed and play a crude version of the game! Note that with the clock frequency of approximately 22 kHz the machine takes only $6 / 22000 \mathrm{sec}$. to produce signals $C_{0}-C_{5}$, so that even the shortest manual press on an entry button will allow plenty of time!
NEXT MONTH: Scoring logic continued.


## EMPLOYMENT

Engineers' salaries have been creeping up during the year. Far more jobs are being advertised with salaries up to 25,000 with the occasional breakthrough to $£ 6,000$ or even more in special cases. But there are still far too many homebased jobs round the $£ 3,000$ mark which, at today's income levels, is almost an insult to qualified engineers.

If you can put up with the climate the best financial openings are still in the Middle East where senior people can earn over $£ 10,000$ tax free and tech. nician engineers $£ 7,500$ tax free plus a whole range of fringe benefits. Scanning through the classified columns the demand for engineers looks as high as ever it has been, but employers are clearly being selective.

How salary rates for engineers will fare during the next few months of free-for-all wage bargaining is anyone's guess but my own view is that engineers will fall back again relative to manual workers who are looking for a norm of $£ 5,000$ in those industries where militancy is rampant.

## PRIVATE SECTOR LAGS

It will be interesting, too, to see whether private sector employees will recover their position relative to those in the public sector. The last IEE salary survey showed that qualified electronic engineers in the public sector were ahead by about $£ 1,000$ a year. And according to the Society of Electronic and Radio Technicians, technician engineers in the nationalised industries were £300 a year better off than their colleagues in the private sector.

It seems odd that while concern is being expressed that fewer and fewer of our young are showing interest in the engineering professions that the IEE,
whose membership has increased at an average of 650 over the past few years has, for the past two years enjoyed a gratifying increase. Last year it shot up by 2,000 and this year it was 1,200. Total membership is now about 70,000 .

This year the IEE's Career Consultancy Service is available to all members. The Service is to run on a pilot scale for two years. If successful and there is a genuine need it will be expanded.

## WHAT'S GOOD FOR GEC ...

To paraphrase the old tag about General Motors we might say what's good for GEC is good for Britain. Sir Arnold Weinstock has done it again. Turnover for the first time has topped £2,000 million and pre-tax profits are up. Export orders nearly doubled to £936 million and GEC has a healthy cash balance in the bank of nearly £500 million.

To keep the pot boiling GEC has started a management game in which all the companies in the group are invited to enter teams. The game starts in September and runs through to next April. HQ of the game is GEC's management college at Dunchurch and each team's management decisions will be processed by GEC Midlands Computer Services Ltd. The idea is to get everyone in the group motivated towards business and what it's all about. Not every employee can directly participate but it is hoped that all will follow the fortunes of their own team.

## THE TIDDLER

Compared with GEC, Racal is a comparative tiddler, but their annual report shows another year of dramatic growth. In 1972 turnover was $£ 21$ million with just over £3 million pre-tax profit. The 1977 figures are $£ 122$ million and $£ 32$ million. The forecast for $1977 / 78$ is for a turnover of $£ 200$ million. The company was formed in 1950 with a capital of £100.

Hard on the heels of Racal's acquisition of Milgo in the United States, Racal recently purchased Hellerman Cassettes Ltd from the Bowthorpe-Hellerman Group. Purchase price was $£ 825,000$ and the acquisition puts Racal-Zonal, the magnetic tape company, firmly in the cassette market.

Don't be surprised if other acquisitions by Racal are announced soon. Plessey, tipped as a possible, is defensive. Chairman Sir John Clark points out that Plessey is doing very nicely, thank you, with sales and profits both up and a record $£ 600$ million order book. While Sir John has been adamant that Plessey has not been talking to the National Enterprise Board, he is coy on the subject of discussions with other private enterprise companies.

## CB RADIO

The lobby for Citizens Band two-way radio has been as active as ever. The prize for industry is said to be a home market in the UK of $£ 100$ million and the creation of 5,000 jobs. The penalty could be unholy pollution of the frequency spectrum. The entrepreneurs are looking enviously at the United States where over 20 million CB radios have been sold and they are looking for a similar bonanza in Europe. Even in the UK, cramped for space and not exactly overflowing with spare cash in the citizens' pockets, exponents of $C B$ are suggesting that UK sales could top a million units a year within two years of the service being sanctioned.

Heady figures, indeed. But it has already been pointed out that there is no guarantee that the sets would be made in UK. The Japanese are already old in the tooth at supplying the American market in which they are the dominating force with over 100 companies in the business. But, says the UK CB lobby, our sets will not be cheap junk on 27 MHz . We are aiming at 230 MHz with 44 channels and the sets will cost between $£ 100$ and $£ 200$ each.

Anyone with an atom on sense knows that the Japanese can and will supply the market whatever the technical specification. In principle, there is no reason why the UK should be denied CB radio. But its possible benefits to the UK electronics industry or to the citizen user needs more consideration than the bandying about of hypothetical statistics.

## MPU INSTRUMENTS

Dr Colin Gaskell, technical manager of Marconi Instruments, warns of gimmickry in the use of microprocessors in instruments. He said at an SERT symposium at Keele University that few instruments are as yet making effective use of them. But he added that commercial security could well be keeping some exciting applications under wraps for the time being. He also warned that MPU technology is moving so fast that those now being used in some instruments may well be obsolete quickly and a buyer may have trouble in getting replacements if they fail in service.

## PO SUCCESS

How nice to be able to congratulate the Post Office. 1 refer to the contract worth $£ 6.75$ million for assistance in planning the Libyan trunk telephone cable network. The contract is especially welcome because it is a repeat order from the same customer and ten times the size of the first consultancy order (worth $£ 650,000$ ) announced iust over a year ago.

## AUTOMTIO SWITH: OFF

A system for automatically switching off a radio or TV receiver when transmissions cease has been patented in BP 1471 585, by Ashok Jain of New Delhi, and Bindu Gandhi of Bombay.

The object is to overcome the disadvantage of simple systems which may shut off the receiver whenever there is a temporary absence of audio or video signal but the transmitter is still on-air, e.g. during extended fadeouts. This is achieved by detecting the presence or absence of i.f. or r.t. signals rather than audio or video.

In the simplest circuit (Fig. 1) a d.c. amplifier receives rectified d.c. con-

Copies of Patents can be obtained from:
the Patent Office Sales, St. Mary Cray, Orpington, Kent Price 95p each

sisting of i.f. or r.f. signals from a receiver, and is connected to power source $S$ through S1. The output of the amplifier supplies the coil of relay RLA1, the relay contact being closed when the coil is not energised. The receiver is thus normally connected to power source $S$ via RLA1.

To provide for automatic shut-down of the receiver when transmissions cease, switch S 1 is closed to activate the amplifier. This is biased so that it has no output whilst still receiving r.f. or i.f. from the receiver. Thus, when the supply of i.f. or r.f. ceases, the amplifier powers the relay to open the contact RLA1 and disconnect the receiver.

Clearly the disadvantage of this simple system is that the amplifier continues to draw current after it has switched off the receiver. This disadvantage is overcome in another circuit where the relay is again energised by the amplifier only in the absence of i.f. or r.f., but is a latched relay. Details are also given for a circuit which switches off the d.c. amplifier and receiver from the power source.

## TVMIN Display <br> 8P 1444080

The Japanese company Matsushita, which makes National Panasonic and Technics equipment, patents (in BP 1454060 ) a novel approach to the digital display of tuning on a radio receiver. The idea behind the invention would also appear to have wider applications, for instance in the display of clock times.

The receiver tuning capacitor is connected by gears to a drum over which runs a continuous loop of opaque paper or photographic film. Thus as the receiver is tuned, the loop advances.

The loop carries a sequence of radio frequencies, encoded as sets of indicia, generally elongated perforations or clear patches of film. To encode three numbers: each set of indicia consists of three columns, and there are seven positions in each column.

A light source is arranged inside the loop and a sensing head lies opposite the light source on the other side of the loop material. The sensing head is laced with the ends of a bundle of optic fibres, the other ends of the fibres being laced into a display panel with three 7-bar numeral windows each in the form of a squared figure-of-eight. In dependence on the indicia positions
on the loop, various combinations of the display bars are illuminated by light picked up from the source to form digits between zero and nine. In this way, any 3 -digit number can be represented, to denote the frequency of a received broadcast station.

Ideally another light source is arranged opposite a photo cell, with the light path between this source and cell interrupted by a set of control perforations on the loop. The photo cell controls illumination of the main light source by solid-state switching circuitry. This ensures digital display only when the loop is in a position fully suited for display, i.e. when all three digits of a tuning frequency are correctly aligned with the optic fibres.

## IV 14 in

BP 1471 369—Yazaki Sogyo KK: Taximeter. An indication of future development trends for taxicab fare meters. A device similar to a calculator, with a single display and single keyboard panel, integrates control pulses (on engine speed and distance travelled, etc.) and stores them in a memory. The stored information can be called up by
the keyboard and each item of data (fee for last fare, total fee for the day, miles travelled without fares) displayed on the readout window as required.

BP 1471 508-Messrs Leclercq, Poirier \& Guichard: Picture-phone Communication System. An elaboration of the basic picture telephone concept (with a camera and monitor at each end of the phone line), which enables a person being called to decide whether or not to accept the incoming call.

Pulses characteristic of the incoming call origin trigger a character generator to signal the incoming caller's identity before the called party picks up his telephone to accept the call and energise his camera.

BP 1466 902-Nissan Motor Co. Ltd.: Gear Interlocking Device. This is another electronic system providing electro-mechanical interlock between functions essential to operation of a car (this time a manual gear change lever) and the seat belt latches.

A weight-sensitive switch under the driver and passenger seats is interlocked with switches associated with the seat belts. In a "no go" condition (i.e. driver or passenger sitting without seat belt fastened) solenoids operate to jam the gear lever controls.

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> 100 MFD 350 V d.c. 90 p ; 200 MFD 350 V.d.c. $00 \mathrm{p} ; 100+400+32$ MFD 275 V d.e. $80 \mathrm{p} ; 100+400$ $\begin{aligned} & +16 \text { MFD } 275 \mathrm{~V} \text { d.c. } 0 \text { Pp; } 150+150 \text { MFD } 350 \mathrm{~V} \text { d.c. } 60 \mathrm{p} ; 150+150+75 \text { MFD } 350 \mathrm{~V} \mathrm{d.c.} 60 \mathrm{p} ; 100 \\ & +100+200+300 \text { MFD } 275 \mathrm{~V} \text { d.c. } 60 \mathrm{p} ; 100+300+200275 \mathrm{~V} \text { d.c. } 60 \mathrm{p} ; 400+200+75+32\end{aligned}$

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## MULTICHANNEL OVERLOAD PROTEGTOR



## Fig. 1

WHERE it is necessary to supply circuitry with several differen voltages at various currents, overload protection can become quite complex. A simple fuse will rarely act quickly enough to protect delicate components

Conventional electronic protection circuits rely on a voltage developed across a sensing resistor by the error current turning on a transistor or thyristor, the latter turning off or reducing the output of the power supply

Fig. 1 illustrates a somewhat unconventional approach to overload protection. It is fairly fast-acting, effective from a few milliamps upwards, is easily added to existing circuitry, and provides complete isolation between the different supplies monitored

When excessive current is drawn by a monitored circuit, the voltage across
a sensing resistor, $R_{s}$, rises above 2 V This causes a parallel wired l.e.d. to light up, the current through the latter being limited by R3. All l.e.d.s are directed on a light dependent resistor, which triggers a thyristor when illuminated, RI limiting the thyristor gate current. The s.c.r drives a relay. Its normally closed contacts (one set for each monitored circuit) open, cutting off all power supplies. A lamp in series with the relay lights up, indicating that the trip has operated. The power supplies remain isolated until reset by S1

The device may be powered by its own supply, or from one being monitored. R2 is selected to limit the relay/lamp current to a suitable level

The minimum operating voltage is about 12 V , at which level R 2 is omitted

The l.e.d.s. are mounted pointing at R4, and it is essential to exclude
extraneous light from this vicinity The number of channels which can be monitored is limited by the number of l.e.d.s it is possible to direct upon the l.d.r., and the sets of relay contacts.
$\mathbf{R}_{\mathrm{s}}=$ ( 2 V divided by required max current) ohms.

Power rating $=\mathrm{R}_{\mathrm{S}} \times$ (max. current ${ }^{2}$
$\mathbf{R}_{\mathrm{S}}$ may be replaced by a fixed and variable resistor, allowing a continuously variable current limit. A wirewound potentiometer of ample rating is essential in this case. A typical set-up would be a 4 ohm 1W resistor and a 250 ohm 1 W potentiometer
B. Woodland,

Harlesden, London

# MAINS TOUCH SWITCH 



T
The circuit diagram of Fig. 1 is for a touch switch offering the advantages of negligible current consumption, complete mains isolation, high noise immunity, great sensitivity and low cost.
The heart of the circuit is a CD4016 or CD4066 integrated circuit containing four independent смоs bilateral switches. Only half of the i.c. is used, but the other half could be incorporated into another channel.
One bilateral switch is wired with positive feedback to form the ON/OFF
latch. The other is used to drive the unijunction transistor oscillator feeding high frequency pulses via isolating transformer T1 to the gate of the Triac. The transformer used in the prototype was made by winding 50 turns of $30 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. enamelled copper wire for each winding, on a 25 mm length of ferrite rod. The two windings must be well insulated from each other. The capacitors across each touch plate should be between 3.3 and 10 nF . Component values are far from critical.
With the 40669 Triac shown, loads of
up to 2 kW can be handled with an adequate heat sink. Without a heat sink, 250W is the limit. If the Triac will not turn on, more turns may be needed on the secondary winding of TI .

The 9 volt battery shown may be replaced by a small battery eliminator if required. The voltage is not critical and may range between 9 and 15 volts.
S. M. Fifield,

Twickenham.

## FLASH TRIGGER

The circuit in Fig. 1 is a multi-mode flash trigger which can be operated as a slave flash, sound operated flash, or simply as an independent flash for nonsynchronised cameras.

Phototransistor TR1 is an OCP71, which can be switched either by the incident light on its junction, or by a voltage at its base. In either case the collector voltage rises causing TR2 to turn on, whose emitter then rises to almost supply voltage, thus firing CSR1 via R3. The voltage at the base of TR2 can be varied by VRI, and this therefore acts as a sensitivity control. To determine the optimum setting, the unit can be operated with S2 set to turn on LPJ instead of firing the flashgun. Limiting resistor R1 protects TR1 from excessive collector current, and similarly R3 protects the gate of CSR1. Resistor R5 is a voltage dropper to enable a $6 \mathrm{~V}, 60 \mathrm{~mA}$ bulb to be used for LP1.

When the unit is used in the soundtriggered mode, the crystal microphone will generate sufficient voltage (via C2) to switch TRI on, without the need for

intermediate amplification. Used in the independent mode, $\mathrm{S} \mid$ acts as the trigger button, by applying bias directly to TR2; this operation of course being irrespective of the sensitivity setting.
It is suggested that the microphone is connected via a phono socket so that it may be unplugged when the unit is being used in the light-triggered slave mode. The output should be connected via a proper flash plug and socket, which will be available from most photographic dealers.

Normally, both electronic and bulb flashguns use the inner terminal of the flash socket as the positive pin, but it may be best to check first using a multimeter.

The sound-triggered flash has many intriguing applications. Photographs may be taken of glass at the instant of shattering, or a champagne cork leaving the bottle, and many other sudden events too quick for the eye to see.
G. Stokes,

Walsall

## NEWS BRIEFS

## Intelligent Instruments

THE effect of the microprocessor on electronic instruments, both present and future, was one of the principal topics at the recent symposium on Electronic Measurement and Instrumentation, organised by the Society of Electronic and Radio Technicians at Keele University. The event attracted a broad spectrum of instrument manufacturers and users, plus the fine weather which has become almost a tradition for SERT symposiums.

The first benefit of the microprocessor has been the simplification of front panel layouts. Placing of controls is far less subject to mechanical constraints, and scaling factors can be taken care of "behind the panel" without resort to cams, linearising adjustments, etc. Digital readout of output level on a signal generator, for example, can be switched to provide the data in volts, microvolts, $\mathrm{dBm}, \mathrm{dBV}, \mathrm{d} \mathrm{B} \mu \mathrm{V}$ or whatever, according to what the test schedule for a particular piece of equipment calls for, with a great reduction in the mental arithmetic required, and in control markings.

The future of the microprocessor, as seen by at least one manufacturer, was the replacement of the presently used minicomputer in a "universal measuring system". Not quite universal; the one restriction is that what you want to measure must be capable of being displayed on the screen of an oscilloscope

The displayed waveform-be it repetitive or transient-is digitised and stored in memory. Then any desired characteristic of the waveform can be computed, be it frequency, period, amplitude, peak or average power, rise or fall time, etc., and displayed
in whatever form and units required. As one speaker remarked, this could make a lot of other instruments obsolete.

In general, it was thought that many instruments of the future would be of the form: a alogue to digital converter; digital processor; digital to analogue converter or other output interface; display

Incidental benefits of using a microprocessor are that it can be used to carry out continuous calibration checks and adjustments, and to diagnose fault conditions, within the instrument, so reducing the m.t.t.r. (mean time to repair). From telling you what repairs are required, the next stage is presumably an instrument that can repair itself!

## SERT Microprocessor Symposium

Readers who have a particular concern in the applications of microprocessors will be interested to learn that a residential symposium, entitled "Microprocessor Systems and Software", has been organised by the Society of Electronic and Radio Technicians for the 26 to 29 September, at the University of Kent.

Five technical sessions are being arranged. The first will be an introduction covering the terminology, the ranges of microprocessors available, the principles of a working system and the need for software. The second will be on basic programming techniques and aids including an overview of programming, machine codes, assemblers and loaders, PROM programming, program development aids, system testing and documentation of software.

These will be followed by three other sessions giving examples of working systems in use, the papers here outlining the original requirements and the associated hardware and software solution.

The papers will be presented by experts from research departments in industry and universities, the Post Office, and research associations. There will be discussion sessions, an associated exhibition, and a full social programme.

Further details and registration forms can be obtained from the Secretary, Society of Electronic and Radio Technicians, Faraday House, 8-10 Charing Cross Road, London, WC2H 0HP

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| AC126 | 0.25 | BD132 | 0.54 | OA7 | 0.55 | 1N4006 | 0.15 | 7412 | 0.26 |
| AC127 | 0.25 | *BD135 | 0.35 | OA10 | 0.55 | 1N4007 | 0.15 | 7413 | 0.45 |
| ${ }^{\text {AC128 }}$ | 0.25 | *BD136 | 0.39 | OA47 | 0.14 | 1N4009 | 0.15 | 7416 | 0.40 |
| AC141 | 0.20 | *BD137 | 0.37 | OA70 | 0.30 | iN4148 | 0.07 | 7417 | 0.40 |
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| AC187 | 0.25 | BD181 | $1 \cdot 38$ | OA91 | 0.08 | 1S929 | 0.08 | 7427 | 0.35 |
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| ACrzo | 0.65 | BDX32 | 2.25 | OA211 | 0.75 | 2N696 | 0.25 | 7437 | 0.42 |
| ACY21 | 0.65 | BDY20 | 1.42 | OAZz00 | 0.85 | 2N697 | 0.18 | 7438 | 0.37 |
| ACY39 | 1.00 | BDY60 | 0.75 | OAZ201 | 0.65 | 2N698 | 0.30 | 7440 | 0.22 |
| AD149 | $0 \cdot 70$ | BF115 | 0.39 | OAZ206 | 0.65 | 2N705 | 0.80 | 7441AN | 0.92 |
| AD161 | 0.75 | BF152 | 0.25 | OAZ207 | 0.65 | 2N706 | 0.12 | 7442 | 0.78 |
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| AF115 | 0.25 | BF160 | 0.30 | OC23 | 2.75 | 2N1132 | 0.28 | 7453 | 0.20 |
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| AF117 | 0.25 | BF173 | 0.39 | OC25 | 0.90 | 2N1303 | 0.37 | 7460 | 0.20 |
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| AF186 | 1.50 | BF178 | 0.45 | OC28 | 2.00 | 2N1305 | 0.45 | 7472 | 0.36 |
| AF239 | 0.45 | ${ }^{\text {BF }} 179$ | 0.45 | OC29 | 2.00 | 2N1306 | 0.50 | 7473 | 0.36 |
| AFZ11 | 2.75 | BF180 | 0.45 | OC35 | 1.50 | 2N1367 | 0.50 | 7474 | 0.40 |
| AFZ12 | 2.75 | BF181 | 0.45 | ${ }^{\circ} \mathrm{C} 36$ | 1.50 | 2N1308 | 0.60 | 7475 | 0.59 |
| ASY26 | 0.45 | 8Fi82 | 0.45 | 0 O 41 | 0.50 | 2N1309 | 0.80 | 7476 | 0.42 |
| ASY27 | 0.50 | BF183 | 0.45 | OC42 | 0.50 | 2N1613 | 0.33 | 7480 | 0.80 |
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