# PRACTICAL <br> E=CTRONICS 

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Total Building costa 4430 P. \&P + Ina. 50 p
E.V.6. Case and looky as above. 6 Transistors 3 diodes. Powered by $9 \mathbf{V}$ battery. Ferrite rod aerlal 3in. loudspeaker, etc. MW/LW coverage. Push/Pull output.
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54. 5 P. \& P. + Ins. 65 p

3 tunable Wave-
and trawle band.
7 stages, 5 tranais.
tors and 2 dlodes,
supersensitive ferrite rod
aerial, attractive black and
gold case. Size
3 3. in approx.
Complete kit of parts including construction plans
Total
ilding Costs:
Total Buzlding costs
E.V.7. Case and looks as above, 7 Tranaistors and 3 diodes. Alx wavebands, MW/LW, Trawler Band SW1 sw2, sW3, powered by 9 V battery. Push pull output Telescopic aerial forshort waves. 3in. Loudapeaker. All parts including Case and Plans. Total Building Costs $\{0 \cdot 3$ P. \& P. + Ins. 65 P

## EIECTRONIC CONSTRUCIION KITS

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battery (not supplied with kit).
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Complete kit of parts $\mathbf{£ 7} \cdot 15$ P.P. and Ins. 55p

E.C.K. 4

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* Electronic Noise Generator
t Electronic Metronome
* 4 Transistor Push/Pull Amplifier

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| 2N696 | 0.22 | 2N3391A | 0.29 | 2N5296 | 0.48 0.50 | AF125 | 0.30 | BC239 | 0.15 | BF179 | 0.43 | LM710 | 0.47 | SL414A |  |
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| 2N698 | 0.82 | 2N3393 | 0.15 | 2N5457 | 0.29 | AF127 | $0-28$ | BC253 | 0.25 | BF189 | 0.36 | LMP41 |  | SL611C |  |
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| 2N706 | 0.14 | 2N3402 | 0.18 | 5459 |  | AF186 | 0.46 | BC258 | 0.18 | BF183 | 0.35 | 8DIL | 0.40 | SL620C |  |
| 2N706A | 0.16 | 2N3403 | 0.19 | N5492 |  | ${ }^{\text {A }}$ F 200 | 0.65 | BC259 | 0.17 | BF184 | 0.30 | 14DIL | 0.38 | SL621C |  |
| 2N708 | 0.17 | 2N3414 | $0 \cdot 20$ | 2N5494 | 0.58 | AF239 | 0.65 | BC261 | 0.25 | BF185 | 0.30 | LM747 | 1.05 | SL623 |  |
| 2N709 | 0.42 | 2N3415 | 0.21 | 2N5496 | 0.61 | AF240 | 0.90 | BC262 | 0.22 | BF194 | $0 \cdot 12$ | LM148 |  | SL640C |  |
| 2N711 | 0.50 | 2N3416 | 0.24 | 2N5777 | 0.45 0.45 | AF279 | 0.70 | BC263 | 0.25 | BF195 | $0 \cdot 12$ | 8DIL | 0.44 | SL641C |  |
| 2N718 | 0.23 | 2N3417 | 0.29 | 2N6027 | 0.45 0.73 | AF280 | 0.79 | BC300 | 0.38 | BF196 | 0.13 | 14DIL | 0.41 | SN76003N |  |
| 2N718A | 0.28 | 2N3440 | 0.59 | 3 N 128 3 N 139 | . 42 | AL102 | 1.00 | BC301 | 0.34 | EFF197 | $0 \cdot 15$ | LM3900 | 0.81 | SN76013N |  |
| 2N720 | 0.57 | 2N3449 | 0.97 | 3N139 3N140 | 1.42 1.00 | AL103 | 1.00 | BC302 | 0.24 | 8F198 | 0.18 | LM7805P | 1.60 | SN76023N |  |
| 2 N 914 | 0.22 | 2N3442 | 1.40 0.15 | 3N140 3N141 | 1.00 0.81 | BC107 BC108 | 0.14 0.14 | BC 303 BC 307 | 0.54 | BF200 | 0.40 | LM7812P | 1.60 | SN76033N |  |
| 2N916 | 0.28 | 2N3638 | 0.15 0.15 | 3N200 | 0.81 2.49 | BC108 | 0.14 0.15 | BC 307 BC 3084 | 0.17 0.15 | BF2254 | 0.23 | LM7815P | 1. 60 | ST2 |  |
| 2 N 918 | 0.32 0.25 | 2N3638A | 0.15 0.27 | 3N200 40361 | 2.49 0.40 | BC109 BC113 | 0.15 0.15 | BC308A BC309C | 0.15 0.20 | BF244 | 0.21 0.45 | LM7824P | 1.80 1.50 | TA4263 |  |
| 2N929 2N930 | 0.25 | 2N3639 2N3641 | 0.27 0.17 | 40361 | 0.45 | BC113 BC115 | 0.15 0.17 | BC309C BC317 | 0.20 0.12 | BF245 | 0.45 0.50 | MC1303 | 1.50 2.50 | TAA300 |  |
| 2N1302 | 0.26 0.19 | 2N3702 | $0 \cdot 12$ | 40363 | . 8 | BC116 | 0.17 | BC318 | 0.12 | BF247 | 0.65 | MC1330P | 2.50 0.90 | TAA350 |  |
| 2N1303 | 0.19 | 2N3703 | 0.13 | 40389 | 0.46 | BC116A | 0.18 | 8C337 | 0.20 | BF254 | 0.10 | MC1351P | 0.80 | TAA611C |  |
| 2N1304 | 0.26 | 2N3704 | . 15 | 40394 | 0.56 | BC117 | 0.21 | BC338 | 0.20 | 8F255 | 0-19 | MC1352P | $0 \cdot 80$ | TAA621 |  |
| 2N 1305 | 0.24 | 2N3705 | . 15 | 40395 | . 65 | BC118 | 0.14 | BCY30 | 1.03 | BF257 | 0.47 | MC1466 | 3.50 | TAA661B |  |
| 2N1306 | 0.31 | 2N3706 | 15 | 40406 | . 44 | BC119 | 0.29 . | BCY31 | 1.06 | BF258 | 0.53 | MC1469 | 2.75 | TBA641B |  |
| 2N1307 | 0.30 | 2N3707 | 0.18 | 40407 | . 35 | BC121 | 0.35 | BCY32 | 1.18 | BF259 | 0.55 | ME0402 | 0.20 | TBA651 |  |
| 2N1308 | 0.47 | 2N3708 | 0.14 | 08 | 0.50 | 日C125 | 0.16 | BCY33 | 0.96 | BFR39 | 0.24 | ME0404 | $0 \cdot 13$ | TBA800 |  |
| 2N1309 | 0.47 | 2N3709 | 0.15 | 40409 | 0.52 | BC126 | 0.23 | BCY34 | 1.00 | BFR79 | 0.24 | ME0412 | 0.18 | TBAB10 |  |
| 2N1671 | 1.54 | 2N3710 | 15 | 10 |  | BC132 | 0.30 | BCY38 | 1.00 | BFS21A | $2 \cdot 30$ | ME4102 | 0.11 | т8A820 |  |
| 2N1671A | 1.67 | 2N3711 | 15 | 40411 | 2.00 | BC134 | 0.13 | BCY39 | 1.50 | BFS28 | 1.36 | ME4104 | 0.11 | TBA920 |  |
| 2N 16718 | 1.85 | 2N3712 | 1. 20 | 40594 | 0. | BC135 | 0.13 | BCY40 | 0.97 | BFS61 | 0.27 | Mu480 | 0.95 | TIL209 |  |
| 2N1711 | 0.27 | 2N3713 | 1. 20 | 40595 | 0.84 | 8C136 | 0.17 | BCY42 | 0.28 | BFS98 | 0.25 | M ${ }^{\text {4 }} 41$ | 1.20 | TIP29A |  |
| 2N1907 | 5.50 | 2N3714 | $1 \cdot 38$ | 40601 | 0.67 0.61 | BC137 | 0.17 | BCY58 | $0 \cdot 30$ | BF×29 | 0.35 | M 4930 | 1.05 | TIP29C |  |
| 2N2102 | 0.60 | 2N3715 | 1.50 | 40602 | 0.61 0.58 | BC140 | 0.68 | BCY59 | 0.32 | BFX30 | 0.34 | M 4991 | 1.45 | TIP30A |  |
| 2N2147 | 0.78 | 2N3716 | 1.80 | 40603 40604 | 0.51 0.56 | BC141 | 0.68 0.23 | BCY70 | 0.17 | BFX84 | 0.30 | M 12955 | 1.00 | TIP30C |  |
| 2N2148 | 0.94 | 2N3771 | $2 \cdot 20$ 1.80 | 40604 40636 | 0.56 1.10 | BC142 BC143 | 0.23 0.25 | $\mathrm{BCY71}$ | 0.22 | BFX85 | 0.35 | M U E340 | 0.48 | TIP31A |  |
| 2N2160 2N2218A | 0.90 0.47 | 2N3772 | 1.80 2.65 | 40636 40669 | 1.10 1.00 | BC143 BC14 | 0.25 0.10 | BCY72 | 0.15 0.75 | BFX87 | 0.25 | M E 2955 | 1.20 | TIP31C |  |
| 2N2218A 2N2219 | 0.47 0.42 | 2N 3773 2N3789 | 2.65 2.06 | 40669 40673 | 1.00 0.73 | BC147 BC148 | 0.10 0.09 | 80115 80116 | 0.75 0.75 | BFX88 BFX89 | 0.30 0.90 | MJE3055 | 0.75 0.65 | ${ }_{\text {TIPP32A }}$ |  |
| 2N2219A | 0.52 | 2N3790 | 2.40 | AC126 | $0 \cdot 20$ | BC149 | 0.13 | 80116 | 0.75 1.00 | BFX89 BFY50 | 0.90 0.30 | MUE370 | 0.65 0.75 | TIP32C |  |
| 2N2220 | 0.25 | 2N3791 | $2 \cdot 35$ | AC127 | 0.40 | BC153 | $0 \cdot 18$ | BD123 | 0.82 | BFY51 | 0.25 | MUES20 | 0.60 | TIP33C |  |
| 2N2221 | 0.18 | 2N3792 | $2 \cdot 60$ | AC128 | 0.35 | BC154 | 0.18 | B0124 | 1.20 | BFY52 | 0.30 | MJES21 | 0.70 | TIP34A |  |
| 2N2221A | 0.21 | 2N3794 | 0.24 | AC151V | 0.27 | ${ }^{8 C 157}$ | $0 \cdot 16$ | 80131 | 0.40 | BFY53 | 0.26 | MP8111 | 0.32 | TIP34C |  |
| 2N2222 | 0.20 | 2N3819 | 0.37 | AC152V | 0.49 | 8C158 | 0.16 | BD132 | 0.50 | BFY90 | $1-27$ | MP8112 | 0.40 | TIP35A |  |
| 2N2222A | 0.25 | 2N3820 | 0.29 | ${ }_{\text {AC153 }}$ | 0.35 | BC160 | 0.78 | BD135 | $0 \cdot 21$ | B 4 Y39 | 0.48 | MP8113 | 0.47 | TIP36A |  |
| 2N2368 | 0.17 | 2N3823 | 0.58 |  |  | BC1678 | 0.15 | 8D136 | 0.22 | Esx20 | 0.28 | MPF102 | 0.39 | TIPfiA |  |
| 2N2369 | 0.20 | 2N3904 | 0.19 0.19 | ${ }_{\text {ACl }}^{\text {A }}$ A 176 | 0.25 0.41 | BC168日 | 0.15 | 80137 | 0.24 | BSX21 | 0.30 | MPSA05 | 0.25 | TIP4IC |  |
| 2N2369A | 0.22 | 2N3906 | 0.19 0.67 | AC176 ACI76K | 0.41 0.40 | BC168C | 0.15 0.15 | 8D138 | $0-25$ | BU105 | 2.50 | MPSA06 | 0.31 | TIP42A |  |
| 2N2646 | 0.55 | 2N4036 2N4037 | 0.67 0.42 | AC176K | 0.40 0.35 | BC1698 | 0.15 0.15 | BD139 | 0.71 | BU205 | 2.50 | MPSA12 | 0.35 | TIP42C |  |
| 2N2647 | 0.98 | 2N4037 2N4058 | 0.42 0.18 | AC187K | 0.35 0.40 | BC169C | 0.15 0.15 | BD140 | 0.17 | C106D | 0.85 | MPSA55 | 0.25 | TIP2955 |  |
| 2N2904 2N2904A | 0.40 0.45 | $2 N 4058$ 2 N 4059 | 0.18 -0.15 | AC188K | O.48 | ${ }^{\text {BC170A }}$ BC171 | 0.15 0.16 | BD529 | 0.20 0.80 | CA3020A | 1.80 | MPSA56 | 0.31 | TIP3055 |  |
| 2N2904A | 0.45 0.47 | 2N4059 2N4060 | 0.15 -0.15 0.15 | ${ }^{\text {AC }} 188$ ) | 0.95 | BC171 BC172 | 0.16 0.12 | BD530 BDY20 | 0.80 1.05 | CA3028A CA3035 | 0.79 1.37 | MPSU05 | 0.65 0.58 | TISA3 |  |
| 2N2905A | 0.50 | 2N4061 | 0.15 | AD142 | 0.57 | BC177 | 0.19 | BF115 | 0.29 | CA3046 | 0.70 | MPSU55 | 0.65 | 2T×301 |  |
| 2N2906 | 0.33 | 2N4062 | 0.15 | AD143 | $0 \cdot 68$ | BC178 | 0.18 | BF117 | 0.55 | CA3048 | 2.11 | MPSU56 | 0.80 | 21×302 |  |
| 2N2906A | 0.42 | 2N4126 | 0.21 | AD14gV | . 14 | BC179 | 0.21 | BF121 | 0.35 | CA3052 | 1.62 | NES55V | 0.70 | 21×500 |  |
| ${ }_{\text {2N29074 }}$ | 0.24 0.20 | 2N4919 | 1.10 | AD162 | 0.69 | BY182 | 0.12 0.12 | BF125 | 0.35 | CA3089E | 1.96 | NE560 | 4.48 | 2Tx502 |  |
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| SN7403 | 0.16 | SN7443 | 0.29 | SN7440 | 0.28 | SN7453 |


| SN7403 | 0.16 | SN7413 | 0.28 | SN7440 | 0.16 | SN745 |
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| SN7440 | 0.16 | SN7420 | 0.28 | SN745 | 0.78 | SN7470 |


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| SN7407 | 0.36 | SN7723 | 0.23 | SN745 | 0.78 | SN7470 |


| SN7447 | 0.36 | SN7423 | 0.23 | SN7446 | 0.84 | SN7472 | 0.29 | SN7486 |
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Pont 75p
PORTABLE PLAYER CABINET 4.50 Modorn denign. gize $16^{\prime \prime} \times 10^{\prime \prime} \times 7^{\prime \prime}$ recine Poat 50 p. covered. Large front grille. Binged Wd. Chrome fttings. Motor board cut for Garrard or BSR deck. Few only, in red and black.


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Iuz Deatity 16.500 gavis Treful responce $\quad \mathbf{2 0 - 1 7 , 0 0 0} \mathrm{cp}$ 8 or 15 ohms modele.

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## AUDITORIUM

I2in. 35 watts
A full ranke reproducer lor bith power. Electric Oailars. public eddrens, multi-spenker deal for ELFFi and Disco heques.
Bass Renonence 35 cpe Flux Denalty 15,000gana Uselul renponse 25-10,000003 or 15 obme model.
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## AUDITORIUM

ISin. 45 watts - 50 Eass Resonance $\begin{aligned} & 35 \mathrm{cps} \\ & \text { Rus Density } \\ & \text { 15,000 ga uss }\end{aligned}$

Post 80p



## TRANSISTORS AND RECTIFIERS

| BC |  | 187 | ${ }^{9}$ | 177 45p* | 183 | 21p |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 107 | 9p* | 168 | 1p | 178 45p* | 185 | 27p* |
| 108 | $90^{*}$ | 169 | 9 | 185 30p* | 200 | 240 |
| 109 C | $90^{*}$ | 170 | 0 | 186 300* | 222 | 30 p |
| 113 | $90^{*}$ | 171 | 8p | 187 34p* |  |  |
| 114 | Op | 172 | dp | 199 34p* | BFX 87 | 23p |
| 115 | 9 P | 173 | 8p |  | BFX 88 | 20p |
| 116 | P | 174 | 15p |  |  |  |
| 117 | $12 p$ | 177 | 12p* | BF RANGE |  |  |
| 118 | 10 p | 178 | 17p | 117 27p* |  |  |
| 119 | $20 p$ * | 179 | 12p* | 137 27p | TIP RAMGE |  |
| 120 | $20 p$ * | 182 | 8 p | 177 24p | 29 | 30p |
| 125 | p | 183 | 8 p | 178 24p | 30 | 30 p |
| 126 | Pp | 184 | ${ }^{\text {p }}$ | 179 24p* | 314 | $33^{\text {P }}$ |
| 132 | 0 | 185 | 13p | 180 27p* | 324 | $33^{\text {p }}$ |
| 134 | 8 p | 186 | 13p* | 181 27p* | 41A | 490* |
| 135 | p | 187 | 14p* | 182 270* | 42 A | $40^{*}$ |
| 136 | 9 p | 212 | 9p |  |  |  |
| 137 | p* | 301 | 190* | AECTIFIERS |  |  |
| 139 | 15p* | 302 | 10p* | 1N4001 1A SOV PIV |  | 4 p |
| 140 | 150* | 303 | 19p | 1N4002 1A 100V PIV |  | 4 p |
| 141 | 19p* | 304 | 10p* | 1N4003 1A 200V PIV |  | 4 p |
| 142 | 15p* |  |  | 1N4004 1A 400V PIV |  | 5 |
| 143 | 15p* |  |  | IN4005 1A 600V PIV |  | $5 p$ |
| 145 | 15p |  |  | 1N4006 14 800V PIV |  | 54 |
| 147 | p | BD RANGE |  | 1N4007 1A 1kV PIV |  | 5 p |
| 148 | 8 p | 115 | 27p* | 1N5400 3A 50V PIV |  | 71p |
| 149 | 8p | 131 | $300{ }^{\text {3 }}$ | 1N54013 3 100V PIV |  | $71 p$ |
| 153 | \%p | 132 | 329. | 1N5402 3A 200V PIV |  | op |
| 154 | 9p | 137 | 270 | 1N5403 3A 300V PiV |  | 3tp |
| 157 | 4 p | 139 | 38 p | 1N5404 34 400V PIV |  | \% |
| 158 | p | 140 | 33 p | 1N5405 3A 500V PIV |  | sp |
| 159 | 8 p | 155 | 3ep | 1N5406 3A 600V PIV |  | 910 |
| 160 | 15p* | 175 | $310{ }^{\circ}$ | 1N5407 3A 800\% PIV |  | $10 p$ |
| 161 | 15p* | 178 | 310 * | 1N5409 34 1kV PIV |  | 10tp |

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| 7451 | 16p | 13p | 11p | CO4023AE | 10p | 17 p | 14p |
| 7453 | 16 p | 13p | 11p | CD4024AE | 72p | 56 p | 46p |
| 7454 | 16p | 13p | 11p | CD4025AE | 19p | 17p | 14p |
| 7460 | 16p | 13p | 11p | CD4026AE | 1.60p | 1:25p | 1.03p |
| 7470 | $27 p$ | 25p | $23 p$ | CO4027AE | 50p | 40p | 34p |
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| 7473 | 30p | 25p | 20p | CD4029AE | $1.06 p$ | $86 p$ | $70 p$ |
| 7474 | 32p | 26p | 21p | CD4030AE | 50p | 40 p | 34 p |
| 7475 | 47 p | 39p | $31 p$ | CD4035AE | 1.06p | ${ }^{86 p}$ | 70p |
| 7476 | 32p | 26p | 21p | CD4040AE | 94p | 74 p | $61 p$ |
| 7480 | 60p | 56p | 42p | CO4042AE | 78p | 60p | 50p |
| 7482 | 75 p | 62 p | 50 p | CO4043AE | 94p | ${ }^{74} \mathrm{p}$ | 61p |
| 7483 | 70p | 65p | 62 p | CD4044AE | $\begin{array}{r}87 p \\ \hline 1.240\end{array}$ | 70p | 58p |
| 7485 | 1-25p | 1.08p | $85 p$ | CD4046AE | $1.24 p$ 50 p | 99p | 80p |
| 7486 | 32p | 26p | 21 p | CO4049AE | 50p | 40p | 34p |
| 7490 7491 | 44p | $39 p$ $55 p$ | 30p | CD4050AE | 50p | 40p | 34p |
| 7492 | $57 p$ | $46 p$ | 36p | CD4056AE | 1.24 p | $99 p$ | 80p |
| 7493 | 45p | 40p | 32p | CD4060AE | $1 \cdot 24 \mathrm{p}$ | 99p | ${ }^{80 p}$ |
| 7494 | 73p | $65 p$ | 58p | CD4066AE | $87 p$ | 70p | 58p |
| 7495 | 70p | 60 p | $54 p$ | CD4068BE | 20 p | 18p | 16p |
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| 74100 | 1.08p | 89p | 78p | CD40708E | 20p | 18 p | 16p |
| 74107 | 35p | 28p | 22p | CD40718E | 20p | 18p | 16p |
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| $\times 25-240$ | 25 Watt Iron 240V E2.95p |
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|  | For all models $\quad ¢ 1 \cdot 25 p$ |
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|  | iron 3/32" 36p |
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|  | iron 5/32" |
| BIT4 | Spare bit Nickel clad for CCN |
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(P.E. Feb. 73 to Feb. 74)

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## Stabilised power supply

Two Linear Voltage Controlled Oscillators 42.05
and one Inverter-all 3 circuit
PCB ( 2 are required)
CB (2 are required
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Two Ramp Generators and Two Input
All 4 circults
PCB (holds all 4 circuits)
Sample-Hold and Noise Generator
PCB (holds both circuits)
Tone Control
PCB
Reverberation Amplifier
Srine Line unit for Reverb. Amp.
Ring Modulator
Peak Level Meter Circuit
$100 \mu \mathrm{~A}$ Panal Meter
PCB to hold Reverb, Ring Mod and Meter
Circuits
Envelope Shaper
Enve
PCB
Voltage Controlled Amplifier and Differential
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(Can be used without the Main Synthesiser to make an musical instrument)
Two Logarithmic Voltage Controlled
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C14.55
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Amplifiers, Mixer and 2 Envelope Shapers
PCB (holds the first 6 circuits)
PCB for both Envelope Shapers
Keyboard Stabilised Power Supply
Printed Circuit Board
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Will modify an audio signal not only from a guitar but from any audio source, producing 8 different swirchable effects that can be further modified by manual controls. focts units in our range.
Componient Set with special foot operated
witches
Alternative component set with panel mounting
switches
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A multi-purpose sound controller, the functions of which include envelope shaper, tremolo, voliceooperated fader, automatic fader and frequency-doubler.
Component 5 et for above functions (exel. 5 W s)
66.58
61.58

Printed circuit board
61.58

Optional extra-additional Audio Modulator, the use of can produce "jungle-drum" rhythms. Component Set (inci. PCE)
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A simple but effective manually controlled unis for introducing the "phasing" sound into live or recorded Component set (incl. PCB)
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For use with the above Phasing Unit to automatically control the rate of phasing.
Component Set (incl. PCB)

COMPONENTS SETS include all conductors, posentiometers and eransormers. Hardware such as cases, sockers, knobs, ete. are not included but most of chese may be bought separately. Fuller details of kits, PCBs and parts are shown in our lises
CIRCUIT AND LAYOUT DIA. GRAMS are supplied free with all onosonics.

PHOTOCOPIES of che P.E. texes for most of the kits are available-prices in
our lists.

## P.E. JOANNA (P.E. May/Sept. 75)

A five-octave electronic piano that has switchable alternative voicing of Honky-Tonk piano, ordinary piano, harpsichord, or a mixture of any of the chree. together with facilities including fast and slow tremolo, loud anid soft pedal switching, and sustaln pedal switching. The power amplifier typically delivers 24 watts into 8 ohms. The PCBs have been redesigned by ourselves making
improved use of the space available.

## Main Power Supply

Tone Generator and Top C Envelope
PCB for Main PSU, Tone Gen \& Top C E.S.
Envelope
Set of PCBs for Envelope Shapers (except Top C)

Voieing and Pre-Amp Circuits
PCB for Voicing and Pre-amp
Power Supply)
Power Ampllfier (incl. separate Power Supply) \&14.50
PCB for Power Amp and PSU
RHYTHM GENERATOR (P.E. Mar./Apr. 74)
Programmable for 64,000 rhythm patterns from $B$ effeces
circuits (high and low bongos, bass and snare drums, ong and short brushes, blocks and soft cymbal), and with sing and usciut.
Tempo, Timing and Legic circuits
PCB for above circuits (double-sided)
PCB for all $B$ effects
Simple mixer (our design) incl. PCB
Alternative mixer with external volume controls incl. PCB
Power Supply for T. T and $L$, and Effects, ind. PC8
(See our
(See our list for Power Supplies for Mixers)
REVERBERATION UNIT (P.W. Nov./Dec. 72)
A high quality unit having mierophone and line inpus pre-amps, and providing full control over reverberation Cevel.
omponent Set (excl. sprine unit)
Printed Circuit Board
Panel Meter ( $50 \mu \mathrm{~A}$ ) (optional)

## WIND AND RAIN UNIT

A manually controlled unit for producing the
named sounds.
Component set inel. PCB

## PHONOSONICS

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## MARKET.

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(P.E. Nov. 1974 to March 1975)

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## Two Voltage Controlled Oscillators

### 65.22

Voltage Controlled Filter and Voltage Reference Circuit Two Envelope Shapers and Two Voltage $\$ 3.41$ Controlled Amplifiers $\quad \mathbf{~ 7 . 2 5}$ Keyboard Controller and Hold Circuits $\quad \$ 2.66$ Keyboard Divider Resiators (select type to suit keyboard used) (all are $2 \%$ tolerance): 2 Octave fl 3 Octave $\mathrm{C1} .48$; 4 Octave 61.96 ; 5 Octave $\mathbf{C 2 . 4 4 .}$ M.F. Oscillator and Detector

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Stabiliser
41.35
63.60
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c9.93
66.42
P.E. MINISONIC MK

Conversion kits and PCBs for updating the MK I version Cow available. Details in our list.

## ENVELOPE SHAPERS

Both of the kits below have manual control over their Attack, Decay, Sustain and Release functions. Both kits inciude PCB. (VCA means Voliage Consrolled Amplifier) $\begin{array}{lll}\text { Envelope Shaper and VCA (P.E. Apr. 76) } & \text { \&5.43 } \\ \text { Envelope Shaper (without VCA) (P.E. Oct. 75) } & 4.16\end{array}$

VOICE OPERATED FADER (P.E. Dec. 73)
For automatically reducing music volume during "talk-over"-particularly usefulfor Disco work or for homemovie shows.
P.E. MINIMIX 6 (P.E, Nov./Dec. 75)

Each of the 6 inpur channels has its own gain, volume and panning controls. The volume of the twin channe outputs are fuliy manually controliable, as are the headphone and pre-fade monitoring facilities. Twin VU Ideal for use with effects and synthesiser kits
-INPUT MIXER
A simple mixer having 8 inputs each of which has a preset level control and which are combined into orie output channel having a preser over-all level control and a coupling our various sound effects and synthesiser kits.
Component set incl. PCB

25 WATT MONO AMPLIFIER (P.E. Sept. 75)
A good general purpose integrated circuit power amplifier typically delivering 25 wates inco 8 ohms. Power band width 20 Hz to 20 kHz 3dB, Input impedance 2 km . Distortion $0.2 \%$. Suitable for use with any, of our sound producing kies.
Component Set incl. power supply
rinted Circuit Board

TREBLE BOOST UNIT (P.E. Apr. 76)
Gives a much shriller quality to audio simnals fed throush
t. The depth of boost is manually adjustable
it. The depth of boost is

C2.83
Component Set incl, PCB
VOLTAGE CONTROLLED FILTER (P.E. Oct. 74)
An independently designed VCF that can be used with
the P.E. Synthesiser
Component Set
Printed Circuit Board
P.E. TUNING FORK (P.E. Nov. 75)

Produces 84 switch-selected freq)
Produces 84 switch-selected requency*accurate tones. ments. Ideal for tuning acoustic and electronic musical instruments alike.
Main Component Set incl. PCB
Power Supply set inel. PCB
P.E. SYNCHRONOME (P.E. Mar. 76)

An accented-beat electronic metronome, providing duple, triple and quadruple times wish full control over the beat rate. Can also be used as a simple drum-beat
phythm generator. Includes power supply. hythom generator. licludes pow
Component Set inel. loudspeaker
C 10.10
Printed Circuit Board
61.70

PEAK LEVEL INDICATOR (P.E. Mar. 76)
A twin-charinel visual display unit for monitoring the
A twin-chaninel visual display Whil ror monitoring the inter-coupling our many sound producing kits to help avoid signal over-loading.
42.15 Component Set incl. PCB (as published)

EXPORT ORDERS are welcoms, though we advise that a current copy of our list should be obtained before ordering as it also shows Export postage rates. All payments must be cash-with-order, in Sterling and preferably by International Money Order or through an English
Bank. To obtain list for Europe send 20p, for other countries send 40 p.
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## OTHER PROJECTS

PHOTOGRAPHS in this advertise ment show two of our units containing some of the P.E. projeets built from our kits and PCBs. The cases were built by ourselves and are not for sale, though a small selection of other cases is available
LIST-Send Stamped Addressed En. velope with all U.K. requests for free list giving fuller details of PCBs, kiss, and other components.
OVERSEAS enquiries for list: Europesend 20 p ; Other Countries-send 40 p .


## KEYBOARDS AND CONTACTS

Kimber-Allen Keyboards as required for many published circuits, including the P, E, Joanna, P.E. Minisonic, and P.E. Synthesiser. The manufacturers claim that these are the finest moulded plastic keyboards available. All octaves are C
3 Octave ( 37 notes) $620 \cdot 50$. 4 Oct ( 49 notes) $\mathrm{E} 23 \cdot 50$. 50 Oct ( 61 notes) $\mathrm{C27}$
Contact Assemblies for use with above keyboards: Single-pole change-over (type SP) as for P.E. Joanna and P.E. Minisonic. Twompole normally open-make-break type DP) as for P.E. Synthesiser. Special contact assembly (type 4 PS) having 4 poles, 3 of which are normally-open make-break rontacts and the fourth is a change-over contact -this special assembly enables THE SAME KEYBOARD to be used with the P.E. Synthesfiser, P.E. Minisonic and the P.E. Joanna simultaneously thus avoiding the cost of more than one keyboard.


PRINTED CIRCUITBOARDS for use with the above contacts and thus eliminating most of the inter-wiring required, are available. Details in our lists.

SOUND-TO-LIGHT (P.E.-Apr./Aug. 71 )
The ever-popular Aurora- 4 or 8 channels each responding to a different sound frequency and controlling its own light. Can be dsed with most audio systems and lampintensities.
A MUST for any Disco. and a fascinating visual display for the A MUS
home.
4 Channel Component Set (excl. thyristors) 8 Channel Component Set (excl. thyristors) Power Supply Component Sex
PCB for 4 frequency channels
PCB for power supply and 8 lamp drivers
iA 400 V , thyristors (I per chan. req.) each
Panel meter $(1 \mu A)$ (optional)
3.CHANNEL SOUND-TO-LIGHT (P.E. Apr. 76)

A simple but effective sound-to-light controller eapable of A serating 3 lampseach of approximately 700 watts. includes power supply, thyristors, and by-pass switches. Component Set incl. PCB

BIOLOGICAL AMPLIFIER (P.E. Jan./Feb. 73)
Multi-function circuits that, with the use of other external equipment, can serve as lie-detecsor, alphaphone, cardiophone exc.
Pre-Amp Module Component Set incl. PCB 63.71 Basie Output Circuits-combined component set with PCBs, for alphaphone, cardiophone, frequency meter and visual feed-back lamp-driyer circuits 65.38

## TAPE NOISE LIMITER

Very effective circuit for reducing the hiss found in most tape recordings. All kits include PCBs
Standard Tolerance Set of Components
Superior Tolerance Set of Components
Regulated Power Supply (will drive 2 sets)
SINE AND SQUARE WAVE GENERATOR (P.E. July 75)
Suitable for audio, digital, or general purpose. Controllable chrough 4 decade ranges 10 Hz to 100 kHz . switched attenu解 PCB for above components $\quad$ 48.88 Power Supply $\quad \$ 5.70$ PCB for Power Supply

HARMONIC DISTORTION FILTER (P.E. Mar. 76)
A simple to operate filter for use in measuring the cotal harmonic distorsion in ampliffers.
Component set incl. PCB
SEMI CONDUCTOR TESTER (P.E. Oct. 73)
Essential test equipment for the enterprising home constructor. While stocks last.
Set of resistors, capacitors, semiconductors. potentiometers, makaswitches and PCB Panal meter ( $500 \mu \mathrm{~A}$ )

## PHOTOPRINT PROCESS CONTROL (P.E, Jan./Feb. 72)

For colour and $B \& W$, and indispensible dark $\&$ room unit for finding exposure, controlling enlarger timing, and seabilising mins voltage. While stocks last.
Component Set (excl. meter)
Pinted Circuit Boar
Panel Meter (I mA)
CAMELO inc. P.C.B. DELIVERYSUBJECTTO AVAILABILITY.

| TRANSIS ACI28 |  |
| :---: | :---: |
|  | p |
| BC107 | 13p |
| BClOb | $13 p$ |
| BC109 | 13p |
| BC147 | 12p |
| BCI48 | 12p |
| BCl 49 | 12 p |
| BCI57 | 13p |
| BCI58 | $13 p$ |
| BC159 | 13p |
| BCI日2L | 12p |
| BCI84 | 12p |
| BC187 | 25p |
| BC204 | $14 p$ |
| BC209C | 14p |
| BC212L | $15 p$ |
| BC213 | 15p |
| BC478 | 28p |
| BCY7I | 22p |
| BDI31 | 44p |
| BDI32 | 54 p |
| BFY50 | 22p |
| BFY51 | 22p |
| BFY52 | 24p |
| BSY95A | 22p |
| MJE2955 | 110 p |
| OC28 | 60p |
| OC71 | 14 p |
| OC72 | 14 p |
| OC84 | 25p |
| ORP12 | 66p |
| ZTX107 | $12 p$ |
| ZTX108 | 71 p |
| ZTX501 | 13p |
| $2 \mathrm{~T} \times 503$ | 15p |
| 2T×531 | 23p |
| 2N706 | 13 P |
| 2N914 | 22p |
| $2 \mathrm{NI}_{3}{ }^{4}$ | 22p |
| 2N2219 | 27p |
| 2N2905 | 27p |
| 2N2905A | 28p |
| 2N2907 | 22p |
| 2N3053 | 18p |
| $2 N 3054$ | $66 p$ |
| 2N3055 | 48p |
| 2N3702 | 12p |
| 2N3703 | 12p |
| 2N3704 | 12p |
| 2N3819 | 35p |
| 2N3820 | 64 p |
| 2N3823E | 39p |
| 2N4060 | 12p |
| 2N4871 | 36p |
| 2N5245 | 51 p |
| 2N5777 | 45p |
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| 709 T05 | 40p |
| 709 8-pin DIL | L 40p |
| 723 T05 | 95p |
| 741 8-pin DIL | L 32p |
| 748 T | 63p |
| 748 8-pin DIL | -63p |
| $\mu A 7805$ T0220 | 165p |
| $\mu A 7808$ T0220 | 165p |
| ${ }_{4}{ }^{\text {A }} 8812$ T0220 | 165p |
| $\mu A 7815$ T0220 | 165p |
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Switched output of $3 / 4 \mathrm{~N}$
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Similar to above. but with press-stud battery connectors $3+3 / 4\}+4 / / 6+8 / 7 t+7 t / 9+9 / 12+12 \mathrm{~V}$ at 250 mA . Also gives 15/18/24V single

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EMI $13 \times \sin 20 W$ bass 8 ohm
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EMI $6+$ in d/cone, roll surr., 8 ohm
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EMI sin mid range
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Fane Pop 33T, 8 or 16 ohm
Fane Pop 50, 8 or 16 ohm
Fane Pop 55, 8 or 16 ohm
Fane Pop 60, 8 or 16 hm
Fane Pop 70. 8 or 16 ohm Fane Pop 100. 8 or 16 ohm
Fane Crescendo 12A, 8 of 16 ohm Fane Crescendo 12BL. 8 or 16 ohm Fane Crescendo 15/100 A, 8 or 16 ohm Fane Crescendo 15/125, 8 or 16 ohm
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> Goodmans Audiom 2008 ohm
> Goodmans Axiom 4028 or 15 ohm
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> Goodmans 8P 8 or 15 ohm
> Goodmans 10P 8 or 15 ohm
> Goodmans 12P 8 or 15 ohm Goodmans 12PG 8 or 15 ohm Goodmans 12PD 8 or 15 ohm Goodmans $12 A X 8$ or 15 ohm Goodmans 15AX 8 or 15 ohm Goodmans 15P 8 or 15 ohm Goodmans 18P 8 or 15 ohm Goodmans Hitax 750P
> Goodmans 5 in midrange 8 ohm Jordan Watts Module, 4, 8 or 15 ohm
> Kef T27
> Ket T15
> Kef B110
> Kef B200
> Kef B139
> Kef ON8
> Kel DN 12
> Kef DN 13 SP 1015 or SP 1017
> Lowther PM6
> Lowther PM6 MK 1
> Lowther PM7
> Peerless K0100T 4 or 8 ohm
> Peerless DT10HFC 8 ohm
> Peerless KO40MPF 8 ohm
> Peerless MT225MCF 8 ohm
> Richard Allan CA12 12 in bass Pichard Allan HP8B Richard Allan LPQB Richard Allan OT20 Alchard Allan CN8280 Richard Allan CN820 Alchard Allan Super Disco 60W 12 in Aichard Allen CG15 15 in bass
> Richard Allan Super Disco 12 in 60 watt Pichard Allan Super Disco 10 in 50 walt Pichard Allan Super Disco Bin 50 watt Padiord BD25
> Padford MD9
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> Aadiord TD3
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> STC 4001G
> Tannoy 10 in HPD
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Helme XLK 35
Helme XKL 40
Helme XLK 50
Kefkit 1
Keikit III
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ELECTRONIC PRACTICE



## A POWERFUL COMBINATION

Britain's new National Exhibition Centre at Birmingham was the setting for this year's International Electrical, Electronic and Instrument Exhibition. This new and spacious venue was most appropriate for the marriage of the IEA and Electrex. appearing together for the first time in one combined exhibition. A report will appear next month. Meanwhile the significance of the event deserves comment.
The close relationship between electronics and electrical engineering is self-evident. But as technologies advance this relationship grows ever closer and today the electricity supply services rely upon electronic instruments and circuits for control and monitoring purposes at all stages, from the power generating station through the varied distribution networks to the ultimate consumer. Conversely, electronic equipment requires electrical power to operate, though electronics is not entirely dependent upon the normal electricity supply. A real measure of independence is enjoyed by electronics because so much equipment can operate from other sources of electrical energy. But since battery manufacture, like all industry, is finally dependent upon mains supplies it would be unwise to make too much of such an argument. Devolution may be fashionable in political circles, but that's not the road for the electronics and electrical industries. Future needs and circumstances can only bring the two even closer together. In such an amalgamation of forces much of our future prosperity, if not existence, depends.

Energy resources has become a commonplace topic, and with very good reason. Indigenous fossil fuels will not last for ever, and there is an undeniable need to start looking for alternative sources of energy from which to produce electricity. For electricity remains the most convenient and flexible form of power, although not the most economical to generate using traditional methods and fuels.
Much work is currently being performed in exploring possible ways of harnessing and using solar radiated energy. But the self-sufficient home is still a dream, despite valiant efforts being made by various individuals and groups. One interesting idea recently mooted is for a two-way system whereby houses could feed current collected from roof top batteries of solar cells into the national grid in times of low domestic demand. Energy so contributed by countless homes could be used to pump water to high level reservoirs where it would be stored for eventual use to drive generators at time of heavy demand. The ambitious scheme envisaged poses mammoth problems, and clearly electronic control would play a vital role in any practical realisation of such a two-way exchange scheme.
No Government is likely to allocate millions of pounds to finance this or any other revolutionary scheme-however ideal and desirable-so long as it is not immediately imperative to do so. Thanks to North Sea oil, the present day fuel situation seems good and adequate. But we may be living in a fool's paradise. The situation will be very different in 25 years time.

Common sense cries out for a National Energy Policy and a bold plan to organise research and engineering resources to concentrate on new alternative methods for electricity generation. May the coming together at Birmingham of the electronic and electrical industries be an augury of some determined and positive efforts in this direction.
F.E.B.

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THis article describes the design and construction of a circuit that can be made to give warning of an intruder opening any of your car doors, boot or bonnet. The circuit is made to operate a relay which repeatedly switches on for three seconds and off for one second. This relay can in turn be made to operate the vehicle horn or any other audible warning device.

The circuit is arranged such that the horn will sidence after a chosen interval only if the car door, boot or bonnet are properly closed again. If, however, the door, etc. are not closed the horn will continue to sound.

It was thought that if the horn was made to sound intermittently it would attract more attention and save the battery and horn. With this interrupted sound, an intruder could not claim he had his horn stuck on.

## DOOR SWITCHES

The first requirement is to fit the vehicle doors, boot and bonnet with switches. These switches should be installed such that when the door, etc. is closed the contacts are open, and closed when the door is open. Reed relays and magnets could be usefully employed if desired, instead of push-buttons.

## CIRCUIT DESCRIPTION

The circuit is required to operate a relay if any of the door switch contacts are made, and to operate that relay at approximately a three second on, one second off ratio. It must also keep doing this for approximately one minute 15 seconds after the door switch has been made, even if the door is closed immediately.

If, however, the door remains open, the circuit must ensure that the relay will continue to operate at the above on/off ratio indefinitely.

To achieve these two requirements, two of the now well-known 555 timers are used. Timer ICl (Fig. 1) is wired in the monostable mode and provides a positive supply to timer IC2 for approximately one minute 15 seconds.

Timer IC2 is wired in the astable mode and provides the on/off pulse to drive the relay RLA.

It is worth noting at this stage that the diode across the relay coil must be a germanium gold bonded type. Other types were tried but did not fully suppress the back e.m.f., causing the timer to be re-triggered.

The values of R5 and C2 determine the length of time the circuit remains operative. With the values shown it will operate for approximately 75 seconds. This is a long time for a car horn to be blowing,


Fig. 1. Complete circuit diagram of the Car Intruder Alarm
but if any constructor thinks it should be longer, he can increase the time by increasing the value of R 5 or C 2 . The data sheet for the timer states that the maximum value of R5 for reliable operation is determined by the value of the threshold current $\left(I_{\mathrm{T}}\right)$ into pin 6 which is $0.25 \mu \mathrm{~A}$ (max).

$$
\begin{gathered}
\mathrm{R} 5(\max )<\frac{V_{\mathrm{CC}}}{3 \times 0.25} \mathrm{M} \Omega \\
\mathrm{R} 5(\min )=1 \mathrm{k} \Omega
\end{gathered}
$$

i.e. for 12 V operation $\mathrm{R} 5(\max )=16 \mathrm{M} \Omega$.

Ideally R5 should be kept as small as possible. C 2 is determined from the formula for the time delay. For $\mathrm{t}=3$ mins with a value of $\mathrm{R} 5=8 \mathrm{M} \Omega$

$$
\mathbf{C} 2=\frac{180}{1 \cdot 1 \times 8 \times 10^{6}}, \text { farads }=20 \mu \mathrm{~F}
$$

It is worth noting that individual tolerances of capacitors can make a mockery of calculations. Allow plenty of time for the capacitor to cool before its timing is checked.
Again, if desired, timer IC2 on/off ratio can be altered.

$$
\begin{aligned}
t_{\mathrm{ON}} & =0.685(\mathrm{R} 7+\mathrm{R} 8) \times \mathrm{C} 5 \\
t_{\mathrm{OFF}} & =0.685 \mathrm{R} 8 \times \mathrm{C} 5
\end{aligned}
$$

where ton and torf are in seconds when R7 and R8 are in ohms and C5 is in farads. Total period $t=$ tox + torf. The same constraints on the choice of values of R7, R8 and C5 apply as given for the monostable mode.

The intruder alarm circuit wired to the 11-pin base



## CAR <br> ALARM <br> CIRCUIT BOARD DETAILS

## COMPONENTS . . .



## Capacitors

| C1 | $100 \mu \mathrm{~F} 15 \mathrm{~V}$ elect |
| :--- | :--- |
| C 2 | $10 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum |
| C 3 | $0.01 \mu \mathrm{~F}$ |
| C 4 | $0.01 \mu \mathrm{~F}$ |

C4 $0.01 \mu \mathrm{~F}$

IC1 TIL111 optically coupled isolator
1C2 555 timer i.c.
IC3 555 timer i.c.

| C5 | $10 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum |
| :--- | :--- |
| C6 | $0.01 \mu \mathrm{~F}$ |
| C7 | $0.01 \mu \mathrm{~F}$ |

Miscellaneous
RLA $6 \mathrm{~V} 410 \Omega$ changeover contacts (R.S. Type 912)

S1 On/off toggle switch
S2 On/off toggle switch (see text)
S3-S8 Miniature push-to-break switches (R.S.) or reed switches and magnets (see text)
11-pin module case, 11-pin screw base, screw base cover (all R.S.)


Fig. 2. Layout of the components on the printed circuit board (left) and printed circuit master shown full size (right). Note that diode D5 is coninected to pin 1 or 2 according to earth system, see Fig 1


Fig. 3. Wiring the unit into a car with a positive earth system. The green l.e.d. (D1) can be mounted inside the car to show that the system is in operation. The audible warning device can be used if it is preferred not to use the car horns

The trigger input, pin 2 , of the 555 is very sensitive and it was found that even the shortest length of wire on this point gave trouble, the main difficulty being that the timer would be re-triggered after the time interval.

To avoid any trouble from the long length of wire required to reach the door switches, it was decided to isolate them from the timer by using a TIL111. This is an optically coupled isolator which consists of a light emitting diode (l.e.d.) and photo-transistor in one 6 -pin dual-in-line package.

The current through the l.e.d. must be limited to 60 mA maximum. The transistor is an $n p n$ type. The base resistor (R4) is chosen to ensure that the transistor is turned fully on when full light is emitted from the l.e.d.

It is arranged that when any door is opened 0 V is applied to the l.e.d. cathode causing it to illuminate, turning on the phototransistor.

The negative going edge at the collector triggers the timer IC . Once triggered, the output (pin 3) goes to +9 V .

If the door is then closed timer ICl will remain in that state, providing supply voltage to IC2, until its time interval is ended, pin 3 then falling to 0 V .

If the door were to remain open, holding the door contact closed, the l.e.d. would remain illuminated, the phototransistor collector and pin 2 remaining at 0 V . In this condition (pin 2 at 0 V ) the time interval does not terminate, i.e. the output pin 3 remains at +9 volts until such time as pin 2 is returned to its high state.
The voltage from a car 12 V battery can rise to as much as 15 volts so a 12 V Zener D3 has been used to stabilise the voltage.
The switch to provide power to the circuit is best situated well out of sight and reach. To indicate that the circuit is on a green l.e.d. (D1) is used. This gives very long life.

## ARMING THE SYSTEM

In order that the owner can set the system and get out of his car the secret switch ( $\mathbf{S} 2$ ) must be installed. This is a switch that has to be actuated once the owner is out of his car with all doors closed. It is not much use going into detail in this article about this switch. There is plenty of scope here for the ingenious.

Fig. 4. Wiring the unit into a car with a negative earth system



Completed printed circuit board showing component layout

## CONSTRUCTION

The circuit board is housed in an 11-pin plug-in module case, which can then be used with a screw terminal, 11 -pin relay base and cover.

The circuit board measures $6.5 \mathrm{~cm} \times 4.1 \mathrm{~cm}$ and the layout is not critical. A full size layout is given in Fig. 2.

The system can be used with either positive or negative earth systems. Some cars employ a horn relay and others switch the horn directly. In either case, whether it be a positive or negative earth system, all cars checked switched the earth side, be it positive or negative.

For positive earth systems strap Terminal 7 to Terminal 1 (Fig. 3). This puts positive (earthed) volts onto one of the relay contacts. For negative earth systems strap Terminal 7 to Terminal 2 (Fig. 4). This puts negative (earthed) volts onto one of the relay contacts.

The sysem described has only been used on a horn relay system and any constructor wishing to switch the horns direct should check the current requirements. The contacts of the relay described in this article are rated at five amps, 150 W .

It is essential to have a suppression diode across the horn relay coil (diode D5, Fig. 1). Its polarity must be changed according to the earth system, and again must be of the type specified.


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# PE <br> DIGISEOPE 

 By R.W.Coles and B. CullenTHIS is the first of a series of articles describing an experimental oscilloscope not much bigger in size than a logic probe with an l.e.d. display substituting for the c.r.t. Although it lacks the capability of the conventional instrument in both bandwidth and resolution of waveshapes, it presents a different approach in design by replacing the usual high voltage analogue circuitry. with TTL logic and interfacing with an $8 \times 10$ l.e.d. matrix.

Of course, with an $X$ and $Y$ scan of a matrix of this density it can sometimes be difficult to identify the shape of wave, this applies particularly to those with a slow rate of change. However, familiarity with the instrument in handling the controls can do much to lessen this problem. Pulses, step functions, etc. are relatively easy to identify and measure. An excellent triggered timebase generator contributes a great deal in really positive locking.

## FRESH APPROACH

It occurred to the writers that with modern l.e.d. and integrated circuit techniques, it might be possible to design an entirely new breed of scope which would be small and handy for portable use, but which would operate from low voltages, even batteries.
The display device itself was the crucial factor, and it was recognised from the start that it would not be possible to match the high definition of a c.r.t. Nevertheless, by using an l.e.d. matrix it seeemed possible that one would be able to see the shape of waveforms, and be able to measure their amplitude and frequency to a reasonable degree of accuracy.
Furthermore, since a matrix display consists of a series of discrete points, it seemed that low cost TTL logic could be used extensively to replace the expensive high voltage analogue circuitry used in conventional scopes.

## NEW GROUND

As finally defined, Digiscope breaks new ground in the oscilloscope hierarchy since it is not much bigger than a logic-probe, and is suitable for handheld operation and the more traditional bench use with a probe attached. The major compromise made when choosing an l.e.d. display was in the definition possible with only 80 data points against the several thousand equivalent points on a similar sized c.r.t. Once this compromise has been accepted, however, the rest of the circuitry becomes much simpler than that of a conventional scope, and the performance in most other areas is equal to or better than that of its predecessors. Most important of all, the eradication of high voltages from the design makes the circuit easy to build using the minimum of hardware.

## CIRCUIT

A block diagram of Digiscope is shown in Fig. 1.1. and it can be seen that at this level there are more boxes in this design than in a traditional instrument! This is not as bad as it at first appears, since most of the boxes can be easily produced with cheap integrated circuits.

The display is built up from a matrix of l.e.d.s wired as eight rows by ten columns, so the drive circuits are required to produce eight separate $\mathbf{Y}$ drives, and ten separate $X$ drives. When the scope is triggered the timebase circuitry causes each column to be enabled in turn from left to right, the duration of each "column enable" being determined by the timebase speed setting. At the end of a sweep the display is blanked and the timebase rests to await a nother trigger pulse.

The output of the Y Amplifier is compared with a series of reference thresholds and the result of this comparison decides which of the eight rows should be enabled at any given time. The interaction of this $X$ and $Y$ scan results in a waveshape trace being plotted on the screen. At slow speeds, individual points of the plot can be observed, but at higher timebase rates the eye integrates the display and observes what appears to be a static plot of the waveform.

# - SPECIFICATION 

DISPLAY
80 l.e.d. matrix ( 8 by 10)
Display area 1.5 in by 1.1 in
AMPLIFIER
Sensitivity 10 mV to 80 V per division
Bandwldth d.c. to at least 1 MHz (d.c. coupled)
5 Hz to at least 1 MHz (a.c. coupled)
Input impedance 1 Megohm
Input coupling d.c., a.c., ground
Maximum input voltage (see text)
Y Shift 8 switched positions

## TIMEBASE

Time/division 100 ns to 10 s per division in 25 calibrated steps

## TRIGGERING

Trigger modes
\(\left.\left.$$
\begin{array}{cc}\text { provides a reference } \\
\text { trace in the absence of } \\
\text { a trigger) }\end{array}
$$\right\} \begin{array}{l}positive or negative select- <br>

able\end{array}\right\}\)| Trigger levelseven switched steps <br> External trigger <br> requires TTL type square <br> edge |
| :--- |

## POWER SUPPLIES

+12 volts at 60 milliamps
-12 volts at 60 milliamps
+5 volts at 350 milliamps
from external mains power unit or batteries (3.5W maximum consumption)

## ROW DRIVERS

The appropriate row is driven by one of eight Row Drivers which consist of open collector gates and pnp transistors which take the required row line up to plus 5 V . The second inputs to each of eight open collector gates are commoned together and used as an overriding blanking input so that the display can be disabled between each sweep.

## TRIGGER CIRCUIT

The sharp transition generated when a particular comparator switches, is ideal as a triggering signal for the timebase, and since each comparator switches at a different threshold voltage it is a simple matter to choose a single comparator output as a trigger by means of a switch.

The chosen trigger signal is buffered by the Trigger Amplifier and then used to trigger a monostable


Fig. 1.1. Block diagram of the Diglscope
in the Trigger Generator circuit. If the Trigger Latch is primed then the pulse from the monostable will reset it. The output of the Trigger Latch is used to enable the Timebase Oscillator which then starts to generate pulses at the selected rate.

## TIMEBASE

In all, there are 24 possible timebase speeds, and to select all these directly, at the Timebase Oscillator would require the switching of lots of bulky $\mathrm{C} / \mathrm{R}$ networks. To overcome this problem, the Timebase Oscillator itself is restricted to only four switched speeds, and thus four C/R networks. Each of these basic frequencies is then divided down in a ROM programmed, two decade, BCD counter to arrive at one of four switch selectable rates. This Timebase Divider circuit divides the selected Timebase Oscillator frequency by $1,2,5,10,20,50$, or 100, to give a total range of speeds which are more than adequate for the job.

The final timebase frequency is fed to a further BCD counter which forms part of the Sweep Generator to step the matrix columns in sequence. The outputs of this counter drive a four-line-to-ten-line decoder the purpose of which is to connect each of
the column lines to ground in turn as the counter counts from 0 to 9 . At the end of a single count from 0 to 9 the Sweep Generator sets the Trigger Latch which in turn shuts off the Timebase Oscillator and blanks the display while awaiting a further trigger.

## POWER SUPPLIES

The power supplies required by Digiscope are plus 5 V at 350 mA , plus 12 V at 60 mA , and minus 12 V at 60 mA , a total of about 3.25 W . The prototype is at present operating with a small mains power unit which will be described later, but there is no reason why existing supplies or batteries should not be used if desired, providing adequate regulation is maintained.

## CONSTRUCTION

From the outset Digiscope was planned as a miniature instrument and a probe like shape was considered essential to the design. The main circuitry is mounted on two strip board decks, one above the other, each measuring $7 \frac{1}{2}$ in by 2 in and spaced only $\frac{t}{2}$ in apart. Because of this dense constructional layout any intending constructors should

## -•BULK COMPONENT LIST•••

To take advantage of any concessions offered by retailers for bulk purchases we include the following list which covers the majority of components used in the Digiscope.

Resistors

| $100 \Omega$ | X10 | $1.8 \mathrm{k} \Omega$ | X1 | $10 \mathrm{k} \Omega$ | X3 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $150 \Omega$ | X1 | $2.2 \mathrm{k} \Omega$ | $\times 2$ | $15 \mathrm{k} \Omega$ | $\times 1$ |
| $330 \Omega$ | X1 | $2.7 \mathrm{k} \Omega$ | X1 | $39 \mathrm{k} \Omega$ | X1 |
| $560 \Omega$ | X2 | $3.9 \mathrm{k} \Omega$ | X2 | $100 \mathrm{k} \Omega$ | X1 |
| $620 \Omega$ | $\times 18$ | $4.7 \mathrm{k} \Omega$ | X3 | 910 k ת | $\times 1$ |
| $820 \Omega$ | X8 | $5.6 \mathrm{k} \Omega$ | X1 | $1 \mathrm{M} \Omega$ | X3 |
| $1 \mathrm{k} \Omega$ | $\times 17$ | $6.8 \mathrm{k} \Omega$ | X1 |  |  |
| $1.5 \mathrm{k} \Omega$ | X1 | $9.1 \mathrm{k} \Omega$ | X1 |  |  |

All $\frac{1}{6} W 5 \%$ metal oxide miniature
Integrated Circuits

| CA3100 | $X_{1}$ | 74145 | $X_{1}$ |
| :--- | :--- | :--- | :--- |
| $\mu A 710 C$ | $X_{9}(8 \mathrm{pin})$ | 7486 | $X_{2}$ |
| DTL935 | $X_{1}$ | 7474 | $X_{1}$ |
| 74123 | $X_{2}$ | 7410 | $X_{1}$ |
| 74160 | $X_{3}$ | 7401 | $X_{2}$ |

Transistors
2N3819 X2
Any silicon non $\quad \mathrm{X} 11$
Any silicon pnp X8
Diodes
Sillcon small signal $\quad \times 23$
RL50-01 (l.e.d.s) X80

Individual component lists will appear as usual with circuit diagrams as they occur.

| Capacitors |  |
| :---: | :---: |
| $0.1 \mu \mathrm{~F} 400 \mathrm{~V}$ | $\mathrm{X}_{1} \quad 24 \mathrm{pF}$ |
| $0.1 \mu \mathrm{~F} 250 \mathrm{~V}$ | X1 100pF |
| $0.1 \mu \mathrm{~F}>20 \mathrm{~V}$ | $\mathrm{X8}$ 330pF |
| 6.8 pF | $\times 1$ |
| Zeners |  |
| 5.6 V 400 mW | X2 |
| 3.3 V 400 mW | X1 |
| 9.1 V 400 mW | X1 |
| 5.1 V 400 mW | X 1 |
| Switches |  |
| DS16A 2-4 | X 2 |
| DS16A 1-8 | $\times 4$ |
| DS16A VAR24 | X1 |
| (ERG Compone | ents Ltd.) |
| Potentiometers |  |
| $1 \mathrm{k} \Omega$ (helical) $\mathrm{X}^{1}$ |  |
| $10 \mathrm{k} \Omega$ (hellcal) $\mathrm{X}^{1}$ |  |
| Miscellaneous |  |
| Formica |  |
| 0.15 in matrix Veroboard, b.n.c. socket |  |

be confident of their ability to operate in such miniature environments, and of course should ensure that their soldering irons and other tools are "mouse" enough for the job!

## CONTROLS

The large number of selector switches and controls required on any practical scope raised serious problems which were solved by using di.i.l switches made by ERG Components. These clever little switches are the same size as a 16 pin d.i.l. integrated circuit, and since they can be soldered directly onto a printed circuit they avoid the bulk and tangle of panel mounted rotary switches.

In all, 23 integrated circuits are used in the design, 13 logic types, and 10 analogue types, all except the op. amp being in dual-in-line packs.

## MATRIX

The display matrix is built up with the cheapest kind of discrete l.e.d.s on a piece of specially modified $0 \cdot 15$ in matrix Veroboard, and when constructed it forms a compact and durable sub-assembly. The display matrix is mounted at the rear of the case and viewed through a red filter.

The case is built up from Formica sheet joined with Araldite, and this material proved ideal because of its strength combined with a thinness which made it possible to build a "tight" protective cladding.

## Y-AMPLIFIER EXAMINED

The basic circuit of the $Y$ Amplifier is shown in Fig. 1.2. There are three parts to the circuit, an input attenuator, a source follower, and a high gain operational amplifier. The performance conditions required from this circuit are as follows.
(a) The input,impedance must be well defined at 1 megohm, regardless of the position of any of the controls.
(b) The attenuator should reduce the input signal amplitude in four switched ranges to achieve sensitivities of: $\quad 10 \mathrm{mV}$ per division 100 mV per division

1V per division
10 V per division
with other controls in the cal position.
(c) The output of the amplifier must produce a 56 V peak-to-peak swing in order to completely fill the screen vertically. (The figure of 5.6 V is arrived at by multiplying the threshold difference, 800 mV , by the number of thresholds to cover a column, 7.)
(d) The gain of the amplifier must be variable in eight switched steps of $1: 2: 3: 4: 5: 6: 7: 8$ to provide a fine sensitivity control over the total range of 10 mV to $80 \mathrm{~V} /$ division.
(e) The amplifier must have a good d.c. performance and a negligible output offset voltage so that output signals are accurately referred to 0 V .
(f) The a.c. performance of the amplifier should be as good as possible, i.e. the bandwidth should extend to at least 1 MHz at all gain settings.
(g) The inputs to the active devices must be protected against accidental application of overvoltages.

## ATTENUATOR

The attenuator is basically a switched potential divider giving a constant 1 megohm resistance to ground at the $Y$ input, but a variable resistance to ground as far as the amplifier itself is concerned.


The fact that the resistance to ground seen by the non-inverting terminal of the amplifier changes with the attenuator setting, makes it impossible to feed the attenuator directly into an op amp since the bias current drawn by the chip would produce a varying input offset voltage across the attenuator resistance and cause drastic shifts in the base level of the trace. Therefore, despite the fact that an op amp in the non-inverting configuration has an input impedance quite high enough to meet condition (a), the bias current problem makes it necessary to install an f.e.t. source follower as a buffer.

## SOURCE FOLLOWER

The f.e.t.s have the well-known advantage of requiring a negligible gate current for biasing, and in fact have most of the properties of a "cathode

## COMPONENTS . . .


follower" valve circuit. The source loads of the f.e.t.s provide a similar and constant resistance to ground for the bias currents of the two op amp inputs and therefore resolve the level shifting problem.
The operational amplifier chosen is the RCA CA3100S, because it gives an excellent bandwidth at very low cost. This choice is certainly not the last word, however, and it is possible to use most op amps with similar specifications, including the inferior 741 and NE531, both of which were used in the development of the Y Amplifier without component changes. It is to be expected that future op amps with a bandwidth better than the CA3100S will become available and could be substituted to improve the overall performance of the instrument.

## VOLTAGE GAIN

The maximum voltage gain required from the amplifier can be deduced from the maximum sensitivity required ( $10 \mathrm{mV} / \mathrm{div}$ ) and the full screen output swing ( $5 \cdot 6 \mathrm{~V}$ ).

$$
\text { Gain }=\frac{5.6 \mathrm{~V}}{7 \times 10 \mathrm{mV}}=80
$$

The minimum gain required is 10 (from condition (d)).

Switch S4 is provided to allow selection between a.c. or d.c. coupling and a reference ground connection, and C2 and C3 in the attenuator ensure that this circuit is compensated for a flat response at higher frequencies. R8 and D1 prevent the gate of TRI being driven into a breakdown condition by an out of range input voltage while set to the more sensitive ranges. VR1 makes it possible to use a nonmatched pair of f.e.t.s for TR1 and TR2 by facilitating the balancing-out of offsets due to differences in the f.e.t.s characteristics. VR2 makes it possible to set the gain precisely.

## Next month: the Timebase and Trigger Amplifier will be described

# Sinlevilluili UPDANI 

## REFERENCE ONLY

If you need to develop a stable reference voltage, then a Zener diode is the simple solution, but unfortunately not always a completely satisfactory one, because a standard Zener suffers from several problems which often require the use of extra components before the required reference can be relied upon.

The most obvious characteristic of a Zener is its slope resistance (i.e. how much does the voltage change with change in current?) and it's true that modern silicon devices have tairly low slope resistances, but where large line and load variations are encountered it is often still necessary to add an external Op-Amp voltage follower to the reference circuit.

Zeners are noisy too ... making them useful as "white-noise" generators but doubtful candidates for use in precision voltage regulators where output nolse can be an unwanted embarrassment. These problems are, however, small beer compared to their fundamental vice which is their shift of reference voltage with change in temperature. There isn't a lot you can do about this one other than putting the reference circuit in a temperature controlled oven, and of course we couldn't normally contemplate that, or could we?

Well, yes, now we can, because of a clever new "Zener" integrated circuit from National, the LM399. The LM399 contains a single monolithic chip incorporating a Zener, an


amplifier, and a temperature stabilised oven circuit all in a tiddly four lead TO46 can. Although on a single chip, the circuit breaks logically into two sections, the reference section and the temperature stabiliser, each with two leads, Fig. 1.

The reference section behaves just like a conventional Zener (only better!) and can be substituted into any circuit in which a Zener is normally used. The temperature stabiliser will operate from any supply between 9 and 40 V and keeps the chip temperature at a very stable $90^{\circ} \mathrm{C}$, regardless of ambient temperature fluctuations, giving the reference section a temperature co-efficient of $0.002 \% /{ }^{\circ} \mathrm{C}$ maximum!
This "Super-Zener" also has a low slope resistance of 0.5 ohms , a wide operating current range, and a very low noise figure into the bargain. What more could you ask?

## FISH WITH CHIPS?

Summer is with us again, and no doubt many readers will be turning their thoughts away from electronics and towards some nautical pursuit such as sea-fishing or cruising. If these readers have any pangs of remorse about neglecting their soldering irons for the season, perhaps the National LM1812 integrated circuit might persuade them to share those summer evenings between their two hobbies without conflict!
The LM1812 looks good even to a land-lubber like me because it contains in a single 18-pin d.i.l. all the necessary electronics to build an ultrasonic sonar transceiver with a 12W transmitter output and a versatile display drive facility. An ultrasonic system like this can be used in a boat as a fish-finding sonar or as a depth echo-sounderibut with
a tew extra components the LM1812 can also be used for amplitude modulated diver communications links, and can even be used in air at lower frequencies in, for example, intruder or burglar alarms.

In a typical role, as an echosounder (Fig. 2), the chip generates a single transmit pulse as the display driver motor passes a zero reference point, the display then Indicates any return signals received during the ensuing $360^{\circ}$ rotation, showing the depth to the sea-bottom or any obstruction.

The output from the transmitter drives a piezo-ceramic transducer (which is also used for reception) via a simple auto transformer, the resonant frequency of the transducer and the transformer being chosen to suit the application, 150 to 200 kHz being typical for echo sounding work. A single paraliel tuned circuit sets the exact frequency of operation of the acoustic recelver and transmitter, keeping external components to a minimum.

The display driver stage would traditionally operate a "peanut-neon" on the end of the motor driven display arm, via a step up transformer, but alternative displays such as l.e.d.s or even c.r.t.s can be catered for if desired.

Applications for the LM1812 are almost endless; this is a thoroughly useful and intriguing new device, with a versatile collection of control and signal inputs too numerous to mention, a terrible temptation to all those who intend to give up chips while they fish!

## DRIVE POWER

It a t.t.l. or MOS logic circuil has to talk to the outside world, it often finds the conditions a bit overpowering out there. Nasty big mains-switching relays and thirsty filament bulbs make your average logic gate tremble at the knees at the thought of those high collector voltages, inductive spikes, and all those dreadful milliamps. It is necessary under these conditions to use a discrete component circuit to carry out the dirty business, leaving the delicate and well brought up logic gate at a proper distance from the distasteful conditions which BFY50s and hairy-chested 2 N 3055 s can easily take in their stride!

If it were just a case of slapping a power transistor on the gate output, then this type of interface wouldn't be too much of a headache, but of course power driver circuits tend to grow like "Topsy", requiring resistors,
diodes, capacitors and perhaps even a second transistor for level shifting and extra current gain.

With problems like these, wouldn't it be nice if someone were to produce a gate-compatible power driver integrated circuit which tackled these difficult loads and didn't need a bevy of other components in support? Well, someone has, a firm called Dionics Inc. has taken a long look at the problem and has produced a device called the DI-445 which offers malled-fist performance in a velvetglove 8 -pin mini-d.i.p. package.

Dionics really did their homework well, because the DI-445 will switch up to 125 mA at 80 V and can be driven from a wide range of logic levels including the CMOS and t.t.I. variety. Not only that but it isn't even necessary to get the "load-ON" logic level right because the DI-445 will operate from either positive or negative true input levels. A fully isolated power diode is provided in the package for use as a "catching diode" when driving inductive loads such, as solenoids or relays, making it unlikely that any ancillary components vouild be required even with the most unfriendly of loads. In short, the new Dionics device could take a load off your mind!

# NEWS BRIEFS 

## Sea Call

WTH more than half of all 'phone calls into and out of Britain handled by undersea cable the speedy repair of cable breakdowns becomes even more important as even bigger complexes are brought into service every year.

To help tackle this problem the first of two specially designed cable ships commissioned by the Post Office has just started service. Claimed as the World's most advanced cable repair ship, the Post Office cable ship Monarch is now fully operational and its sister ship. CS Iris, will be operational in the Autumn.

As part of an exercise to gain further sea experience. a Royal Navy Wasp helicopter was, for the first time. landed on her helicopter deck whilst on the move. This manœuyre was to test and calibrate Monarch's glide path indicator equipment, a device used to give the helicopter pilots a correct approach reading.

## Microprocessor Tuior

To help fill the need for a form of microprocessor tutor kit which is based on the CDP18000 series of microprocessors with 256 bytes of random access memory. Outputs are registered on a 2 -digit hexadecimal display and a set of eight switches is provided for manual inputs.
The Microtutor is intended as an experimental/educational system and is accompanied by a manual written in a language designed for people with only a basic electronics background.

Microcomputer concepts are demonstrated by sample programmes, which are claimed to be simple to load and run on the tutor. All system signals can be direotly observed, and an external option socket allows users to add their own circuits, typical examples being suggested in the manual.

The tutor may also be used for prototyping COSMAC systems in applications which do not justify the use of more sophisticated programming aids.

Further information on the CDP COSMAC Microtutor and further systems can be obtained from RCA Solid State-Europe. Dept P.E. Sunbury-on-Thames. Middlesex. TW 16 7HW.



## SOLAR ENERGY

The United States are as ambitious in their harnessing of the energy of the Sun as they were with other space programmes. The project known as Powersat has a programme to launch up to thirty stations in orbit. The source of the scheme as reported earlier in Spacewatch is the Boeing Company. Each of the Powersats will give 10,000 MW at ground level, about equal to two of the present maximum output stations. The ground aerial would be about 8 km in diameter.
With these plans go the methods of launching the materials for the giant structures that need to be built in space to provide the microwave energy. The heavy lift vehicle needed for the task is provisionally scaled to be some 158 ft in diameter and 189 ft high. The power required for this vehicle would be provided by 22 oxygenhydrogen engines.

## SATCOM LAUNCHES

The RCA Satcom system began in 1973 and has been a highly beneficial and successful system. This was the first of the United States own domestic systems and used capacity leased from Canada using the Anik satellites. Since then there have been cost improvements and tariffs have been reduced so that voice grade circuits can now be rented for rather less than the normal terrestrial links.

The second RCA Satcom 2 was launched on March 26th and was to be finally in its appointed place at the end of June. This will be a geostationary position at 135 degrees west and will complement Satcom 1 which is at 119 degrees west.

Together the satellites will provide 48 channels, each one capable of one colour transmission. 1,000 two way voice circuits or 64 million bits of data per second. Satcom 2 will provide facilities to Alaska, Hawaii and the 48 states. Each of the four dish aerials will be oriented to cover a particular section of the territory.
The satellite weighs $2,0001 \mathrm{l}$ and when the solar panels are open the span is 37 ft . The main body measures $5 \mathrm{ft} 4 \mathrm{in} \times 4 \mathrm{ft} 2 \mathrm{in} \times$ 4 ft 3 in . It is a three axis stabilised vehicle and has the power for a life of eight years with all operations continuous.

The satellite was launched by a three stage Delta launch vehicle. The first stage was a modified Douglas Thor booster using nine strap on solid fuel motors. The booster itself utilised liquid oxygen and liquid hydrocarbon propellant. The second stage was powered by a liquid fuel engine which was gimbal mounted to suit control during the second stage burn. The third stage was spin stabilised. The satellite first went to 140 miles height and then three days later with the aid of an "apogee kick" motor was transferred to its geostationary position.

## FEED HORNS

The fixed assembly of four reflector aerials are offset for direction by the use of feed horns. This system makes for stable operation. There are a number of special RCA products employed. One of these is the graphite fibre epoxy material for the microwave filters, the waveguide sections and the antenna feeds.

Frequency and polarisation interleaving is achieved with the separate channels, 24 of them, by use of the transponder and the four antennas. Each channel has a 34 MHz usable bandwidth within a 500 MHz allocation. The dielectric reflectors use embedded wires in conducting grids to provide cross polarisation isolation. This is how the 24 channels are achieved.

## CONTROL SYSTEM

The satellite control system is very comprehensive. The attitude sectioh employs a sealed high speed wheel at $4,000 \mathrm{rpm}$, with a separate earth sensor and closed loop magnetic roll control. The RCA stabilised attitude control system provides three axis control by virtue of its gyroscopic rigidity provided by the wheel and the servo exchange of angular momentum with the craft main body. The inertial stability permits attitude determination by a single roll/pitch sensor of the earth's horizon. Magnetic torque is used and there is no need for moving parts. The system maintains orientation during the normal orbital operation, adjustment of
orbit and the injection of manoeuvies. The pointing capability is $\pm 0.21$ degrees about roll $\pm 0.30$ degrees about yaw and $\pm 0.19$ degrees about pitch.

For orientation the satellite uses 12 hydrazine thrusters in a closed loop for north/south. east/west rotation keeping. During a period of 7 minutes every three weeks this loop with its rate gyro will be energised to modulate the north/ south station keeping thrusters and compensate for residual thruster misalignment or mismatch to maintain attitude control.

For thermal control of heat absorption and heat rejection in order that the components and equipment stay within their operating limits (10 to 30 degrees Centigrade). space type mirrors and thermal insulation is employed to give passive control.

## POWER AND PROPULSION

The power system consists of two folded solar array panels and three nickel cadmium batteries. The maximum output of the power supplies is 740 watts regulated at 35 volts. This falls to 550 watts at 35 volts after eight years. The batteries supply the power during the two eclipse periods each year. The Sun oriented solar arrays and direct arrays balance the efficiency and weight ratio. With the main body of the spacecraft always aligned vertically, a single axis clock controlled drive shaft maintains the array toward the Sun. The area of the solar cells is 71.5 square feet. The use of distributed convertors through the system guards against power supply failure in toto. There is one in each of the 24 travelling wave tube amplifiers.

The propulsion system of the satellite is designed for the operational life of eight years. It carries 2161 b of hydrazine propellant in four tanks. The thrusters can be controlled directly from the ground control station. Thrusters can be selectively fired to cover spin/axis control in the transfer orbit as well as velocity control in synchronous orbit. The hydrazine reacts with a catalyst to provide the energy thrust from 12 engine assemblies. Maintainance of the longitude position and equatorial orbit inclination of 0.1 degrees needs 21 minutes of thrusting every three weeks. The apogee kick motor has a solid propellant fuel and $2,000 \mathrm{lb}$ orbit transfer thrust.

The command and telemetry system uses two omni antennas and controls all functions. The frequencies for these are 6.424 GHz , and for the beacons 3.701 to 4.199 MHz . Logic level commands are distributed to the spacecraft by a demodulator.

# $15-240$ WAT 

The HY5 is a mono hybrid amplifier ideally suited for all applications. All common input functions (mag Cartidge, tuner, etc.) are catered for internally, the desired function is achleved either by a multi-way switch or direct connection to the appropiate pins. The internal volume and tone circulis merely require connecting to external potentiometers (not included). The HY5 is compatible with all I.L.P. power amplifiers and power supplies. To ease construction and mounting a P.C. connector is supplied with each pre-amplifier.
EEATURES: complete pre-amplitier in single pack: multi-function equalisation: tow noise: low distortion: high overtoad: two simply combined for stereo
APPLICATIONS: hi-li: mixers: disco: quitar and organ; public address.
SPECIFICATION: Inputs-magnetic pick-up 3 mV ; ceramic pick-up 30 mV ; tuner 100 mV : microphone 10 mV : auxiliary $3-100 \mathrm{mV}$ : input impedance $47 \mathrm{k} n$ at 1 kHz . Outputs-tape 100 mV ; main output 500 mV A.M.S. Active Tone Controls-treble $\pm 12 \mathrm{~dB}$ at lokMz; bass $\pm 12 \mathrm{~dB}$ at 100 Hz . Distortion- $0.1 \%$ at 1 kHz : signal/noise ratio 68 dB . Overioad- 38 dB on magnetic pick-up. Supply Voliege- $=16-50 \mathrm{~V}$. Price $\mathbf{5 4 . 7 5}+59$ p VAT. P. \& P. Pree
HY5 mounting board B.1. $48 p+6 p$ VAT. P. \& P. free
The HY30 is an exciting New kit from I.L.P. It features a virtually indestructlble I.C. with short circuit and thermal protection. The kit consists of: I.C., heatsink. P.C. board, 4 resistors. 6 capacitors, mounting kit, together with easy to follow construction and operating instructions. This amplifier is ideally suited to the beginner in audio who wishes to use the most up to date technology available. FEATURES: Complete kit: low distortion: short. Open and thermal protection: easy to bulld. APPLICATIONS: updating audio equipmant; guitar practice amplitier; test amplifier: audio oscillator. SPECIFICATION: Output Power-15W R.M.S. Into en. Distortion-0.1\% at 15W. mput Sensitivity500 mV . Frequency Response $10 \mathrm{~Hz} \mathbf{- 1 6} \mathrm{kHz}-3 \mathrm{~dB}$
Price $54.75+59 \mathrm{D}$ VAT. P. \& P. free
MY50 ieads iLP s total integration approach to power amplitier design. The amplifier features an integral heatsink together with the simplicity of no external components. During the past three years the amplifier has been relined to the extent that it must be one of the most reliable and robust High Fidelity modules in the World. FEATURES: low distortion; integral heatsink: only five connections: 7 amp output transistors: no external components.
APPLICATIONS: medium power hi-fl systems; low power disco: guiter amplifier
SPECIFICATION: Input Sensitivity -500 mV . Output Power- 25 W A.M.S. into 8 n. Load Impedance46 . Distortion- $0.04 \%$ al 25 W at 1 kHz Signal/Noise Ratio- 75 dB . Frequency Response- 10 Hz -$-5 \mathrm{kHz}-3 \mathrm{~dB}$. Supply Volisge- -25 V Size $-105 \times 50 \times 25 \mathrm{~mm}$. Price $\mathbf{5 6} \mathbf{2 0}+\mathbf{7 7} \mathrm{p}$ VAT. P. \& P. free
The HY120 is the baby of I.L.P.'s new high power range, designed to meet the most exacting requirements including load line and thermal protection this amplifier sets a new standard in modular design.
FEATURES: very fow distortion; Integral heataink; load line protection; thermal protection: five connections: no external components.
APPLICATIONS: ht-f: high quality disco: public address: monitor amplifier: gultar and organ.
SPECIFICATION: Input Sensitivity- 500 mV . Output Powar-60W R.M.S. Into 8n. Load Impedance416 n . Distortion- $0.04 \%$ at 60 W at 1 kHz . Signal/Noise Ratio- 90 dB . Frequency Response- 10 Hz $45 \mathrm{kHz}-3 \mathrm{~dB}$. Supply Vollage $\pm 35 \mathrm{~V}$. Size. $114 \times 50 \times 85 \mathrm{~mm}$.
Price $£ 14 \cdot 40+£ 1 \cdot 16$ VAT. P. \& P. Iree
The HY200 (now improved to give an output of 120 watts) has been designed to stand the most rugged conditions such as disco or group while still retaining true hi-fi performance.
FEATURES: thermal shutdown: very low distortion: load line protection: integral heatsink: no external components.
APPLICATIONS: hi-fi: disco: monitor; power'slave; industrial; public address.
SPECIFICATION: Input Sensitivity- 500 mV . Output Power-120W A.M.S. into 8 n . Load Impedance-$4-16 \Omega$. Distortion- $0.05 \%$ at 100 W at 1 kHz . Signal/Noise Ratio 96 dB . Frequency Response- 10 Hz $45 \mathrm{kHz}-3 \mathrm{~dB}$. Supply Voltage $=45 \mathrm{~V}$. Size $114 \times 100 \times 85 \mathrm{~mm}$
Price $£ 21 \cdot 20+£ 1 \cdot 70$ VAT. P. \& P. Aree
HY400
240 W into $4 \Omega$
The HY400 is I.L.P.'s "Big Daddy" of the range producing 240 W into $4 \Omega$ ! It has been designed for high power disco or public address applications. If the amplifier is to be used at continuous high power levels a cooling fan is recommended. The amplifier includes all the qualities of the rest of the family to lead the market as a true high power hi-fidelity power module.
EEATURES: thermal shutdown: very low distortion; losd line protection: no external components. APPLICATIONS: public adaress: disco: power slave: industrial
SPECIFICATION: Output Power-240W A.M.S. into $4 \Omega$. Load Impedance- $\mathbf{- 1 6 \Omega}$. Diatortion- $0.1 \%$ t 240 W at 1 kHz . Signal/Noise Ratio- $2 d \mathrm{~dB}$. Frequency Response- $10 \mathrm{~Hz}-45 \mathrm{kHz}-3 \mathrm{~dB}$. Supply Voltage $- \pm 45 \mathrm{~V}$. Input Sensitivity -500 mV . Size- $114 \times 100 \times 85 \mathrm{~mm}$.
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# Paris Components Show '76 By D. Gibson articipation by exhibitors 

Pfrom 28 countries and well over 1,000 stands made this year's "Paris Components Show" a must for mosi people in the industry. Officially designated the Salon International des Composants Électroniques, it drew over 72,200 visitors.

## POWER TRENOS

From such a galaxy of products displayed it is difficult to single out individual items. However, a number of the more interesting components and equipments do give useful food for thought. For example, the transistor sounding the death $k$ nell for valves did not sound so convincing a statement when examining a millimeter tube capable of delivering $1,000 \mathrm{~W}$ at 40 GHz . That power is for the c.w. mode; if pulsed, the tube gives $10,000 \mathrm{~W}$ at $\mathbf{9 5 G H z}$ ! It also has a life of many thousands of hours.

It would, of course, be foolish to "knock" transistors in terms of power since they have become established in many applications of lower dissipation. One such component shown, was housed in an innocent TO83 package and was rated at 350 W at up to 50 kHz . Not in the gigahertz range but still quite good enough to convert many a treasured hi fi speaker into an audio fuse.

Doubtless such devices will be a useful addition to the armoury of pop groups where such specifications will be loudly acclaimed-very loudly.


## The TAD2002 8 watt amplifier

## LACK OF SUPPORT

In the lower power audio range interest was aroused in the TDA2002. Basically a class-B audio amplifier it has been specifically designed for car radios and gives 8 W into a $2 \Omega$ ? load. Perhaps the most striking thing about it is the marked lack of discrete supporting components required to form the complete amplifier; four capacitors and two resistors.

It would appear to be a useful component for simple record player amplifiers, modulators for amateur uses and many other applications.

## SPOTLIGHT

Transistors with impressive power ratings were not the only items of interest. Indeed, a device described as an electro-optic timing switch proved to be positively intriguing.

It incorporates an elapsed time indicator in which a drop of clear liquid moves through a mercury-filled capillary tube at a constant rate established by a fixed electric current through the tube.

This clear liquid "dot" is used as a lens to focus light from a built-in illuminator on to one or other of a pair of photo-sensitive sensors. The light is initially directed on one of the sensors, which is conductive, closing its associated load circuit. At the end of the cycle the control "dot" shifts the light to the other sensor, causing it to become conductive, and the first sensor to switch "open". Clearly a design which required a spot of pure genius.

## FRENCH CONNECTION

With over 575 foreign exhibitors there was no shortage of ideas from overseas. But the French, too, offered a fair share of interesting exhibits. One such company showed a 24 -position slide switch rated at $250 \mathrm{~V} 0 \cdot 1 \mathrm{~A} \cdot 50 \mathrm{~Hz}$. They are suitable for direct insertion into p.c.bs and other switch combinations are also available.

Perhaps a switch-tuned radio offering instant selection of up to 24 stations without the need to fiddle about tuning would be a good application?

A switch with a difference describes a new relay system which really amounted to a do-it-yourself kit. The armature module is the basic element and this plugs into its allotted place in a rack. Around this one can add various time, contact and memory modules.
Each additional module is a snapon unit and all modules butt together offering immediate automatic mechanical operation without any need for linkage assembly. Thus one starts with the basic armature assembly module and then adds such modules as are required for the system desired.

## TRAMSFORMATON

A transformer designed for television sets hardly seems an item to cause much interest, and yet one company from Belgium described

Jean Renaud multiway slide switches

just such an item as innovative. It has been designed for switched: mode power supplies and has the useful advantage of offering regulated secondary voltages. This means that many of the regulated voltages which were previously taken from the line scanning stage can now be derived from the switched-mode supply circuit.

An added benefit is that scanning stage power requirements are reduced and circuit reliability is improved. Apart from meeting scanning stage power requirements, this transformer also provides supplies to the set's h.f. and l.f. stages as well as for the picture tube's heater voltage.

This could be the start of a trend (certainly in Europe) to use switched-mode power supplies in entertainment-type equipment.

In this kind of supply, the mains is taken directly to rectifier(s) and used to power switching semiconductors which feed a smaller, usually ferrite transformer. The a.c. voltage. now at very much higher frequency. is rectified and smoothed and fed out as a source of d.c. voltage. The smoothing components can be very much smaller since the frequency is very much higher. A dramatic decrease in size is possible using this technique.

## MULTI-HEAD

This year's show covered 28,000 square metres and would have been even bigger if the section dealing with measuring instruments had been included. However, this was left out because another exhibition in Paris was mainly devoted to this specific area. This did not mean that no instruments were present and many of these were included in the electronics production area.
Here, such items as multi-head p.c.b. drilling machines were displayed. These were controlled bv a paper tape which dictated where each head should drill a hole in the $x$ and $y$ axis. Many thdusands of holes can be automatically drilled in this way with an accuracy of typically $\pm 0.01 \mathrm{~mm}$. Some of these multi-head equipments also allow for an automatic change of drill.

## A GOOO START

Very few areas of application escaped attention at this exhibition. The automotive market is orie example where electronics manufacturers and designers are cultivating an increasing interest.

For one German exhibitor it was the contact breaker which was the focus of attention. The idea is to do away with nasty physical metal switching contacts which get dirty, pit and wear. This company is trying opto electronics as a possible solution. The second objective is to tie this in with transistorised ignition.

Some vision-phone (Visiophone) equipment on show. Quite clearly aTele-phone!


The latter approach permits currents exceeding 8A which, together with reverse voltages of over 400 V are superior to conventional systems.
The total system concept features a light barrier installed in the distributor comprising a l.e.d. functioning as a transmitter and a photosensitive transistor as a receiver. In the 3 mm long infra red optical path a slotted cap rotates on the distributor shaft and allows a light beam to pass at the firing point. The make/break ratio determining the cut-in period of the primary circuit depends upon the slot width. The light from the phototransistor is amplified and directly acts upon the firing transistor (a power darlington type) into whose emitter-collector circuit the primary winding of the ignition coil is collected.

While development is still going on, figures and results to date show that this company is off to a good start!

"Do-lt-yourself" relay system modutes

## REDS IN CONTROL

With ultrasonics making some headway in remote control for things like television receivers it would seem that such a system would develop from strength to strength. But no-there's a rival, and a serious one, too. It's our old friend infra red coming up fast off the rails with a challenge which iust cannot be taken lightly.

Ultrasonic waves have a disadvantage that harmonics of the line timebase may lead to acoustic disturbances. The new remote control system offers up to 31 control functions and employs binary coding to keep peripheral outlay to a minimum.
Transmitted commands are selected by an $8 \times 4$ button matrix arranged on the transmitter. The
receiver contains the interpreting circuit and three storages for analogue functions such as sound, colour saturation, brightness, with the associated analogue-to-digital converters.

## ON THE LIGHT SIDE

An impressive sight on one stand was a lone battery. It has a shelf life of ten years and during that time can be relied upon to provide a minimum of 30 hours power. Useful for applications such as batterypowered burglar alarms or emergency torches it also featured elsewhere in a complete flashlight which was claimed to function happily from $-53^{\circ} \mathrm{C}$ to $+75^{\circ} \mathrm{C}$.

Low level light tubes are always impressive but when one enters a completely dark room and still finds that the tube can resolve the scene there is something unnerving about it all-makes undressing in the dark a bit silly.

A French company displayed one such tube which has a sensitivity comparable to that of photographic film rated at 100,000 ASA. For the non-photographic reader this means that scenes with an illumination as low as one hundred thousandth of a lux can be televised at 25 images per second (a normal broadcast standard) with a resolution of over 700 t.v. lines per image.

Remember the early projection television tubes? Well, the military for one still uses them, or rather more sophisticated versions. One such beasty gives a very bright ( 10,000 foot candles), fine trace2,000 t.v. lines.

It is employed to project dynamic images on to a screen of several metres square. Multi-coloured images are produced by superimposing the images from a number of tubes each with its own particular colour phosphor.

These are just a handful of the many items shown at the Paris show. Space available allows but brief mention of midget power supplies, valves which give $500,000 \mathrm{~W}$ and tiny variable capacitors which measure only 1.9 mm in diameter.
Perhaps the most staggering thought is that all these new things will be old by next year when, of course, there will be the 1977 Salon International des Composants Électroniques.

# C Stirling Sound Droducts 

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SS110 similar to Ss ios but more powerful giving low into dohms. Mk. 3
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Add this to your unstabilised supply to obtain a steady
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$240 \mathrm{v}-50 \mathrm{~Hz}$ from your 12 v car battery. 25 watt- $54.75 \quad 300$ watt (24v)- $\mathbf{5 2 5} .45$ 40 watt- $£ 8.27$ 75 watt-\{12.03 150 watt- 221.27 400 watt (12v) $£ 39.0$ 150 watt- £21.27 $\quad 500 \mathrm{watt}(24 \mathrm{v})$ - $1 \mathrm{~kW}(58.18$ 300 watt (12v)-£33.03 1.5 kW (110v)- £127.00 All above invertors are in kit be purchased bullt up in maral case but may for use. Price list sent on receiol of s.a. Prices include post 8 packing.
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# The All-Electronics Show 

## By G. Godbold

ITT seems as if we can add another trade show to the Spring "walkabout" on the evidence of attendance at Park Lane's Grosvenor House where more than 160 companies had stands for the All-Electronics Show during the three days-13th to 15th April.

In the grand surroundings of the Great Room and Ballroom there was plenty to attract and interest particularly in the way of new items.

## INSTRUMENTS

Not least in today's increasingly bewildering world of electronics is the mass of test and measuring equipment available. In this area, synonymous with technical excellence, but at a price, is Tektronix who also market under the Telequipment name.

On display was a couple of portable diagnostic aids to the TV serviceman; the Telequipment D32, a dual-trace 10 MHz oscilloscope and the S 22 , a single trace 5 MHz model. Both of these are extremely compact, weighing just 10 lb with a relatively large screen area in relation to control fascia. Operation can be from mains or rechargeable batteries.

Prominently figured was the Tektronix TMS 500 series of test and measurement modules. These represent single function instruments which readily combine to form a console sharing a common power rail. Over 30 of these plug-in modules make up the series.

Other new scopes seen were the Dynamco 7500, a dualtrace portable instrument with a vertical sensitivity of 10 mV /division over a 40 MHz bandwidth and the OS 4000 digital oscilloscope, the star of the Gould Advance range which combines a conventional 10 MHz scope performance with a digital memory system capable of storing signals up to 450 kHz . An output module is incorporated which processes the contents of the digital memory to provide a hard copy output for use with a chart recorder.

Other new Gould Advance products were the SG200 seven range signal generator covering 160 kHz to 230 MHz and the DMM7A, a $3 \frac{1}{2}$ digit d.v.m. with a 10 A measuring capability on both a.c. and d.c.

Claiming a world scoop B \& K Labs showed their 2131 Digital Frequency Analyser designed to measure and display octave and third octave spectra in real time with outputs available to analogue or digital peripherals.

The Molex 4130 series of d.i.f. switches


## SEMICONDUCTORS

Last year the Philips giant absorbed Signetics and as a result of this it seemed rather strange to see the names Mullard and Signetics sharing stands.

Amongst the broad range of i.c.s and f.e.t.s on display was the MOSPOWER VMP-1. This rather remarkable f.e.t. has a power rating of 35 W with maximum allowable current of 2 A . The breakdown is 60 V and the drive current is nanoamps. This growth area will represent a serious challenge to traditional bipolar devices particularly in high speed power switching. Signetics also displayed a $150 \mu \mathrm{~W}$ triple op amp which draws only $50 \mu \mathrm{~A}$ from a $1 \frac{1}{2} \mathrm{~V}$ supply.

A mong the Mullard items were the new range of Locmos i.c.s, which now total 70 , microprocessors and the recently introduced field programmable logic arrays.
Making its UK debut for Plessey was the SP750B high speed comparator which is claimed to be the world's fastest. Also on show by Plessey Traffic and Instrumentation was their VISTA scanning system designed to check p.c.b.s for defects after screen printing and plating.

## CASES

About a decade ago lunch-boxes were a modish container for constructors' projects. With the enormous variety of metal and plastic cases both in colour and geometry there is no excuse for tatty containerisation. Lektrokit, a name familiar to all constructors, made a new contribution with their range of "Transistek" modular instrument cases. Totalling 25 models in eight different styles, colours and sizes, they are of low cost and professional standard. Support trays and aluminium are available for components and p.c.b.s.
The Imhof-Bedco display also included a new range of plastic boxes, called IMboxes, all of which had integral slots for accepting p.c.b.s.

## PASSIVE COMPONENTS

Departing from the traditional toroid shape and showing a significant cost-saving, Reading Windings introduced a "square toroid" range of transformers. Aimed at the audio and instrumentation market, the output power range extends through 5-120VA offering high, efficiency with low radiation.

New from Molex Electronics was the 4130 series of multi-position d.i.I. binary option selection switches. Designed for mounting onto a p.c.b. they feature single and ganged lever action with up to ten discrete channels.

Practical Electronics also had a static display of projects past, present and future.

Telequipment D32 dual-trace 10 MHz battery'mains 'scope



THE receiver section of the system is both sensitive and selective and has been designed for use with the transmitter described last month, which operates at around 27 MHz (dependent on crystal; see later).

It is a very stable crystal controlled superhet based on the TBA651 integrated circuit which is a complete radio including r.f. amplifier, oscillator, and i.f. amplifier. The package of the TBA651 is unusual since it is a 16 -pin quad-in-line, sockets being difficult to obtain.
The manufacturer's data gives a maximum operating frequency of the oscillator of 30 MHz and the receiver can be operated from a supply of $4 \cdot 5-16 \mathrm{~V}$, although the author has found the best performance to be at 9 V , using the crystal oscillator circuit as shown in Fig. 7.

## CIRCUIT DESCRIPTION

From the circuit shown in Fig. 7, it will be seen that L1, C3 are a tuned circuit to the received r.f. signal, L2, C4 coupling the r.f. amp to pin $1 . \mathrm{R} 1$ is the untuned load of the r.f. amplifier with C5 coupling to the mixer stage at pin 4 . The external components of the oscillator L3, L4, C7 and the crystal are connected to pins 6 and 7. The mixer output is coupled to the i.f. input pin 13 by C8 and T1. The second and final i.f.t. T2 connected to pin 10, has the detector D1, i.f. filter R3, C12, C13, and detector load R6, connected to the i.f. secondary. The a.g.c. reference voltage is connected to pin 15 by R4 whilst $\mathrm{C} 14, \mathrm{C} 15$ are a further i.f. filter. The delayed a.g.c. is controlled by R5, CII, both values being adjustable to suit the delay required. It is important that C16 is included, since R6 is above ground potential. An ' S meter' can be connected across R 6 if required: for this purpose, pins have been included on the board.


## CRYSTAL OSCILLATOR

For those not familiar with the channels used for radio control in the 27 MHz band, a table has been included which may be of some use (Table 1).
The Toko transformers T1 and T2 are for an intermediate frequency of $455 / 470 \mathrm{kHz}$.

Table 1
The colour codes used for different crystal trequencies

| Channel <br> Colour | Transmitter | Receiver |  |
| :--- | :--- | :---: | :---: |
|  | MHz | I.F. 455 kHz | I.F. 465 kHz |
| Brown | 26.995 | 26.540 | 26.530 |
| Red | 27.045 | 26.590 | 26.580 |
| Orange | 27.095 | 26.640 | 26.630 |
| Yellow | 27.145 | 26.690 | 26.680 |
| Green | 27.195 | 26.740 | 26.730 |
| Blue | 27.245 | 26.790 | 26.780 |

## RECEIVER

Fig. 7. Circuit diagram of the receiver section


COMPONENTS ...

## Resistors

| R1 | $1.5 \mathrm{k} \Omega$ | R3 | $1.5 \mathrm{k} \Omega$ | R5 |
| :--- | :--- | :--- | :--- | :--- |
| R2 | $150 \Omega$ | R4 $33 \mathrm{k} \Omega$ |  |  |
| All resistors | $\frac{1}{8} W$ | 330 | carbon |  |
| R6 | $10 \mathrm{k} \Omega$ |  |  |  |

## Capacitors

C1 $10 \mu \mathrm{~F} 25 \mathrm{~V}$ elect.
C2 22 pF ceramic
C3 22 pF ceramic
C4 10 nF ceramic
C5 1nF ceramic
C6 $10 \mu \mathrm{~F} 3 \mathrm{~V}$ elect.
C7 47pF ceramic
C8 1 nF ceramic
C9 $0: 1 \mu \mathrm{~F}$ plastic or paper
C10 $0.1 \mu \mathrm{~F}$ plastic or paper
C11 $100 \mu \mathrm{~F} 6 \mathrm{~V}$ elect.
C12 $0.01 \mu \mathrm{~F}$ plastic or paper
C13 $0.01 \mu \mathrm{~F}$ plastic or paper
C14 $0.9 \mu \mathrm{~F}$ plastic or paper
C15 $0.1 \mu \mathrm{~F}$ plastic or paper
C16 $0.1 \mu \mathrm{~F}$ plastic or paper
C17 $40 \mu \mathrm{~F} 25 \mathrm{~V}$ elect.

## Semiconductors

*IC1 TBA651 D1 OA 90
Inductors, transformers
L1 13 turns 5 mm dia. former 28 s.w.g.
L2 4 turns 5 mm dia. former $28 \mathrm{~s} . w . g$.
L3 $\quad 1.5 \mu \mathrm{H}$ r.f. choke
L4 $80 \mu \mathrm{H}$ r.f. choke
*T1 Toko YRCS 11098 AC2 455kHz (1st i.f.)
*T2 Toko YHCS 11100 AC2 455 kHz (3rd i.f.)

## Miscellaneous

XL1 27 MHz R/C band, selected to suit transmitter XL1
Aladdin 5 mm coil former
Printed circuit board $(60 \times 80 \mathrm{~mm})$
and p.c.b. pins
*The i.f. transformers and i.c. may be obtained from Ambit International, 37 High Street, Brentwood, Essex CM14 4RH


Fig. 8. P.C.B. master and component layout of the receiver

## THE PRINTED CIRCUIT BOARD

The circuit is first drawn on a piece of single sided p.c.b. $(60 \mathrm{~mm} \times 80 \mathrm{~mm})$ using a p.c.b. marker pen. The surplus copper is then removed by immersing the board in etchant and then the holes drilled using a No. 59 drill.
The base is removed from the 5 mm coil former using a fine toothed saw and a 5 mm hole drilled in the p.c.b. as in Fig. 8, a small round file will be found useful in opening out the hole to the required size so that the former is a good fit. Do not fit the coil former at this stage as it is better to wind the coils first-details will be shown later.

All the components are next assembled on the board and soldered using a fine tipped iron and thin gauge solder, then the i.c. can be fitted and soldered, taking the usual care as with all semiconductor devices.


Fig. 9. Winding jig for LI and L2

## COIL WINDING DETAILS

If difficulty is found in obtaining the two r.f. chokes then they can be wound as follows: for L3 wind 30 turns of $40 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. enamelled copper wire on to a $\frac{1}{4} \mathrm{~W} 100 \Omega 2$ resistor and for L 4 wind 250 turns of the same gauge on to another 10052 resistor.

The coil winding details for L1 and L2 are shown in Fig. 9. A piece of wood $15 \mathrm{~mm} \times 50 \mathrm{~mm} \times$ 100 mm and three 50 mm nails are assembled as shown.

The bottom coil L 1 is first wound by putting a few turns of wire around nail 1 at A, then winding 13 turns on to the former in a clockwise direction, but leaving a space at the bottom of 2 mm to enable the former to fit in the hole in the p.c.b.

When 18 turns have been wound, finish the wire on nail 1 at B . The other winding is done in a similar way, then the ends on the coil are secured with a suitable glue and when dry the wires cut at the nail ends.

## TESTING AND ALIGNMENT

First examine the p.c.b. for any poor soldered connections and short circuits between the copper, Next connect a 9 V battery via a multimeter and measure the current taken by the receiver; this
should be about 10 mA . Remove the multimeter and connect it to points A-B on the receiver with a low d.c. voltage range selected.

Remove the aerial from the transmitter and switch it on at a distance of a few feet from the receiver. Now adjust the core of T 2 for maximum reading on the meter and then adjust T 1 in a similar way. Repeat the operation to ensure maximum output.

The core in L1/L2 can now be adjusted for maximum reading on the multimeter.

If a signal generator is used, then typical figures for sensitivity are as shown in Table 2.

Table 2
Sensitivities and meter/'scope measurements obtained when receiver is tested with a signal generator

| Signal <br> Generator | Voltmeter | L.F. on 'scope |
| :---: | :---: | :---: |
| $\mu \vee$ | $V$ | (Modulation |
| 10 | 0.2 | 65 |
| 18 | 0.5 | mV |
| 20 | 0.8 | 1,000 |
| 50 | 1.0 | 1,500 |
| 70 | 1.5 | 15 (noise) |

## TTL INTERFACE

The interface unit is necessary for two reasons: first it reshapes the pulses that have been distorted in the receiver due to L/C circuits and bandwidth limitation and secondly it presents to the TTL circuitry a pulse of the correct polarity and ampli-tude-both these features being important with any TTL equipment as it requires a fast rise time pulse for correct operation.

## CIRCUIT DESCRIPTION

The circuit diagram is given in Fig. 10.
The signal from the receiver is fed to the base of TR1 which is unbiased and is held cut off by R7. A positive going pulse switches on TR1 which unlatches TR2 (being half a Schmitt trigger) while TR3 (the other half) switches on.

The emitter follower TR4 switches off due to TR3 collector falling to a low positive potential. When the signal pulse goes negative and the cut off point of TR1 is passed, the Schmitt resets quickly and TR4 is again switched on.

The net result of this fast switching is to produce an output very similar to that of the transmitted pulse train.

## CONSTRUCTION

The p.c.b. design for the circuit described above requires a piece of board measuring $44 \mathrm{~mm} \times$ 42 mm with all resistors $\frac{1}{8}$ watt and BC108 transistors for TR1-TR4. Pins were used for wire take off points as soldering to the copper board with wire is seldom satisfactory.

The component layout and p.c.b. master are given in Fig. 11.
contimued on pages 572 and 582

## INTERFACE



## COMPONENTS . . .

Resistors
R7 $33 k \Omega$
R8 $100 \mathrm{k} \Omega$
R9 $47 \mathrm{k} \Omega$
R10 $27 \mathrm{k} \Omega$
R11 15k $\Omega$
R12 $47 \mathrm{k} \Omega$
R13 1k $\Omega$
R14 $5.6 \mathrm{k} \Omega$
R15 1-2k $\Omega$
All resistors $\frac{1}{8} \mathrm{~W} 5 \%$ carbon
Transistors
TR1-4 (4 off) BC108
Miscellaneous
Printed circuit board ( $44 \times 42 \mathrm{~mm}$ ) and p.c.b. pins


Fig. II. Component layout and p.c.b. master for the interface section

## DECODER



COMPONENTS . . .

| DECODER |
| :--- |
| Resistors |
| R16 $3.3 \mathrm{k} \Omega$ |
| R17 $680 \Omega$ |
| R18 $1 \mathrm{k} \Omega$ |
| R19 $39 \mathrm{k} \Omega$ |
| All resistors $\frac{1}{\mathrm{~B}} \mathrm{~W} 5 \%$ |
| Capacitors |
| C18 $30 \mu \mathrm{~F} 10 \mathrm{~V}$ tantalum |
| C19 $0.022 \mu \mathrm{~F}$ plastic or paper |
| Semiconductors |
| TR5 2 N 3708 |
| IC2 74121 |
| IC3 7490 |
| IC4 7442 |
| Miscellaneous |
| Printed circuit board $(70 \times 55 \mathrm{~mm})$ and |
| p.c.b. pins |

Fig. 12. Circuit diagram of the decoder


FIg. 14. Component layout and p.c.b. master for the decoder


## KARNAUGH MAP DISPLAY

A Karnaugh map is a simple visual representation of a number of two-state functions, and may be used to obtain a slmplified Boolean expression from a truth table. The mapping is usually done with pencil and paper -this unit uses 16 l.e.d.s to portray the output function of a loglc clrcult having four input varlables.

## Plus... <br> RADIO CONTROL SYSTEM PART 3 DIGISCOPE...PART 2

## PRACTICAL

## ELECTRONICS

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a) reference oscillator
b) divider chain
c) decoder circuits
d) display inhibit circuits
e) display driving circuits.

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... and how it works
A crystal-controlled reference is used to drive a chain of 15 binary dividers which reduce the frequency from $32,768 \mathrm{~Hz}$ to 1 Hz . This accurate signal is then counted into units of seconds, minuţes, and hours, and on request the stored information is processed by the decoders and display drivers to feed the four 7 -segment LED displays. When the display is not in operation, special power-saving circuits on the chip reduce current consumption to only a few microamps.

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Get new life out of your old tubes with this fluorescent tube economiser

By A. J. Bassett

THis article describes a device which will readily light a large fluorescent tube from a 12 V car battery without need of a choke or starter. The tube will start instantly without flickering, and may glow even more brightly than if it were used with 240 V mains power. It is even possible to light an old tube which will no longer function at all on mains power, and when used for this purpose the device may truly be described as a tube-economiser, especially when one considers that it can quickly be built, mainly from scrap!

## CIRCUIT OPERATION

The circuit of the device is shown in Fig. 1.
For the non-technical person this is simply a fluorescent lamp which can be switched on or off by means of a switch in the 12 V supply from a car battery or other source capable of giving about 5 amps.
Technically it is a simple power oscillator which forms a push-pull inverter supplying power to the fluorescent lamp tube by way of a high voltage winding on the transformer.

The power transistors operate in a switching mode. Although in theory they should dissipate only a little heat, this is not realised in practice due to various sources of inefficiency in the circuit.
The more astute constructor will observe that there is no apparent bias on the transistor bases to cause them to conduct and initiate oscillations. The circuit in fact relies on the leakage present in the germanium transistors to generate a small impulse in the primary winding which "kicks" the circuit into oscillation at switch-on. If silicon devices were used, a bias arrangement would have to be incorporated due to their much lower leakage. After trying various germanium transistors, OC35s were finally chosen as they provided consistently high performance in this arrangement.

## BASE DRIVE

The numbers of turns on the base drive windings (nominally only three turns each) may be easily altered if it is required to increase or decrease the base drive to the power transistors, and this may be
done individually to compensate for one running hotter than the other.

By changing the values of R1, R2, the currents flowing in TR1, TR2 may be made to match yet more closely, and this results in greater efficiency.

By raising the value of R1, less current will flow in TR1 and likewise by raising the value of R2, less current will flow in TR2.
If the values of R1, R2 are lowered, a greater current will be taken and the fluorescent tube will glow more brightly. However, care must be taken not to exceed the maximum current rating of the transistors, which is 6 amps . A 7 amp fuse is used in the circuit.

## FREQUENCY OF OSCILLATION

This is dependent upon a number of factors, principal ones being supply voltage, number of turns on the primary, and the nature and dimensions of the ferrite material of the core. If an annoying whistle is produced, the frequency may be raised by reducing the number of turns on the primary until it becomes inaudible. However, this results in an increase in current consumption, and this should be allowed for as described under the "Base Drive".

## TUBE OPERATION

The tube operates in a somewhat different manner to that when 240 -volt mains is used.
Because it is started by the action of high-voltage pulses from the oscillator on the gas in the tube, there is no need of heaters, choke or a starter and the tube will start from cold.

Although in theory this means that the cathodes will have a shorter life, the author has found that in practice tubes which will no longer operate from 240 V mains will run quite happily from the oscillator for many extra months. So in these days of ecology-consciousness and material re-cycling, it makes a lot of sense to squeeze extra life out of your old fluorescent tubes by using such a tube economiser!

Because the frequency of operation is so much higher, the tube provides, when adjusted for ultra-

## COMPONENTS . . .

Resistors
R1, $2 \quad 8.2 \Omega 2.5 \mathrm{~W}$
Transistors
TR1, 2 OC35

## Transformer

Line output transformer obtained from a scrap t.v. and associated hardware

## Miscellaneous

Silicone grease, 500 sq.cm. of 16 s.w.g. aluminium sheet, $18 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. insulated copper wire, $30 \mathrm{~s} . \mathrm{w} . g$. insulated copper wire. FS1 7A



Fig. 1. The circuit diagram of the inverter
sonic operation, about 40,000 pulses of light each second compared with only 100 pulses per second from 50 Hz mains. This relies much less on our persistence of vision, and provides a more steady source of illumination.

## CONSTRUCTION

Mount the OC35 transistors onto a piece of sheet aluminium of 16 s.w.g. or heavier and having a surface area of $500 \mathrm{sq} . \mathrm{cm}$. or more. (Two separate smaller pieces may be used, one per transistor.) No insulators are needed, as, if the power supply is negative earth, the metal will provide the required earth connection to chassis. For positive earth systems the transistor cases must be insulated, or $n p n$ devices used. A small quantity of silicone grease is placed between each transistor and the aluminium sheet or heat-sink to assist in transfer of heat from the transistors to the metal.

## TRANSFORMER DETAILS

Obtain the line output transformer from a scrap t.v. set, and dismantle it, being careful not to break the ferrite core. Retain the ferrite core, which usually comes apart in two C-shaped parts. Keep also the core mounting components.

The next stage is to prepare new windings for the transformer. The primary consists of 16 turns of 18 s.w.g. insulated copper wire, centre tapped. At each end of the primary a further three turns of thinner wire are wound in the same direction.

Make two bobbins of thin cardboard or cartridgepaper according to Fig. 2. They may be dipped in quick-drying varnish and allowed to set in a warm dry place.
When the bobbins are dry, wind the 16 -turn primary winding onto the stoutest one in a single even layer, bringing out a loop a few centimetres long at the centre tap as shown in Fig. 3a. Over each half of this winding, wind three more turns of thinner wire, which may be the same gauge as the secondary winding. The wire may be held in place with tape.

## SECONDARY WINDING

The secondary is wound using a much thinner insulated wire, 30 s.w.g. being suitable. It is pilewound in two layers. A method of winding which the author recommends is shown in Fig. 3b.

Leaving a few centimetres of wire at the beginning for connecting purposes, pile wind ten successive bundles of 20 turns each along the length of the bobbin. Obviously each bundle must occupy less than 2.4 mm of the length of the bobbin, so it is a good idea to mark the bobbin into ten equal intervals before you start winding. This gives the first 200 turns. Cover with insulation, and repeat the winding back along the bobbin to give another 200 turns. Secure this with tape. After testing, it may be dipped in varnish.

Fit the C -cores into the windings so that the transformer has the appearance of Fig. 3c, and clamp firmly together. The clamps are not shown in the diagram as different transformers use clamps of a variety of shapes. When tightening the clamps be sure not to crack the C-cores as they are brittle, so only moderate pressure should be used.

## TESTING

Connect the primary winding of the transformer to the two power transistors as shown in Figs. 1 and 3 , by way of resistors $\mathrm{R} 1, \mathrm{R} 2$. If the 12 V power is connected now, a high-pitched whine should be heard, and the unit should consume a current of about 3 amps . Whenever the unit is switched on, be careful that the secondary wires are kept out of contact with anything they should not touch, as a high voltage appears across them.

When the fluorescent tube is connected, the pitch should rise, and may become inaudible as it reaches the ultrasonic range of frequency.

Switch off the unit and disconnect the 12 V supply. Now connect one end of the secondary winding to the two pins at one end of a fluorescent lighting tube. Similarly connect the other end of the secondary winding to both pins at the other end of the tube. As a high voltage will be present, this winding and all connections should be well insulated before the oscillator is switched on.

When you switch on, the tube should light up immediately. If it does not, try another tube, or increase the number of turns on the secondary winding.

Too many turns, however, may cause the device to consume too much current and the transistors to overheat: Alternatively, the oscillator may stall, giving a dim, flickery light.

## INSTALLATION

The transformer should be installed in a cabinet or enclosure so that the high voltages of the secondary winding are out of harm's way. The transformer may be mounted in place using the original fixing arrangements, but care should be taken not to mount it in close proximity to sheet steel; the reason being that the high-frequency magnetic field in the vicinity of the transformer can cause steel objects to heat up quite considerably at a distance of a few centimetres. Indeed the author found that, especially if the frequency of oscillation is ultrasonic, temperature produced in a piece of metal placed within the core could melt solder!

Fortunately this effect is only apparent to a large degree with magnetic metals, and the aluminium heat-sink with the power transistors can be mounted fairly close to the transformer. The heat-sink becomes warm in use due to heat produced within the power transistors, so should be provided with adequate ventilation.

The connection between the tube and the transformer should be directly wired-there is no need of chokes, starters or capacitors, and if any of these are present in the lampholder, disconnect them from the tube.

Ensure that the tube housing and any exposed metal surfaces are correctly earthed.

## USES

Where a 12 V battery is the only source of electrical power for illumination, this steady light, coupled with the reduction of shadows when a long tube is used, make the tube economiser far preferable to the use of incandescent bulbs.
Obviously the same advantages hold for use in power-cuts together with the advantage that the economiser is cheap to build, and can be put together quite quickly.


## TURBULENCE

Nobody would welcome, say, five years of absolute stability more than the semiconductor industry Alas, the overall situation is as turbulent as usual but while the agony of the manufacturers is as intense as ever, the savage competition and struggle for market shares is, as 1 pointed out a couple of issues ago, beneficial to the consumer.

The two latest casualties are both Canadian-based. The brave hopes of Microsystems . International Ltd., which started up in 1969 were never realised. The company, a subsidiary of Northern Electric, never became profitable and in 1974 is said to have lost £20 million.

Another Canadian company, Siltek, also in the CMOS trade and founded in 1972, might have put in a bid for MIL's assets but is now in receivership following the price-cutting battle between market leaders like RCA and Motorola.

Siltek couldn't stand the heat despite being largely funded by the Canadian government. So hopes of a rescue from Siltek have foundered and the latest news is that MIL's huge investment in the paraphernalia of semiconductor manufacturing, estimated at some £6 million, was scheduled for public auction in the first week of June.

Motorola has run into difficulties in fabricating $I^{2} \mathrm{~L}$ microprocessors and is reported to.be withdrawing from the technology and concentrating on $n$-channel MOS which has been outstandingly successful. Texas Instruments appears to be pressing on with I2L but with a delayed launch date. The I2L technology, if it can be realised successfully, gives a large increase
in operating speed but pays a penalty in noise immunity and packaging density.

Meantime, the battle for supremacy in the market place continues and according to recent forecasts the market for microprocessors in Western Europe will top E 300 million by 1984. Motorola claims to hold 15 per cent of the total world market this year and 35 per cent of the 8-bit market and, as I write, is threatening to slash prices for the small user (i.e. in quantities up to 100) by another 50 per cent. Mullard in the U.K. has already cut the small-quantity price of its Signetics 8 -bit NMOS from $£ 38$ to $£ 18$ in $1-24$ quantities. In 100-999 quantities the price drops dramatically to $£ 12$.

A speaker at the recent Seminex exhibition and conference in London suggested that the current commodity life for semiconductor devices was now only two years before obsolescence set in.

## INSTRUMENT NEWS

After years of flat trading, electronic test and measuring instruments are perking up. Bright news is that the MRCA Tornado automatic test equipment (ATE) contract has now been finally firmed up in a European consortium led by Marconi-Elliott Avionic Systems as prime contractors and product managers.

The amount of the contract is not revealed either in test stations to be built or in cash, but it seems likely, taking into account the number of aircraft involved and the way they are expected to be deployed by Britain, Germany and Italy, that there won't be much change out of $£ 50$ million. MEAS's partners are Siemens in Germany and Selenia in Italy with British Aircraft Corporation and Rohde \& Schwarz as sub-contractors.

Good progress was reported by Malden Electronics. This new company is virtually the old and wellestablished Venner Electronics, formerly a part of the multinational AMF Corporation. Former director of AMF, David Ollington, bought the electronics interests at the turn of the year and although the new company has a different name it is carrying on where Venner left off and is still making established products of the old firm like timercounters, sine and square wave generators and test instruments for the Post Office, and will continue to support existing Venner instrument users with spares and service.

The new company reports six months work in hand and some potentially substantial orders in the pipeline. New instruments are also in development and these will be announced later in the year.

An export boost, through a marketing agreement with the Data Tech Division of the Penril Corporation located in Santa Ana, California, is expected by Racal Instruments. American engineers have already been to England to receive technical instruction on the Racal 99 Series of counter-timers and frequency meters, introduced last autumn, and on a number of communications test instruments.

The 99 Series of instruments are a complete family all built round a single LSI chip of considerable complexity designed by Racal and manufactured in the c.d.i. process by Ferranti. The instruments are said to have sold well in Europe since the original launch. In the United States they are to bear the Data Tech label but will all be custom-built by Racal for the U.S. market.

The largest of the wholly British instrument companies, Marconi Instruments, has just launched a new range of low-cost digital frequency meters using a custom built MOS-LSI chip. Large scale integration cuts down assembly costs and improves reliability. The TF2432 going up to 560 MHz is priced at $£ 380$ but for the "lowfrequency" engineer (how low is low?) who is content with 10 Hz to 80 MHz , the TF2430 comes at a bargain price of $£ 165$ with a standard crystal with a stability of 1.3 parts in $10^{\circ}$ per deg C .

For long the slumbering giant in test gear, Philips has just introduced two new oscilloscopes, part of a programme to double the company's market share in the test gear market from its present five per cent during the next two or three years.

## HAPPY RETIREMENT

After more than thirty years of service with the Radio \& Electronic Component Manulacturers' Federation (RECMF), Arnold C. Bentley retired as Director on May 6.

Affectionately known throughout the industry as Ben, he joined the RECMF as Assistant Secretary in 1945 when he retired from the Royal Air Force. In his later years, in addition to his other duties, Ben was deeply involved in the problems of component standardisation and international harmonisation. He was a founder-member of the Committee of European Passive Electronic Component Manufacturers' Associations (CEPEC) in 1965 and was President for the year 1969/70.

While wishing Ben a happy retirement we also extend a welcome to the new Director, William Barrett, who joins RECMF after five years as Director of the Scientific Instrument Manufacturers' Association (SIMA).


THIS automatic stop circuit was designed to stop a model railway train for a pre-determined time at a station. It requires only two connections to the track and needs no external supplies.

The trains can approach the section from either direction and the delay will still operate.

## CIRCUIT DESCRIPTION

The circuit diagram and track connections are shown on Fig. 1. As can be seen, there are only two connections to the track; these can be connected either way round.

With no engine on the track, all the voltages in the circuit will be zero, hence the transistors and the thyristor will not be conducting.

Suppose a train now appears. When it enters the station section it will stop as there is no current path by the bridge circuit.

The positive output from the bridge will now rise to the locomotive supply, and Cl will commence to charge via VR1. At this point in time TR1 base is negative with respect to its emitter, hence TR1, TR2 and the thyristor are still turned off.

When Cl charges to half the locomotive supply, TR1 will turn on, turning TR2 on, which in turn fires the thyristor.

There is now a through path via the bridge for the locomotive current and the train will start again.

## DELAY TIME

The delay time that the train spends in the section is determined by VRI and C1. R1 is included to prevent Cl being shorted to the positive output of the bridge.

Because the emitter of TR1 is held at half the locomotive supply, and Cl is being charged by the locomotive supply, the time at which TRI turns on,
and hence the delay time, is largely independent of the locomotive supply. The train can approach the section at speed or at a crawl and it will stop for the same time.

Diode D5 discharges Cl when the thyristor fires. This ensures that Cl starts charging from the same point each time, giving a consistent delay time.

## COMPONENTS . . .

Resistors
R1-R3 $1 \mathrm{k} \Omega$ (3 off)
R4 $10 \mathrm{k} \Omega$
R5 $1 \mathrm{k} \Omega$
R6* $820 \Omega$ see text
All $\frac{1}{2}$ W 10\% carbon
Capacitors
C1 $250 \mu \mathrm{~F}$ elect. 25 V
C2 $4,700 \mu \mathrm{~F}$ see text
C3 $250 \mu$ F elect. 25 V
Potentiometer
VR1 $100 \mathrm{k} \Omega$ miniature
Semiconductors
D1-D4 Silicon Bridge Rectiffer REC 63 (R.S.)
D5, D6 1 N4001
TR1 2N3704
TR2 2N3703
CSR1 Any 1 A 50 V thyristor (see text regarding holding current)

## Miscellaneous

0.1 in Veroboard $3.3 \mathrm{in} \times 1.8 \mathrm{in}$


Fig. 1 Circuit of the Auto-stop unit

Once fired, a thyristor stays on as long as it is passing current. Unfortunately, a locomotive motor and the track itself causes the current to be intermirtent. The circuit was built originally without R6, D6, C2 and it was found that the locomotive proceeded in short bursts. This could be caused with dirt on the commutator or the track breaking the locomotive supply. Here the thyristor would turn off, and the locomotive would have to wait for Cl to charge again.

## TIME CONSTANT

C2 and R6 provide current for the thyristor to keep a small current flowing through the thyristor during the intermittent breaks in the locomotive current. The thyristor will stay on for a time determined by the time constant C2 and R6.

R6 is determined by the holding current of the thyristor (usually about 5 mA ) and the time required (and hence C2) by the length of the station section.

The author found 820 ohms and $4,700 \mu \mathrm{~F}$ for R6 and C2 gave good results with the thyristor used and a section length of six inches.
If the time constant is made too long, the train following will not stop at the station as the thyristor will still be conducting.

Capacitor C2 is charged by the locomotive via D6, and this charging action softens the stop of the train, giving a somewhat fast ramp.
The start is unramped, but the circuit was designed for simplicity of design and connections rather than sophistication.

## CONSTRUCTION

The circuit was built on standard 0.1 in pitch Veroboard as shown in Fig. 2.


Fig. 2 , Vero cutting details and component layout

The circuit is very uncritical with regard to transistors and thyristor types although some experimentation with the values of R6 and C2 may be required as explained above.

The value of VRI and Cl give a delay time of up to 10 seconds. This can be increased further by increasing the value of Cl .
Note that C 2 is mounted external to the printed circuit board because of its size.

## CONNECTION TO TRACK

The station section should be an isolated section with one common rail as shown in Fig. 1. The two track wires from the printed circuit board are then connected as shown. These wires can be connected either way round and the track polarity is not important.
To test the circuit, put the controller on full and connect a 12 V 1 W bulb across the track in the station section. It should shine at full brilliance then dim out as C2 charges. After a few seconds delay it should come back on again.
A locomotive can now be tried. If it does not restart or it pulls away in a series of small jerks the values of C2 and R6 probably require adjusting.
If it is required to have the auto-stop facility switchable, simply connect a switch across the a.c. inputs of the bridge to short the circuit out. The train will proceed through the section without stopping.

## MAIL BAG

The on-going increase in postal and telephone charges does not seem to have made any difference to our post bag or our telephone bell. Enquiries continue to flood in.
We find that there are two points we are constantly mentioning. In the first place we just cannot afford to reply to any readers' letters, particularly those not assoclated with projects we have published, unless they are accompanied by a stamped addressed envelope. Were we to undertake to do so our post bill would become astronomic.

We cannot deal with technical enquiries by telephone. Readers should write in, giving details of symptoms and perhaps some test point readings, when requesting technical help so that we can at least give the relevant author some idea of the problems involved.
Finally, whilst we normally supply details as to source of components in each project we do assume that the constructor refers to advertisements and has an awareness of general sources. Thus, where goods are generally available we do not specify a source. You could save the cost of a letter by reading the advertisement pages first.

[^3]RADIO CONTROL continued from page 572

## THE DECODER

The decoder receives the pulse train from the receiver and interface boards and detects the sync pulse allowing a b.c.d. count to commence for decoding in the decimal decoder. The output is presented to the inputs of the servo drive boards.

## CIRCUIT DESCRIPTION

The positive going pulse train (a typical one is shown in Fig. 13) from the receiver is fed to the base of TR5 (Fig. 12) which inverts the pulses at its collector. The negative edge of the pulse operates the monostable (IC2) causing an expansion of the pulse to take place set by $\mathrm{t}=\mathbf{C 1 9}$ (R19 2 k ) $\log _{e} 2$ and with the values selected gives an ouptut pulse length at output Q of 0.6 ms .

This 0.6 ms pulse is fed to the $A$ and reset $I$ inputs of IC3 (decade counter), whilst the unexpanded pulse train is fed to the reset 2 input of IC3. When the 0.5 ms sync pulse arrives at the reset 2 input the counter is reset to 0000, whereupon it proceeds to count until the counter reaches 1001 , when the internal 'b.c:d.' resets will occur.

The reset pulse is 0.1 ms (this being the difference between the 0.6 ms monostable output and the 0.5 ms sync pulse). The b.c.d. output of IC3 is connected to the b.c.d. input to the decimal decoder IC4, which provides a decimal output of $1-9$.

These are negative going outputs and each is repeated every 20 ms or so, and offset from each other by 0.25 ms , which is the pulse spacing in the train.


Fig. 13. Typical input waveform to TR5 with channel 3 operated

## CONSTRUCTION

The layout of the printed circuit is shown in Fig. 14 and measures $70 \times 55 \mathrm{~mm}$. The location of all i.c.s should be noted before they are soldered as they are difficult to remove afterwards if wrong.

A fine tipped soldering iron with small gauge solder prevents solder runs and short circuits. There are few discrete components on the board, but note the connections of TR 5 which is of the form TO92a with the collector in the centre.

On completion of the soldering, the author found that washing the board with a small stiff brush and a drop of Turpentine removed all flux deposited during soldering. The result then, is a clean board that can be examined before connecting the supply.

Next month: The final part of this series will describe the servo amplifier, servo drive and relay drive sections.

## PRTENTE REDUETNO.

## EXTENOING PATENTS

It is interesting to note that patents are only very occasionally. and under very special circumstances, extended. The record for extension is held by BP 524 443, in the name of Georges Valensi. This patent, which dates back to January, 1939, expired, not after the normal 16 years' maximum life span of a British patent, but in June, 1971, after 32 years, having been extended several times.

The patent covered the original idea for using differential circuits to produce a series of comparisons of the various colour voltages in a colour TV system, thereby to produce a representative colour signal voltage which could be transmitted with relatively reduced band width. The extensions were granted to Valensi (and EMI) because the invention was obviously of dramatic significance and its exploitation was delayed first by the War and second by the late involvement of this country in colour TV.

Now that the patent has finally expired, its contents are, of course, free for any colour set manufacturer to use.

## IMPROVED BASS

In BP 1406 427, the Ferrograph Company of Slough, Bucks., suggests that the bass response of 'a reflex cabinet can be extended with less risk of undesirable resonance effects by oxtending the reflex aperture into the cabinet.

The conventional form of a bass reflex port is shown dotted in Fig. 1. The new idea is to make a port of much larger diameter and much greater length. To accommodate this greater length, the port is formed as an L-shaped pipe that extends first back into the cabinet and then upwards at right angles to behind the drive unit, Fig. 1.

The whole cabinet, including the L-shaped pipe, is filled with sound absorbent material such as long fibre wool or plastics fibre. In practice it is found that, with an enclosure of $2 \mathrm{ft} \times 1 \mathrm{ft}$ 3ins $x$ 1ft 6ins, the pipe should be between 1 ft and 3 ft long with an optimum length of 2 ft .

## WHY NOT A TRADEMARK?

We have previously reported on the continuing rise in official patent fees.

Individuals and firms active in the electronics field should not forget the other valuable source of commercial protection-Registered Trademarks. Many of the household names in electronics are registered trademarks, and although trademark fees are also continually rising, the cost of securing and maintaining a trademark may prove insignificant in comparison with the commercial benefits which accrue.

Anyone doubting the commercial benefit of a trademark need only recall the last time they asked for a branded electrical product by name and in consequence legally obliged the salesman to sell them only goods from the firm of origin denoted by the brand name.

Individuals or firms launching a new electronic product and seeking to establish whether the name of their choice is already owned by a competitor must currently either search for themselves in the Public Search Room of the Trademarks Registry or employ a searcher or trademark agent to do the job for them. Failure to carry out such a check before launching or importing and advertising a new product can prove to be an extremely expensive mistake if the chosen name subsequently proves to be someone else's property. It is, however, a common occurrence.

$$
4
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BP 1406427


Fig. 1

The Trademarks Registry (in the same building as the Patént Office, Southampton Buildings, Chancery Lane, London WC2) has on file a quarter of a million registered trademarks, but somewhat surprisingly a search through these marks must still be conducted by hand.

Although the simplicity of unambiguously classifying a trademark name or logo (as opposed to the difficulty of classifying the content of a lengthy patent document) should make trademarks ideal subjects for automated data storage and retrieval techniques, there is still no positive move in this country towards automation. This is partly because trademarks can give perpetual protection (Trademark No. 1, "Bass for Beer" is now 100 years old) and still in force, for there are now two quite different classification systems running alongside each other.

Although legislation dating back to 1938 provides for elimination of the older, obsolete classification, this has never been implemented, and there is currently even talk of attempting to devise a system for automating concurrent searches through both classifications. As this would, in some cases, involve searching through more than 20 separate classes for different filings of a single word used in a single situation (there are in all 50 classes in one classification and 34 in the other), the idea must surely be a non-starter.

## IV BRIEF

BP $1 \quad 426$ 962-Granada TV Rentals Ltd.: Stereo Signal Generator. A full circuit diagram, complete with values, is given for a clever system of locally producing multiplex stereo transmission signals (for instance for shop demonstration of a receiver) using a colour television chrominance chip.

BP $1427 \quad 133$ - Smiths Industries Ltd.: Vehicle Monitoring and or Controlling Apparatus. A very complex digital system for automatically monitoring virtually every performance parameter of a motor car, converting sensor signals into pulse trains and providing automatic control when necessary.

# Using Mos dieqtal L.C. 

By D.B. JOHNSON-DAVIES \& A.M. MARSHALL ga,

## PART 7

THis series has, so far, been restricted to a consideration of complementary mos devices at the small-scale integration level. That is to say to the level of gate packages, monostables and Schmitt triggers. Many of the devices and circuits have been fairly routine conversions of popular tTL configurations into cmos logic. The fact that it has been possible to do this in a comprehensive way with a new technology possessing very special properties of its own, is important. These special properties of cmos logic-high noise immunity, low power dissipation and wide operating voltage range-achieve a greater relevance and clarity when presented in a setting already made familiar by the widespread use of TTL.

## MSI CMOS

TTL gained its ascendancy over previous logic families more for its ability to provide a wide range of medium-scale integrated mSI functions than for its speed. This wide variety of MS1 functions made it the standard choice even for users who did not require its speed but, nevertheless, had to pay the penalties that go with it.
Similarly what is really attracting users to смоs is its ability to pack a much greater density of functions on to a chip. cmos provides an even bigger range of MSI and LSI devices, some of which have the unique property of embracing both the digital and analogue worlds.
Table 7.1 gives a list of standard cmos devices, above the gate level of complexity, for which there are no equivalent devices in any other logic family. These are, of course, additional to the dozens of other functions that go to make cmos a comprehensive logic family.
What immediately becomes apparent from looking at the list is the number of high-density, complex sequential logic functions that cmos technology has made possible. These are the MSI and LSI flipflops, shift registers and counters that considerably simplify sequential logic design. They have, as will be shown, created new areas of application.
When the ability of cmos to switch or multiplex analogue as well as digital signals is also taken into consideration, we find that смOs provides the designer with entirely new and powerful methods of solving familiar problems.

## D TYPE FLIP.FLOP

Before discussing these devices and associated circuits, it is worth taking a brief look at how смоs technology achieves this high packing density in sequential logic functions.

The basic cell from which cmos shift registers and counters are constructed is known as the "D" type latch flip-flop (Fig. 7.1). This consists simply of two inverters and two transmission gates. The transmission gate, described earlier in this series (Fig. 2.2)

Table 7.1-Standard CMOS devices

| Part No. | Description |
| :---: | :---: |
| 4006 | 18-stage static shift register |
| 4016 | Quad bilateral analogue switch |
| 4017 | Decade counter/divider |
| 4020 | 14-stage binary counter/divider |
| 4022 | Divide-by-8 counter/divider |
| 4024 | 7 -stage binary counter/divider |
| 4031 | 64 -stage static shift register |
| 4033 | Decade counter/divider with 7 -segment outputs |
| 4034 | 8-stage static shift register |
| 4040 | 12-stage binary counter/divider |
| 4045 | 21-stage counter |
| 4046 | Digital phase-locked loop |
| 4051, 2, 3 | Bidirectional analogue multiplexers |
| 4055, 56 | Liquid crystal display drivers |
| 4059 | Programmable divide-by-"N" counter |
| 4060 | 14-stage binary counter/divider and oscillator |
| 4062 | 200-stage dynamic shift register |
| 4067 | 16-channel multiplexer/demultiplexer |
| 4511 | BCD to 7-segment latch decoder driver |
| 4517 | Dual 64-bit static shift register |
| 4521 | 24 -stage binary frequency divider |
| 4534 | 5 -decade counter |
| 4536 | 24-stage programmable timer |
| 4549 | Successive approximation A/D register |
| 4553 | 3-digit coünter |
| 4557 | Variable length (1 to 64 bit) shift register |
| 4562 | 128-bit static shift register |
| 4566 | Time-base generator |

is formed by connecting a p-channel and an $n$-channel-mos transistor in parallel. The result is an excellent single-pole, single-throw switch with no offset voltage.
The simplicity of the cmos flip-flop comes from the fact that the two transmission gates literally switch the inverters into one of two states at each transition of the clock. To operate the transmission gates the clock (C) and its complement ( $\overline{\mathrm{C}}$ ) are required. When the clock is low, TGI is on and TG2 is off. So $Q$ follows the complement of the input $D$. When the clock is high, TGI is off and TG2 is on. In this condition the data is memorised in the closed loop latch formed by the two inverters and TG2.

Cascading two of the flip-flops of Fig. 7.1 and using NOR gates instead of inverters to give SET and RESET facilities forms the standard cmos masterslave "D" type flip-flop (Fig. 7.2). In operation the
logic level at the input " $D$ " is delayed before appearing at the $Q$ output by half a clock cycle (hence the "D" meaning delayed).

The cmos part number is 4013 and it consists of two "D" type flip-flops. The pin connections and truth table are shown in Fig. 7.3. A modified version of the " $D$ " type flip-flop is formed by additional gating at the input to form two inputs, called J and K. This is the dual J-K flip-flop, 4027 (Fig. 7.4). The extra inputs make the 4027 especially useful in control logic, as well as in sequential counting.

## DIVIDERS AND CLOCKS

The 4013 and 4027 are useful in simple dividers. where it is required to divide by two, three or four. For higher numbers it is more economical to use cmos dividers from Table 7.1 such as the 4017 or 4022 which will be described later.


Fig. 7.1. Basic Cmos " $D$ " flip-flop. The transmission gates switch in opposition at each clock transition. When TG1 is on, TG2 is off and the complement of the $D$ input appears at the $Q$ output. At the next clock transition, TG1 is off and TG2 is on and the data is memorised in the closed-loop latch formed by the two inverters and TG2.


Fig. 7.3. Truth table and package diagram of the 4013 dual " $D$ " type flip-flop


Fig. 7.2. The "D" type master/slave flip-flop. With SET and RESET at " 0 ", the NOR gates are alternately cross-coupled to form latches by TG2 and TG4. When the clock is low, TG1 and TG4 are on and TG2 and TG3 are off. Thus the "Slave" is latched and the data is entered into the "Master". When the clock goes high, the transmission gates reverse their states. The "Master" latches the data which is directly applied, through TG3, to the output of the "Slave" (Q)


| 1 | $k$ | $a_{n}$ | $a_{n+1}$ |
| :---: | :---: | :---: | :---: |
| 0 | 0 | $0 n$ | $a_{n}$ |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 |
| 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 0 |

Fig. 7.4. Truth table and package diagram of the 4027 dual J-K flip-flop


Fig. 7.5. Divide-by-2 circuits using the 4013 " $D$ " type flip-flop and the $4027 \mathrm{~J}-\mathrm{K}$ flip-flop


Fig. 7.6. Three-phase clock circuit using the 4013. The 4013 is connected to form a divide-by-3 circuit and the addition of a NOR gate provides three outputs of sequential non-overlapping pulses


Fig. 7.7. Four-phase clock circuit. The divide-by-4 arrangement is decoded by a quad NOR gate to provide four sequential outputs


Fig. 7.8. Digital low-pass filter. Any signal at the input must last for at least two clock pulses, otherwise it will be suppressed by the reset action of the first flip-flop


Fig. 7.9. The 4013 as a simple monostable


Fig. 7.10. Maximal-length pulse sequence generator. All possible combinations of $0 s$ and 18 in the shift register (except the all-zero state) will appear at the output before repeating

Fig. 7.5 shows divide-by- 2 arrangements using the 4013 or the 4027. Fig. 7.6 is a divide-by- 3 circuit using 4013 dual " $D$ " type flip-flop. The addition of a 2-input NOR gate ( $\frac{1}{4} 4001$ ) provides simple decoding to convert the divider into a 3 -phase clock. The three outputs give sequential non-overlapping pulses.

Similarly in Fig. 7.7, a quad 2 -input NOR gate (4001) decodes à divide-by- 4 circuit to produce a four-phase clock. An application of these last two circuits would be to commutate the 4016 bilateral analogue switch, and thus produce a simple electronic equivalent of the electromechanical uniselector.

## LOW-PASS FILTER

The $S$ and $R$ inputs, which are provided for asynchronous setting and resetting, can be used to convert the divide-by-4 circuit of Fig. 7.7 into a digital low-pass filter (Fig. 7.8).

This circuit is very useful for the suppression of spurious signals of high amplitude and long duration. It requires a simple adjustable oscillator connected to the CLOCK input to control the filter characteristics. The action of the filter is such that the input signal must endure for at least two clock pulses before it is transmitted to the output. Otherwise it is suppressed by reset action of the first flip-flop.

## MONOSTABLE

Because of the very high input impedance and the high threshold voltage of cmos, the 4013 flip-flop is useful in a variety of timing applications involving large time constants. Fig. 7.9 shows the 4013 connected to operate as a monostable or one-shot. The output pulse width ( $t$ ) is approximately 0.66 RC .

Because of the very high input impedance $R$ can be as high as 10 megohms. Its minimum value is restricted to around 20 kilohms by the limited output current available from cmos. However, this gives a ratio of $500: 1$ for the output pulse width by varying $R$ alone.

The capacitor $C$ discharges when its voltage reacfies the threshold of the reset input of the flipflop. The diode ensures that it discharges quickly. However, a lower limit of its value is determined by the flip-flop's requirement of a minimum reset pulse width of 125 nanoseconds. The minimum value of C would typically be $0.003 \mu \mathrm{~F}$.

With $\mathbf{C}$ and R both variable it is possible to adjust the output pulse width in the ratio of $10,000: 1$.

## M-SEQUENCE GENERATOR

An economical noise generator can be constructed by cascading two 4013 flip-flops as a four-stage shift register, Fig. 7.10. When the outputs of the register are fed back to the input, as shown, via an exclu-sIVE-OR gate and the clock is started, the output will cycle through all possible combinations of 0 s and is in the register, except the all zero condition, before repeating. The all zero condition has to be avoided since it would latch-up the register.

In the circuit shown, the sequerice will repeat after ( $2^{4-1}$ ) bits i.e. 15 bits have been generated. This is the maximal-length pulse sequence ( $m$-sequence) for a four-stage shift register. One application is the generation of noise where the harmonic spectrum is related to the clock frequency.

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The wire which used to feed the ignition coil (via the tacho loop) is now connected to the emitter of TR1. This is turned on by R2. TR2 is modulated by the contact breaker, R3 making the load minimal. TR2 thus modulates TR1 and causes current pulses to reach the tacho.

The coil lead on the original was of the resistance type (part of the car's ignition boost system). If the car to which the system is to be fitted does not have a resistance lead, some adjustments of RI may be necessary. This may also be the case for tachos of different sensitivity.

The unit is so small that it can fit in the case of the C.D. ignition system.

F. C. Dunford.<br>London.

1

## tacho slaye

THIS circuit is designed to enable current. impulse tachos to be used on cars fitted with electronic ignition.

Sometimes the pulsed coil lead can be looped through the tacho pickup coil, but in my case this was not possible because the loop is internal and the impulse lead permanently connected to the ignition feed. In any case this unit avoids very drastic rewiring.


Fig. 1

## SOUND TO L/GHT SYSTEM

THE circuit operates on a nine volt supply, the gate of the s.c.r. or triac being connected to the positive rail via a 330 ohm resistor in parallel with a 100 microfarad capacitor. The cathode or MT1 in the case of a triac is taken to mains neutral.

Ths arrangement means that the s.c.r. or triac is normally turned on. The input from the sound source is put through an isolating transformer and the secondary is connected to the base and emitter of the transistor TR1. Thus when a sufficiently strong signal causes the transistor TR1 to turn on, an alternative current path is provided and this causes the s.c.r. or triac to turn off. The $50 \mathrm{k} \Omega$ resistor VR1 is used to bias the transistor TR1 to alter the light/dark periods.

The circuit has been found to perform well in a Disco and if fairly low wattage lighting in suitable colours is used, can produce quite pleasing effects between
records where the standard, lighting is normally ultra-violet.
K. P. White.

Brighton. Sussex.


AFTER surveying the various designs for quiz buzzers, I did not find one that was simple enough to warrant construction of a buzzer system catering for six or more contestants. This prompted the development of the circuit shown below, Fig. 1.
The action of the lighting is simple. R2 holds CSR1 gate at ground potential until S1 is depressed. This connects R5 to the gate via R1, the gate "ON" voltage is gained and CSR1 switches on, lighting the lamp LP1. Resistor R1 limits the gate current when the gate is shorted to the anode by the switch S1.

If thyristors with particularly sensitive gates are used, then small capacitors. 1OnF, may have to be connected across the gate resistor R2. This is to prevent the CSR from switching on due to noise, which can be picked up by the long cable connecting the contestant's pushbutton to the control box.

The inhibition circuit action is niore complex, but only uses one steering diode per lamp circuit instead of several. When CSR1 is switched on, the p.d. across R2 $(600 \mathrm{mV}$ with the resistance values as shown) is sufficient to switch the CSR on. As the gate currents and p.d.s required to turn the CSRs on varies from device to device, then R2 may have to be changed to suit.

Once CSR1 is switched on and lamp LP1 lit, point B is pulled down to near ground potential; it will be, approximately 0.7 V above 0 volts 1 due to the p.d. across the current conducting CSR. Likewise point A will be at near ground potential, at 1.0 to 1.4 volts depending upon the diode D1. Thus if S2 is pushed the potential difference at point C will be the same (Fig. 2). So the p.d. across R4 will be 0.3 V giving insufficient drive to the CSR gate to turn it on.
This circuit may be wired up to cater for any number of contestants just by adding more switching modules. Although TIC45s and 1S120s, were used in the prototype there is no reason why other types of thyristor and diode should not be used, bearing in mind that the correct functioning of the circuit is dependent upon the gate currents of the CSRs. and the diodes should be chosen for minimum voltage drop across their junctions. The resistances of R1 and R2 are determined by the current requirements of the CSR gates, and should be chosen to allow just enough current through to the gate to turn it on.

To give an audible indication as well as visual, a buzzer may be incorporated into the circuit. The circuit shown below in Fig. 3 is a




simple transistor switch. When all the lights are out, then point A will be at the supply rail voltage, 10 V ; and the transistor base will be reverse biased, cutting TR1 off. Once a lamp has been lit, point A will be brought down to iV above ground potential switching TR1 and the buzzer on.

Once the buzzer has been activated it will operate until the unit has been reset, thus indicating to the quiz master that the buzzer requires resetting before the next question is set.

The buzzer can be a 6 V or 9 V relay wired to buzz; these are more economical on current than bell
buzzers, which may require a separate battery from the rest of the buzzer unit as they operate at different voltages, usually $4 \cdot 5 \mathrm{~V}$ (Fig. 3b).

All the cables used to wire up the cuntestants' switch buttons should be the same length (to ensure equal cable resistance), and if very long cable is used then the value of R5 may have to be reduced to compensate for the resistance of the wire.

Diodes D1-Dx should be germanium devices, as the voltage drop across the junction will be at a minimum.
P. Culverhouse, Stevenage, Herts.


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## fuse failure warning

THE circuit in Fig. 1 gives a visual indication when the fuse fails.

The equation for calculating R1 is $\mathrm{RI}=50 \mathrm{VH}_{\mathrm{FE}}$ where V is the supply voltage and $\mathrm{H}_{\mathrm{FE}}$ the current gain of the transistor.

If the supply exceeds 40 volts a transistor with larger $\mathrm{V}_{\mathrm{re}}$ should be used.
N. Ruiz.
London.


Fig. 1

## elegtronic dice gircuit

THe normal method employed to generate random numbers for use in an electronic dice is to use a manual switch to control a high frequency oscillator that feeds pulses into $\mathbf{a} \div 6$ counter whose outputs are displayed using a seven-segment display. The method descnibed here follows the same idea but instead of a seven-segment display, $a$ set of seven l.e.d.s is used giving all the possible patterns normally seen on a "manual" dice. This approach has the attraction of being economical in component requirement and in line with the games and other uses to which dice are normally put.

In this circuit, CMOS components were used to alleviate the problem of a power supply (the dice can be run from a PP3 9 volt battery taking less than 10 mA ) and other extra components.

From Fig. I it can be seen that if we want to have signals to drive the seven I.e.d.s of the die face, we only need four distinct signals: $a, b$, $c$, $d$, as there are three duplicated signals. Using the notation ABC for the output of the three-bit binary counter in Fig. 1, we can express the required secondary signals as follows:

$$
\begin{gathered}
a=\frac{2}{} \text { or } 3 \text { or } 4 \text { or } 5 \text { or } 6= \\
\bar{A} B \bar{C}+A B C+A \overline{B C}+A \bar{B} C+ \\
+A B C=\overline{A B}+\bar{C} \\
b=6 \text { only }=A \overline{B C} \\
A \overline{B C}+ \\
+\bar{A}=4 \text { or } 5 \text { or } 6= \\
d=1 \text { or } 3 \text { or } 5=\bar{A} \cdot(\bar{C}+\bar{B}) \\
\overline{A B C}+\bar{A} \overline{B C}+\bar{A} \bar{B} C=C \cdot(\bar{A}+\bar{B})
\end{gathered}
$$

Using the counter values 0 and 7 ( $A B C$ and $A B C$ ) as "don"t care" signals. we can reduce these signal equations to:

$$
\begin{aligned}
a=\mathbf{A}+\mathbf{B} ; \quad \mathbf{b} & =\mathbf{A} \cdot \mathbf{B} ; \mathbf{c}=\mathbf{A} \\
\mathbf{d} & =\mathbf{C}
\end{aligned}
$$

Four dual-in-line package chips will suffice for the circuit- $2 \times$ MC14207 and $2 \times$ MC14011 providing the decoding logic for the l.e.d. signals, the high frequency oscillator and the three-bit register made from J-K flip flops and NOR gates.

Using the normal convention for numbering the pins on a diil. chip, the full circuit is shown in Fig. 2.
D. Relf,

Cranbrook

Fig. 1



N this circuit an SN7493 four bit binary counter i.c. is used to switch motors on either side of a model boat so giving directional control by bringing about an imbalance in the forces propelling the boat and so turning it. Two motors are used, one on either side of the main propulsion unit (as far from it as possible).

Motor $\mathrm{M}_{\mathbf{I}}$-moves the boat to starboard; $\mathrm{M}_{2}$ to port.
A pulse is received by the RC receiver in the boat and transmitted to TR1 base triggering the i.c. which counts one.
Only two outputs of thè i.c. are used so the output codes obtainable are 00 . used for half ahead, 01 to turn one way, 10 to turn the other and 11 for full ahead. The outputs of the i.c. are taken to TR2 and TR3 which operate relays controlling $M_{1}$ and $M_{2}$ so that they operate in sequence.
D. Osborne, Carlisle.

## DIGITAL MODEL CONTROL



Fig. 2

## NOVEL MEMORY

THE cincuit in Fig. I will be recognised by most of your readers as being a memory element. The usual method of operation is to have the inputs $A$ and $B$ at logic 1. The output $Q_{1}$ is $\bar{Q}_{2}$. If $A$ goes to logic $0, Q_{1}$ is set to logic 1 and $Q_{2}$ to logic 0 . This state continues even when A returns to logic 1. Similarly, if $B$ goes to logic 1, $Q_{2}$ is set at logic 1 and $Q_{1}$ at logic 0 .

A novel method of operating this circuit is as follows. The input $B$ is normally kept at logic 1. The
output $\mathrm{Q}_{1}$ is kept at logic 1 (assuming that A has been at logic 0 . after B last roturned to logic 1), no matter what input is seen at A. If a negative going pulse appears at the input $B$. the output $Q_{1}$ will be $\bar{A}$. If $A$ is at logic 0 , no change in the state of $Q_{1}$ is observed. If, however, $A$ is at logic 1, the output $Q_{2}$ goes to logic 0 and will remain in this state until A goes to logic 0 and resets $Q$, to logic 1. irrespective of the condition of IB. Typical waveforms are shown in Fig. 2.

## P. N. Mobson, Sheffield.


fig. 2


# POUTIT ARISIITG 

P.E. DIGI-PROBE (April 1976)

It has been brought to our attention that DIGIPROBE is the registered trade mark of KaneMay Ltd., of Welwyn Garden City, Herts. The instrument described in our April issue and entitled "PE Digi-Probe" has no connection with the above Firm nor its products. We apologise for any inconvenience that may have been caused because of the entirely coincidental choice of this almost identical name by our contributors for their own original design.

## DIGITAL FREQUENCY

 METER (June 1976) On page 505, the circuit diagram for the high impedance buffer (Fig. 13a) shows the base of TR14 connected to the gate of TR15 and also to "ground". This is, of course, incorrect and the base should only be connected to the "ground" line.
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