## PRACTICAL

## APAIL 1975 <br> .



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Our May issue will be published on Friday, April 11, 1975

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SA100 $\mathbf{E 1 2 . 5 0}$| Carriage |
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| free | 100W RMS 45-70V 120 watt module complete with builtin supply-extra heavy duty $E 22.500_{600}^{\text {carr. }}$.



THE SAIOO MODULE

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## UNSTABILISED-READY WIRED

| PU45 |  | ¢5.45 | ${ }_{\substack{ \\\text { Carriage } \\ \text { 300 }}}$ |
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| P U70 |  | ¢8.45 |  |
| STABILISED |  |  |  |
| PS45 | Suits 2 SA 35 r | ¢4.45 | Carrizge |
| MT45 | ${ }_{\text {Tranter }}^{\substack{\text { Trasformer for } \\ \text { abe }}}$ | 63.50 | Carrize |
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This well tried Pre.Amp mixes two decks, handles any This well tried Pre-Amp mixes two decks, handles any
ceramic cartridge, and features mic over-ride plus ceramic careridge, and features mic over-ride plus
separate full range bass and treble controls on both mic and deck inputs. Ample headphone power is available for P. F. L. May be used for mono and is
mains operated. Fited with sturdy screening case. mains operated. Fitted with sturdy screening case.
Controls: Micvol, bass, treble. Left/Rightfade, deck volume, bass, treble, h/phone selecr, vol, Mains. Size $17 \frac{1}{2}$ in $\times \operatorname{3in} \times 4$ in deep.

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Thousands sold of this extremely popular mono Pre.Amp. A mic input may be fitted using the VA30 (see below). Low consumption from a 9 V battery. Controls: H/phone select, vol, Left deck vol, Right deck vol, bass, treble, master vol. Size 124 in $\times 3$ in $\times 2$ in deep.


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Only 5AXON can supply such incredible value for money. This unit features 3 kW power handling, fullowave control, bass, middle, treble AND master controls. Twin loudspeaker jacks for "through "connections. It may be used free standing or will panel mount next to either of the above. Also features unique CFBACX circuitry for extra wide range response. Size 12 in $\times 3$ in $\times 2 \frac{1}{2}$ in deep. Professional standards at a price you can afford!
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M6HL $\mathbf{£ 1 9 . 5 0} \underset{\substack{\text { Carr. } \\ 50 p}}{ } \quad £ 29.50 \underset{50 \mathrm{p}}{\mathrm{C}_{\text {arr }}}$. Featuring multiples of our VA 30 module, the M4 HL and M6HL fulfil the requirements of all clubs, groups. etc. Where a high quality mixer is required. Each channel has one high and one low impedance inpuz.
plus volume, treble and bass controls. Input impedances may, if required, be easily changed The M4HL has four channels, and one output, and the M6HL six channels (12 inputs) and a master control and two outputs. Either unit may be used free-standing or panel mounted. These mixers will feed all types of amplifier. Recommended for their versatility and high performance, and excellent value for moner.
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TMK MODEL TW5OK
46 ranges, mirrar
scale. $50 \mathrm{k} / \mathrm{VDC}$
$50 \mathrm{k} N \mathrm{AC}$.
.
$0.25 / 1.25 / 2.5 / 5 / 1$
$25 / 50 / 125 / 250 /$ $500 / 1000$. AC Vo
$1.5 / 3 / 5 / 10 / 25 / 50$
$125 / 25050$ $125 / 250 / 500 /$
1000.0 c current
$25 / 50 \mathrm{~A} / 2.5 / 5 / 25 /$
$50 / 250 / 500 \mathrm{~mA} / 5 /$
10 m 10A. Resistence:
$10 \mathrm{k} / 100 \mathrm{k} / 1 \mathrm{Me}$
 $10 \mathrm{k} / 100 \mathrm{k} / 1 \mathrm{Meg} / \mathrm{M}$
10 Meg ohrms. -20 to +81.5 dB OUR PRICE E12.50 P\&P 20 p

## MODEL C7080EN Giant 6" mirrop scale. 20,000 opv. $0 / 0.25 / 1 / 2.5 / 10 /$ $50 / 250 / 1000 /$ $50002 / 00$ $50 / 250 / 1000 /$ 5000 DC. $0 / 2.5 / 10 / 50 / 250$ $1000 / 5000 \mathrm{~V}$ AC. $0 / 50 \mathrm{~A} / 1 / 90 /$ $0 / 50 \mathrm{u} / 1 / 90 /$ $100 / 500 \mathrm{~mA} / 10 \mathrm{~A}$ $0 \mathrm{C} .0 / 2 \mathrm{k} / 200 \mathrm{k}$

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$\frac{\text { KAMODEN } 360 \text { MUL TIMETER }}{}$ High sensitivity,
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\section*{

##  <br> AC $5^{\prime} \mathrm{m}$ overl ed. $2.5 /$ 1000 $50 / 2$ AC. 0.01 500 Res $1 / 1$ $1 / 1$ $10 /$ 0 ec 162 140 tes $0 U$

OUR PRICE £17.50 P \& P40p Overload protected,
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DC current:- $10 / 250 \mathrm{u}$ A/2.5/25/250
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Features AC current
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$0 / 25 / 10 / 50 / 250 /$
$500 / 1000 \mathrm{~V}$ AC.
$500 / 1000 \mathrm{~V}$ AC.
$0 / 50 \mathrm{~A} / 1 / 10 / 100$
$0 / 50 \mathrm{LA} / 1 / 10 / 100$
$m A / 1 / 10 \mathrm{DCC}$
$0 / 100 \mathrm{~mA} / 1 / 10 \mathrm{~A}$
AC . $0 / 5 \mathrm{~K} / 50 \mathrm{k} / 50$
$5 \mathrm{Meg} / 50 \mathrm{Meg}$.
$5 \mathrm{Meg} / 50 \mathrm{Meg}$.
Decibels: -20 to 2 dB .
OUR PRICE E19.95 P\&P 30p and lament for field
Knife edge pointer.
86 mmm . mirror scale.


Panges. $100 \mathrm{TV} / \mathrm{Ctron}$
Panges: $100 \mathrm{mV} /$
$0.5 / 2.5 / 10 / 25 / 50 / 100 / 250 / 500 / 1000$ $0.5 / 2.5 / 10 / 25 / 50 / 100 / 250 / 500 / 1000$
$V$ DC. $0.5 / 2.5 / 10 / 25 / 50 / 100 / 250 /$
$500 / 1000 \mathrm{~V}$ $500 / 1000 \mathrm{~V}$ AC. COUrrent: $50 \mathrm{uA} / 0.5 /$
$1 / 5 / 10 / 50 / 250 \mathrm{~mA} / 1 / 5 \mathrm{~A}$ DC. $0.25 /$ $1 / 5 / 10 / 50 / 250 \mathrm{~mA} / 1 / 5 \mathrm{~A}$ DC. $0.25 /$
$0.5 / 1 / 5 / 10 / 50 / 250 \mathrm{mAl/15A}$ AC. Res. istance: $0.5 / 10 / 100 / 200$ ohms $/ 1 / 3 /$
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90 mm . Supplied in carrying case com. plete with leads.
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High sensitivity
tester. 200,000 op
tester. 200,000 op
Overload protecte
Mirror csale.
Ranges: $0 / .06 / .3$
$3 / 30 / 12 / 600 /$
Ranges:-0/.06/
$3 / 30120 / 600 /$
1200 V DC $0 / 3$
1200 V DC. $0 / 3$
$12 / 60 / 300 / 11200$
$V \mathrm{AC} .0 / 6 \mathrm{uA}$
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$1.2 \mathrm{~mA} / 120 \mathrm{~mA}$
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300/600/1200V DC
$0 / 30 \mu \mathrm{~A} / 6$
$60 / 300 \mathrm{~mA}$
60/300 mA
$12 \mathrm{Amp} .0 / 10 \mathrm{~K}$
$1 \mathrm{~m} / 10 \mathrm{~m} / 100$
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Battary operated,
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vangas. Large $4 \%^{\circ}$ "
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$149 \times 117 \times 60 \mathrm{~mm}$
$0.3-12000 \mathrm{~V}$ DC.
$3-300 \mathrm{~V}$ RMS AC 8-800V P.P.
DC current $0 . \% 2-$
12 mA . Resistence


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4 p to 2000 MOHms . Decibels: -20 to
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$100.0000 \mathrm{pv} .61 / a^{\prime \prime}$
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short circuit check.
Sensitivity 100.000 opv OC. $5 k / V A C$
DC Vols: $0.5 / 2.5 /$ $10 / 50 / 250 / 1000 \mathrm{~V}$
AC $3 / 10 / 50 / 250 /$
LC. $1 / 1000 \mathrm{~V}$ DC,
current $10 / 100 \mathrm{ua}$

current $10 / 100 \mathrm{ua}$
$10 / 100 / 25 / 10 \mathrm{~A}$
$10 / 100 / 2.5 / 10 \mathrm{~A}$. Resistence:
$1 \mathrm{k} / 10 \mathrm{k} / 100 \mathrm{k} / 10 \mathrm{Meo} / 100 \mathrm{M}$ $1 \mathrm{k} / 10 \mathrm{k} / 100 \mathrm{k} / 10 \mathrm{Meg} / 100 \mathrm{Meg}$ ohms.
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instrument to
instrument to
test reverse leak
current and DC
ficationt. Ampli
NPTor of
fication factor of
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etc. $4^{-}$squ are
clear scalo meter.
internal batteries.
Complete with

instructions, lead


OUR PRICE $£ 17.50$
U4341 Multimeter 27 ranges. 16,700opv Overload protected. Ranges: $0.3 / 1.5 / 6 /$
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$300 / 750 \mathrm{~V} / .45 \%$ $300 / 750 \mathrm{~V}$ AC.
Current: $0.06 / 0.6 /$ $6 / 60 / 600 \mathrm{~mA} D C$.
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$300 \mathrm{~mA} / 6 / 12 \mathrm{~A}$
$300 \mathrm{~mA} / 6 / \mathrm{Meg} /$
100 Meg.
$-2010+50 \mathrm{~dB}$
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Transistor tester measures, Alpha, Beta
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OUR PRICE £19.95 P\&P 250 SWR METER Model SWR3
Handy SWR meter for
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ment, with bulti-in field strength meter. Accuracy
$5 \%$ Impedence $52^{\prime}$ Indic5\%, Impedence 52 $2^{2}$ In
ator $100 u$ DC. full scale 5 section cotlapsible antenns. Size $145 \times 50 \times$ OUR PRICE E4.25 P\&P 30 p CI5 PULSE OSCILLOSCOPE


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Will measure $A C$ and $D C$ volts, $A C$ and DC current. and rasistance in a
totalof 20 ranges. The largelight emitting diode display will resd up polarity. Indication of positive and negative overlosd is also provided The instrument is fitted with a combined carrying handleand bench stand and sockets art provided for the connect
external power supply.
external
RANGES
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$100 \mathrm{~mA}, 1000 \mathrm{~mA}$.
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RESISTANCE: $1 \mathrm{k}, 10 \mathrm{k}, 100 \mathrm{k}, 1000 \mathrm{k}$ OUR PRICE E59.95 P\& P 50 p RUSSIAN CI16 Double Beam OSCILLOSCOPE 5 MHz pass band. amplifiers Rectang Cwibr atod triggerided swep from o. 2 usec .
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Volume control for impedence Frequency
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STEREO
HEADPHONES
Feather weight
(5 oz) Dynamic
providing high quality
reproduction at s budget price. Sofe removable ear pads and sire: 28 mm . Impedance : 8 ahms . Frequency response: $30-13000 \mathrm{H}$
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 This varisatile $P$ amplifier. This varsatile unit has a RMS and operates on any $10-1$ DC source, negstive or positive ground and uses only 1.84 as rated output. Supplied complete withmounting brackets etc, pluefull mounting brackets etc, pluefull installation and operating
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controls smabi

completa mixing

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We carry a tremendous range of Worn pocmet and desk cal
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MUSIC MASTER AM100


BINATONE OIGITAL CLOCK

A.C. 240 V o

Size approx. $61 \times 3 \times 3 \frac{1}{2}$ inches.
OUR PRICE $f 450$ PRP50.
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integrateo CIRCUIT
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mounting board
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| z60 Power Amp. | ¢7.45 | P\& P 15p |
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For balancing and gain seivection of loudspoakers with additional
facility for stere maility for stere switching. Two
 gain controls. spaakers on-off side
switch, stareo headphon OUR PRICE $£ 2.25$
win stereo

eparata volume controls outs and channel. Operaten from gV battery INPUTS: 5 mV end 100 mV .
OUTPUT: 50 mV Der chann
OUP PA:
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## SEW PANEL METERS <br> SEW PANEL METERS ARE STOCKED AT OUR 3 IISLE ST., 311 EDGWARE RD.. \& 152 FLEET ST., BRANCHES or order by post.

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| :---: | :---: | :---: | :---: |
| 50VA .. .. .. 53.90 |  |  |  |
|  |  |  |  |
|  |  |  |  |
| 800-500A .. 53.85 |  |  |  |
| 100-0.100uA.. | c3.80 |  |  |
| 1mA .. .. .. 63.75 |  |  |  |
| 10 mAA .. |  |  |  |
|  |  |  |  |
| 50 ma ... .. | ${ }_{5} \mathbf{5} 3.75$ | $10 \mathrm{~V} D C$ |  |
| 100 mA500 mA | c3.75 | 20 V DC. |  |
|  | ${ }^{3} 3.75$ | $50 \vee O C \cdot$ | c3.75 |
| 1ADC$6 A D C$ | C3.75 | $300 \vee$ OC.. |  |
|  | 63.75 | 15 V AC.. | c3 85 |
| 10A OCSVOC | 63.75 | 300 V AC .. |  |
|  | ¢ 3.75 | VU Meter | ¢4.00 |
| CLEAR PLASTIC MODEL SW100 <br> Size: $100 \times 80 \mathrm{~mm}$ |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
| $1 m A{ }^{\text {1 }}$ - .. | C4.40 |  |  |
| 1ADC .. -. | c.40 |  |  |
| GADC .. | c4.40 | 150 V AC. |  |
| 20V DC. | C4.40 | 150V AC .. | c4 65 |
| 300 V DC... .. | c4.40 | VUMeter ${ }^{\text {c }}$ | C5 00 |
| EDGWISE MODEL PE70 <br> Size: $90 \times 34 \mathrm{~mm}$ |  |  |  |
|  |  |  |  |
| 50uA .. .. .. C4.25 |  |  |  |
| 100uA    <br> 200uA    <br> .. . . 54.20 <br> 1.15    |  |  |  |
|  |  |  |  |
| 500uA $\quad . \quad . .54 .00$ |  |  |  |
| 60-0-50uA - 1420 |  |  |  |
| 100-0-100uA.. 6435 |  |  |  |
| 300V AC.. .. 44.06 |  |  |  |
|  |  |  |  |


| *Items with asterisk are Moving Iron type, all others are Moving Coil |  |  |  |
| :---: | :---: | :---: | :---: |
| CLEAR PLASTIC MODEL SD830 Sire: $110 \times 83 \mathrm{~mm}$ |  |  |  |
| 50ua | C4.40 |  |  |
| 1004A .. .. | c4.36 |  |  |
| 20004 | C4.30 |  |  |
| 50-0-50uA ${ }^{\text {a }}$.. | c4.35 |  |  |
| 100-0-100UA.. | c4. 30 |  |  |
| 1mA ... .. .. | c4.20 |  |  |
| 5 mA | c4.20 |  |  |
| 10mA.. .. .. | ¢4.20 |  |  |
| 50 mA .. | C4.20 | 10 V DC | ${ }^{64.20}$ |
| 100mA .. .. | ¢4.20 | $20 V$ OC .. | c4. 20 |
| 500 mA | ¢4.20 | 6OV DC | c4. 20 |
| 1ADC .. .. | ${ }^{6} 4.20$ | $300 V$ DC .. .. | c4. 90 |
| SADC | 64.20 | 15 VAC .. .. | c4. 30 |
| 10A DC .. .. | c4.20 | $300 \vee$ AC .. |  |
| 5VDC .. .. | f4.20 | VU Motor .. .. |  |



| $4810 \times 50 R D$ | $01-4.3881$ |
| :---: | :---: |
| 3 LISLESI. WC2 | $01-4378204$ |
| 34 LISLE ST. WC2 | 01-4379135 |
| lis EDGWARE RD. W2 | 01.723 9769 |
| 193 EDGWARE RD. W2 | 01.723611 |
| 207 EDGWADE RD. W2 | 01.7233271 |
| 311 EDGWAME RD. W2 | 01-262 0387 |
| 346 EDGWAME RD. W2 | 01.7234453 |
| 362 EDGWA的 RD. W2 | 01.7234194 |
| 109 FLEET ST. ECA | 01-353 5812 |
| 152/3 fLEET ST. EC4 | 04-353 2833 |
| 10 TOTTENHAM CT. 10. | 01.6371232 |
| 27 TOTTENHAM CT. RO | 01.6363715 |
| 33 TOTTENHAM CT. RD. | 01-636 2605 |
| 42/45 TOTTENHAM CT. RO. | 01.6360865 |
| 257/8 TOTTENHAM CT. RD. | $01-5000670$ |

6 SOUTH ST. ROMFORO



$4 \mathrm{tin} \times 3$ in METER. $\quad 30 \mu \mathrm{~A}$, $50 \mu \mathrm{~A}$ or $100 \mu \mathrm{~A}, 63.85$. 11 p P. \& P .

$500 \mu$ A 70 p. $5 p$ P. \& P.


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Model UD-i30. Frequency response 50 $15,000 \mathrm{c} / \mathrm{s}$. Impedance Dual 50 K and 600 ohms, 66.55. IIp P. \& P.
$42 \times 42 \mathrm{~mm}$ meters $100 \mu \mathrm{~A}, 500 \mu \mathrm{~A}$, $1 \mathrm{~mA}, 500 \mathrm{~mA}, 62.76 .11 \mathrm{p}$ P. \& P.
$60 \times 45 \mathrm{~mm}$ meters $50 \mu \mathrm{~A}, 100 \mu \mathrm{~A}$, $500 \mu \mathrm{~A}$ and 1 mA VU meter, $\mathbf{£ 2} \cdot 92$. IIp P. \& P

Edgewise meters $90 \mathrm{~mm} \times 34 \mathrm{~mm}$ $1 \mathrm{~mA}, \mathbf{\& 3} \cdot \mathbf{4 0}$. Ifp P, \& $P$.


3 WATT STEREO AMPLIFIER
64.30. 10p P. \& P.

All above prices include $8 \%$ V.A.T. LARGE S.A.E. for List No. 11 Special prices for quantity quoted on request.

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| :--- |

Hllustrated is the DD61 dimmer system Contains: six 1 kW slider dimmers type PD1000, ix outlet sockets, a master contron a mains system in
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2-preset arrangement Future system, availebi with 2 kW
dimmers. Specials made. DD61 \&97-20 inc. VAT and P. \& P. DD261 $\varepsilon 117.72$ inc VAT and $P$ \& $P$

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Model SL800 sound to light converter. Modulates the light in time with sound. Built-in microphone. No connections to speaker required Simple wiring-similar to dimmer. Rating 800 W

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So what's new?
A.comprehensive hi-fi system, combining the enjoyment and satisfaction of build-it-yourself (without too much struggle) ... a real value-for-money feeling... and results of the highest quality.

It's the new Sinclair Project 80.

## How does Sinclair Project 80 work?

Project 80 is a comprenensive set of hi-fi modules, or sub-assemblies. Amps... pre-amps ... FM tuners... stereo decoders ... control units... everything you need to assemble hi-fi units. They're all designed to look alike and they're all completely compatible with each other. 5 imply decide on the specifications of the unit you want to build... buy the necessary modules ... connect them ... and house them.

No need to buy everything at once for your eventual set-up. All the modules are designed so that you can add to them as your system grows - whether or not it's based on Project 80.

This applies to refinements, like filters ... to up-grading, adding a second set of amps, say, for greater output... or to real innovation, like quad. (Add a Project 80 quad decoder, a power supply, a pair of amps, and a pair of speakers - and your stereo's gone quad.)

## Is it difficult to build?

Not at all. The modules are complete in themselves. All you do is connect them to your turntable ... your speakers... or to each other. It's absorbing, but if you can solder wires to a 5 -pin DIN plug, you can build a complete system with Project 80.

And if you're not so hot with a soldering iron? Use project 805 . Project 805 uses Project 80 modules, but provides special clip-on tagged wire connections absolutely no soldering required.

And, of course, both Project 80 and Project 805 come complete with instructions for easy, step-by-step assembly. But if you do run into problems, just call our Consumer Advisory service who are always happy to help.

## OK. Where dol go from here?

Over the page! There vou'll see'for yourself the exacting specifications to which Sinclair Project 80 modules are made, and you'll see some suggested systems.

As you skim the suggestions, remember all project 80 modules are backed by the remarkable no-quibble sinclair guarantee, Should any defect arise from normal use within a year, we'll service the modutes free of charge. What


## Choose the Project 80 modules that are right for you.



## Project 80 pre-amp/control unit

The control centre of Project 80 With its distinctive white-on-matt-black styling and plastic control sliders, it's a pleasure to look at, as well as to use

Specification
19' : in $\times 2 \operatorname{in} \times 3 / 4$ in.) Separate slider controls on each channel for treble, bass and volume. inputs: PU magnetic - 3 mV (RIAA corrected). ceramic - 350 mV .


## Project 80 FM tuner

Excellent reception from a tuner onlv $3^{3}$ in long $x^{3 / a}$ in deep ${ }^{\prime}$ Styled to match Project 80 control unit

## specification

( $3^{\prime}$ : in $\times 2 \operatorname{In} \times I_{4}$ in ) Tunes 875 MHz to 108 MHz . Detector: IC balanced


Project 80 stereo decoder
Designed for use with Project 80 FM tuner. Sold separately to


## Project 80 active fliter unit

Eliminates scratch and rumble (high and low-frequency noise).

Radio 100 mV . Tape 30 mV . S/N ratio: 60 dB . Frequency range: 20 Hz to $15 \mathrm{kHz} \pm 1 \mathrm{~dB}$. Outputs: 100 mV and tape plus AB monitoring. Press buttons for PU, radio and tape. Operating voltage: $20 \mathrm{~V}-35 \mathrm{~V}$.

Price. £13.95 + VAT
comcidence IIC equivalent to 26 transistors) Distortion: $03 \%$ at 1 kHz for $30 \%$ modulation. Sensitivity: $5 \mu \mathrm{~V}$ for 30 dB signal to norse. Output: 100 mV for $30 \%$ modulation. Aerial imp: $75 \Omega$ or $240-300 \Omega$. Features: dual varicap tuning, 4 -pole ceramic filter. switchable AFC Operating voltage: $23 \mathrm{~V}-30 \mathrm{~V}$
Price: $£ 13.95$ + VAT
keep down the price of a mono FM system, but also to make the stereo decoder available for use with existing mono $F M$ tuners.

Specification
$11^{1}$ in $\times 2$ in $\times$ 3/a in.) 1 IC equivalent to 19 transistors. LED stereo indicator glows red.
Price: $\mathbf{E 8 . 9 5}+$ VAT

Specification
(4)/4in $\times 2 \operatorname{in} \times \frac{3 / 4}{} \mathrm{in}$.) Voltage gain: -0.2 dB . Frequency response: filter at zero: $36 \mathrm{~Hz}-22 \mathrm{kHz}$; HF (scratch) out: variable 22 kHz to $5.5 \mathrm{kHz}, 12 \mathrm{~dB} /$ octave slope; LF (rumble) out: -28 dB at 28 Hz , 9 dB /octave slope.
Price: E. 7.45 + VAT


Project 80 power amplifiers
Two different amplifiers, designed to be used separately or combined, with Project 80 modules or as add-ons to existing equipment. Protectedagainst short circuits and damage from mis-use

240 Specification
( $21 / 4$ in $\times 3$ in $\times 3,4$ in. $) 8$ transistors. Input sensitivity: 100 mV Jutput: 12 W RM5 continuous into $8 \Omega(35 \mathrm{~V})$. Frequency response: $30 \mathrm{~Hz}-100 \mathrm{kHz} \pm 3 \mathrm{~dB}$. 5/N ratio: 64 dB . Distortion: $0.1 \%$


## power supply units

Range of power supply units to match desired specification of final system.
p75 Specification
Unstabilised. 30 V output. including mains transformer
Price: $E 5.95+$ VAT
at 10 W into $8 \Omega$ at 1 kHz . Voltage requirements: $12 \mathrm{~V}-35 \mathrm{~V}$. Load imp: $4 \Omega-15 \Omega$; safe on open circuit. Protected against short circuit.

Price: $\mathbf{£ 5 . 9 5 + \text { VAT }}$
260 Specification
$12^{1 / 4}$ in $\times 3^{4 / 5}$ in $\times 3^{3 / 4}$ in. 112 transistors. Input sensitlvity: $100 \mathrm{mV}-250 \mathrm{mV}$. Output: 25 WRMS continuous into8 $\Omega(50 \mathrm{~V}$ ). Frequency response: 10 Hz to more than $200 \mathrm{kHz} \pm 3 \mathrm{~dB} .5 / \mathrm{N}$ ratio: better than 70 dB . Distortion: $0.02 \%$ at 10 W into $8 \Omega$ at 1 kHz . Voltage requirements: $12 \mathrm{~V}-50 \mathrm{~V}$. Loadimp: $4 \Omega$ min; max safe on open circuit. Protected against short circuit.
Price: $\mathbf{E} 7.45+$ VAT

P26 Specification
Stabilised. 35 V output Including mains transformer.

Price: $\mathbf{E 8 . 9 5}+\mathrm{VAT}$
P285pecification
Sta bilised. Output adjustable from 20 V to 60 V approx. Re-entrant current limiting makes damage from overload or even shorting virtually impossible. Without mains transformer.
Price: E8.45 + VAT


## Project 80 SO quadraphonic decoder

Combines with and exactly matches Project 80 control unit for true quadraphonics. This unit is based on the CBS SQ system and is a complete quadraphonic decoder, rear channel pre-amp and controf unit.
Specification
(91/2 in $\times 2$ in $\times 3,4$ in.) Connects with tape socket on Project 80
control unit or similar facility on any stereo amplifier. Separate slider controls on each channel for treble, bass and volume. Frequency response: 15 Hz to $25 \mathrm{kHz} \pm 3 \mathrm{~dB}$. Distortion: $0.1 \%$. S/N ratio: 58 dB . Rated output: 100 mV . Phase shift network: $90 \pm 10 \cdot 100 \mathrm{~Hz}$ to 10 kHz . Operating voltage: $22 \mathrm{~V}-35 \mathrm{~V}$.
Price: E18.95 + VAT

## Some system suggestions from Sinclair


sinclair 016 speaker
Original and uniquely designed speaker of outstanding quality.
Specification
(933/8 in square $\times 4^{3 / 4}$ in deep.) Pedestal base. All-over black front. Teak surround. Balanced sealed sound chamber. Special driver assembly. Frequency response: 60 Hz to 16 kHz . Power handling: up to 14 WRMS. impedance: $8 \Omega$.

Price: $£ 8.95$ + VAT

## Project 805 ampliflerkit

Containsfollowing Project 80 units:
Project 80 control unit
$2 \times 240$ power amplifier modules
$1 \times$ P25 power supply unit
Masterlink unit
On/off switch
plus pre-cut wiring loom with clip-on tagged wire connections, nuts and bolts, instruction manual.

Price: $£ 39.95^{\circ}+$ VAT

## Project 8050 quadraphonic add-on kit

Converts your existing stereo hi-fi system to quad using solderless connections.
Contains following Project 80 units:
Project 80 SQ quad decoder/rear channel pre-amp and control unit
$2 \times 240$ power amps PZ5 power supply unit Masterlink unit On/off switch
plus pre-cut wiring 100 m with clip-on tagged wire connections, nuts and bolts, instruction manual.
Price: £ $44.95+$ VAT

1. Ouadraphonic system: 25 W per channel RMS

Pre-amp/control unit + quadraphonic decoder $+4 \times 260$ amps $+2 \times$ PZ8 mains power supplies $+(2 \times$ mains transformers) + ( $4 \times$ equivalent speakers) + (turntable) . Total Project 80 cost: $£ 79.60+$ VAT.

## 2. Stereo amplifier: 12 W per channel RMS

Pre-amp/control unit $+2 \times 240$ amps + PZ6 power supply + t $2 \times$ Q16 speakers. Total Project 80 cost: $£ 52.70+$ VAT.
3. Stereo tuner/amplifier: 12 W per channel RMS Pre-amp/control unit + FM tuner + stereo decoder $+2 \times 240$ amps + PZ6 power supply $+2 \times 016$ speakers. Total Project 80 cost : $£ 75.60+$ VAT.

## Other applications

4. PA system
(Mic) + pre-amp/control unit + Z40 amp + PZ6 power supply
$+2 \times 016$ speakers. Total Project 80 cost: $£ 46.75+$ VAT.
5. Convert existing mono record-player to stereo Pre-amp/control unit + Z40 amp + Q16 speaker. Total Project 80 cost: $£ 28.25+$ VAT.

## What more can we tell you?

The basic facts are covered on these two pages. And you'll find Project 80 at stores like Laskys and Henry's.
But before you look, why not get really detailed information? Clip the FREEPOST coupon for the fully. illustrated Project 80 folder - today!

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UK1 60 I.C. Mono Amp. 8 Watt Speeifications Aux input sensitivity: PHONO inpus sensativivity
Output impedance:
Power supply:
Transistors:
ic:
SALE $f 6.04$
UK80 Oscilloscope Calibrator
UKg2 Telephone Amplifier
UK1 10 Stereo Amp. 5+5 Watt
UK115 Hi-Fi Mono Amp 8 Watt
UK135 High Impedance Pre-Amp
UK140 Low Impedance Pre-Amp
UK167 C.C.I.R. Stereo Pre-Amp
UK195 Miniature Amp 2 Watt
UK255 Level Indicator
UK270 I.C. Amp 6 Watt
UK275 Microphone Pre-Amp
UK375 27MHz Crystal Calibrator
UK415/C Resistor Substitution Unit
UK425/C Capacitor Sub Unit
UK435/C Stab. Power Supply
$0=20 \mathrm{VDC} 1 \mathrm{Amp}$
UK445/C L.F. Watt Meter $5 \mathrm{mV}=15$ Watt
UK465 Quartz Crystal Tester
UK470/C Crystal Calibration Marker Gen
UK555R/C 27 MHz Field Strength Meter
UK610 Power Supply 24V DC 0.5A
UK615 Power Supply 24V DC 1 Amp
UK630 Stab. Power Supply 6V-7-5 9V-12V D
UK640 Auto Light Dimmer 200 Watt
UK670 Buffer Batt. Charger 12-16V 200 mA
UK710/C Four Channel A.F. Mixer UK765 Multiple Stereo Junction Box $4 \times$ Standard Jack Output
UK835 Guitar Pre-Amp.
UK840 Car Burglar Alarm
UK 855 Electronic Fuzz Box
UK860/C Photo Timer
UK885 Capacitive Contact Alarm
UK900 R.F. Crystal Oscillator $\mid 20=60 \mathrm{MHz}$
UK905 R.F. Crystal Oscillator
UK910 R.F. Mixer $12=170 \mathrm{MHz}$
UK915 R.F. Amp. $12=170 \mathrm{MHz}$
UK920 R.F. Mixer $2 \cdot 3=27 \mathrm{MHz}$
UK925 R.F. Amp. $2 \cdot 3=27 \mathrm{MHz}$
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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SN7400 | 0.14 | 0.13 | 0.12 | SN7454 | 0.15 | 0.14 | $0 \cdot 13$ |
| SN7401 | 0.15 | 0.14 | 0.13 | SN7460 | 0.15 | 0.14 | $0 \cdot 13$ |
| SN7402 | 0.15 | 0.14 | 0.13 | SN7472 | 0.28 | 0.27 | 0.25 |
| SN7403 | 0.15 | 0.14 | 0.13 | SN7473 | 0.33 | 0.32 | 0.30 |
| SN7404 | 0.18 | 0.18 | 0.15 | SN7474 | 0.33 | 0.32 | 0.31 |
| SN7405 | 0.18 | $0 \cdot 16$ | 0.15 | SN7475 | 0.51 | 0.49 | 0.47 |
| SN7408 | 0.18 | 0.18 | 0.15 | SN7476 | 0.35 | 0.32 | 0.31 |
| SN7410 | 0.15 | 0.14 | 0.13 | SN7480 | 0.50 | 0.47 | 0.44 |
| SN7412 | 0.23 | 0.21 | 0.19 | SN7483 | 0.85 | 0.89 | 0.83 |
| SN7413 | 0.33 | 0.32 | 0.31 | SN7486 | 0.34 | 0.33 | 0.31 |
| SN7417 | 0.30 | 0.29 | 0.28 | SN7489 | $3 \cdot 56$ | 3.33 | 2.96 |
| SN7420 | 0.15 | 0.14 | $0 \cdot 13$ | SN7490 | 0.49 | 0.48 | 0.48 |
| SN7427 | 0.29 | 0.218 | 0.27 | SN7491 | 0.99 | 0.94 | 0.8 |
| SN7430 | 0.15 | 0.14 | 0.13 | SN7492 | 0.54 | 0.50 | 0.47 |
| SN7432 | 0.31 | 0.29 | 0. 27 | SN7493 | 0.51 | 0.50 | 0.47 |
| SN7437 | 0.31 | 0.29 | 0.27 | SN7495 | 0.73 | $0 \cdot 68$ | 0.64 |
| SN7440 | 0.15 | $0 \cdot 14$ | 0.13 | SN7496 | 0.83 | 0.78 | 0.73 |
| SN7442 | 0.70 | 0.68 | 0.64 | SN74107 | 0.35 | $0 \cdot 34$ | 0.31 |
| SN7445 | 0.99 | 0.92 | 0.88 | SN74121 | $0 \cdot 36$ | 0.34 | 0.31 |
| SN7447 | 0.98 | 0.96 | 0.87 | SN74123 | 0.70 | $0 \cdot 68$ | 0.84 |
| SN7450 | 0.15 | 0.14 | 0.13 | SN74145 | $0 \cdot 69$ | 0.85 | 0.79 |
| SN7451 | 0.15 | 0.14 | 0.13 | SN74157 | 0.87 | $0 \cdot 81$ | 0.72 |
| SN7453 | 0.15 | 0.14 | 0.13 | SN74175 | > 0.99 | 0.95 | 0.84 |

TTL may be mixed to qualify for quantity prices

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| 1N4001 | 41p | ZTX107 | 10p | ZTX302 | 20p | $27 \times 313$ | 14p |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1N4002 | 5p | $2 \mathrm{TX108}$ | $8 \pm p$ | 2TX303 | 16p | ZTX500 | 14p |
| 1N4003 | 6 8 | ZTX109 | 11\%P | ZTX310 | 10p | 2TX501 | 15p |
| 1 N 4004 | 6 p | 2TX300 | 14p | 2TX311 | 12 p | ZTX502 | 20p |
| 1N4148 | 4 P | 2TX301 | 15p | 2TX312 | 12p | ZTX503 | 16p |

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THE very successful infiltration by electronics into miscellaneous and apparently unrelated fields is based upon the ready ability to transform different forms of energy into electrical energy, and vice versa. Without those devices embraced by the term "transducers" electronic circuitry would be limited to purely electrical or electronic functions.

Many energy transforming devices used for industrial or scientific measuring and control purposes fall clearly into the class of precision instruments. They are usually designed for specific applications in association with hydraulic, pneumatic, or mechanical systems. These kind of applications are seldom encountered by the amateur. Apart from such highly specialised transducers, there are more commonplace devices and some-like the microphone and loudspeaker-have been around for a very long time and indeed antedate the introduction of the now generally used term transducer. There are also those simple home made devices which, in comparison with their elegant commercial counterparts, seem hardly to qualify for the rather impressive title of transducer. Yet they ably perform essential energy conversion roles in some electronic system or another.

The touch plate and the moisture sensor are examples of transducers of the most rudimentary form which can be fashioned from commonplace materials and work very successfully with standard electronic circuitry. Of course far more complex and intricate devices can also be made by the amateur who is adept in mechanical matters. For example, the electrodynamic type of instrument used in the Marine Speedometer (February issue).

There is also a good variety of semiconductor devices which are used for sensing or transducing purposes. The developments in this area have been of particular value and importance to amateurs, for they have opened up additional useful applications for circuitry without introducing undue mechanical problems. One of the more recent and noteable innovations in solid state transducers is the gas or vapour detector. This device has now joined the ranks of other semiconductor devices which can sense atomic radiations, heat, light, sound and pressure. (Only taste appears to be lacking at present, but surely it can only be a matter of time before this sense is covered as well?)

As a matter of fact, this marked paralleling. of human senses tempts us to indulge in a little whimsical speculation. Can we expect one day the arrival of the thought-responsive transducer, $a_{1}$ device that will permit the control of electronic apparatus without the necessity of physical contact of any kind? Some extra-sensory perception devotees believe they really are on the track of this esoteric solution to some of life's little problems. And there has actually been a suggestion that the humble Zener diode could be a possible candidate for this post. Who knows, maybe some readers are currently engaged in pitting their wits against the undisciplined host of particles within that tiny component following the recent Probability Anomaly Detector article.

Frankly, though, we confess having doubts that any redundancy amongst orthodox transducers will result from this battle of mind over matter. So we return to wholly substantial and materialistic matters by drawing attention to the new series starting this month dealing with those important intermediaries-transducers.
F.E.B.

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F. E. BENNETT

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## RECIPROCITY LAW

Experienced photographers will be aware of the reciprocity law, on which all exposure measuring devices are based

$$
T \times I \fallingdotseq K
$$

It expresses the fact that the product of time ( $T$ ) and intensity ( $I$ ) must be constant ( $K$ ) for a given sensitised material (intensity being itself the product of subject brightness and the f stop used). Although it is well known that this law breaks down at extremes of shutter speeds and low light levels, it holds good over the normal ranges encountered in camera exposure. The photographer carefully measures light intensity and, with film speed ( $K$ ) set, the (Reciprocity Law) calculator on the meter will compute the aperture/speed combinations for the material in question.

Having carefully measured camera exposure and accurately developed or reversed the material, the printing stage arrives. Here is where the Law goes out of the window!
Readers appreciate that there are small variations in mains voltage taking place the whole time, especially if a hospital or factory is nearby. More important, however, is that photographic high intensity enlarging lamps are slightly over-run, operating on a similar principle to the photoflood lamp. This means that their reaction to fluctuations is more highly-geared than with household lamps. When voltage alters, so does intensity ( $I$ ) and, if the timer gives precise intervals, " $K$ " is meaningless.

To compensate for intensity changes, exposure time must be reciprocal. If the intensity falls, exposure time must be extended-and vice versa. Fig. 1 shows the percentage changes in the light output from an 150 W enlarging lamp as voltage is decreased, the 75 W lamp following much the same


Fig. 1. Effect of mains voltage on enlarging lamps
pattern. A 10 per cent error in the exposure of papers such as Ektacolor 37 RC will be most noticeable and really demand a reprint. Ideally, all mains supply fluctuations require compensation so that repeatable results are possible. When switched to the "Compensated" mode, this timer will give the same light dosage for a given setting of the interval switches, despite any unevenness in supply voltage.

## CIRCUIT

The complete circuit is shown in Fig. 2. Pin 5 of ICI is normally supplied with about half the rail voltage, this potential altering all intervals pro rata. When S6 is switched to "Exact", pin 5 is connected in this manner. With S6 set to "Compensated", pin 5 is supplied with an amplified version of small changes in a.c. mains voltage and thus makes the timed interval reciprocate. S3 and S2 are simple make switches, but the circuit is otherwise solid state.


Fig. 2. Complete circuit of the timer


The completed front panel showing positioning of controls

Output from the timer i.c. is used to switch a thyristor in series with the primary winding of the transformer, whose secondary controls the gate of a triac and hence the lamp itself. Though a little more expensive than pure relay operation, this method is positive and reliable in obviating problems associated with relay points.

Supply voltage is nominally 12 V , with 15 V a safe upper limit. R8 may be adjusted upwards if DI passes too much current. Reset switch S 2 is connected to pin 4 of the i.c., and the start switch to pin 2. Both are held at positive potential by R3 and R2 to avoid spurious operation and are triggered by a negative going pulse when either switch is closed. The neutral side of the a.c. mains supply is ground with respect to the i.c. and its power supply. Threshold and discharge pins 6 and 7 are connected together in this application. From this point the charge on the timing capacitor Cl , being fed through the timing chain, is sensed and the i.c. output switches off when the charge on Cl reaches two-thirds of the positive rail voltage. This capacitor should be either a bead tantalum or the closer tolerance tantalum specified.

## RESISTOR CHAIN

Using the formula $t=1 \cdot 1 R C$, any combination of resistor and capacitor may be used to arrive at the basic 1 second unit. However, it is best to keep the value of $R$ as low as possible to avoid the risk of leakage currents. Mathematically, these produce just over 1 second, but capacitor tolerance and the ability to adjust VR1 and VR2 make these values ideal.

As the accuracy of the finished timer depends almost entirely on Cl and the timing chain, good quality components are essential-and the extra cost not exorbitant. Resistors should not be overheated when being fitted to S7 and S8 and the associated wiring must be kept clear of other components. New switches have better contacts than old ones.

Any "stops" should be removed from rotary switches S7 and S8 so that the pointers may be turned in any direction. Timing resistors must be used in the 11 o'clock positions (though R25 may be considered superfluous) to avoid an open circuit and consequent waste of expensive material. Though it would be simple to select a set of ordinary 20 per cent resistors using a bridge or meter, 2 per cent metal oxide types are more stable in use and are thus recommended.

COMPONENTS

| Resistors |  |
| :--- | :--- |
| R1 |  |
| R2 | $2 \mathrm{k} \Omega$ |
| R2, R3 | $22 \mathrm{k} \Omega$ (2 off) |
| R4 | $2 \cdot 2 \mathrm{k} \Omega 5 \%$ |
| R5 | $56 \mathrm{k} \Omega$ |
| R6 | $1 \mathrm{k} \Omega$ |
| R7 | $330 \mathrm{k} \Omega 5 \%$ |
| R8 | $68 \Omega 1 \mathrm{~W}$ wirewound |
| R9 | $1 \mathrm{k} \Omega$ |
| R10 | $82 \mathrm{k} \Omega 1 \mathrm{~W}$ |
| R11 | $1 \mathrm{k} \Omega$ |
| R12 | $1 \mathrm{k} \Omega$ |
| R13 | $470 \Omega$ |
| R14 | $5 \cdot 1 \mathrm{k} \Omega 5 \%$ |
| R15-R25 $51 \mathrm{k} \Omega$ (11 off). All metal oxide $2 \%$ |  |
| R26-R36 $510 \mathrm{k} \Omega$ (11 off). All metal oxide $2 \%$ |  |
| All $\frac{1}{2} \mathrm{~W} 10 \% \mathrm{carbon}$ except where stated |  |

## Capacitors

C1 $22 \mu \mathrm{~F}$ tantalum elect. 16 V
C2 $125 \mu \mathrm{~F}$ elec. 16 V
C3 $0.1 \mu \mathrm{~F} 900 \mathrm{~V}$

## Semiconductors

| TR1 | BC108 |
| :--- | :--- |
| IC1 | NE555V |
| CSR1 | 400 V 1 A Thyr |
| CSR2 | 400 V 10 A Triac |
| D1 | Z12 12V Zener |
| D2 | IN4005 (2 off) |
| D3, D4 | IN4001 (2 |
| D5 | TIL 209 l.e.d. |

Potentiometers
VR1 $10 k \Omega$ sub-minature preset VR2 $2 k \Omega$ sub-miniature preset VR3 $50 \mathrm{k} \Omega$ linear

## Switches

S1 Miniature d.p.d.t. slide switches
S2, S3 Press-to-make switches (2 off) (see text)
S4 D.p.d.t. toggle 250 V
S5 S.p.s.t. toggle 250 V
S6 D.p.d.t. slide switch
S7-S8 12 way rotary (2 off)
Transformers
T1 Douglas type MT238C5. Pri. 240V, Sec. 3-0-3V, 200 mA
T2 Brazenose 12012/1. Pri. 240V, Sec. 12-0-12V, 100 mA

## Miscellaneous

1 Pointer knob $1 \frac{1}{4} \mathrm{in}, 2$ long pointer knobs $2 \frac{1}{4} \mathrm{in}$, 4 way terminal block, 16 s.w.g. aluminium for small heat sink; Case (see text), 4 rubber feet, FS1 2A fuse and holder, L1 (see text)


Fig. 3. Underside of control panel. Flying leads terminate at Veroboard sub-chassis (Fig. 4)


Fig. 4. Veroboard sub-chassis showing assembly and wiring


A close-up of the reset switch assembly and the $1 \frac{1}{2}$ in $\times 1 \frac{1}{4}$ in aluminium heat sink for CSR2

With S6 in the "Compensated" position, operation of the chip will be affected by mains voltage. R7 and VR2 form a potential divider, half rectified by D2. A small portion of this voltage is fed to the base of TR1 through R6. R5 provides automatic transistor stabilisation and the collector of TR1 reflects changes in mains voltage. A rise in voltage at the slider of VR2 will reduce collector voltage and shorten the interval, the opposite applying when voltage falls.

## LAMP SWITCHING

Output from pin 3 of ICl is fed to the gate of CSRI through R9. The thyristor, primary winding of T1 and R10 are connected across the mains supply "so that the transformer is energised by a positive signal on pin 3 . The 6 V secondary winding of T 1 produces an a.c. signal, which is fed through R13 to the gate of the triac CSR2. The enlarger lamp is connected in series with the triac across the mains supply.

L1 and C3 form a transient suppression network which is necessary as triacs may easily be destroyed by high level pulses in the supply. In addition to fitting a 2A fuse in series with the lamp, it is advisable always to use a low resistance series limiterbecause the triac could be destroyed before the fuse blows in the event of short-circuit lamp failure. It is suggested that a 5 ohm 100 W wire-wound resistor ( $\mathbf{R}_{\mathrm{L}}$ ) be fitted externally between timer and enlarger.

As this resistor gets hot, it is inadvisable to include it in the timer in view of the accuracy required of the timing components.

Focus switch $S 5$ by-passes the thyristor so that the lamp may be turned on for an unlimited period.

## CONSTRUCTION

This should commence with the Veroboard subchassis, cutting details being shown in Fig. 4. Because some copper strips carry mains voltage and others the 12 V circuits, it is necessary to be even more careful than usual that the track is cut correctly and that solder bridges are not formed inadvertently. Veropins are best used where connections are made with inflexible flying leads.

LI consists of 50 turns of $22 \%$ s.w.g. enamelled copper wire on a short piece of $\frac{1}{6}$ in ferrite rod, but anything fairly similar will suffice. After winding the coil should be warmed up with a hair-dryer and. Araldite applied. This will cut down any tendency to buzz when the timer is operating.

T1 is small enough to mount with an adhesive, reinforced by copper wire loops passed over the mounting tags and soldered into the track. Some versions of the Douglas MT238 CS transformer have the tappings emerging from the underside of the coil (in the present case, they are brought out from the top) and could then be soldered into the board directly, with appropriate changes in the cutting pattern.

Timing resistors $\mathbf{R 1 5 - R} 25$ should be soldered round S7, bearing in mind that they progress anticlockwise when viewed from below. Even though 2 per cent resistors are being used, it is a wise precaution to measure the total value of RI5-R24 by setting S 7 to junction $\mathrm{R} 24 / \mathrm{R} 25$. R26 should be chosen to match this value and VR3 and RI4 must obviously be excluded in making this match. R14 itself is a limiting resistor (which adds $1 / 10$ second to all timing intervals) and is included to prevent damage to the i.c. if the start switch is operated with all pointers set at zero.

S7, S8, VR3 and R14 wired in series form the timing resistor chain. These may be temporarily connected to the sub-chassis by thin, flexible wires to test the basic timer circuit. With 56 bridged to the "Exact" position and 12 V applied to the i.c. supply rail, the constructor can ascertain that CSR1's gate is being supplied from pin 3 of the timer. S3 and S2 will have to be jury rigged for this test and the 12 V may be supplied from a battery or from T 2 alone connected to mains supply.

The Veroboard sub-chassis with all components mounted in position


Fig. 5. Top panel dimensions and drilling details. The black Perspex operating bar shown unshaded stands proud of the panel by means of a spacer placed under the focus switch S5


## CASEWORK

Fig. 5 gives top panel dimensions and drilling data, whilst Fig. 3 shows the underside of this panel and connections of its components to the sub-chassis. The majority of the hardware is attached to the underside of the panel, the sub-chassis being slung on supports which ensure that it clears the switching associated with the operating bar. The Veroboard supports are tapped with 8 B.A. threads and two paxolin washers should be placed between board and supports before finally screwing down. A large hole must be drilled in the block used to mount T2 so that the light emitting diode pilot can be fitted through the panel underneath the transformer.

Where timers are concerned, a control panel of insulating material is to be preferred as it minimises the possibility of leakage between switches. Formica or Paxolin are suitable, but Perspex ( $\frac{\pi}{16}$ in opaque white) has been used for this timer. Apart from being a fairly good insulator, Perspex is easy to work and neat in appearance. It may.be sawn with a hacksaw, roughly finished with a metal file and finally finished with wet emery and metal polish. Screwholes in the top of the panel may be minimised by attaching components T2 and the terminal block to pieces of tapped Perspex, then cementing to the underside. Holes may be tapped in the usual way, using white spirit as the lubricant and ensuring that the taper/plug is not allowed to clog.

The box covering the timer has also been made of Perspex, clear in this case, secured by screwing into
tapped blocks cemented to the underside of the panel. By rubbing down sawn edges on wet emery placed on a sheet of glass, good mating of the parts of the box can be achieved when cementing together.

The panel should overlap the box itself so that the timer may be free standing on four rubber feet or flush mounted into a darkroom control panel.

## OPERATING BAR

The black Perspex operating bar ( f in thick) stands proud of the panel by means of a spacer placed under the focus switch S5. This bar is intended to flex so that pressure on either end will operate the associated switches. As one side of both S3 and S2 is at mains potential-and photographic workers are handling wet processes-these two switches are inside the timer, rather than simple contacts under the 'bar. "Pushers" cemented to the underside of each end of the bar protrude through holes in the panel to óperate simple switches. Strips of phosphor-bronze have been used, though old relay contacts would probably be equally suitable. The spacer in the middle of the bar is cemented to it and the assembly held in place by countersunk screws through the underside of the panel. Focus switch S 5 will probably have to be recessed into the panel to ensure that it clears the underside of the sub-chassis and that its retaining collar can be attached. Two small, thin leather pads on the underside of the extreme ends of the bar will prevent a click as S3 or S2 are operated.


A tag-board mounted resistor ( $R \mathrm{~L}$ ) is used as a series limiter

Triac CSR2 is mounted on a small heatsink attached to the panel by small tapped blocks. As the load will not normally exceed 150 W , the size is not critical; 10 or 16 s.w.g. aluminium should be usedas large as will conveniently fit between S2 and sub-chassis mounting pillar. Main terminal 2 is electrically common to the heatsink, so the triacs connections should be sleeved and the sink should not touch other components.

A short busbar connects together one side of S2 and S3, one side of S5. centre tap of the secondary winding of T2 and one lead for the pilot. When finally assembled, this busbar is connected to the common neutral line at point 10 on the Veroboard.



Fig. 6. Variations of a 10 second setting with changes in supply voltage produced by the timer with S6 in the compensating mode. This is close to the ideal implied in Fig. 1
S7 and S8 make it easy to set these in the dark by feel, whereas the less critical setting of VR3 can be closely estimated. In practice, the timer will be set immediately the test strip has been assessed, filtration changes made and before the lights are turned off ready for the next print to be made. It is reassuring to be able to check the setting in darkness to avoid wasting an expensive sheet of colour material!
The operating bar, carrying the most used controls, are nearest to the worker and easily operated in darkness. Positions of S3 and S2 may need to be reversed depending on the timer's position relative to the enlarger, dry bench layout and whether the user is left-handed. S3 nearest to the enlarger seems to be a natural choice. Any or all of the operating bar controls could be paralleled for foot operation, thus freeing the hands for shading and other manual control.

The timer will work comfortably at 210 V or below, the author's own darkroom version having been in use during the power crisis. It will make for more consistent exposure, particularly where colour emulsions are concerned. Whilst falling mains voltage affects the colour temperature of the lamp to a small degree, the average variation will not have any practical effect, nor call for filtration changes of more than about 025 Y - which can safely be ignored. Used with a filter Nomogram, this timer should enable the user to produce consistent prints of high quality with the minimum of wastage.

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## NEWS BRIEFS

## Speakers In The Sun

How better to announce a new range of speakers than at a conference held in Malta? This clearly, is the view of the newly recreated Marsden Hall International company who recently held demonstrations in a luxury hotel in Malta of their latest range of quality loudspeakers.

Extending from a shelf-mounting 10 W capacity unit up to a free standing 50 W studio module, the range is split into two sections called respectively Annexe for powers up to 30W r.m.s. and Symphony for the two upper capacity modules of 35 and 50 W rating.

The units are attractively styled in either Teak or Walnut, with detachable fronts either fabric or filter foam covered. Indeed, the foam-covered versions can be supplied in any of 26 different colours.

Of course. the critical test of a loudspeaker is not really the appearance, although this counts for a great deal in some circles, but the audio performance. Here, Marsden Hall went to some lengths to present their equipment as completely as possible.

They organised an audio demonstration of the various units in stereo pairs with the ability to switch from one pair to another at will. This allowed the listener to compare the six models one with another at ease. A selection of styles of music and types of orchestration was presented to show the ability of the equipments to cope with transients, bass and treble response, and so on.
Predictably, the larger studio units came out on top in all sections, but after all, the demonstration was given in a fairly large suite and it is under such circumstances that the full bass response can really benefit. However. all the units, including the smallest Annexe XL10 pair performed as well as one would expect, if not better.

It is quite educational to switch from a triple-driver system of 50 W capacity to the smallest shelf-mounting model which uses only a six-inch base unit and a single three-inch tweeter. There is bound to be some colouration of response and a step-by-step run through the range shows this up as fairly minimal, often appearing with switching in one direction and not the other.

Prices are well in line with current ranges, running from $£ 44.50$ per pair for the smallest shelf-mounting model to $£ 172 \cdot 50$ per pair for the Symphony 4522 studio units. The two smallest units are both shelf-mounting, the largest of the Annexe range can be used as a shelfmounted or a freestanding unit whilst both the Symphony units are freestanding and can be optionally supplied with castor stands.

The prices mentioned are the recommended retail values and we understand that the units are to be available from specialist $\mathrm{Hi}-\mathrm{Fi}$ dealers throughout the U.K.


#  Loapartid INTRODUCTIONance Heat Light Speed Force Load Sound By P.R.ALLCOCK istance Force Load Sound Frequency Distance Heat 

## INTRODUCTION TO SERIES

This series of articles is intended as an introduction to the vast range of transducers that exists today. Instrumentation engineers are constantly challenged to satisfy the increasing demands made by their colleagues in other areas of specialisation and may be called upon to measure an almost infinite variety of physical phenomena.

Broadly defined, a transducer is any device by means of which energy, available in one form, may be changed to energy in another form. Energy can exist in various forms such as electrical, mechanical, acoustical and thermal and often the output energy of a transducer is in the electrical form. Devices which convert electrical power into, say, mechanical force also come within our broad definition but are often classified into a separate group known as electrical machines. Some of these devices are very important to the electronic engineer and small rotary motors, stepping motors and related devices crop up very frequently, as, for example, in equipment using tape or paper as a recording medium. Often the input energy will be in mechanical form and the first section of the transducer may then perform a conversion from say applied force to displacement.
The subsequent conversion of displacement to electrical energy would take place in a second section and could employ one of the many principles available such as: piezo electric effect, differential transformer, capacitance resistance or inductance variation, photo electric effect, magnetostriction, etc. The nature of the electrical output from the transducer depends on the principle involved in the design and may be analogue, digital, frequency modulated or some form of pulse train. In fact a transducer may be based on almost any combination of the various mechanical and electrical arrangements available.
Some examples of commonplace transducers are listed below and these will be covered in the series.

Measurement Required Possible Transducer<br>Shaft rotation or position<br>Linear displacement<br>Coded optical disc<br>Variable resistance element<br>Temperature<br>Ultrasonic sound waves<br>Mechanical strain<br>Liquid flow<br>Thermistor or variable resistance devices<br>Piezo-electrical material Resistive or semiconductor strain gauge<br>Turbine type flow-meter

IN General terms a transducer is a device which converts (or transduces) energy from one form to another. This definition is rather all-embracing since it includes devices such as electric motors, car engines and turbines whereas, at least through common usage, the term normally refers to devices of a somewhat more specialised nature.

In one category we have devices that can convert an electrical input stimulus into a mechanical output response, such as occurs in the moving coil loudspeaker, whilst in a second category we can group those devices that convert some physical quantity, property or condition to an electrical output signal as occurs for example with a pick-up cartridge.

It should be noted that transducers are not restricted to the use of an electrical signal at the input or output but such devices are by far the most common today due to the widespread use of electrical and electronic techniques in control, instrumentation, automation and measurement and the relative ease of processing or modifying such signals. For example, in many industrial processes the electrical output of a- transducer is used, either directly or after processing, as a feedback signal in a servo-loop to control the output of the system in a specific manner.

In other applications a transducer might be connected to a readout device, such as a counter, tape printer or digital meter, and used to provide quantitative measurement information to an operator. Since it is not possible to control a process without measurement of one or more variables it is clear that transducers play an important part in a wide variety of modern engineering systems and measurement processes.

## OPERATING PRINCIPLES

The operating principles of the majority of transducers in common use are straightforward, but in practice the utilization of these principles often involves very careful design and precision engineering in order that defects, which might otherwise limit the device accuracy, are kept to a low level. Even with careful manufacture, environmental factors such as temperature, vibration, shock and stray magnetic or electric fields, must be taken into account if the best accuracy is to be obtained.

Often several basic principles are used together to achieve the required output. The term measurand is often used to denote the quantity, condition or property which the transducer translates into the required output signal and in some cases the transducer does not respond directly to the measurand but to a related variable.

For example, transducers designed to measure acceleration are often activated by a displacement or force which is related in a known way to the acceleration.

[^2]The "two-stage" principle is illustrated in Fig. 1.1. The first section translates the measurand into-a displacement or stress and this in turn acts as the stimulus for the second stage which produces, the requisite output. The second stage may generate the electrical output directly from the output energy of the mechanical transduction stage in which case it is known as self generating. The alternative form, which requires an external power source for excitation, is usually called a passive transducer.

## TRANSDUCER CLASSIFICATION

It is an almost impossible task to classify the whole range of transducers now in use. However it is feasible to group them on the basis of their fundamental operating principles as in Table 1.1, even though they may be used in, or have evolved from, widely different applications.

Even with such grouping it may be difficult to classify a particular transducer uniquely because of overlap in the various selection parameters used. For example a thermistor might be classed as a variable resistance device or alternatively as a thermal device. Strictly speaking the thermal energy input is the measurand which can be related to temperature but two distinct modes of operation are possible.

If the electrical currents are kept sufficiently small the resistance will be dependent only on the heat input whereas if larger currents are permitted some selfheating will occur and the temperature will depend on two sources of heat.

## VARIABLE RESISTANCE TRANSDUCERS

In the moving contact type, the measurand, either directly or indirectly, causes a change in the resistance of an electrical element. This change is usually caused by either a moving contact system or some physical or chemical action. The basic principle of a moving contact system is illustrated in Fig. 1.2 and here changes in liquid level are used to move a sliding contact along the resistance element BC .

TABLE 1.1
SOME ELECTRICAL PHENOMENA THAT CAN BE EMPLOYED IN TRANSDUCER OPERATION

| Resistive | Electromagnetic |
| :--- | :--- |
| Capacitive | Thermo Electric (Voltaic) |
| Inductive |  |

Inductive

| Photo Resistive | Ionisation |
| :--- | :--- |
| (Conductive) |  |
| Photo Electric (Emissive) | Electrolytic |
| Photo Voltaic |  |

Photo Voltaic
Piezo Electric
Potentiometric
Piezo Resistive
Magneto Resistive

If we assume that the resistance between $A$ and $B$ is zero when the tank is empty the resistance $R_{\text {AI3 }}$ for a liquid level of $h$ will be $R_{0} \cdot h$ where $R_{0}$ is the resistance change per unit height of liquid. If we assume also that $R_{0}$ is constant i.e. the resistance element is perfectly uniform over its whole length $L$ we can express the output voltage as $V_{0}=E\left(\frac{h}{L}\right)$ volts providing no current is drawn via the output terminals (Fig. 1.4).
In some application it may be desirable to use a resistance element that is not uniformly wound in which case the output voltage is no longer given by the above equation but by

$$
V_{\mathrm{o}}=E\left(\frac{R_{\mathrm{AB}}}{R_{\mathrm{BC}}}\right)
$$

The way in which $R_{\mathrm{AB}}$ varies with height $h$ determines the characteristic law of the transducer since $E$ and $R_{\mathrm{BC}}$ are fixed. For example the resistance element $R_{\mathrm{BC}}$ could be wound on a thin wedge shaped sheet of insulating material as shown in Fig. 1.3. The resistance per unit length, $R_{0}$, is not constant for this case and will in fact increase uniformly as the slider moves from $B$ to $C$ as long as the resistance wire is sufficiently fine

and the element carefully wound so that the turns cannot move as the contact passes over their surface.

For the wedge profile shown in Fig. 1.3 the variation of $R_{0}$ with height $h$ will be as indicated in Fig. 1.5. The resistance per unit length is obviously a function of $h$ since as the slider moves along the resistance element the length of each turn of the resistance wire increases slightly. The variation of $R_{0}$ can be written as

$$
R_{\mathrm{o}}=R_{1}+\frac{h}{L}\left(R_{2}-R_{1}\right)
$$

If we put $h=0$ we find $R_{\mathrm{o}}=R_{1}$ and for $h=L$ (the limit of slider travel) we find $R_{0}=R_{2}$.

To determine the resistance between AB or BC we need to add together the resistance of all the turns of wire between the two points of interest. This is achieved mathematically by integration and for the section between A and B we have

$$
\begin{aligned}
R_{\mathrm{AB}} & =\int_{o}^{h} R_{0} d h \\
& =h R_{1}+\frac{h^{2}}{2 L}\left(R_{2}-R_{1}\right) .
\end{aligned}
$$

At full height $h=L$ and substitution in the above equation then gives $R_{1 \mathrm{BC}}=L \cdot\left(\frac{R_{1}+R_{2}}{2}\right)$ which is simply the length of the element multiplied by the average resistance per unit length.
The variation of output voltage with height for this type of element is illustrated in Fig. 1.5 which also shows, for comparison, the output of a uniformly wound element operating from the same supply voltage.

The wedge element gives the greater rate of change of output voltage providing the tank is at least half full $\left(h>\frac{L}{2}\right)$ whereas the uniform element gives the larger output voltage for all heights except $h=L$ at which level both types give the same output voltage.


## LAWS

The details discussed so far have shown that it is possible to control the characteristic law of the transducer by suitable choice of resistance variation with length of travel. In some applications it is the angular rotation of a shaft that activates the moving contact and in this case also it is possible, by choice of shape for the resistance wire former, to produce a given characteristic such as a sine or cosine variation of resistance against angular position.

Tapered resistance elements are sometimes used in bridge circuits to open out an otherwise cramped scale at the extremes of angular travel. The log and reverselog audio volume controls use graded regions on the track with different resistance values per unit angle of rotation in an attempt to give a straight line piecewise approximation to the specified law.

Obviously mechanical friction has to be kept to a minimum in devices that have to operate with low forces and wear of contact and wire can limit the useful life. . The wire used in some precision potentiometers is very fine to give good resolution and excessive currents can easily destroy the element, especially when the sliding contact is near to one end of the range of adjustment, due to the concentration of heat over a small region.

## OUTPUT LOADING EFFECTS

No mention has been made of the loading that will occur if the transducer output is fed to a resistance which is not large relative to the element resistance. This form of loading is illustrated in Fig. 1.6 where the load on the output is represented by the inclusion of $R_{\mathrm{L}}$. If $\alpha$ represents the fraction of the resistance element between $A$ and $B$ and ' $R$ represents the total resistance of the element the circuit is simply that of a potentiometer having an upper portion of $(1-\alpha) R$ and a lower portion of $\alpha R$ in parallel with $R_{\mathrm{L}}$.

Analysis of the circuit gives the output voltage $V_{o}$ as:-

$$
\begin{aligned}
V_{\mathrm{o}}= & \frac{\alpha E}{1+\frac{\alpha R}{R_{\mathrm{L}}}(1-\alpha)}= \\
& \frac{\text { (Ideal Unloaded Output Voltage) }}{1+\alpha K(1-\alpha)}
\end{aligned}
$$

where $K$ is the ratio of transducer element-resistance to loading resistance.

For a given value of $K$ we see that the output voltage is correct for $\alpha=0$ and $\alpha=1$ (i.e. $V_{o}=0$ and $V_{o}=E$ at the limits of travel) but at intermediate settings of $\alpha$ the output is in error and will always be less than the (ideal) true output.

In any reasonable system $K$ should be considerably less than unity in which case the output is lower than the true value by approximately $[100 \alpha(1-\alpha) K]$ per cent. The maximum error for a given $K$ value occurs, as might be expected, when $\alpha=0.5$ and is of the order $25 K$ per cent low providing $K$ is small. For $K=0 \cdot 1$ (i.e. $R_{\mathbf{L}}=10 R$ ) the maximum error at mid-travel would be about 2.5 per cent low.

## THERMISTORS

A thermistor is a heat-sensitive semiconductor resistor with a relatively large negative temperature coefficient of resistance, although thermistors having positive temperature coefficients are also available. A typical device will exhibit a resistance drop of about 4 per cent per degree temperature rise.

Unlike the p-n junction of a semiconductor diode or transistor, the thermistor does not depend on the effects that occur at a p-n interface and is not manufactured by doping silicon or germanium with impurities: Instead, a thermistor is made, using a sintering process, from mixtures of the oxides of metals such as manganese, nickel, cobalt, iron, copper, titanium and magnesium. Leads are attached to metallised areas on the thermistor body or connected during the controlled heating processes.
A very wide variety of shapes and sizes are now available ranging from small beads to large plates or rods.

A protective coating of epoxy or fused glass is often provided and some types are available in glass envelopes, either evacuated or gas filled. Resistance valiues at $25^{\circ} \mathrm{C}$ range from about $1 \Omega$ to several $\mathrm{M} \Omega$.

Early devices were very variable in characteristic and it was difficult to match the characteristics of two similar thermistors. Fortunately improvements in manufacturing techniques have virtually eliminated the shortcomings of the early devices and thermistors are now available which are stable with time, matched and interchangeable to within a fraction of a degree over wide temperature ranges.

Thermistors now rival thermocouples in many applications since stable amplification is invariably required with thermocouples due to their low output voltage (typically of the order of $50 \mu \mathrm{~V} / \mathrm{deg} \mathrm{C}$ ). The main advantage of the thermistor over the thermocouple is sensitivity. The output of the thermocouple is determined by the choice of the two metals and cannot be changed.

Typical thermistor bridge or potentiometer circuits can give output voltage changes of $100 \mathrm{mV} / \mathrm{deg} \mathrm{C}$ which is some 2,000 times that of an equivalent thermocouple. The useful temperature range of thermistors is considerably less than for the thermocouple, being limited to about $-100^{\circ} \mathrm{C}$ to $+400^{\circ} \mathrm{C}$ whereas thermocouples can operate over a range of thousands of degrees. The thermocouple also has a more linear output since its output voltage per unit temperature change is more nearly constant.

## THERMISTOR POTENTIOMETER

A simple potentiometer using a thermistor is shown in Fig. 1.7. Obviously the same current flows through both the thermistor and the fixed resistor. If the current is sufficiently small the self-heating of the thermistor will be negligible and its resistance will therefore depend on the ambient temperature. If this temperature rises the thermistor resistance will fall (assuming a negative temperature coefficient) and the current will increase. The resulting increase in voltage across the fixed resistor can be used as an indication of temperature but the voltage-temperature characteristic will not be linear.

The resistance-temperature relationship for a thermistor is usually approximated by the equation

$$
R=A \mathrm{e}^{\beta / T}
$$

where $R$ is the resistance at temp. $T^{0}$ Kelvin, $A$ and $\beta$ are constants for the particular thermistor, $T$ is the absolute temperature in Kelvin, and $e$ is the base of natural logarithms, $2 \cdot 7183$. By taking logarithms of both sides of this equation we see that $\log _{e}\left(\frac{R}{A}\right)=\frac{\beta}{T}$. This inverse relationship is sketched in Fig. 1.8.

For two temperatures $T_{1}$ and $T_{2}$ and corresponding


A selection of Mullard thermistors, (a) Diac NTC type, (b) Plate NTC type, (c) PTC type, (d) Rod NTC type, (e) Rod VDR type, (f) Bead in glass type
resistance values of $R_{1}$ and $R_{2}$ respectively we can write:

$$
\frac{R_{1}}{R_{2}}=\frac{A e^{\beta} / T_{1}}{A e^{\beta / T_{2}}}=e^{\beta}\left(\frac{1}{T_{1}}-\frac{1}{T_{2}}\right)
$$

which shows that the ratio of the two resistances depends only on $\beta$ for given values of $T_{1}$ and $T_{2}$. The value of $\beta$ usually lies in the range $2,000^{\circ}$ to $5,500^{\circ}$ Kelvin and is specified by the manufacturer. Also it is usual to quote a typical resistance value at some specific temperature, often $25^{\circ} \mathrm{C}$. If the variables with subscript 2 are taken as the given $25^{\circ} \mathrm{C}$ values, then

$$
R_{1}=R_{25} e \beta\left(\frac{1}{T_{1}}-\frac{1}{298}\right)
$$

which allows $R_{1}$, the resistance at $T_{1}$ Kelvin, to be evaluated in terms of the resistance value at $25^{\circ} \mathrm{C}$. For example if a particular thermistor has $R_{25}=1 \mathrm{k} \Omega$ and $\beta=5,000$ the resistance at $0^{\circ} \mathrm{C}$ will be

$$
R=1,000\left\{e 5,000\left(\frac{1}{273}-\frac{1}{298}\right)\right\} \Omega=4,660 \Omega
$$



Fig. 1.9. A simple thermistor bridge circuit

Fig. 1.11. A simple thermistor thermostat
Fig. 1.10. The curves of a positive and a negative temperature coefficient thermistor

The resistance has changed by nearly five times for a temperature change of 25 deg C .

One disadvantage of the simple circuit of Fig. 1.7 is that the output voltage (across the fixed $1 \mathrm{k} \Omega$ ) will vary with battery voltage which renders any calibration useless. This can be avoided by using a null method as shown in Fig. 1.9.

## BRIDGE CIRCUITS

The bridge is balanced, by adjustment of $R$, to give zero output voltage. For this condition the resistance $R$ and that of the thermistor must be equal and hence the temperature can be determined. Since the bridge circuit is always balanced when a reading is taken changes in the battery voltage have no effect on the null point providing current levels are sufficiently low to prevent self-heating.

When used at low current levels the thermistor responds to the ambient temperature as the measurand. However in some applications the most significant heating effect is due to power dissipated in the thermistor itself. As the current through the thermistor rises, from some initial low value, the voltage drop across the device rises. The onset of self heating eventually occurs and at a certain temperature the voltage stops increasing since the current increase is offset by the falling resistance.

Further increase in current (and power) causes the voltage to fall below this maximum value which is typically in the range $40^{\circ}$ to $90^{\circ} \mathrm{C}$. The temperature for maximum voltage drop depends on the $\beta$ factor and ambient temperature. Another feature which is important in some applications is that of thermal time constant. An abrupt change of ambient temperature or power dissipation causes an exponential type change in the thermistor body temperature.

As mentioned earlier the temperature coefficient is large and can be shown to be equal to $-\mathrm{B} / \mathrm{T}^{2}$ per degree for negative temperature coefficient devices.

For positive temperature coefficient devices the coefficient is only positive over a finite temperature range as illustrated in Fig. 1.10 and it is usually difficult to express the resistance-temperature variation by a simple equation.

An interesting application of an n.t.c. thermistor is shown in Fig. 1.11 where the circuit behaves as a thermostat by operating the relay when the temperature falls to a predetermined level, say $t_{0}$. Resistor $R_{1}$ is set equal to the resistance of the thermistor at the specified $t_{0}$. At temperatures above $t_{0}$ the differential action of TR1 and TR2 is such that TR2 is passing current whilst TRI is off due to the fact that the voltage across $R_{1}$ is greater than that across $R_{T}$.

As the temperature falls these two voltages become more nearly equal and eventually current starts to flow in TR1 at the expense of that in TR2. The current in TR1 flows via the relay coil and the resulting increase in voltage drop causes the base voltage of TR2 to fall. This further increases the current in TR1 and due to the regenerative action TR1 turns fully on and TR2 turns off.

The relay operates due to the flow of collector current in TR1 which aids the existing relay current that flows via $R_{1}$ and $R_{2}$. Due to the positive feedback effect the turn-on is rapid but the temperature will have to rise well above $t_{0}$ before the circuit resets itself. This effect is known as hysteresis.

Ideally $R_{2}$ should be made $4.6 \mathrm{k} \Omega$ so that the bridge formed by the four "resistance arms" is balanced at the trigger temperature. Under these conditions the circuit is relatively insensitive to variation of supply voltage $V_{\text {cc. }} \quad R_{3}$ determines the current levels for TR1 and TR2 once $V_{C C}$ and $R_{T}$ are fixed. A thermistor, relay. and battery in series can act in the same way but operation will be very dependent on supply voltage and the switch-on instant will be less well defined.
Next month: Resistance thermometers, strain gauges, thermocouples and thermopiles.


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## PHOBOS THE POTATO MOON

During the mission of Mariner 9 to make a photographic survey of Mars, some 32 high resolution pictures were oblained of Phobos, the small Martian moon. These pictures were fortunately able to cover some 80 per cent of the satellite's surface and the first atlas of this small moon has now been published. These have been "cleaned up" using the special computer technique. The atlas was compiled by a group of scientists from Cornell, Stanford, Caltech and NASA, four very active establishments.

It is common practice, in setting up an atlas, to use a sphere on which the pictures can be placed and oriented. However, Phobos presents a special problem of its own for it is irregular in shape, more like a potato. It is thought that as the satellite has such a very small gravitational field it has not been pulled into a harmonious shape as it cooled. It is perhaps a quirk of cosmological humour that it so closely resembles the familiar potato.

Many models were tried out and T. Duxbury at Caltech finally arrived at a solution. The satellite radii are found to be $11.5,15 \cdot 5$ and 9.5 kilometres. Duxbury settled on a triaxial ellipsoidal system of co-ordinates.

This reference grid was computerfitted with the information and the results suggest that Probos is in synchronous rotation around Mars. It keeps the same face to Mars and therefore behaves as the Earth's own satellite. The longest axis points toward Mars and the intermediate one of 11.5 kilometres is in the orbital plane, the shortest axis being normal to the plane.

Phobos is very heavily cratered and the surface density is close to saturation. The initial mapping reveals some 50 craters. Seven have been given names though one is only a partial crater. This is the Kepler Ridge. Others are named Roche, Wendell, Sharpless. D'Arrest, Todd and the largest complete one, Stickney. The variations of the high to low points of the craters are as much as 20 per cent of the radius.

When compared with the Earth's Moon the feeling is that Phobos was formed as an independent unit very early in the life of the Solar System. Extensive fracturing seems to have taken place as a result of meteoritic impacts. The landscape appearance has been the result of meteor impacts. No doubt also the smalliness of the body renders it liable to considerable geological change.

The structure generally suggests that Phobos is solid rock and the evidence is the manner of the cracks that can be seen. There is too little gravitation for it to be conglomerate. It is possible that the satellite is only part of a larger body that disintegrated a very long time ago.

As with all these "guestimations" alternatives are also offered. Whatever the conjectures may be there is now a good case to include a special programme to be set in operation at the time of the Mars Viking missions already planned.

## HELIOS

In a past "Spacewatch" issue some details of a planned space probe were given. This or rather the first of two probes called Helios has been launched on behalf of West Germany by NASA.

The project was given the goahead in 1969. The probes weigh some 357 kilogrammes and will operate with a 192 day orbit. They will, at the apogee point, be within $45,000,000 \mathrm{~km}$ of the sun, about onethird of an astronomical unit. Special thermal control will come into operation on these missions.

The first probe will steer into the ecliptic, the plane of the Earth's orbit, between Mercury and the Sun. Its final distance from the Earth will be some $300,000,000 \mathrm{~km}$. The primary mission will last 120 days. However, the life of the probes will be at least 18 to 20 months.

These two probes will also be supplementary, in the sense of data gathering, to that being obtained by Explorer 47 and 50 and also that from Pioneers 10 and 11 . The actual orbit of the probes will have a perihelion of 0.3 of an Astro-
about 1.0 Astronomical Unit with respect to the Sun.

The tasks set are very extensive, the instruments will investigate the magnetic field, density, speed and direction of the Solar Wind particles (electrons, alpha particles and protons). 1t will be possible to evaluate the spatial gradients involved in those quantities. The study of the electron plasma oscillations will be made by radio which exhibits the characteristic Type III outbursts.

Other experiments will look at the interplanetary dust, its gradients, density and direction and its exact composition. X-ray examination of the surface of the Sun will be undertaken.

## ODD BODY

Helios is an unusual shape for a probe with a 16 -sided central body and truncated cones top and bottom, each with 16 facets. The solar cells are disposed over the surface of the probe.

Because of the extreme temperatures involved, some $370^{\circ} \mathrm{C}$, nearly 90 per cent of the Sun's radiation must be reflected. This makes very stringent demands on the probe's thermal regulation system. The solar cells may not be subjected to a greater temperature than $+165^{\circ} \mathrm{C}$. The interior temperature must be kept within the limits of $-10^{\circ} \mathrm{C}$ to $+20^{\circ} \mathrm{C}$.

This problem is partially solved by using special SSMs, second surface mirrors, developed by NASA. They are made from fused silica and covered on the underside by a thin film of silver which is further covered by a dielectric. These cells have a high emissivity and will prevent the central body from heating up to $800^{\circ} \mathrm{C}$. This is the sort of temperature to which the aerial will be subjected when extended even though the probe is revolving at 60 r.p.m.

However, the mirrors alone are not sufficient to hold off the Sun's radiation which amounts to some 22,400 watts/sq.m. A special thermal insulation system consisting of layers of spaced metallised foil is also used. The foil is held apart by nylon mesh to avoid heat bridges. Other ingenious controls consist of bimetal levers which operate the louvres to allow the experiments to be' carried out. They are also used to control the inside temperature.

Control of the mission is being carried out at the Command Station at Weilheim, Germany, with a 30 metre dish built specially for these missions. Also in use for transmission of commands and the reception of data is the Planck Institute 100 metere radio telescope at Eifel.
non-technical musicians will give the synthesiser a wide berth. Musical readers of P.E. are probably not typical as their interests will be technical: lets face it-the constructional articles by Douglas Shaw and Alan Boothman call for a high standard of technical ability and understanding if the correct results are to be obtained.

## THE TREND

Our educational system should ensure that music has full rein in years to come. At the same time, the rising number of flat-dwellers makes the ordinary piano something of a problem in their homes. So the electric piano might well become more common, albeit the 5 -octave manual imposes its limitations (an 8 -octave instrument would present no technical problems, but would defeat the object by taking up the same space as an upright). The harassed parent could provide budding pianists with headphones through the five-finger exercise stage-surely a selling point!

Craftsmanship is rapidly becoming scarce. The special skills required in making violins, for instance, may become both expensive and hard to find as time passes. Woodwind, brass, pianos-indeed all acoustic instruments - are craftsman-made and inflation could make these difficult to afford for future generations.

## CONTROL

Although electronic components are currently in short supply, they ought to remain relatively cheap in this age of technology. So there is a strong possibility that electronic musical instruments will progressively supplement-or even sup-plant-acoustic instruments in classical music.

Where money is to be earned in music, it is certainly not in the serious field. Pop groups have proved that they have money (and even instruments) to burn! Many of them use synthesisers and other sophisticated equipment, but the end-product is often hideous!
If the serious music fraternity looks with trepidation to the future, it will have to come to terms with electronics and start to lay down guidelines to manufacturers for the types of electronic instrument it would like to see made. Failing this, pop groups will continue to give the impression that cacophony is the only end-result, to the detriment of electronics and good musical taste.


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## P.E. KIT REVIEW

SINCLAIR

## SCIENTIFIC CALCULATOR



## BUILDING THAT COUNTS

The pocket calculator is now firmly entrenched as a part of our way of life. It appears in almost every shop window alongside stationery, toys, cassette recorders and sweets. It comes in various forms extending from the simple four-function up to the sophisticated multi-function accountant's and engineer's dream, and can be acquired in the ready-made or even the kit style.

Recently just such an instrument became available, the Sinclair Scientific. which utilises Polish notation and logarithmic and trig functions to carry out calculations currently only available in far more complex machines.

## THE KIT

The Scientific kit comes carefully wrapped and packaged in a compartmented foam plastic moulding complete with all parts, very comprehensive instructions and a variety of guarantee conditions suited to those who either fail or meet problems at the constructional stage.

Assembly is certainly simple if one has had prior experience with i.c.s, soldering and plastic models, but it should be stated that the job is not one for the totally inexperienced if they also have no tools. For example, a quality miniature soldering iron is a must and miniature sidecutters and snipe-nosed pliers a very useful adjunct.

The writer found a need for a model-maker's scalpel at one stage in gaining a tight fit of the keyboard assembly in the very nicely designed outer case. A small amount of mould flash was intruding on the area to be occupied by the keyboard.

In fact assembly went ahead smoothly and without any real hitch thanks to the clear and precise instructions. Indeed these are directed to all possible purchasers and take the uninitiated right through the various problems to be met such as tools, handling calculator chips, cleanliness and so on in a very helpful manner.

The only part of the assembly which might pose problems to some concerns the keyboard and here it would be wise for any first-timer to follow the instructions very carefully. Its not that one damages the unit in the process, more that one has to do the job several times to get it right.

In fact, the case and mechanical assembly tend to appear to be somewhat flimsy on first inspection but the design is quite interesting, making use of the inherent flexibility and strength of the plastic material to give at one and the same time a strong and attractive case. The external appearance is not marred by fixings of any sort and yet it is a simple matter to take apart and reassemble the case.

The only point of serious criticism which came to light concerns the battery connectors. In the Scientific as in most of the Sinclair range made to the same standard, use is made of small plated spring clips which are soldered to the circuit board. These at one and the same time form the connectors and retainer clips for the batteries. Whilst each clip has a projection which engages in a hole in the board, this effects location rather than mechanical retention, so only the soldered joint provides the necessary strength.

Under normal desk use this is no problem, but the vagaries of pocket use over a period of three months have caused one terminal to come loose twice. The first time the soldering was questionable and the second occurred within a few days of first use anyway, so the problem appears to be removed with due attention to one's soldering.

## OPERATION

In use the Scientific takes some time to acclimatise to. Unlike its four-function associates such as the Cambridge, it makes use of so-called Polish notation for the normal fourfunction mathematics. Thus to add $A$ and $B$ one enters $A+$ and then $B+$. Take $B$ from $A$ one enters $\mathrm{A}+$ and then $\mathrm{B}-$.

Equally, it requires "programming" by way of pushing one of two extra buttons in order to carry out
the extra functions such as providing logs and trignometric functions.
However, this is little hardship for the results obtained. The machine is sufficiently small to fit into the palm of the hand and be operable by the thumb of that hand if one wishes to show off just a little.

Clearly, at such a low cost and in such a small package, one cannot expect to get all the refinements of a $£ 400$ equipment or even, for that matter, of an $£ 80$ item. However, the Scientific gives results well in keeping with the job it is supposed to do. For example, taking the value of logs it gives. All those tried by the writer have matched four figures tables and, indeed, six figure tables most of the time. Of course, the machine only indicates to five figures, and to some this might be a disadvantage.

One or two simple tests indicate the value of the machine. Take $2^{64}$ up to that value and then back down again to 2 , The result is 2.0001. Not bad.

Some of the Trig functions seem, in the face of accurate tables, to leave something to be desired, and it is wisest to learn just when one ought to bring a calculation out of the machine and make suitable notes in this area.
So far any errors of note on the Scientific have been clearly traceable to the user and not the equipment and the more one uses it the easier it becomes to replace the slide rule.



Experiments to detect the existence of Extra Sensory Perception (ESP) normally involve two people: the subject being tested and an operator to turn over cards and record their symbol or value. A common problem in these experiments is a gradual reduction in performance due to boredom and lack of interest.

The electronic apparatus described here was developed so that the subject alone could test himself and so that interest could be maintained by introducing a game element into the testing by using "biofeedback".

## CONTROL LAYOUT

The layout of the detector and its controls are shown in the photographs. When the sPIN button is pressed the circuitry oscillates until the button is released when the circuit freezes at either "left" or "right".

The choose lamp lights to indicate that a choice must be made between left and right. The subject then presses either the LEFT button or the RIGHT button. If he has chosen correctly, lamp LP1 lights, the CORRECT counter counts on by one digit and the totals counter (at the rear) also counts on by one digit.

If he guesses wrongly, only the rotals counter is activated. The SPIN button is used again to spin left or right and the subject chooses again and so on. If the subject makes two successive correct guesses, then lamp LP2 lights as well as lamp LP1 (and both counters count on one).

As long as he makes more correct guesses, more lamps light until all four are illuminated and these stay illuminated so long as he guesses correctly. If he chooses the wrong button, all the lamps go out and he must begin again, trying to build up a row of lights. If required, the counters provide information for statistical analysis.

## THE SYSTEM

The circuit is largely composed of cheap 74 -series TTL integrated circuits. The block diagram is shown in Fig. 1 and the detailed logic in Fig. 2.

Block MV1 is an astable multivibrator which alternates high and low or "left" and "right" at about five thousand times per second.

It may be made to oscillate at a higher frequency if required by reducing the values of two capacitors C1 and C2. In any case these capacitors should have equal capacitance so that the multivibrator spends equal lengths of time in its "left" and "right" positions.

There is no possibility of the operator guessing the state of the circuit by the length of time he presses the SPIN button because the multivibrator is spinning all the time, not just when the SPIN button is pressed.

The output from the multivibrator is fed to the clock pulse input of a 7472 gated J-K flip-flop (BS1), which will not accept the pulses from MV1 unless all J and K inputs are at logic 1. (Positive logic is used throughout, i.e. logic 0 equals zero volts and logic 1 equals about +4 volts). These J, K inputs are controlled by SPIN switch S1 coupled to a "bistable latch" (Ll).

The bistable latch is required to overcome problems of switch contact bounce and is formed from two of the NAND gates in a 7400 pack.

When Sl (whose wiper is earthed) is in position "a" so that input "a" is at logic 0, input " $b$ " is at logic 1. Output C is then at $\log$ ic 1 and output $\overline{\mathrm{C}}$, at logic 0 , is connected to the $\mathrm{J}, \mathrm{K}$ inputs of BS 1 (see Fig. 2). If SPIN switch S1 is pushed so that contact " $b$ " is earthed, the outputs $\bar{C}$ and $\bar{C}$ of the bistable latch reverse, the $\mathrm{J}, \mathrm{K}$ inputs of BS 1 are then at logic 1 and the output from multivibrator MV1 is accepted into flip-flop BS1, causing the outputs ( Q and $\overline{\mathrm{Q}}$ ) to alternate at 5 kHz .

If $\mathrm{S1}$ is released so that contact " $a$ " is earthed, output $\overline{\mathrm{C}}$ goes to logic 0 , thereby freezing flip-flop $B S 1$ in either a "left" $(Q=1, Q=0)$ or "right"


Fig. 1. Block diagram of the ESP Detectcr


## PR1-TRT-2N3704

Dr - D14 - WNOH
——- internal Le comatetions
Fig. 2. Complete logic diagram of the ESP Detector


Fig. 3. Layout of the integrated circuits and associated components on the perforated board. Circled letters indicate connections to Veroboard (Fig. 4). This shows the board viewed from below (i.e. wiring side)
$(\mathrm{Q}=0, \overline{\mathrm{Q}}=1)$ condition. This condition is then to be guessed by the operator and his response compared with Q or $\overline{\mathrm{Q}}$.

## THE OPERATOR RESPONSE

The left and right operator-response switches S2 and S3 are each connected to a bistable latch formed from each half of a 7400 quad nand pack (IC4). The output from each bistable latch is fed to the clock pulse input of a JK flip-flop (BS2 or BS3), contained in pack IC5 which is a 7473 dual J-K flip-flop. Initially the Q outputs of both BS2 and BS3 are at logic 0, but after the operator's input from L1 with S2 or from L2 with S3, the Q output of either BS2 or BS3 will change to logic 1.

The Q output of BS2 with the Q output of BS1 is fed into one AND gate of one half of a 7451 dual 2-wide 2 -input AND/OR/invert pack IC6, while the Q output of flip-flop BS3 with the $\bar{Q}$ output of BSI is fed into the other And gate of IC6a.

If the operator has guessed correctly (i.e. if the Q output of BS1 and the Q output of BS2 are both at logic 1, or if the $Q$ output of BS3 and the $Q$ output of BS1 are both at logic 1), then, and only then, will the output $F$ change from logic 1 to logic 0.

This output $F$ is fed to an inverter whose output is connected to a monostable (MS1).

## CORRECT COUNTER

The monostable was built using a 7400 quad nand pack.

When (after a correct guess) the signal from the inverter goes from logic 0 to logic 1 , the monostable output rises to logic 1 for about 0.1 seconds. Passed through base resistor R5, this signal drives TR5 into an "on" condition for an equal length of time so that the correct counter counts on one digit. Diode D9 protects TR5 from the back e.m.f. of the counter's coil.
The other half of AND/OR/INVERT pack IC6 is used to detect whether either BS2 or BS3 has been activated by switch S2 or S3 and if so, it then activates monostable MS2 (through an inverter), which drives TR6 to energise the totals counter. The output $G$ (changing from logic 1 to logic 0 ) also switches off TR7, to switch off the choose lamp. This lamp remains off until the circuit is reset through switch S 1 when the output G again rises to logic 1.

The resetting of the activated flip-flop BS2 or BS3 (depending on whether S2 or S3 was pressed) is obtained at the same time that the SPIN switch SI is pressed, by connecting the output from latch L1 (which changes from logic 1 to logic 0 as S1 is pressed) to the clear inputs of both BS2 and BS3.

## PREVENTING CHEATING

There is a further connection between the two flip-flops BS2 and BS3: the $\bar{Q}$ output of each half is connected to the J input of the other half and each $K$ input is earthed to logic 0 . This ensures that once either S2 or S3 has activated one of these flip-flops, then both flip-flops are locked and are


Fig. 4. Layout of the transistor circuits on the Veroboard panel
not affected by further operations of S2 and S3. This prevents the operator from trying one switch and then the other and from pressing one switch repeatedly to increase his score.

## LAMP DECODING AND DRIVING

The remainder of the circuit consists of IC9 and IC8, the row of lamps LP1 to LP4 and their drivers (Fig. 2). Pack IC9 is a 7493 4-bit binary counter containing four flip-flops $p, q, r, s$ and a NAND gated reset. The $F$ output (which indicates correct guesses) is fed to the input of IC9. If only one pulse is fed into IC9, the output of $p$ is at logic 1 while $\mathrm{q}, \mathrm{r}, \mathrm{s}$ are at logic 0 . The p output is routed via diode D1 and R1 to TR1 which switches on lamp LP1.

If another pulse is received, p goes to logic 0 and $q$ goes to logic 1 which is routed via diodes D2 and D3 to both TR1 and TR2 so that both lamps LP1 and LP2 light. Similarly for three pulses, both p and q are at logic 1 and their outputs are combined by gates (two of the NaND gates in 7400 pack IC8; the other two gates are not required).

Diodes D4, D5 and D3 ensure that three lamps light. For higher numbers of correct guesses (up to fifteen), the logic 1 outputs of $r$ and $s$ are routed via diodes D6, D7, D8, etc. so that all the lamps light. These lamps remain on so long as the two inputs to the reset are not both at logic 1. However, if the operator guesses wrongly, the output $F$ will remain at logic 1 and the output of MS2 will rise to logic 1, thus operating the NAND reset and switching off the lamps.

While all this may seem rather complicated, the actual construction is straightforward.

## CONSTRUCTION

In the prototype, the ten i.c.'s ICl to IC10, the capacitors and the diodes D11 to D14 were mounted on 0.1 in matrix plain s.r.b.p. board. See Fig. 3 and the photographs. Alternatively, 0.1 matrix Veroboard may be used providing the strips are cut away so that each i.c. is isolated from the others.

The connections for each of the i.c.'s (viewed from underneath) are also shown in Fig. 3.

The suggested order of construction is:

1. After labelling each i.c. from beneath the board, make the common connections between pins on the same i.c., working from Fig. 3. Use a soldering iron with a small bit and low power, touching the pins on the i.c. for the least possible length of time.
2. Make the interconnections between the i.c. soldering as before and working methodically. These connections are most easily made with colour coded wire and as each connection is made, it should be ticked off on Fig. 3.
3. Connect diodes D11 and Di2 to pack IC1. Connecting pins inserted in the matrix holes are useful here.
Similarly with diode D13 to pack IC7 and D14 to pack IC10, capacitors C 1 and C 2 to pack IC1, C3 to pack IC7 and C4 to pack IC10.
4. Connect the common earthed (negative) line to all the i.c.'s. This should go round all the i.c.'s like a ring main circuit.
5. Connect the common positive line round all the i.c.'s.
6. It is recommended that the supply line is decoupled every five packs by connecting capacitors C5, C6 between the positive and negative lines at two points on the board (see photograph).
All the lamp drivers and counter drivers are mounted on Veroboard. Fig. 4 shows the gaps to be cut in the copper strips and the component layout.

Rewind the coils of the two counters if they are not already six volt operating. In the prototype the counters were standard PO 4 -digit non-resettable with an operating voltage of about 24 V . These were dismantled to gain access to the solenoids from which all the turns of 40 s.w.g. wire were removed. The coils were then rewound with as many turns as possible (as neatly as possible) of 32 s.w.g. enamelled copper wire. The counters, when reassembled, should operate easily and firmly on 5 V or less (drawing 150 to 200 mA ).


Photograph showing the internal layout of the ESP Detector

## COMPONENTS . .

```
Resistors
    R1-R4 4.7k\Omega (4 off)
    R5, R6 680\Omega (2 off)
    R7 4.7k\Omega
    All }\pm10%\pmW\mathrm{ carbon
Capacitors
    C1, C2 0.1 \mu F (2 off) (see text)
    C3,C4 100 F F 10V elect
    C5, C6 0.1 \mu (2 off)
```


## Transistors

```
TR1-TR7 2N3704 (7 off)
```


## Diodes

```
D1-D14 1N914 or any general purpose silicon diodes (14 off)
Integrated circuits
\begin{tabular}{ll} 
IC1, 3, 4, 7, 8, 10 & SN7400N (6 off) \\
IC2 & SN7472N \\
IC5 & SN7473N \\
IC6 & SN7451N \\
IC9 & SN7493N
\end{tabular}
Miscellaneous
CO1, CO2 PO type four-digit counters (2 off) (see text)
LP1-LP5 6 V 60 mA lamps with 4 amber and one green holder (5 off)
B1, B2 Type 8003 V batteries (2 off)
S1-S3 Single pole changeover pushbutton
S4 Single pole on/off
\(0 \cdot 1\) in matrix Veroboard 4 in \(\times 2\) in
0.1 in matrix perforated board \(12 \mathrm{~cm} \times 10 \mathrm{~cm}\)
32 s.w.g. enamelled copper wire
Case to suit
```


## THE CASE

The prototype was housed in a box made from chipboard with an aluminium fascia. The counters may be held by aluminium straps to the walls of the case. Since we are using the principle of biofeedback to reinforce learning, the rotals counter should be wrapped in foam rubber to reduce noise while the correct counter should be held firmly against the side of the box so as to give a satisfying clunk on each correct occasion.

The i.c. board was mounted horizontally on wooden blocks while the Veroboard was mounted vertically adjacent to the batteries which were held in place by wooden blocks and an elastic band (see photograph). Rub-on lettering for CHOOSE, SPIN, etc. improves the apearance of the completed detector.


Photograph of the integrated circuit board

## TESTING

On first switching on, it may happen that all five lamps light. If so, press the switches until they all go off. On then pressing the SPIN switch, the choose lamp should light and remain on when the SPIN switch is released.

Pressing the left or the right switch should have no effect but releasing it should cause the totals counter to be activated. Check that this is absolutely reliable, always counting on one digit when either the left or the right switch is pressed and released after the choose lamp is lit. Releasing the left or right switch may also have the effect of activating the correct counter and lamp LP1.

Check that this counter is also reliable and always counts when another lamp lights or when the completed row remains on.

If the detector has been built for interest only, no further testing is necessary. However, any statistical analysis will rely on a $50 / 50$ chance of the spinning circuit freezing in the left or the right condition. This can be tested in two ways:
(a) by taking the output of the astable multivibrator MV1 (pin 8, IC1) to the Y-plates of a cathode ray oscilloscope and running the X-timebase at about 5 kHz . The trace should show a square

Table 1: Confidence Levels

| Excess over the mean (in standard deviations) |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 2.0 | 2.2 | 2.4 | 2.6 | 2.8 | 3.0 | $3 \cdot 2$ | 3.4 | 4.0 | 5.0 |
| Odds (1in) <br> Confidence <br> Level (\%) | $\begin{aligned} & 20 \\ & 95.5 \end{aligned}$ | $\begin{aligned} & 40 \\ & 97.2 \end{aligned}$ | $\begin{aligned} & 60 \\ & 98 \cdot 4 \end{aligned}$ | $\begin{gathered} 100 \\ 99.1 \end{gathered}$ | $\begin{gathered} 200 \\ 99.5 \end{gathered}$ | $\begin{gathered} 400 \\ 99.7 \end{gathered}$ | $\begin{gathered} 800 \\ 99 \cdot 9 \end{gathered}$ | $\begin{aligned} & 2,000 \\ & 99 \cdot 9 \end{aligned}$ | $\begin{aligned} & 10,000 \\ & 100 \end{aligned}$ | $\begin{aligned} & 2,000,000 \\ & 100 \end{aligned}$ |

wave with an equal mark/space ratio. If not, it implies that the capacitors C1, C2 are not equal in value and should be changed or modified (by adding smaller capacitors in parallel with the lesser of C 1 or C 2 ) until the mark/space ratio is $1: 1$ ).
(b) by pressing the SPIN and (say) the RIGHT button repeatedly for a large number of times (without touching the other button) and noting the scores on the correct and totals counters. Over a large number of results, the CORRECT number should tend towards one half of the total number of tries. Since we are testing a random process, the correct number will not be exactly one half the total number even if the probability of left / right is exactly $50 / 50$. However, almost all runs of 100 guesses should have $50 \pm 10$ correct results.
If, after repeating this several times, there is a clear bias, the value of one of the capacitors should be adjusted to reduce the bias. A small remaining bias will not affect the statistics providing the subject believes there is no bias.

## STATISTICAL ANALYSIS

In deciding whether a result is due to ESP or purely to random chance, it is usual to calculate the odds against the event occurring by chance.

The odds against a run of $x$ correct answers occurring by random chance is easily calculated by $\frac{1}{2^{x}}$. For example, the record run in the prototype was 14 correct in a row, which by pure chance would occur $\frac{1}{2^{14}}=\frac{1}{16,384}$ i.e. once in 16,384 guesses.

However, calculating the odds against getting $x$ correct answers in a session of $n$ guesses is rather more complex. The formula is $\frac{n!}{x!(n-x)!2^{n}}$ where $n$ ! (pronounced "factorial $n$ ") is calculated by multiplying $n$ by all the whole numbers less than $n$. For example: $10!=10 \times 9 \times 8 \times 7 \times 6 \times 5 \times 4 \times 3 \times 2 \times 1$ $=3,628,800$. For small values of $n$, with a lot of cancelling, this is straightforward. For large values of $n$, the calculation becomes tedious, although using Stirling's approximation the formula becomes

$$
\frac{(n / 2)^{n}}{(n-x)^{n-x} x^{x}} \times \sqrt{\frac{\mathrm{n}}{6 x(n-x)}}
$$

which, although it looks more complicated, is easier to evaluate (using logs to calculate the powers).

A simpler and sufficiently accurate way to decide whether a result is sufficiently unusual to be accepted as proof of ESP is to calculate the "standard deviation" of the results from the formula:

$$
\text { standard deviation }=\frac{\sqrt{ } n}{2}
$$



## Photograph of the transistor panel

## CONFIDENCE LEVEL

Table 1 can then be used to determine the "confidence level" of the experiment. In psychology and biology experiments where similar methods are used, odds of 1 in 20 ( 5 per cent) are normally accepted as proof, providing similar or greater odds are indicated whenever the experiment is repeated. These 5 per cent odds are usually quoted as a " 95 per cent confidence level". Naturally all results (for a particular individual) must be included for these calculations, not just good results or sessions with high scores.

## EXAMPLE

A particular subject, in nine sessions (each of 100 guesses) obtained the following scores: 49, 52, 48, $54,57,55,60,56,52$.
(a) The total number of guesses, $n=900$.
$\therefore$ The standard deviation $=\frac{\sqrt{n}}{2}=\frac{\sqrt{900}}{2}=\frac{30}{2}=15$
(b) The number of correct responses, $x=483$.

The "mean" of the 900 guesses $=900 \times \frac{1}{2}=450$
$\therefore$ The number of correct responses exceeds the mean by $483-450$.

$$
\begin{aligned}
& =33 \text { responses } \\
& =\frac{33}{15} \text { standard deviations } \\
& =2 \cdot 2 \text { standard deviations }
\end{aligned}
$$

From Table 1, an excess of 2.2 standard deviations represents a 97.2 per cent confidence level (or odds of about 1 in 40) and, if repeatable, indicates the presence of ESP in the individual who is being tested.

The requirement for a compact, lightweight and versatile logic tester for use with the current range of integrated circuits has long been recognised by many electronics hobbyists, particularly those who are not fortunate enough to possess an oscilloscope. The Logic Probe to be described not only fulfills this need, but has the added advantage of providing a facility for use in the field. It can be quickly assembled and the components purchased at relatively low cost.

## CIRCUIT DESCRIPTION

The Logic Probe is basically comprised of two readily available integrated circuits which are the SN7413 dual 4 -input nand gate (Schmitt Trigger), and the 9601 retriggerable monostable multivibrator; the various logic outputs of the integrated circuits being fed, via suitable series resistors, to light emitting diodes to provide visual indication of the logic states of the circuit under test.

The two 4 -input NAND gates are connected in series ats shown at Fig. 1. A steady logic 1 at the probe will produce a logic 0 at the output of the first gate, a potential difference will exist between points ' C ' and 'D' on the circuit diagram and LP2 will be illuminated. A steady logie ' 0 ' at the probe will produce logic ' 1 ' at the output of the gate and a logic ' 0 ' at the output of the second gate, a potential difference will now exist between points ' $A$ ' and 'B' and LP1 will be lit.

## THREE L.E.D.s LIT

In the case where the logic under test consists of a stream of d.c. pulses having an equal mark / space

## COMPONENTS

Resistors
$\begin{array}{ll}\text { R1 } & 470 \Omega \\ \text { R2 } & 470 \Omega \\ \text { R3 } & 33 \mathrm{k} \Omega \\ \text { R4 } & 470 \Omega\end{array}$
All 10\% $\frac{1}{8}$ watt carbon
Capacitor
C1 $2 \cdot 2 \mu \mathrm{~F}$ elect. 10 V
Semiconductors

| IC1 | SN7413 |
| :--- | :--- |
| IC2 | 9601 (Fairchild) |
| D1 | 1N4001 |
| D2 | OA200 |
| LP1-LP3 | TIL209 l.e.d.s (3 off) |

## Miscellaneous

Veroboard (see text) and Veropins, plastic tubing, wooden dowels, wire, miniature crocodile clips
ratio, the monostable will trigger on the negative edge of the first pulse and LP3 will be illuminated and will remain lit for the period determined by the time constant provided by Cl and R3. Should further pulses occur during this period IC2 will retrigger. In addition LP1 and LP2 will follow the positive and negative logic, thus with a square wave all three l.e.d.s will be lit.

Consider now input logic which consists of negative going pulses where the pulse width is narrow (Fig. 2). As the d.c. state at the probe is predominantly positive (or logic ' 1 ') a predominant logic ' 0 ' will exist at the output of the first gate causing LP2 to light, also the monostable will trigger on the first negative edge and cause LP3 to light; thus, with a stream of negative going narrow pulses, LP2 and LP3 will be lit.


Fig. 3. Component layout and wiring details


Fig. 4. Logic Probe housing details


Completed logic indicator probe showing the three light emitting diodes and the supply leads with crocodile clips attached

In the reverse case (e.g. positive going pulses) LPI and LP3 will be lit.

In either case, where the pulse repetition rate is low-say in the order of a few hertz-all three l.e.d.'s will flash on and off.

## CHOICE OF I.C.s

The 7413 was chosen because it has a defined hysteresis and is capable of responding to input pulses having slow rising edges. The Fairchild 9601 is a reliable 'one shot' and will generate a wider pulse from a narrow input pulse when used in the
manner described. The probe will detect pulse widths of fractions of a micro-second. The diodes D1 and D2 are merely safety devices, D1 prevents damage to the i.c.s should the battery leads be inadvertently reversed, whilst D2 prevents a reversal of voltage reaching pin 13 of the monostable during the discharge of the electrolytic capacitor.

## MATERIALS

The basic materials, excluding the electronic components, required for the construction are as follows-
(a) One $4 \frac{1}{2}$ in length of plastic lubing having an inside diameter of $\frac{3}{4}$ in (the author used plastic water piping purchased from the local building suppliers).
(b) One piece of Veroboard ( $0 \cdot 1$ in matrix) $4 \frac{1}{2}$ in in length and 3 in wide.
(c) One suitable length of wooden dowelling having a diameter of $\frac{3}{4}$ in for the probe ends, an alternative would be to use suitable corks.
(d) One metal probe. This can be made from a length of 16 swg wire or a pin.

## CONSTRUCTION

Layout of the components on the Veroboard is given in Fig. 3. The Veroboard should be drilledwhere indicated-to break continuity of the copper strips, and the i.c.s and the probe soldered to the appropriate strips. Fig. 4 shows the Logic Probe housing.


A selection of readers suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought. Any idea published will be awarded payment according to its merits. Why not submit YOUR IDEA?

## voltage controlled DIMMER

THE circuit of Fig. 1 uses TR1 as a constant current source to charge the timing capacitor Cl at a rate determined by the applied control voltage at the base of TR1. At the correct level the charge voltage fires the unijunction TR2 to trigger CSR1 in turn via the trigger transformer T1.

In fact the timing of the circuit can be synchronised to mains waveform by using unsmoothed 15 V power.

On the prototype a 0 V control voltage gives full power and reduction to zero occurs at about -12 V . A smaller value of control voltage could be used by increasing the value of Cl .

The transformer T1 was made up from two 40 -turn windings of 30 s.w.g. enamelled copper wire on a length of ferrite rod. The CSR1 can be a triac or thyristor; in the prototype it was a SC45D.
M. Lawrance,

Helston.
Cornwall

State-of-the-art piano design with additional choice of harpsichord or honky-tonk voicing. Keyboard is fully touch sensitive. Piano effect is further heightened with soft and sustain pedals. Other features includ'e easy tuning, two speed vibrato and stool-integrated amplifier.

## I.C. PULSE GENERATOR

A handy test instrument for analysing TTL circuits. Wide operating speeds covering 0.1 Hz to 100 kHz in six switched ranges. Pulse width is also variable from 1 s to 1 us . An output reed relay enables it to be used with electromechanical systems.

## EIECTRONIC DIRECTION INDICATOR

Electronic substitute for conventional thermal flasher. Advantages are: immediate indication when switched, long term reliability and an emergency flashing facility.


MAY 1975 ISSUE ON SALE APRIL 11, 1975

N the previous article (February) we described the Ceefax and Oracle system in fairly general terms. This month we are going to examine some of the more detailed provisions of the system. A complete working unit is of little interest at this stage owing to the fact that the present system is still rather tentative and experimental.

## SPECIFICATION

The main technical features given in the joint BBC, IBA. BREMA specification are as follows:

1. Dat:a pulses are transmitted during the television field-blanking interval using a bit-rate of 6.9375 megabits per second.
2. Each page consists of 24 rows of 40 characters using both upper and lower-case characters. coded using the ISO-7 code. A special top row called the page-header carries information for control and display purposes.
3. All data-words are eight bits in length; parity protection is used for the character data words while Hamming Codes are used for addressing and control purposes.
4. News flashes and sub titles are provided.
5. Every page-header will carry clock-time information to provide a display and to permit the automatic time-selection of certain pages.
6. Control characters are used to provide colouring and flashing of selected words.
7. A simple graphics facility is provided.

This list is taken from the specification which goes into considerably more detail regarding the definition
of terms like "page", "magazine", etc. Readers interested in a full understanding of the system are recommended to read this publication*. In this article we shall confine our attention to the more interesting technical features of the system.

## LINE ALLOCATION

Lines 17 and 18 are already allocated; they are in fact used internationally for insertion test signals. However, providing their use is restricted to this country they are available for data transmission.

Lines 13 and 14 are free, but research studies have shown that some receivers frame flyback times creep into this region. For this reason data pulses on lines 13 and 14 would be visible to some people. It is anticipated that, as the system develops, manufacturers will reduce flyback times to release these and possibly other lines to allow a better service.

## RUN-IN AND FRAMING CODE

Fig. 2.1 shows how the data is fitted onto the television line scan. The clock run-in period of 16 bits is to synchronise the internal clock which determines the position of the data.

The clock run-in starts 1010 . . . for sixteen bits ending with a " 0 ". This is followed by a framing (starting) code which identifies the start of the message. The clock run-in and starting code are the same on every line 101010101010101011100100.

[^3](a)

(b)


Fig. 2.1. Arrangement of data on TV lines. (a) shows a normal data line and (b) the page header line

This code has been carefully chosen for ease of recognition as it signifies the start of the data signal. From this point on, the clock, which has been previously synchronised determines the position of any eight bit character along the line scan period.

This clock is also vital when it comes to reading the data onto the TV screen because characters must be displayed in their correct positions along the line scan.

## ROW ADDRESS GROUP

The framing or start code is followed by the row address group. It is very important to recognise the row address correctly, errors here could not only put a row in an incorrect position, but also interfere with another row. Consequently the row address is heavily protected by the Hamming Code, discussed later.

On most lines the framing code is immediately followed by the data bits, the exception being the occasional page-header line. Page-headers only occur on lines 18 or 331, but manufacturers are advised to organise receivers on a basis of code recognition, rather than line recognition, to allow for future developments.

Page-headers only occur on row 0 which must be recognised in order to deal with the page-header line correctly. The page numbers, tens and units, are also protected with the Hamming Code and must be dealt with in the appropriate manner.

## PARITY CODE

The data are transmitted in blocks of eight, seven bits for the character; the eighth is a parity bit. The eight bits are transmitted in sequence the first bit being the least significant bit in binary encoded signals. Thus in binary notation we have;
Signal

Binary weight | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $2^{0}$ | $2^{1}$ | $2^{2}$ | $2^{3}$ | $2^{4}$ | $2^{5}$ | $2^{6}$ | 0 |
| M | M | M | M | M | M | M P |  |

where $M$ are the message bits and $P$ is the parity bit.
The parity bit is " 1 " or " 0 " dependent upon odd or even message bits. The parity bit always makes an odd number of l's. If an even number of l's occurs, an extra (noise) pulse has been received and the signal is in error.

It is obvious that if two noise pulses occur the parity check will not reveal an error; however, some errors on the message signals can be tolerated.

## hamming code

Signals protected by the Hamming Code are less liable to give errors because the Hamming Code employs four parity bits.

$$
\begin{array}{lllllllll}
1 & 2 & 3 & 4 & 5 & 6 & 7 & 8 \\
\mathbf{P} & \mathbf{M} & \mathbf{P} & \mathbf{M} & \mathbf{P} & \mathrm{M} & \mathbf{P} & \mathrm{M}
\end{array}
$$

Again the bits are transmitted in numerical order beginning with number 1 , the least significant bit. The four parity checks are carred out as follows:

Parity check A is carried out over bits 1268 Parity check B is carried out over bits 2348 Parity check $C$ is carried out over bits 2456 Parity check D is carried out over all bits.

Table 2.1 shows the Hamming Code. A few minutes study of this code shows how the four parity checks can lead to information showing which bit is in error.

Table 2.2 shows how the various checks are used to correct some of the errors which may occur. Although Table 2.2 shows how parity bits may be complemented ( 1 's changed to 0 or 0 's changed to 1) it is only necessary to correct bits $2,4,6,8$, the message bits.

Because the Hamming Code reduces the number of message bits available, four bits giving the sixteen different states shown on Table 2.1, two blocks of eight bits are allocated for control and row address as shown in Fig. 2.1. This arrangement gives a total of $16^{2}$ (256) combinations of data which are protected by the Hamming Code.

Table 2.1: Hamming Code

| Decimal Message Value | Bit Position Number |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| 0 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| 2 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 |
| 3 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 |
| 4 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 |
| 5 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 |
| 6 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 |
| 7 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
| 8 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |
| 9 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 1 |
| 10 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 1 |
| 11 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 1 |
| 12 | 1 | 0 | 0 | 0 | 0 | 1 | , | 1 |
| 13 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 |
| 14 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| 15 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 |

Bits 1, 3, 5, $7=$ Protection bits
Bits 2, 4, 6, $8=$ Message bits

Table 2.2: Error Correction Table

| Parity Check |  |  |  |
| :--- | :---: | :---: | :---: |
| D | C | B | A |$\quad$ Action

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Fig. 2.2. Character code for data broadcasting. This table shows both alphanumeric and graphics information, the latter being selected by the appropriate control code in columns 1 and 2. (Reproduced from Joint Specification)

## CHARACTER INFORMATION

The character information employs seven bits, 128 (2`) different combinations. Fig. 2.2 shows how these 128 combinations are used. A matrix of $8 \times 16$ is used to produce 128 different characters.

Rows are identified by bits $1,2,3,4$, whilst columns are identified by bits $5,6,7$. Thus if we wish to display the " $£$ " sign we will have a code 11000100. Similarily the " $\&$ " sign would be 0110010 , the parity bit being " 0 " in each case to give odd parity. A "?" sign on the other hand would be coded 11111101, where the parity bit is " 1 ".

The control instructions located in columns 0 and 1 ( 000 and 100 ) are reproduced as a blank space, but following characters are produced according to the instruction given. For example, a white number I coded 10001100 would be preceded by an instruction-space code 11111000. However, if the number was to be reproduced in red it would be preceded by the instruction-space code 10011000. In each case the last bit is the parity bit.

> An example of an ORACLE page using the graphics facility


## GRAPHIC SYMBOLS

As the system has a graphics facility there are alternative graphic symbols given in each location which must be generated when called for. If instead of a red 1 the red graphic symbol was required the graphics space signal would be given. For example :
start 12345678
12345678
gives a red 1
10011000 followed by 10001100

## 10001001 followed by 10001100 gives red spots.

Thus the specification virtually dictates the design of the ROM unit discussed in last month's article. Before discussing the ROM organisation in more detail it will pay to re-examine how the RAM stores the transmitted data.

## RAM ORGANISATION

The RAM can be organised in a variety of ways by receiver manufacturers, but from an operational point of view it is best to visualise a matrix $40 \times 24$, giving 960 locations, where each location can store seven bits of information. This then corresponds to the 40 characters and 24 rows of each page in the system.

Each row is identified by its row number ( 0 to 23 ), derived from the control and row-address group. The columns are numbered 1 to 40 and are derived from the synchronous clock.

The clock starts its division (by 40) on receipt of the framing (start) signal. Fig. 2.3 illustrates this arrangement. The row information is processed by the Hamming decoder logic which also determines if the row is " 0 " or not (i.e. the page header). If the row is " 0 ", further Hamming logic is used on the next 16 characters which give the page numbers.

The synchronous clock ensures that the whole row is filled. After this nothing more happens until


Fig. 2.3. (left) Diagram showing RAM visualisation. Each of the squares represents seven bits of data

Fig. 2.4. (right) A typical cell matrix. This particular character would be produced by the code 1000001
another line of data appears complete with its clock run-in signals, framing code and row coding, then the next row in the RAM is filled with data.

Each row of the memory may be filled at random. The transmission of each row need not be sequential and in all probability will not be. Read, write, inhibit and reset logic is also required in association with the RAM in order to fully control its function.

When filled the RAM can be visualised as the representing page of information to be written on the television screen. This idea is shown in Fig. 2.3, line 4 illustrating a programme timetable: 6.40 pm PLAY, followed by line 5, 9 pm NEWS. Of course these characters are actually stored in a seven bit binary store.

## ROM ORGANISATION

The ROM unit is rather more complicated than it appears from our preliminary examination. The character must be generated over 14 lines, so each line reproduces only a small part of the character. In addition, each line reproduces a small part of each of the 40 characters which appear along any particular row.

The ROM unit converts the parallel coded information from the RAM board to a serial output of dot information which modulates the CRT during scan. The ROM's themselves contain character information on a $7 \times 5$ dot matrix

Fig. 2.4 shows cell 1000001 (capitalA) as given by the character codes in Fig. 2.2.

The sequence of operations is as follows:

1. The clock counter (synchronised with the clock run-in pulses) access the first letter stored in the RAM, in this case a capital A.
2. The ROM is addressed so that the cell containing A is activated. (Cell 1000001 , Fig. 2.4.)
3. A television line scan counter has already activated the first line address input to every part of the ROM. (Line 1, Fig. 2.4.)
4. The contents of the ROM are transferred to a shift register of six bits. (The sixth bit is the -space between characters.)
5. In this example the contents from line 1 of the ROM transferred to a shift register are white, white, black, white, white. (Line 1, Fig. 2.4.)
6. A 6 MHz phase-locked counter shifts the information serially óut of the shift register to


An operator entering data into the ORACLE system
modulate the CRT with a dot representing the top of the " $A$ ".
7 The clock counter addresses the RAM to find the next character, " $B$ " for example.
8. The ROM cell containing " $B$ " is addressed by the RAM ( 1000010 in Fig. 2.2). This cell already has its line 1 input activated.
9. The top of the " $B$ " is transferred to the shift register and modulates the tube as described for the " $A$ ".
Proceeding thus, scan 1 moves from character to character of the row stored in the RAM, addressing the appropriate coordinates of the ROM for each character in the row. The ROM modulates the scan line with the top of each character in the row.

At the end of the row, scan line 2 on the ROM is activated to modulate the CRT with the dots required on the second line scan. The seven scan lines of character are reproduced by this means and followed by four blank lines of inter-row scan.

## ADDITIONAL FEATURES

In the most simple systems reproduction of alphanumeric characters will be all there is to the ROM, but in more advanced receivers there will be provision for graphics and character rounding. These additional features will increase the complexity of the system considerably as will some of the other provisions of the specification.

To summarise the various systems in terms of increasing complexity, we have:

1. Alphanumeric display.
2. Upper and lower case alphanumeric characters.
3. Graphics display.
4. Flashing display.
5. News flash and sub titles superimposed on the picture.
6. Coloured displays. Red, green, yellow, blue, magenta, cyan and white.
7. Timed displays. Some data may only be transmitted once during the day. A time clock system can be incorporated to capture this signal.
We can look forward to the time when manufacturers put receivers onto the market and we can see the various ways in which they overcome the complexities of the system to provide the maximum of facilities at a minimum cost.
 ated door-push type). It produces a novel sound with ample volume for an average house and a life of several months between battery changes with normal use.

## OPERATION

The circuit, Fig. I, uses an oscillator, ICI, capable of producing five different tones. The tune piayed includes up to fifteen time-slots of equal length, and during each of these time-slots the appropriate tone is selected, from ICl , using the pre-programmed diode matrix shown in detail in Fig. 2.

The oscillator's output is fed via a simple transistor amplifier to a loudspeaker LSI.

The time-slots are generated by a second oscillator, IC2. feeding a divide-by-sixteen binary counter, IC3, which in turn addresses a 4 to 16 line de-multiplexer. IC4.

IC4's seventeenth output is used to turn a latching relay RLA off. thereby removing power at the end of the tune.

## TONE GENERATION

By now the reader should be familiar with the ' 555 " timer and its use as an astable multi-vibrator. The frequency of ICl may be changed by altering any of the timing components identified jn Fig. 1 as RI to $5, \mathrm{RII}$ and Cl . Here the five different tones are generated by switching in one of the pre-set resistors VR1 to VR5 via the transistors TR1 to TR5. This is achieved by switching the associated transistor base towards ground (0V) potential using a diode matrix.

## AUDIO AMPLIFICATION

The amplifier is a very simple class A design which in fact introduces a great deal of distortion, but as the output waveform is unimportant this is ignored. Preset VR6 acts as a volume control.

Any small loudspeaker should suffice ( $3 \Omega$ to $50 \Omega$ ); the prototype used a $2 \frac{1}{4} \mathrm{i}$. speaker of unknown impedance removed from an old transistor radio.

## CLOCK AND DE-MULTIPLEXING

This section of the circuit makes use of a second 555 timer IC2, running at about 4 Hz to drive a binary counter IC3 (SN7493). The components C6 and R19 connected to IC3 set-zero input ensure that the counter is always started in the set-zero state such that the tune played starts from the chosen beginning.
The four output lines from IC3 (A, B, C and D) are then fed to a de-multiplexer IC4, which decodes the binary information into a selection of one of sixteen; that is, each of the sixteen outputs 1 to 16 is sequentially taken from logic 1 level to logic 0 for about 0.25 s , only one output being at 0 at any one time.

The value for $V_{\text {Ont }}$ in the logic 1 state of an SN74154 can be as low as $2 \cdot 4 \mathrm{~V}$. As it was felt that the transistors TR1 to TR5 would not be held fully off by these voltages, the values of resistors R1 to R10 were made lower than would appear necessary to act as pull-up resistors for the IC4 outputs, thus avoiding problems of leakage currents through the transistors.

## DIODE MATRIX

As the outputs from the SN74154 are so-called "totem-pole", each has to be diode or-gated and isolated from the others. Hence the use of a diode matrix.
If desired IC4 can be replaced with the pin-compatible, open-collector output version SN74159. This removes the need for the diodes which would then be replaced with wire links. The saving in cost of the diodes, however, does not offset the extra cost of the SN74159 and for this reason it was not used in the prototype.

The matrix of Fig. 2 is for a facsimile of the beginning of the Beethoven 9th symphony. Pauses may be introduced into the music by leaving the appropriate input to the matrix open circuit. This is demonstrated in Fig. 3 which gives the matrix for playing part of "Colonel Bogey".

## LATCHING RELAY

As noted, the seventeenth output from IC4 goes to logic 1 on starting the machine. This holds TR8,

## MUSICAL DOORBELL GIRCUIT DETALLS



Fig. 1. General circuit diagram of the electronic doorbell


Fig. 2. The diode matrix used in the prototype to produce the first bars of the Beethoven 9th


Fig. 3. Diode matrix alterations to produce Colonel Bogey

and hence the relay RLA on. Thus if the relay contacts are momentarily shorted (by the door-push) the circuit will latch and stay in that state until the tune has finished and the output of IC4 goes low, turning TR8 and the relay off. If the SN74159 is used for IC4 a $2 \cdot 2 \mathrm{k} \Omega$ resistor must be connected between this output and the +6 V rail.

## CONSTRUCTION

All the components, with the exception of batteries, loudspeaker and switch are soldered onto one piece of Veroboard, the size used being that made especially for mounting in a Lektrokit box. However, any piece of 0.1 in Veroboard of $39 \times 41$ or more holes will do.

The component layout for the Beethoven's Ninth version is given in Fig. 4, whilst the alterations necessary for playing Colonel Bogey are shown in Fig. 5.

The choice of box for mounting the circuit is left to the constructor. The prototype was built in a die-cast box $6 \frac{3}{4} \times 4 \frac{3}{3} \times 2 \mathrm{in}$ which also contains the speaker and four HP1I batteries required to power the machine.

If this method is used it is worthwhile cutting the Veroboard to fit in the box before mounting the components as shown in the photographs and Fig. 4. Make sure none of the copper strips short to any of the many protrusions in these boxes. As can be seen from the photographs, a miniature $3 \frac{1}{2} \mathrm{~mm}$ jackplug and socket were used to accept the wires from the door-push; the socket used was the type with a switch on the back, bent in such a manner that it closed when the plug was inserted. This switch turns the power to the electronics off when the plug is removed.

Holes, for mounting the board on pillars, if required, may be drilled in several places if some thought is given to not breaking any used strips and care is taken not to short any strips together or to earth. Further removal of copper with a spot face cutter may be needed.

The relay RLA used is a d.i.l. packaged device with its own protection diode. Fig. 8 shows how the batteries are mounted in the prototype but constructors may wish to use different batteries or a suitable holder for their particular requirements.

## TESTING AND SETTING-UP

The tone adjusting pre-sets are, for convenience, numbered in ascending order of frequency: VR1 the lowest tone used and VRS the highest. First of all remove IC4 from its socket, set VR6 to about $\frac{1}{3}$ of its travel clockwise, solder one end of a length of wire to earth ( 0 V ) and solder a temporary link across the terminals to the push switch. Touching the free end of the earthed wire to either end of any of the diodes should produce a tone from the speaker. By running the wire along the diodes corresponding to pins 1 to 16 respectively on IC4, the tune should be produced if the pre-sets have been correctly set. With a bit of practice and a good ear for pitch, the correct positions can soon be found.

If IC4 has been soldered in place then remove the end of the diode going to pin 1 on IC4, short

## COMPONENTS . . .



## Capacitors

| $\mathrm{C}^{*}$ | $0 \cdot 22 \mu \mathrm{~F}, 100 \mathrm{VW}$ polyester (see text) |
| :--- | :--- |
| C 1 | $0 \cdot 1 \mu \mathrm{~F}, 100 \mathrm{VW}$ polyester |
| C 3 | $2 \times 47 \mu \mathrm{~F}, 63 \mathrm{VW}$ Tant bead |
| C 4 | $2 \cdot 2 \mu \mathrm{~F}, 35 \mathrm{VW}$ Tant bead |
| C 5 | $10 \mu \mathrm{~F}, 25 \mathrm{VW}$ Tant bead |
| C 6 | $1 \mu \mathrm{~F}, 35 \mathrm{VW}$ Tant bead |

Transistors

| TR1 to 5 | BCY70 |
| :--- | :--- |
| TR6, 8 | BC107 |
| TR7 | BFX85 |

Diodes
D2 et al Any general-purpose miniature silicon device such as OA200, 1N914, IS914, etc. (15 or 32 if required)

Integrated Circuits
IC1, 2 NE555, MC1455G etc. ( 555 chip), 2 off
IC3 SN7493
IC4 SN74154, (A. Marshall)
IC5 SN7472 (if needed, see text)
IC6 SN74154 (if needed, see text)

## Relay <br> RLA D.I.L. mounting reed relay, Chromasonic Electronics

## Miscellaneous

Veroboard; suitable case; heat sink for TR7; i.c. sockets, particularly IC4 and IC6; loudspeaker, batteries or battery; wire; bell-push, etc.

[^4]
out C5 to positive 6 V . Now proceed with the steps from "set VR6" as above, making sure not to touch the wire on those ends of the diodes going directly to IC4 as this may be damaged. Always connect to the negative diode ends.

Remove the shorting link from the push (door) switch connections and either replace IC4, or remove the short from the capacitor and re-solder


Fig. 5. Veroboard madifications for the Colonel Bogey version
the diode. On wiring in a door switch and pressing it the doorbell should now function properly.

If the tune is plaged too fast or too slow this can be remedied by increasing or decreasing the value of R18. If, however, it is made to run too fast the device will not turn itself off, in which case the smoothing capacitor C 4 should be reduced in value.


Fig. 4. Veroboard component layout and cutting details for the prototype electronic doorbell


Fig. 6. Sketch of the board and battery mounting used in the prototype. Note that the loudspeaker is mounted on the lid of the diecast box.

## WARNING

Do not allow any of the potentiometers VR1 to VR5 to be set fully anti-clockwise as this could damage IC1. If the Colonel Bogey version is built, note the changes marked with an asterisk in component values given in the parts list.

When the batteries start to run low the first part of the circuit to fail will be the counter IC3. This causes the device to emit a single tone that can anly be stopped by removing the batteries. If you are at all tone-deaf get someone else to set the tones for you as even if it is set slightly off key it will sound very unpleasant, and can send connoisseurs of classical music mad at 100 yards.

## ALTERNATIVES

The matrix could be wired on to a separate board which plugs into the main board via an edge-connector. Using this method a whole library of tunes could be built; one for each day of the week perhaps? The tunes would, of course, have to be made up of the


Fig. 7. Doubling the time-slot capacity to produce a longer melody requires this circuit
same five notes otherwise the pre-sets would have to be altered each time.

If this is done it becomes economical to use the SN74159 for IC4 as diodes would thus be saved.

The circuit can, if wished, be expanded to play thirty-one beats, as shown in Fig. 7. Here IC5 (SN7472) is used as a divide-by-two counter with complementary outputs such that, on starting the machine Q is low, enabling IC4 to decode the first sixteen time-slots, whilst the high $\bar{Q}$ on the second strobe input of IC6 (SN74154) dis-enables this. On IC3's sixteenth count IC5 is toggled into its other state turning off IC4 and enabling IC6 to decode the second sixteen beats.
This modification will obviously necessitate the use of a larger diode matrix, but as this can be drawn to resemble a musical scale, working out the circuit is an easy matter.

It may be found that with or without the previous modification, more than five notes are required. This can be readily achieved by the addition of an extra output line in the matrix plus an extra transistorswitched pre-set resistor for each additional note.

Both these modifications will need a larger board and a re-designed layout for their construction.

As this circuit stands, each note follows on immediately from the preceding note. To make the tune sound more realistic short pauses between notes can be introduced by gating the output of the demultiplexer with the clock pulse such that notes are produced only when the clock's output is in the "low" state.

To do this, the output of IC1 (pin 3) should be taken to one of the strobe inputs of IC6 and/or IC4 (pin 18), the latter having first been disconnected from ground.

A sensible value for the pause-to-note ratio (mark : space ratio of the clock) is $1 / 10$. To achieve this the clock circuit must be modified by the addition of a diode between pins 6 and 7 , anode to pin 7 , and suitable alteration of R17 and R18. These then become $3 \cdot 6 \mathrm{k} \Omega$ and $33 \mathrm{k} \Omega 2$ respectively for the Beethoven 9 th tune and $4.7 \mathrm{k} \Omega 2$ and $47 \mathrm{k} \Omega$ respectively for the Colonel Bogey version. Other melodies would obviously require experimentation.

## DICE Conversion

A n agreement, signed in February, gives Marconi Communication Systems Ltd. exclusive world wide manufacturing and marketing rights of the Digital Intercontinental Conversion Equipment (DICE) developed by engineers of the Independent Broadcasting Authority.
The latest version of DICE can convert 525 -line NTSC colour pictures, as used in the U.S.A. and Japan, into the 625 -line PAL or SECAM pictures used in most other parts of the world, and vice-versa. DICE is, by a comfortable margin, the world's fastest computer.
Standards conversion is essential, not only for "live" relays via satellite, but also when programme material on video tape is exchanged between countries working to different television picture standards.
A number of different types of standards converters have been developed over the years, but IBA engineers were the first to develop a unit based on digital techniques to eliminate the need for careful alignment and adjustment and to provide conversion without perceptible picture impairment.

It is claimed that DICE solves the technically complex process of conversion between systems using 30 pictures a second, as in North America and Japan, and those using 25 , pictures a second, as in Europe, entirely electronically and with negligible distortion and is capable of satisfying a global demand for high quality pictures by satellite. It also allows programme companies making television recordings for world-wide sale to offer high quality pictures no matter which standard was used during production.

## Another Component Source ?

Another American component distributor has decided to enter the U.K. market. Cramer Electronics of Newton, Mass., is one of the largest distributors of components in the U.S. They are "broad-line" distributors offering very wide range of components with particular emphasis on the less glamorous but vitally essential nonactive components and hardware items, right down to the humble grommet.

The U.K. distribution centre to be set up in the London area will be under the control of David Griffin who has been appointed Managing Director of U.K. operations and also Marketing Manager/Europe for the Newton, Mass. (U.S.) based firm.

Although Cramer Electronics is an industrial distributor, the possibility of offering a "one-off" service to individuals is not entirely ruled out. David Griffin is aware of the needs of the constructor market and has indicated his willingness to examine this particular area.

## Portable Heait Monitor

${ }^{\mathrm{N}}$ close co-operation with Danish doctors, Simonsen \& Weel have developed a new portable, battery operated combined defibrillator/memory/scope. This unit is especially designed for the resuscitation of patients suffering from acute heart diseases.

The cardio-aid is very useful to on-the-spot ambulance staff. A feature of the unit is its capability to be linked to hospital staff from the ambulance by radio link. This enables the ECG, the heart activity of a patient in electrical form, to be sent to a qualified doctor at the hospital. The doctor can then instruct the ambulance staff, via the radio link, of any emergency treatment that needs to be carried out during the journey to the hospital.

## POInIS Dilsillt

MARINE SPEEDOMETER (February 1975)
The last paragraph on page 121 should read: "Ranges of 0 to 10 and 0 to 20 knots are obtained by selecting either VR2 alone or VR3 and R17 in series to replace VR2. Selection can be by S1 (VR3, R17 and S1 are not shown) or S1 can be part of S2 which thus becomes a three-pole three-way switch with 'Range 1', 'Range 2 ' and 'off' positions."
The Zener diodes D1 and D2 in Figs. 1 and 3 should be reversed.

Further, the co-ax outer should be connected to the battery common line in Fig. 2.
P.E. MINISONIC (January 1975)

In Fig. 3.12 pin 4 of 1 C1, MIXER 2 should be shown connected to -9 V line by means of connection at column 104, tenth strip down.

In Fig. 4.6, R14 should be disconnected from pin 6 and connected to TR5 emitter. Pins 2 and 6 should be bridged with a 100 pF polystyrene capacitor.

## GAS DETECTOR (September 1974)

Some readers have experienced trouble over the starting of oscillation after setting up has been carried out. It is suggested that the setting-up procedure be altered as follows.
Connect the "dummy" load as described but set VR1 to the fully anti-clockwise position and not the opposite as originally described. This places it in the position for producing the shortest mark-space ratio.
Now adjust VR1 till the load resistors are hot but not burning to the touch. Switch off and then on again to confirm the oscillator is indeed operating.

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| SINCLAIR |  |  |  |  |  |  |  |
| Stereo 80 Pre-Amp Z40 Amplifier Z60 Amplifier PZ5 Power Unit PZ6 Power Unit PZ8 Power Unit PZ20 Power Unit |  |  | 811.35 <br> L5. 10 <br> 26. 60 <br> 55.00 <br> 2.7.50 <br> 27.00 <br> $\$ 4.75$ | Project 80 F.M. Tuner Project 80 Decoder Project 80 A.F.U. Cambridge Calc. Camb. Memory Scientific I.C. 20 KH |  |  | 11.35 |
|  |  |  | £7.50 |  |  |  |
|  |  |  | 16.90 |  |  |  |
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|  |  |  | $815 \cdot 50$ |  |  |  |
|  |  |  | ¢17.30 |  |  |  |
|  |  |  | 16.65 |  |  |  |
|  |  |  |  | [ |  | $\varepsilon$ |  | 5 |
| BC107 | 0.12 |  |  | 0.13 | 2N3053 | 0.17 |  | 0.07 0.07 |
| BC108 | 0.11 | BC214L |  | 0.13 | 2N3055 | 0.50 | 1 N 4002 | 0.07 0.05 |
| BC109 | 0.13 | BCY70 |  | 0.17 | 2N3442 | 1.50 | ind148 | 0.05 0.09 |
| BC182 | 0.11 | BCY71 |  | 0.22 | 2TX304 | 0.26 0.35 | ${ }_{709}{ }^{\text {A }}$ 91 | 0.09 0.38 |
| BC183 | 0.11 | BCY72 |  | 0.13 0.23 | $2 \mathrm{TX504}$ | 0.35 0.50 | 709 | 0.38 0.39 |
| BC184 | 0.11 | BFY50 | 0.23 0.22 | OC23 | 0.50 0.70 | $\begin{aligned} & 741 \\ & 747 \end{aligned}$ | 0.39 0.90 |
| $\mathrm{BC1}^{\text {BC22L }}$ | 0.11 0.13 | BFY51 | 0.22 0.22 | OC28 $0 \mathrm{OC35}$ | 0.70 0.60 | 747 SL301B | 0.90 0.75 |
| BC212 BC213 | 0.13 0.13 | $\begin{aligned} & \text { BS } \times 20 \\ & \text { TIP41A } \end{aligned}$ | 0.22 0.45 | OC35 | 0.80 0.30 | SL301B CA3046 | 0.75 0.75 |
| BC 213 BC 214 | 0.13 0.13 |  | 0.85 0.95 |  | 0.30 0.25 | CA3555 | 0.75 0.85 |
| 7400 | 0.19 | 7420 | 0.20 | 7475 | 0.80 | 7492 | 0.70 |
| 7402 | 0.20 | 7430 7472 | 0.20 | 7476 | 0.42 | 7493 | 0.67 |
| 7404 | 0.20 | 7472 | 0.34 | 7483 | 1.25 | 74107 | 0.45 |
| 7410 | 0.20 | $\begin{aligned} & 7473 \\ & 7474 \end{aligned}$ | 0.48 | 7486 | 0.48 | $7412 \dagger$ | 0.51 |
| 7413 | 0.35 |  | 0.42 | 7490 | 0.66 | 74141 | 1.00 |

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2N2369A
2N2646
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$\begin{array}{ll}\text { 2N2904 } & 0.22 \\ \text { 2N2904A }\end{array}$
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2N2906A 0.21 Orange
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$2 N 3055$ 2N3390
2N3391
 $17 \begin{array}{lll}2 N_{3391} & 0.23 \\ 2 N_{33914} & 0.23 & 2 N_{51} 7 \\ 2 N 3392 & 0.29 & 2 N_{517} \\ 2 N 5190\end{array}$ 0.12
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| .80 | 2N2907 | 0.22 | 2N4061 | 0.11 | AD150 |
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| .20 | 2N2924 | 0.14 | 2N4062 | 0.11 | AD 161 | | 2N2924 | 0.24 | 2N4062 | 0.11 | AD161 | 1.15 | 0.50 |
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| BC 171 |  |  |  |  |  |  |
| 2N4 |  |  |  |  |  |  |
| 2N2925 | 0.17 | 2N4126 | 0.20 | AD162 | 0.50 | $B Y 182 L$ | | 2N2925 | 0.17 | 2N4126 | 0.20 | AD162 | 0.50 | BCi72 |
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| 2N4289 | 0.34 | BCi82 |  |  |  |  |
| 2N2926 |  | ON161 | 1.20 | BY |  |  | $\begin{array}{ccccc}58 & \text { 2N2926 } & & \text { 2N4919 } & 0.84 \\ .99 & \text { Green } & 0.12 & \text { 2N4920 } & 0.99 \\ 20 & \text { Yellow } & 0.11 & \text { 2N4921 } & 0.73\end{array}$ AD162

AF109ABC182 | AF115 | 0 |
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| AF116 | 0 |
| AF11 | 0 |

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| BD 136 | 0.11 BD136

$\qquad$
AF125
AF126
AF127
$A F 139$




| Resistors |  |  | Tant Beads |  |
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| W | Tol | Price | Value | Price |
| * | 5\% | 1 p | 0.1/35 | 14p |
| $t$ | 5\% | 1+p | 0.22/35 | 14 p |
| $t$ | 5\% | 2p | 0.47/35 | 14 p |
| 1 | 10\% |  | 2.2/35 | 14 p |
| 2 | 10\% | 6p | 4.7/35 | ${ }^{18} \mathrm{p}$ |
| 2 | 5\% | 7 p | 10/16V | 18 p |
| 5 | 5\% | 9p | 47/6.3V | 20p |
| 10 | 5\% | 10p | 100/3V | 20 p |

## Veroboard

$$
\begin{aligned}
& \text { Copper } \\
& 0.1 \quad 0.15
\end{aligned} \quad \text { Plain }
$$

$2.5 \times 34 \mathrm{in}$
$2.5 \times 51 \mathrm{in}$
$25 \times 5 \sin$
$3 i \times 3 i n$
$34 \times 51 \mathrm{n}$
0.1
36 p

## Integrated Circults-TTL Reductlons

|  | SN7400 | 0.16 | SN7409 | 0.33 | SN7430 |
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| SN7450 | 0.16 | SN7480 | 0.75 |
| SN7451 | 0.16 | SN7481 | 1.25 |
| SN7482 | 0.87 |  |  |
| SN74S3 | 0.16 | SN7483 | 1.20 |
| SN7454 | 0.16 | SN7484 | 0.95 |
| SN7460 | 0.16 | SN7485 | 1.58 |
| SN7470 | 0.30 | SN7486 | 0.45 |
| SN7472 | 0.30 | SN7490 | 0.65 |
| SN7472 | 0.44 | SN7491 | 1.10 |
| SN7473 | 0.10 |  |  |
| SN7474 | 0.46 | SN7492 | 0.75 |
| SN7475 | 0.59 | SN7493 | 0.65 |
| SN7476 | 0.45 | SN7494 | 0.85 |


| SN7495 | 0.80 | SN74151 |  |  |  |
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| SN7496 | 1.00 | SN74153 | 1.09 | SN74176 |  |
| SN74100 | 2.10 | SN74154 | 1.66 | SN64180 |  |
| SN74107 | 0.43 | SN74155 | 1.53 | SN74181 |  |
| SN74118 | 1.00 | SN74157 | 1.09 | SN74190 |  |
| SN74119 | 1.92 | SN74160 | 1.58 | SN74191 |  |
| SN74121 | 0.57 | SN74161 | 1.50 | SN74192 |  |
| SN74122 | 0.80 | SN74162 | 1.58 | SN74193 |  |
| SN74123 | 0.72 | SN74164 | 2.01 | SN74196 |  |
| SN74141 | 1.09 | SN74165 | 2.01 | SN74197 |  |
| SN74145 | 1.44 | SN74167 | 4.10 | SN74198 |  |
| SN74150 | 1.44 | SN74174 | 1.80 | ) |  |


| SN74141 | $1 \cdot 00$ | SN74165 | 2.01 | SN74197 |
| :--- | :--- | :--- | :--- | :--- | :--- |
| SN74145 | 1.44 | SN74167 | $4 \cdot 10$ | SN74198 |
| SN74150 | 1.44 | SN74174 | 1.40 | SN74100 |

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| PIV | 50 | 1002 | 200400 |  | 600 | 8001000 |  | green and yellow |  |  |  |
| 1.5 | 15p | 17p | 20p | 22p 2 | 25p 2 | 27p 30p | 0.16 diameter 31p |  |  |  |  |
| 3 | 15p | 17p | 20p | 22p 25 | 25p | 27p 30p |  | diamet | 33p |  |  |
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| TN914 | 7 p | BA110 | 0 25p | BA154 | 12p | - BYZ10 | 35p | OA70 | $71 p$ | OA91 |  |
| 1N916 | $7 p$ | BA115 | 5 7p | BY100 | 15p | P BYZ11 | $32 p$ | OA73 | 10 p | OA95 |  |
| AA119 | ${ }^{7} \mathrm{~F}$ | BA141 | 1 17p | $8 \mathrm{BY126}$ | 6 15p | BYZ12 | 30p | OA79 | $7 p$ | OA200 |  |
| AA129 | 15p | BA142 | 2 17p | $8 \mathrm{8Y127}$ | 7 17tp | - OA9 | 10p | OA81 | Ap | OA202 |  |
| BA100 | 15p | BA144 | 4. 12p | BY140 | - 51 | 1 OATO | 20p | OA85 | 10p | OA210 |  |



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## Mall Order

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TRY OUR NEW GLASGOW
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Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned. All quoted prices are those at the time of going to press.

## ARITHMETIC TUTOR

To complement their range of computer educational aids, Limrose Electronics Ltd. have recently introduced a Digital Arithmetic Tutor.

This advanced logic trainer has been designed for teaching the principles of binary arithmetic and fourbit data word manipulation to senior students who have completed a basic course in combinational and sequential logic circuits.

The tutor consists of three general purpose 4 -bit shift registers, two 4-bit synchronous binary counters, one 4 -bit comparator, one 4-bit adder a carry store, one J-K master-slave flip-flop, four 2-input NAND gates, four 2 -input and gates and three logic inverters. Two manual single pulse generators and one continuous clock pulse generator are also available.
Logic states are displayed on the front panel by means of red light emitting diodes and interconnections are made using a 1 mm gold-plated terminal pin and patch lead system.

The equipment is accompanied by an ithustrated instruction book containing numerous computer circuits which can be constructed on the equipment. These circuits include loading and shifting of registers, 1 's and 2 's complements. addition and subtraction of 4 -bit numbers by serial and parallel methods and multiplication and division by the
shift-and-add-method. The instruction book also deals with the Octal Number System, binary fractional notation and overflow conditions in fixed point arithmetic.

Ideally suited for Universities and schools. prices of the tutor vary (approximately $£ 150$ ) and further information can be obtained from Limrose Electronics Ltd., 8-10 Kingsway, Altrincham, Cheshire, WA14 1 PJ.

## POCKET MULTIMETER

No bigger than a packet of cigarettes. the new ICE Microtest 80 multimeter combines several unique features in one compact and accurate instrument.

Covering 8 fields of measurement and 40 ranges, the meter has a $20,000 \mathrm{ohm}$ per volt sensitivity, with 2 per cent accuracy on the a.c. and d.c. scales.

The design of the meter incorporates automatic electronic regulation for zero ohms, and the movement is protected against an overload of 1,000 times in the ohmic ranges before automatic cut-out.

The mirror scale of the meter enables clear and accurate reading. Each meter is supplied with a comprehensive instruction manual, a protective case and test leads.

A brief technical specification for the meter is as follows: volts d.c., in 6 switched ranges, 100 mV to $1,000 \mathrm{~V}(20 \mathrm{k} \Omega / \mathrm{V})$; volts a.c., in 5 switched ranges. 1.5 V to $1,000 \mathrm{~V}$ ( $4 \mathrm{k} \Omega / \mathrm{V}$ ). Current d.c. in 6 switched ranges, $50 \mu \mathrm{~A}$ to 5 A ; current a.c., in 5 switched ranges, $250 \mu \mathrm{~A}$ to $2 \cdot 5 \mathrm{~A}$. Resistance, in 4 switched ranges, low ohms to ohms $\times 100$. Capacitance measurements are available from $25 \mu \mathrm{~F}$ to $25,000 \mu \mathrm{~F}$.

The meter is powered by a 1.35 V mercury battery which. in normal usage, will last up to 3 years, and a complete range of extra accessories is available. Amongst the accessories are a temperature probe $\left(-50\right.$ to $\left.+200^{\circ} \mathrm{C}\right)$, gaussmeter and a luxmeter probe.


Limrose Electronics Digital Tutor


The Microtest 80 multimeter is priced at $£ 11.95$ ex. VAT, with special terms for quantity purchase. and is available direct from Electronic Brokers Ltd., 49 Pancras Road, London, NW1 2QB.

## PROFESSIONAL <br> MICROPHONE

A professional moving coil microphone with an excellent specification and features is announced by Beyer Dynamic (GB) Ltd.

Designated type M88. it has a hypercardioid polar pattern, a frequency response of 30 Hz to $20,000 \mathrm{~Hz} \pm 2 \cdot 5 \mathrm{~dB}$, a high output, and is manufactured to a standard setting transient response and front-to-back ratio. This means that any two random units can be used as a stereo matched pair. Side attenuation at 120 degrees is approximately 23 dB and the EIA sensitivity rating is $-144 \mathrm{~dB} / \mathrm{m}$.
The design and features of the M88 make it ideal for both indoor and outdoor applications and it will withstand the rigours of professional usage, being completely unaffected by humidity and extreme temperatures. The rated load of the microphone is greater than 1,000 ohms and the 200 ohm low impedance balanced output is at a level of $-51 \mathrm{~dB} / \mathrm{m}$ at 1 kHz .
The microphone is available in two versions: the M88N with standard DIN connector at $£ 81.05$ plus VAT and the M88N(C) with Cannon connector at $£ 83.35$ plus VAT. Each microphone is supplied with a protective carrying case. Optional extras include windscreen. stands and cable transformer.

Further information and nearest stockists can be obtained from Beyer Dynamic (GB) Ltd., 1 Clair Road, Haywards Heath, Sussex, RH16 3DP.

## NOTE

The SEAS loudspeaker kits, mentioned last month, are only available in 8 ohm versions.



## EXPORTS HOLDING FIRM

Few in the electronics industry regretted the passing of 1974 with its succession of elections, budgets, industrial unrest, inflation andi general economic stress. At least 1975 promises a higher level of stability and now we are well into the first quarter of the year there are already signs that whatever the depression in the home market, mainly in entertainments products, the world is still Britain's market and that trade remains at a substantial level.

The New Year saw a flurry of activity starting with confirmation from the British Overseas Trade Board that support for exhibitions at overseas trade fairs and for outward sales missions would be greater than ever before.

Few of these exhibitions, however, were specifically for electronics companies. Their turn comes a little later in the year with a major participation in a Communications Exhibition in Moscow at the end of May as just one of the highlights in a full programme.

Quick off the mark in 1975 were Marconi-Elliott Avionics with a £1-25 million order for automatic test equipment for the US Navy and US Air Force. The equipment is for checking out head-up displays for A-7D and A-7E Corsair 2 aircraft. At the same time, sister company Marconi Communication Systems was announcing a £3 million contract for colour TV studio and OB equipment for switchover from monochrome to colour in the Egyptian TV service.
A few days later a contract worth £200,000 was announced for Marconi Mark 8 colour TV cameras for use in America.

But it's not only the truly exotic equipment that sells well. Few people are aware that Plessey, as well as being big in radar, electronic telephone exchanges and sophisticated sonar systems, have a thriving business in electronic igniters for gas stoves and boilers. In January, Plessey Windings expanded European activities by signing a contract with S.I.T., France, who will beef up markets in France, Belgium and Luxembourg.

This new agreement supplements existing arrangements in the Far East and North America. The North American breakthrough is regarded as significant because gas igniters is a cut-throat business over there. Plessey Windings is a major supplier to Caloric Corp., Pennsylvania, a principal cooker manufacturer.
Racal Group looks as vigorous as ever with Racal-Milgo high speed modems being ordered from Poland, Racal-Thermionic selling £220,000 worth of data recording equipment to French airports, and Redac Software selling computeraided design software packages to Finland, bringing Redac customers in Scandinavia up to eight including names like Saab, Asea and Tandberg. These orders all came in early January.

Muirhead celebrated the New Year with a nice little order for 23 weather chart recorders for Kenya, Uganda and Tanzania. This brought Muirhead's export orders for this type of equipment to $£ 325,000$ in three months. Chairman Sir Raymond Brown commented that Muirhead will continue aggressive exploitation of the export potential of its products.

Smaller companies are also doing well. Membrain, for example, has installed an automatic tester for computer back-planes in West Germany. It will test over 2,000 points in less than a minute. The order was taken just before Christmas. The installation was operational by January 17. Nice work!

A heartening aspect of this brief survey is that so many of the orders, and they are only a short selection, are from countries such as the United States, France and West Germany which are themselves fully developed in the electronics industry. Another is the wide geographical spread.

## WHIZZ KIDS

Gordon Pope, who led the Advance Electronics expansion programme from £800,000 turnover when he ioined Advance as general manager in 1963 to today's $£ 11$ million, now rejoices in the title of Vice-President, Gould Instruments and Electronics, Europe. This follows the acquisition of Advance by

Gould last October. We may expect to hear of further'vigorous developments in the months ahead.

You've got to move fast to keep up with Tom Jermyn who started making transistor pads in his garage as a part-time hobby and built up Jermyn Industries to a mini-multinational operating in the USA and Europe as well as the UK. But Peter Smitham, who has become the top man in the Group, second only to Tom Jermyn, is no slouch. He leapfrogged into the top spot only a little over a year after joining Jermyn to set up Mogul Electronics as Jermyn's second component distributor company. He came to Jermyn with a fine record behind him. He was the youngest divisional manager in ITT, Eurode, and has a fine academic as well as business record.

Present plans are to expand Jermyn to $£ 20$ million turnover by 1980. Latest Jermyn company is Solek Ltd., manufacturing heat pipes, a product which Jermyn Industries has been pioneering in the UK for the past two years.

And how about Clive Sinclair, well known to our hobbyist readers through his electronic kits and, more recently, through pocket calculators? Predictions in some quarters that he would overtax his capacity last year were not realised and he bounced into 1975 with vet another round of price cuts on his established calculators, some new models, and a new electronic multimeter.

On February 1 the standard Cambridge calculator came down to $£ 12.95$ (very near the £10 I was predicting in this column a year or so ago) and the Scientific came down to $£ 19.95$. His new range, announced at the same time, are equivalent to desk-top models though Sinclair prefers to call them "hand-held" indicating they will go in a brief-case, if not in the pocket. He claims to hold some 30 per cent of the calculator market in the UK. plus considerable exports with a production of some 70,000 units a month.

While majoring on calculators he is not nealecting his other product lines. His original electronic multimeter, despite its low price, was a flop. It didn't sell at all well. Nobody liked the Nixie indicators. This year's model is very superior and has an 8 mm 7 -segment LED readout on a front panel that looks and is highly professional although, at under £60, the instrument is at the low end of the price range. The old DM1 was not for me. But I'm now using the new DM2 very happily in mv own workshop.

This year should also see Sin clair's lona-awaited pocket TV set coming to the market place as well as an electronic digital watch.

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TRANSDUCER IMPROVEMENTS

In BP1 364 669, STC Ltd. suggest some simple improvements to moving-iron transducers of the type used in some loudspeakers and telephone handsets. The basis of the technique described could be of interest to the experimenter.

Fig. 1 shows a conventional, rocking armature transducer (either a microphone or loudspeaker). A permanent magnet has pole piece members between which an armature is pivoted. The armature is coupled to a diaphragm and speech coils are wound on the pole pieces.

The passage of speech currents through the coils causes the armature to rock, or rocking of the armature induces currents through the coils. The arrangement works well until the armature is deffected too far and sticks or "freezes" against one pole of the magnet.

Diaphragm Fig. 2 shows the patented arrangement for preventing such freezing. The armature carries small magnets, one each side of its pivot point. The armature magnets lie between two end magnets and two front magnets. The polarity of the magnets is such that the armature floats freely in the central position as shown.

If currents are passed through the coil wound round the armature in such a direction as to make the leit hand end of south polarity, then that end moves upwards towards the end magnet 1. Reversing the current direction causes an opposite movement, and the


Fig. 1.


Fig. 2.
passage of an alternating current through the coil produces a corresponding vibration of the diaphragm. Similarly, movement of diaphragm induces currents in the coil.

Because of the repulsive forces between the magnets there can be no freezing of the armature and relatively rough-and-ready constructional techniques will provide a floating armature of low stiffness.

## SPARK-FREE SUPPLY OF A.C.

In BP 1366 134, Victor Products (Wallsend) Ltd., of Northumberland, claim a simple electromagnetic coupling system for supplying. a.c. supplies to loads in hazardous environments, such as inflammable gas atmospheres.

The obvious risk under such situations is that a spark will be generated when a connection is made or broken. It is not new to suggest using two separable halves of a transformer to provide sparkfree connection and disconnection. The primary of a transformer is provided in a wall socket and the secondary is included in a plug. When the plug is pushed into the socket the flux from the primary induces current in the secondary. The problem is ensuring that the primary does not drain current and overheat when the plug is disconnected.

The inventors show in their patent a socket with a primary winding and a plug with a secondary winding. To prevent current drain from the primary when the secondary is removed, an inductor is connected in series with the primary and a capacitor is connected in parallel with the inductor and primary. This parallel circuit is in series with a further inductor.

When the plug (sec) is coupled with the socket (pri) the circuit is tuned to the supply frequency. When the secondary is separated the inductance of the circuit decreases to cause detuning. Thus the current flowing through the primary also decreases.

As an example of component values, where the effective inductance of the primary decreases from 100 mH to 10 mH on removal of the secondary, a $0.16 \mu \mathrm{~F}$ capacitor and a 8 mH inductance was used to provide effective limiting for a supply frequency of 1 kHz .

## SPOT WELDING

In BP1 370 003, the Grumman Aerospace Corporation of Long Island, New York, explains how it is possible to spot weld by using a massive magnetic field. This creates a pressure on a metal workpiece and deforms it.

The Americans now suggest generating stress waves electromagnetically, to render the metal momentarily plastic. This they claim to have achieved with a device and circuit as shown in Fig. 1.

The power supply comprises a d.c. source, switch S1 and a capacitor bank. The capacitors are charged by the d.c. source when S1 is closed.

When charging is complete, switch S2 is closed to feed a high amperage current pulse of short duration to a pancake coil. The coil produces a high intensity magnetic field pulse which intersects a driver of aluminium or hardened copper. The driver acts as a one turn secondary winding of a transformer and the massive current which is induced in the driver sets up a high intensity magnetic field around it.

The electromagnetic repulsion created by the interaction of the two high intensity fields generates a stress wave in the driver, which is propagated through a focussing cone towards a tip. The combined effect of the backing mass and the stress from the tip deforms the workpiece and causes a spot weld.

The inventors claim success with voltages of between 4 and 7 kV . focusing cones of hardened steel, and a pancake coil of 18 turns of rectangular copper wire.



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Viscount ill amplifier - yolume, bass, treble and balance, controls, plus switches for monol steres on/off function and bass and treble filters. Plus headphone socket. Specification
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Garrard SP 25 deck with magnetle cartridge, de luxe plinth and cove
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Complete System $\mathbf{1 6 2} \cdot 00+£ 5 \cdot 50$ p 8 p .

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Viscount III amplifier (As system la).
Two Duo Tyoe III metched apeakers-Enclosure size approx. $27^{\prime \prime} \times 13^{\prime \prime} \times 11 z^{\prime \prime}$
Finished in teak simulate. Drive units $13^{\prime \prime} \times 8^{\prime \prime}$ bass driver. and two $3^{\prime \prime \prime}$ (appror.) tweaters. 20 watts R.M.S.. 8 ohms frequency range- 20 Hz to 18.000 Hz .
Complete System $\mathbf{£ 8 2 \cdot 0 0}+\mathbf{£ 6} \cdot 50 \mathrm{p} \& \mathrm{p}$

PRICES: SYSTEM 1a

| Viscount Ill R102 |
| :--- |
| amplifier |$\quad \$ 27 \cdot 00+51 \rho \& p$

2 Duo Type lla speakers $£ 26 \cdot 00+£ 5 \cdot 50$ D \& D
Garrard SP 25 with Mag. cartridge
and cover $\quad\{21 \cdot 00+£ 1 \cdot 75 p \& p$
lotal: 14 -00
Available complete for only: $\mathbf{5 6 2 . 0 0}$
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## PRICES: SYSTEM 2

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amplitier $127 \cdot 00+51 p d p$

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## TECHNICAL SPECIFICATION:

Pre-amp - Output - 200 mV .
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## Console size -

Unit Closed $-173^{\prime \prime} \times 133^{\prime \prime} \times 8 \frac{33^{\prime \prime}}{}$ (approx.) Unit Open $-35 \frac{3^{\prime \prime}}{4} \times 13 \frac{3^{\prime \prime}}{4} \times 4 \frac{1}{2 \prime \prime}$ (approx.)

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MI 13 in $\times 8$ in $150 \mathrm{~d} / \mathrm{c}$ 8 ohm
M. 3 In $\times$ oin $450 \mathrm{t} / \mathrm{tw} .3 .8$ or 15 ohm

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\] Ref. Amps. Weight Size cm. Secondory Windings P \& P Nef. Amps Weight
No. 12 V 24 V 16 oz 1110.50 .25
\begin{tabular}{ccccc}
0.5 & 0.25 & & 8 & 4 \\
1.0 & 0.5 & 1 & 4 & 6 \\
2 & 1 & 1 & 12 & 7 \\
4 & 2 & 2 & 12 & 8 \\
6 & 3 & 3 & 8 & 8 \\
8 & 4 & 5 & 8 & 9 \\
10 & 5 & 6 & 4 & 9 \\
12 & 6 & 6 & 12 & 9 \\
16 & 8 & 8 & 12 & 12 \\
20 & 10 & 11 & 8 & 1 \\
30 & 15 & 15 & 8 & 1
\end{tabular}
\(4.8 \times 2.9 \times 3.50 .12 \mathrm{~V}\) at \(0.25 \mathrm{~A} \times 2\) \(\begin{array}{ll}1.47 & 23 \\ 1.74 & 30\end{array}\)


MINIATURE TRANSFORMERS WITH SCREENS
Ref. mA
No. mA Weig \(\qquad\)
\(\begin{array}{lll}2.8 \times 2.6 \times 2.0 & 3.0 .3 \\ 6.1 \times 5.8 \times 4.8 & 0.6 .0\end{array}\) \(\begin{array}{ll}6.1 \times 5.8 \times 4.8 & 0.6 .0 .6 \\ 3.9 \times 2.6 \times 2.9 & 9.0 .9\end{array}\)
\(4.8 \times 2.9 \times 3.5 \quad 0-9.0 .9\)
\(1006.1 \times 54 \times 48 \quad 0.8 .9,0-8-9\)
\(1127.0 \times 6.4 \times 6.100-8-9.0-8-9\)
\(\begin{array}{llll}4 & 4.8 \times 2.9 \times 3.5 & 0-15,0-15\end{array}\)
\(4 \quad 6.1 \times 5.8 \times 4.8 \quad 0-2000\)
\(8 \quad 7.0 \times 6.1 \times 6.1 \quad 20-12-0.12-20\)
\(\begin{array}{ll}12 & 8.3 \times 7.7 \times 7.0 \\ 4 & 8.3 \times 7.0 \times 7.0\end{array}\) 20-15-20, \(0-15-20\)
\(0-15-27,0-15-27\) \(\begin{array}{ll}0-15-27, & 0-15-27 \\ 0-15-27, & 0-15-27\end{array}\) \(\begin{array}{ll}\epsilon & \\ .54 & 10 \\ .84 & 30 \\ 1.41 & 13 \\ 1.56 & 19 \\ 1.92 & 30 \\ 3.30 & 38 \\ 1.43 & 19 \\ 1.93 & 30 \\ 2.17 & 38 \\ 3.46 & 3 \\ 3.00 & 3 \\ 3.85 & 3\end{array}\) FOR V.A.T. INCLUDINGP\&P.

> BARRII electronics
> 3. THE MINORIES, LONDON EC3N 1BJ

> TELEPHONE: 01-488 3316/8
> NEAREST TUBE STATIONS ALDGATE \& ALDGATE EAST



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\section*{REGEIVERS AND COMPONENTS}
\begin{tabular}{|c|c|c|}
\hline \multicolumn{3}{|l|}{\begin{tabular}{l}
BETA DEVICES \\
for better PRICES
\end{tabular}} \\
\hline TRANSISTORS &  & DIODES \& RECT, \\
\hline AC187/188 & 709 C T0.99 0.80 & 1N914 0.04 \\
\hline PR. 0.40 & 709C D.I.L. & 1 N 41480.04 \\
\hline BC107) & 741 C T0.09 & BY1.27 0.14 \\
\hline BC108 0-00 & 741C D.I.L. & 1N4001/2 0.0 \\
\hline BC109C 0.11 & 728 C D.1.L. & 1274009/4/5 \\
\hline BC147/8/9 0.10 & 747 C D.I.L. 0 & 1N4006/7 \\
\hline BCY70/71/720-18 & 7480 D.I.L. & BRIDOI \\
\hline BFX \(66 / 87 / 88\) & & W01 1A \\
\hline 0.80 & 5 Watt Audio I.C. & 100 v 0.80 \\
\hline BFY50 0.18 & TBA 800 I2.60. & W06 1A \\
\hline BFYS1/52 0.18 & Dita tree with & \(600 \mathrm{~V} \quad 0.30\) \\
\hline OC28 00.45 & every order. & ZETER \\
\hline \begin{tabular}{ll} 
OC36 & 0.25 \\
2N2646 & 0.20
\end{tabular} & D.L.L. B0CKETS & BZY88 3-3- \\
\hline 2N3053 0.14 & 8.pin 0.18 & \(33 \mathrm{~V} 5 \% 0.00\) \\
\hline \(2 \mathrm{~N} 3055 \quad 0.88\) & 14.Pin 0.12 & 1 Watt 8.8- 0.18 \\
\hline 2N3442 81.40 & 16-Pin 0.14 & 200V \(6 \% 0.18\) \\
\hline \(2 \mathrm{N3773}\) 20.20 & & L.E.D. \\
\hline TIP41A 0.74 & All grices & \({ }^{209}\)-Red 0.17 \\
\hline 40836 S1.00 & include V.A.T. & L.E.D. Clip 0.08 \\
\hline \multicolumn{3}{|l|}{C.W.O. PLUS P.P. 10p TO BETA DEVICES,} \\
\hline
\end{tabular}

VALVEs, TRANsISTORs, styll. Valyes 1930 to \(1975.1,500\) types. Many obsolete. List 15 p . Transistors list isp. Ntyli list 10p. N.A.E. for quotation. ('OX RADIO (SLSSEX) LTD., The larade, East Wittering, sussex. Tel.: West Wittering 2023.
\begin{tabular}{|c|c|c|}
\hline \multicolumn{3}{|l|}{} \\
\hline Branded Com & penents-Full & 8 \\
\hline Tha & 8.9's & LIN i.c.e DIL Price \\
\hline & & 709C O.38 \\
\hline AC125, 6/7/8 & CRS 1110 0.s & 723 \\
\hline \multirow[t]{2}{*}{\[
\begin{aligned}
& \text { AD } 140: 149 \\
& \text { AOT } 1 / 162
\end{aligned}
\]} & CRS1/20 0.60 & \multirow[t]{2}{*}{P41C SPECIAL} \\
\hline & \[
\begin{array}{ll}
\text { CRS } 140 & 0.45 \\
\text { CRS } 340 & 0.55
\end{array}
\] & \\
\hline A0961/162 AF114/5/6/7 AF118 & \multirow[b]{2}{*}{speclal} & \\
\hline AF118 BC107/8:9 & & . 40 \\
\hline BC147.8.9 & I.C. OFFEM & 309 K \\
\hline \multirow[t]{2}{*}{\[
\begin{aligned}
& \mathrm{BC} 1823.4 \\
& \mathrm{BC} 2123 / 4
\end{aligned}
\]} & \multirow[t]{2}{*}{5 watt
Radio Pair} & \multirow[t]{2}{*}{L} \\
\hline & & \\
\hline \multirow[t]{2}{*}{BD131.132 0.9} & fadio Pair ZN414, TBAEOO & \({ }^{18} \mathrm{P}\) Pin \\
\hline & \$2.15 peir & \({ }^{16} \mathrm{p}\) \\
\hline \(\begin{array}{ll}\text { BF194/5/6 } & 0 \cdot 10 \\ \text { BFY } 50 / 51 / 52 & 0 \cdot 10\end{array}\) & FREE DATA & FULL Rance \\
\hline \(\begin{array}{ll}\text { BY127 } & 0.25\end{array}\) & \begin{tabular}{l}
Singly \\
ZN 51.10
\end{tabular} & Over 1,500 seml* \\
\hline MJE370 0.82 & \multirow[t]{2}{*}{\[
\begin{aligned}
& \mathrm{ZN} \mathrm{EI} .18 \\
& \mathrm{TBAE} 5 \cdot 85
\end{aligned}
\]} & conductor types. \\
\hline MJE371 0.73 & & 7400 ceries at \\
\hline \(\begin{array}{ll}218 & 0.19 \\ 906 & 0.13\end{array}\) & \multirow[t]{2}{*}{} & \multirow[t]{2}{*}{low prices. Varoboard, pote. cape. otc. stc.} \\
\hline 2N2926 all 0. & & \\
\hline 2 N 3053 O 0.15 & \multirow[t]{2}{*}{\begin{tabular}{l}
DIN PLUOS \\
2, 3. 4, 5 ( \(1+60^{\circ}\) )
\end{tabular}} & \multirow[t]{3}{*}{\begin{tabular}{l}
Send for FREE fist. \\
S.A.E. please
\end{tabular}} \\
\hline \({ }^{2 N} 305400.42\) & & \\
\hline 2N3055 & \(\left(240^{\circ}\right) 6\) pin 0.12 & \\
\hline \multirow[t]{3}{*}{\[
\begin{aligned}
& 2 \mathrm{~N} 3702: 3 / 4 \\
& 2 \mathrm{~N} 3705 / 6 \\
& 2 N 3707 / 8 / 8 \\
& 2 N 3019
\end{aligned}
\]} & \multicolumn{2}{|l|}{BAIDGE AECTIFIERS} \\
\hline & p.i.v. IA & 2A 4A 6A \\
\hline & SoV 0.20 & \(\begin{array}{llll}0.30 & 0.45 & 0.55\end{array}\) \\
\hline B2Ye & 100 V & 0.38 \\
\hline \multirow[t]{3}{*}{} & \multirow[t]{2}{*}{200 V 400 V} & 0.40 .54 \\
\hline & & 0. 00 \\
\hline & \[
\begin{aligned}
& 400 \mathrm{~V} \\
& 600 \mathrm{~V}
\end{aligned}
\] & \(\begin{array}{llll}0.45 & 0.70 & 0.87\end{array}\) \\
\hline \multicolumn{3}{|l|}{\multirow[t]{2}{*}{\begin{tabular}{l}
200/301 BALLAMDS LANE \\
LONDON N12 SNP ( \(01-445518\) ) MAIL ORDEA ONLY. Cabn with order. Orders under \&3 plus 15p P. \& P. Add V.A.T. to total
\end{tabular}}} \\
\hline & & \\
\hline
\end{tabular}
\begin{tabular}{|c|c|c|c|c|c|}
\hline  & DUC & & \multicolumn{3}{|l|}{\begin{tabular}{l}
DEPT. 24 \\
23 AVERY AVENUE \\
HIGH WYCOMBE BUCKS.
\end{tabular}} \\
\hline \multicolumn{6}{|l|}{\begin{tabular}{l}
4 ELEMENT FM sTEDEO \(\mathbf{E s} \cdot 80+V A T\) and 35 p P. \& P. \\
18 ELEMENT TV \\
\(\mathrm{E} 2+\mathrm{VAT}\) and 35 p P. \& P. \\
10 ELEMENT TV \\
\(\mathbf{5} 1.75+\) VAT and 35p P. \& P. \\
New design, superior quallity, inciudes mounting bracket. complete with instructions.
\end{tabular}} \\
\hline AC 126 BC107 BC108 BC109 BC113 & \[
\begin{array}{r}
20 p \\
\text { ep } \\
10 p \\
10 p
\end{array}
\] & \[
\begin{aligned}
& \text { BC148 } \\
& \text { BCY70 } \\
& \text { BFY51 } \\
& \text { 2N3055 } \\
& \text { NKT218 }
\end{aligned}
\] & 4p
18p
15p
45p
\(100 p\) & INg 14 IN4004 BZY88C5V6 BZY8AC 5 LM301 LM741 & \[
\begin{array}{r}
4 p \\
8 p \\
6 p \\
9 p \\
9 p \\
37 p \\
24 p
\end{array}
\] \\
\hline \multicolumn{6}{|c|}{ADD 8\% VAT + 10p P. \& P. per £ under £5} \\
\hline \multicolumn{6}{|c|}{C.W.O. MAIL ORDER ONLY} \\
\hline
\end{tabular}

TURM YOUR SURPLUS capacitors, transistors etc., into cash. Contact COLES-HARDING \& CO. P.O. Box 5, Frome, Somerset. Immediate cash settlement.
\begin{tabular}{|c|c|c|c|c|c|}
\hline \multirow[t]{4}{*}{with Dola clip 1p} & & & 0.125 & D & \multirow[t]{4}{*}{\begin{tabular}{l}
D.I. \\
SOCKET \\
8 pin \\
\({ }_{14}^{129}\) pin \\
13p
\end{tabular}} \\
\hline & \multicolumn{2}{|l|}{ED} & 15p & 13p \({ }^{8}\) & \\
\hline & \multicolumn{2}{|l|}{GREEN} & 27. & 33 p & \\
\hline & \multicolumn{2}{|l|}{YELLOW} & 270 & \({ }^{33} \mathrm{p}{ }^{13}\) & \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{INFRA-RED LED with Data}} & \multicolumn{4}{|l|}{\multirow[t]{2}{*}{\(550 \mu \mathrm{~W}\) axial lead, 49p 1.5 mW TO46, \(£ 1.10\)}} \\
\hline & & & & & \\
\hline \multicolumn{4}{|l|}{\multirow[t]{3}{*}{\begin{tabular}{l}
OPTO-I8OLATORS with Date \\
\(11744.5 \mathrm{kV}, 150 \mathrm{kHz}\) \\
4350 2.5kV. 5 MHz \\
ع2. 25
\end{tabular}}} & \multicolumn{2}{|l|}{\multirow[t]{3}{*}{\begin{tabular}{ll} 
Op. AMPs & \\
709 all & \(25 p\) \\
7418 pin & 229 \\
748 D.i.L. & \(38 p\)
\end{tabular}}} \\
\hline & & & & & \\
\hline & & & & & \\
\hline \multicolumn{2}{|l|}{\multirow[t]{2}{*}{\[
\begin{aligned}
& \text { THYR18TOA } \\
& \text { TOS } 14 \\
& \text { TOBS } 3 \mathrm{~A}
\end{aligned}
\]}} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{\begin{tabular}{ccc}
50 V & 100 V & 400 V \\
25 p & 27 p & 48 p \\
27 p & 35 p & 50 p
\end{tabular}}} & \multicolumn{2}{|l|}{\multirow[t]{3}{*}{(1)}} \\
\hline & & & & & \\
\hline \multirow[t]{15}{*}{} & \multirow[t]{3}{*}{\[
\begin{aligned}
& 15 p \\
& 15 p \\
& \text { 15p }
\end{aligned}
\]} & \multicolumn{2}{|l|}{} & & \\
\hline & & \multicolumn{2}{|l|}{} & \multicolumn{2}{|l|}{BHA 0002 15W I.C. AMP 52.50} \\
\hline & & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{(er}} & \multicolumn{2}{|l|}{} \\
\hline & \[
\begin{aligned}
& 3 p \\
& 10 p \\
& 10 p
\end{aligned}
\] & & & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{in 4001 IN4002}} \\
\hline & \multirow[t]{2}{*}{149} & \multicolumn{2}{|l|}{2N3704} & & \\
\hline & & \multicolumn{2}{|l|}{\multirow[b]{2}{*}{}} & \multicolumn{2}{|l|}{in4002
in4004} \\
\hline & \(11 p\) & & & \multicolumn{2}{|l|}{} \\
\hline & \multirow[t]{2}{*}{110
12
12} & \multicolumn{2}{|l|}{(1) \({ }_{\text {2N3823 }}^{\text {2N3818 }}\)} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{OAA1
OA91}} \\
\hline & & \multicolumn{2}{|l|}{\multirow{3}{*}{VOLTAGE REGS.}} & & \\
\hline & 15 & & & \multicolumn{2}{|l|}{0 OA5} \\
\hline & \multirow[t]{2}{*}{\[
\begin{aligned}
& 22 p \\
& 12 p \\
& 120
\end{aligned}
\]} & & & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{OAz200
OAz22}} \\
\hline & & \multicolumn{2}{|l|}{} & & \\
\hline & \(1{ }^{10 p}\) & \multicolumn{2}{|l|}{L129 Plastic. SV
600 mA
\(\mathbf{E 1 . 4 0}\)} & \multicolumn{2}{|l|}{ERIO} \\
\hline & \multirow[b]{2}{*}{\(11 p\)} & \multicolumn{2}{|l|}{\multirow[t]{2}{*}{Uned OCB4}} & \multirow[t]{2}{*}{2A 100 V} & \\
\hline & & & & & V \(\quad 36 \mathrm{p}\) \\
\hline \multicolumn{6}{|l|}{PRICES INCLUSIVE + 10p P. \& P. (1st class)} \\
\hline \multicolumn{6}{|l|}{ISLAND DEVICES, P.O. Box 11, Margate, Kent} \\
\hline
\end{tabular}

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ALL HIGE STABILTY-EXTRMERY LOW LEAEAGE


 \(0.47 \mu \mathrm{~F}\left(1^{\circ} \times{ }^{\circ}\right)\)
\(0.5 \mu \mathrm{~F}\)
\(01^{\circ}\)
\(\left.\mathbf{I}^{\circ}\right)\)


TANTALUM BEAD CAPACFTORS-Values avaliable: \(0.1,0 \cdot 22,0.47,1 \cdot 0,2 \cdot 2,4 \cdot 7,6.8 \mu \mathrm{~F}\) at \(15 \mathrm{~V} / 25 \mathrm{~V}\) or 35 V ;
\(10.0 \mu \mathrm{~F}\) at \(16 \mathrm{~V} / 20 \mathrm{~V}\) or \(25 \mathrm{~V} ; 22.0 \mu \mathrm{~F}\) at \(6 \mathrm{~V} / 10 \mathrm{~V}\) or 16 V ; \(10 \cdot 0 \mu \mathrm{~F}\) at \(16 \mathrm{~V} / 20 \mathrm{~V}\) or 20 V ; \(22.0 \mu \mathrm{~F}\) or \(6 \mathrm{~V} ; 100 \cdot 0 \mu \mathrm{Fat} 3 \mathrm{~V}\). ALL at 10p each. 10 for \(95 \mathrm{p}, 50\) tor 54.
TRAFSISTORS: |BC183/183L 119| BFY50 20p \begin{tabular}{lr|ll|ll} 
BC107/8/9 & \(9 p\) & BC184/184L & \(18 p\) & BFY51 & \(20 p\) \\
BC114 & \(18 p\) & BC212/212L & \(14 p\) & BFY52 & \(20 p\)
\end{tabular} \begin{tabular}{ll|ll|ll} 
BC147/8/9 & \(10 p\) & BC5 \(57 / 558\) A & \(12 \%\) & AF178 & \(80 p\) \\
BC153/7/8 & 189 & BF194 & 12p & OC71 & \(18 y\)
\end{tabular} \begin{tabular}{ll|ll|ll} 
BC182/182L & \(11 p\) & BF197 & 189 & 18C71 & 2N3055 \\
\hline
\end{tabular}
POPULAR DIODES-1N914 ©p, 8 for 45p, 18 for 90p: 1N9168p. 6 tor \(45 p, 14\) for \(90 \mathrm{p} ; 18445 \mathrm{p}, 11\) for 50 p .24 for
1; 1N \(4148 \mathrm{p}, 6\) for \(27 \mathrm{p}, 12\) for \(48 \mathrm{p} ; 1 \mathrm{~N} 40015\) p; IN 4002 6. IN40(13 \&it; IN4004 7p; IN4005 7tp; IN4006 8p; 1N40078/p.
LOW PRICE ZEMRR DIODES- 400 mW , Tol, \(\pm 5 \%\) at 5 mA . Valuea a vailable: \(3 \mathrm{~V}, 3.3 \mathrm{~V}, 3.6 \mathrm{~V}, 4.7 \mathrm{~V}, 5.1 \mathrm{~V}, 5.6 \mathrm{~V}\), \(\begin{array}{lll}6.2 \mathrm{~V}, & 6.8 \mathrm{~V}, & 7.5 \mathrm{~V}, 8.2 \mathrm{~V}, 9.1 \mathrm{~V} .10 \mathrm{~V}, 11 \mathrm{~V} .12 \mathrm{~V}, 13 \mathrm{~V}, \\ 13.5 \mathrm{~V}, & 16 \mathrm{~V}, 16 \mathrm{~V}, 18 \mathrm{~V}, 20 \mathrm{~V}, 22 \mathrm{~V}, 24 \mathrm{~V}, 27 \mathrm{~V}, 30 \mathrm{~V}, 33 \mathrm{~V}\end{array}\) ALL at 7 p esch, 6 for \(39 \mathrm{y}, 1\) it tor \(84 \mathrm{p} . \mathrm{SPECLAL}\) OFFER: 100 Zenera for 15.50 .
Rfsistons-HIgh stablity, low noise carbon film \(5 \%\) 1W at \(40^{\circ} \mathrm{C}\). \({ }^{\frac{1}{2} \text { W }}\) at \(70^{\circ} \mathrm{C}\). E12 series only-from \(2 \cdot 20\) to 2.2Mg. ALL at 1p each, 8 g for 10 of any one value, 70 p
for 100 of any one value. BPECIAL PACK: 10 of each for 100 of any one value. APECGIAL PACK value \(2 \cdot 2\) a to \(2 \cdot 2 \mathrm{Ma}\) ( 730 realstora) 25.
EILICOK PLABTIC RECTIFIERS- 1.5 mmp , brand new wire ended DO27: 100 P.I.V. 7 p ( 4 for 26 p ); 400 P.I.V. 8 p
 MUBITILATUER VERTICAL PRESETS-0.1W only; ALL at 5peach: \(500,100 \Omega, 220 \Omega, 470 \Omega, 680 \Omega\), 1k 0 , \(2 \cdot 2 \mathrm{k} \Omega, 4.7 \mathrm{k} \Omega, 6.8 \mathrm{k} \Omega, 10 \mathrm{k} \Omega, 15 \mathrm{k} \Omega, 22 \mathrm{k} \Omega, 47 \mathrm{k} \mathrm{Q}\), \(100 \mathrm{Ka}, 250 \mathrm{~g}, 680 \mathrm{ka} \mathrm{a} .1 \mathrm{Ma}, 2.5 \mathrm{M}, 5 \mathrm{M}\).
PLEASE ADD 10p POAT AND PACKING ON ALL ORDERS BELOW ES. ALL EXPORT ORDERS ADD COBT OF BEA/AIRMAIL.
Bend S.A.E. for lits of addtions
Bend S.A.E. for liats of additional ex-stock itema.
Wholeanle price lists available to bona fide companles.
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Tel.: Whirall \(484 / 465\) (9TD
Tel.: Whirall \(484 / 465\) ( 1 TD 0948 09 )
(Propra,: Minicost Trading Ltd.)

BRAND NEW DOMPONEMTE EY RETURN, Electrolytics, \(16 \mathrm{~V}, 25 \mathrm{~V}, 50 \mathrm{~V}, 0.47,1 \cdot 0,2 \cdot 2\) \(4 \cdot 7,10 \mathrm{mF} .4 \mathrm{p} ; 22,47,4 \frac{1}{2}(50 \mathrm{~V}, \mathrm{ED}) ; 100\), 8 (50V, 7p); 220, 7f (50V, 14); \(1000 / 25 \mathrm{~V}, 15 \mathrm{p}\) Subminiature bead-type tantalums, \(0.1 / 35 \mathrm{~V}, \quad 0.22 / 35 \mathrm{~V}, 0.47 / 35 \mathrm{~V}\), \(1 \cdot 0 / 35 \mathrm{~V}, 2 \cdot 2 / 35 \mathrm{~V}, 4 \cdot 7 / 35 \mathrm{~V}, 10 / 20 \mathrm{~V}, 22 / 16 \mathrm{~V}\) \(47 / 6 \mathrm{~V}, 100 / 3 \mathrm{~V}, 9 \mathrm{p} . \mathrm{Mylar}\) Film \(100 \mathrm{~V}, 0.001\), \(47 / 6 V, 1003\).
\(0.002,0.005,0.01\),
\(0.02, ~ 2 \frac{1}{2} ; 0.04,0.05,3 p\) \(0.002,0.005,0.01,0.02,21 ; 0.04,0.05, ~ 31\)
Mullard tubular polyester 400 V E 6 geries, \(0.001,0.022,3 p ; 0.033-0.1\), 4p. Mullard polyester 160 V tubular or 250 V miniature for vertical mounting, E6 sertes, \(0.01-0.047\), 3p; \(0.088,0.1,42 ; 0.15,0.22,5 ; 0.33,6 \mathrm{~F} ; 0.47\), ? ; \(0.68,10 \mathrm{p} ; 1.0,12 p, 1 \cdot 5 / 250 \mathrm{~V}, 16 \mathrm{p} ; 2 \cdot 2 /\) 63 V E12 series \(2 \% 1.8 \mathrm{pF}-47 \mathrm{pF}, 2 \frac{1}{2} \mathrm{p} ; 56 \mathrm{pF}-\) 330 pF , 3p. Plate ceramics 50V E6 series \(470 \mathrm{pF}-47,000 \mathrm{pF}\), 2p. Polystyrene 63V, E12 series \(10 \mathrm{pF}-1,000 \mathrm{pF}^{2}, 2 \frac{1}{2} \mathrm{p}, 1,200 \mathrm{pF}\), \(10,000 \mathrm{pF}, 3 \frac{1}{2} \mathrm{p}\) Miniature highstab. carbon film resistors if E12 series 5\% (10\% over \(1 \mathrm{M} \Omega) 1 \Omega-10 \mathrm{M} \Omega, 1 \rho ; 1 \mathrm{~N} 4148\), 31p; 1 N 4002 5p; iN4006, 7p. Postage 10p, Prices VAT Chesterfleld Road, Sheffield, 88 0RN.

\section*{R.T. SERVICES \\ (MAIL ORDER ONLY)}

77 Hayfield Rd., Salford 6, Lancs.
12 Volt I Amp Trickle Charger. © 1.85 P.P FM Tuner with R.F. Stage and A.G.C. 3 transistors, neg. earth, \(2 \frac{1}{2} \times 2 \times 1 \frac{1}{2}\) in with circuit, \(\mathrm{f} 1 \cdot \mathbf{3 7} \frac{1}{2}\) inc. P.P.
Crouzet Geared Motors, 30 r.p.m. New, fl. 54 inc. P.P.
UHF TV Tuners. Transistorised, \(£ 1.85\) inc. P.P.
Panels, with I.C's on \(7 \frac{1}{2} \mathrm{p}\) per I.C. min. order IO I.C's.
Transformers. \(7.5 \mathrm{~V}+7.5 \mathrm{~V} \frac{1}{2} \mathrm{~A}, £ 1\) inc. \(P . P\). \(12-0-12 \mathrm{~V}, 100 \mathrm{~mA}, \ldots 1.10\) inc. P.P. \(9-0-9 \mathrm{~V}\), \(100 \mathrm{~mA}, \mathrm{E}^{\prime} 10\) inc. P.P. 29 V 50 mA , 85 p inc. P.P. \(6-0.6 \mathrm{~V}, 100 \mathrm{~mA}\), \(£ 1 \cdot 10\) inc. P.P.
Transformer. 24 volt, approx. 1 amp
\(6.3 V\) CT approx. \(500 \mathrm{~mA}, \mathrm{E} 1 \cdot 40\) inc. P.P.
Transformer. 20-0-20 yolt, approx. 2 amp \(+6.3 V\), 63 inc. P.P
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