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## THIS IS THE FIRST PAGE OF THE GREAT BI－PAK SECTION

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> 0.22
0.22 AD16：
AD161



| 0019 | 0.38 |
| :---: | :---: |
| OC 20 | 0.70 |
| Ot2\％ | 0.42 |
| （23 | 0.48 |
| c： | 0.62 |
| 0 C 25 | 0.42 |
| 0 O＂6 | 0.28 |
| OC：8 | 0.55 |
| 00：29 | 0.55 |
| $0 \mathrm{OC}_{3}$ | 0.48 |
| 0036 | $0 \cdot 55$ |
| OC4 1 | 0.22 |
| OCl2 | 0.27 |
| OC44 | $0 \cdot 17$ |
| OCl | 0.14 |
| OCio | （．11 |
| 0 C 71 | $0-11$ |
| OC72 | 0.16 |
| 0 OCH | 0.18 |
| Oぐう | 0.17 |
| 0 Caj | 0.17 |
| $00 \%$ | 0.28 |
| OCSI | 0.17 |
| $0 \mathrm{C81D}$ | 0.17 |
| OC8： | 0.17 |
| OC8：1 | $0 \cdot 17$ |
| OC83 | 0.22 |
| OC84 | 0.22 |
| OC139 | 0.22 |
| OC＇ 130 | 0.22 |
| OC169 | 0.28 |
| OC170 | 0.28 |
| OC17 | 0.28 |
| （C）200） | 0.28 |
| $00^{2} 201$ | 0.31 |
| OC－20 | 0.31 |
| OC： 23 | 0.28 |
| 0 C 204 | 0.28 |
| $00^{2} 205$ | 0.38 |
| 0 C 309 | 0.44 |
| OCP71 | 0.48 |
| OR1＇te | 0.48 |
| ORP60 | 0.44 |
| ORP＇61 | 0.44 |
| P＇346A | 0.22 |
| P397 | 0.46 |
| ST 140 | 0.14 |
| 8T141 | 0.19 |
| T1543 | 0.33 |
| UT4t | 0.30 |
| $\because 6301$ | 0.21 |
| 9C302 | 0.21 |
| $2(1303$ | 0.21 |
| 26：304 | 0.27 |
| 2 CO 30 m | 0.44 |
| 26308 | 0.39 |
| 26309 | 0.39 |
| 26339 | 0.22 |
| 2 G 33.39 A | 0.18 |
| $2 \mathrm{C344}$ | 0.20 |
|  |  |



| BC148 | 0.11 | 13113＇ | 0.50 |
| :---: | :---: | :---: | :---: |
| 4 Cl 19 | 0.13 | HD138 | 0.55 |
| HC150 | 0.20 | BD）34 | 0.61 |
| BCLIol | 0.22 | BDIso | 0.68 |
| HC102 | 0.19 | нD）${ }^{\text {cos }}$ | 0.88 |
| $1 \mathrm{Clis3}$ | 0.81 | （1）17\％ | 0－66 |
| 18Clu4 | 0.33 | HD176 | 0.68 |
| 1 CL 107 | 0.20 | В1757 | 0．72 |
| HC1\％s | $0 \cdot 13$ | HDII8 | 0.72 |
| 1 ClOg | 0.13 | 3D179 | 0．77 |
| HC160 | 0.50 | Bulro | 0.77 |
| BC161 | 0.55 | B1185 | 0.72 |
| 13C16i | 0.13 | BD18t | 0.72 |
| 130168 | 0.13 | BI）189 | 0.77 |
| 13C．169 | 0.13 | 3D188 | 0.77 |
| BC170 | 0.13 | BD189 | 0.83 |
| 13C17 | $0 \cdot 18$ | HD190 | 0.83 |
| 1015： | 0.16 | 13D19\％ | 0.84 |
| 16C173 | $0 \cdot 18$ | BD196 | 0.84 |
| BClit | 0.16 | BD197 | 0.98 |
| $13 C 175$ | 0.24 | BD198 | 0.98 |
| HC17\％ | 0.21 | 13 J 199 | 1.05 |
| BC178 | 0.21 | 13D20 | 1.05 |
| 1 C 179 | 0.21 | 13D $20 \overline{3}$ | 0.88 |
| ACl80 | 0.27 | BD： 206 | 0.88 |
| 1C181 | 0.27 | BDerat | 1.05 |
| BC＇182 | 0.11 | 13D208 | 1.05 |
| BCJM2L | 0.11 | BDYe | 1.10 |
| BC153 | 0.11 | 13F115 | 0.27 |
| 13C183L | 0－11 | HF117 | 0.50 |
| HC184 | 0.13 | 13Fil8 | 0.77 |
| 13C184L | 0－13 | BFIIG | 0.77 |
| 1 CC 186 | 0.31 | BF121 | 0.50 |
| 16188 | 0.31 | BF12．9 | 0.55 |
| ［C：07 | 0.12 |  | 0.50 |
| B6：208 | 0．12 | BF127 | 0.55 |
| HC209 | 0.13 | BF152 | 0.61 |
| BC゙2\％ | 0.12 | HF153 | 0.50 |
| BC2 13 L | 0.12 | 13F154 | 0.50 |
| BC＇SI4L | 0.16 | 13F155 | 0.77 |
| BC225 | 0.28 | B $\mathrm{F}^{1} 156$ | 0.58 |
| $\mathrm{BC}^{2} 26$ | 0.38 | BF゙107 | 0.61 |
| BCY30 | 0.27 | BF1处 | 0.81 |
| BCY31 | 0.29 | BF159 | 0.68 |
| BCY32 | 0.33 | HFItio | 0.44 |
| BC： 33 | 0.24 | 1FF162 | 0.44 |
| BCY34 | 0.28 | BF163 | 0.44 |
| 1 CCY 70 | 0.18 | 13F164 | 0．44 |
| BCY゙1 | $0 \cdot 20$ | 15165 | 0.44 |
| BCY\％ | 0.16 | HF167 | 0.24 |
| 13CZ10 | 0.22 | BF173 | 0.24 |
| 13CZ11 | 0.88 | BF176 | 0.38 |
| HCZ12 | 0.28 | 8ト17\％ | 0.39 |
| 13D121 | 0.88 | BF178 | 0．33 |
| 13D123 | 0.72 | Briz9 | 0.33 |
| 131）124 | 0.88 | BF゙I80 | 0－33 |
| BDi31 | 0.55 | AFIB］ | 0.33 |
| 131）13： | 0.66 | HF゙182 | 0.44 |
| 13D143 | 0.72 | ［FF183 | 0.44 |
| BDIs， | 0.44 | Bド1く4 | 0．28 |
| 13DI36 | 0.44 | 13ド18 |  |

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0.55 0.55
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| :--- | :--- |
| N 2220 | 0.24 |
| 22221 | 0.22 |


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0.13
0.13

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$2 \mathrm{~N}+0 \mathrm{C}^{2}$
N 4285
$\begin{array}{ll} \\ 2 N+2886 & 0.19 \\ 2 N 4287 & 0.18\end{array}$
$\begin{array}{ll}2 \mathrm{~N} 4286 & 0.19 \\ 2 \mathrm{~N} 4.287 & 0.18 \\ 2 \mathrm{~N}\end{array}$
N 4289
$\mathrm{~N}+290$
N 4291
$\mathrm{~N}+292$
$\mathrm{N}+292$
$\mathrm{~N}+293$

$\begin{array}{ll}2 N 301 & 0.44 \\ 0.55\end{array}$
$\begin{array}{ll}233024 & 0.46 \\ 28302 & 0.46\end{array}$
$\begin{array}{ll}28034 & 0.67 \\ & 0.03\end{array}$
$\begin{array}{ll}28.306 & 0.93 \\ 24.307 & 0.98 \\ 28321 & 0.62\end{array}$

| 3.322 | 0.4 |
| :--- | :--- |
| 0.4 |  |

$\begin{array}{ll}1323 & 0.62\end{array}$
$\begin{array}{ll}321 & 0.77 \\ 3327 & 0.77 \\ 701 & 0.48\end{array}$
$361-0.44$

IODES AND RECTIFIERS

| A119 | 0.09 | 15133 0.23 | （1． 10 | 0.38 |
| :---: | :---: | :---: | :---: | :---: |
| A A120 | 0.09 | 13YItit 0.55 | 0 Al | 0.08 |
| A 1129 | $0 \cdot 09$ | BYX38／30 | 0.70 | 0.08 |
| A1Y30 | 0.10 | 0.48 | 0.774 | 0.08 |
| A．213 | 0.11 | BYZ10 0.39 | 0.81 | 0.08 |
| B． 100 | 0.11 | 13Y\％11 0.33 | 0 0．80 | 0.10 |
| BAll 6 | 0－23 | 15 Cl <br> 12 | O．4．0 | 0.07 |
| 13A1：20 | 0.24 | BYZ13 0．28 | 0 O91 | 07 |
| 13.148 | 0.18 | HYZIti 0－44 | 0.495 | 0.08 |
| BA154 | 0.13 | 13Y\％17 0．38 | O．4200 | 0.07 |
| bAlón | 0.18 | $13 Y \mathrm{Cl} 18 \quad 0.39$ | OA202 | 0.08 |
| B，Alm | 0.15 | BYZ19 0－31 | 81110 | 0.08 |
| 15 Y 100 | 0.17 | Clibz | Sbla | 0.08 |
| 13Y101 | 0.13 | （0ayl Exq．） | $1 \times 34$ | 0.08 |
| 13 ${ }^{\text {a }} 10$ ． | 0.19 | 0.06 | 1234： | 0.08 |
| BY114 | 0.13 | C6tis］－ | $1 \mathrm{Nal4}$ | 0.07 |
| BY 126 | 0.16 | （0）A70－0．479） | $1 \times 916$ | 0.07 |
| HY127 | 0.17 | 0.07 | 1N＋143 | 0.07 |
| By128 | 0.17 | 0．15 0．39 | 18021 | 0.11 |
| 30 | 0.18 | OAJsle 0 | 18951 | 0.07 |

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| MODEL | CD66 | GR16 | 3015 F <br> Minitron | AII <br> indicalors $0.9+$ Decimal point. All side <br> viewing. Full data for all types gvailable on request. |
| :---: | :---: | :---: | :---: | :---: |
| Anode Voltage ( $V_{\text {, }} \mathrm{l}$ c) | 170 min | 17 mmin | 5 |  |
| Cathode Current (mA) | $2 \cdot 3$ | 14 | 8 |  |
| Numerical Height (mm) | 16 | 13 | 9 |  |
| Tube Height (mm) | 47 | 32 | 29 |  |
| Tube biameter (mm) | 19 | 13 | 12 wide |  |
| I.C. Driver Rec. | $\begin{gathered} \mathrm{BP} 41 \text { or } \\ 141 \end{gathered}$ | $\begin{gathered} 13 P 41 \\ 141 \end{gathered}$ | BP47 |  |
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Three switched stareo inputs, and rumble and scratch filters are features of the PA 100 . which also has a STEREDO/MONO switch, volume, balance and cuntinuously varlable bess and trebic controis.

## SPECIFICATION <br> Frequency Response

Inputs: Distortion
Inputs: 1. Tape Head
$20 \mathrm{~Hz}-20 \mathrm{kHz} \pm 1 \mathrm{~dB}$
125 mV into 50 Ki 3. Magnetic P.U

35 mV trito 50 ka
Allinput voltages are for an output of 250 mV . Tape and P.U. inputs equalised to RIAA curve within $\pm 1 \mathrm{dH}$ from 20 Hz to 20 kHz Treble Control
+15 dB at 20 kHz
$\begin{array}{cc}\text { Filters: Rumble (8igh Pass) } & 100 \mathrm{~Hz} \\ \text { Scratch (Low Pass) } & 8 \mathbf{k H z}\end{array}$
Signal/Noise Ratio $\quad$ better than - Rod
Input overload Supply
+260 volle at 20 mA
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$292 \mathrm{~mm} \times 82 \mathrm{~mm} \times 35 \mathrm{~m}$
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## Sinclair Project 60

# Now-the Z.50 Mk. 2 

## with built-in automatic transient overload protection


#### Abstract

When originally introduced, the Sinclair $Z .50$ proved how it was possible to design and produce a popularly priced modular power amplifier having characteristics to challenge the world's costliest amplifiers. Many thousands of 2.50 s are now giving excellent service day in, day out. But we have also learned that constructors do not always use their 2.50 's ideally. That is why we have introduced modifications whereby risk of damage through mis-use is greatly reduced and performance further enhanced. The 2.50 Mk. 2 has improved thermal stability, more accurately regulated D.C. limiting to ensure more symetrical output voltage swing and clipping and still less distortion at lower power. $Z .50 \mathrm{Mk} .2$ is compatible with all other Project 60 modules, and may be incorporated to advantage in existing systems. Eleven sificon epitaxial planar transistors are now used. two more than in the original 2.50 ; circuitry has been re-designed, making this versatile high performance amplifier better than ever.




Z .30 the power amplifier for quality and economy

with
free manual
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technical specifications
Input impedance 100 K s
Input (for 30 w into $8 \Omega$ ) 400 mV
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Max supply voltage $45 v$ ( $4 \Omega$ to $8 \Omega$ speakers) ( $50 \vee 15 \Omega$ speakers only)
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Load impedance - minimum : $4 \Omega$ at $45 v$ HT
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The Z 30 provides excellent facslities for the constructor requiring a high fidelity audio system of less power than that available from Z.50's Using a power supply of 35 voits, $Z 30$ will deliver 15 watts RMS into 8 ohms, or 20 walts RMS into 3 ohms using 30 volts. Total harmonic distortion is a fantastically low $0.02 \%$ at 15 watts into 8 ohms with signal to noise ratio better than 70 dB unweighted Input sensitivity 250 mV into 100 K ohms. Size $80 \times 57 \times 13 \mathrm{~mm}\left(3 \frac{1}{6} \times 2 \frac{1}{4} \times \frac{1}{2}\right)$ Z.30, 2.50 and $Z 50$ MK. 2 modules are compatible and interchangeable

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Each Project 60 module is tested before leaving ourfactory and is guaranteed to work perfectly. Should any defect arise in normal use. we will service it at once and without any charge to vou, if it is returned within two years from the date of purchase. Outside thisperiod of guarentee a smatl charge (typically f 1.00 ) will be made. No charge is made for postage by surface mall, Air Mall is charged at cost

## Typical Project 60 applications

| System | The Units to use | together with | Units cos |
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| Mains powered record player | 2.30, PZ.5 | Crystal or ceramic P.U. volume control etc | £9.45 |
| 12W. RMS continuous sine wave stereo amp. for average needs | $\begin{aligned} & 2 \times 2.30 \mathrm{~s} \text {. Stereo } \\ & 60 ; \text { PZ. } \end{aligned}$ | Crystal, ceramic ormag PU., F.M. Tuner, etc. | £23.90 |
| 25W. RMS continuous sine wave stereo amp. using low efficiency (high performance) speakers | $\begin{aligned} & 2 \times 2.30 \text { s, Stereo } \\ & 60: \text { PZ. } 6 \end{aligned}$ | High quality ceramic or magnetic PU. F.M. Tuner. Tape Deck. etc. | £26.90 |
| 80W. (3 ohms) RMS continuous sine wave de luxe stereo amplifier. (60W. RMS into 8 ohms) | 2x2.50s, Stereo 60; PZ.8, mains transformer | As above | £34.88 |
| Indoor PA. | Z.50, PZ.8, mains transformer | Mic.. guitar. speakers. etc.. controls | $£ 19.43$ |

F.M. Stereo Tuner (£26) \& A.F.U. (£6.98) may be added as required

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## Stereo 60 Pre-amp/control unit



Designed specifically for use on Project 60 systems, the Stereo 60 is equally suitable for use with any high quality power amplifier Since silicon epitaxial planar transistors are used throughout, a really high signal-to-noise ratio and excellent tracking between channels is achieved. Input selection is by means of press buttons, with accurate equalisation on all input channels. The Stereo 60 is particularly easy to mount.
SPECIFICATIONS-Input sonsitivities: Radio - up to 3 mV Mag. p.u. 3 mV correct to R.I.A.A. curve $\pm 1 \mathrm{~dB} .20$ to 25.000 Hz . Ceramic p. U. - up to 3 mV Aux-up to 3 mV . Ourput: 250 mV Signal to noise ratio: better than 70 dB . Channel matching: within 1 dB . Tone controls: TREBLE +12 to -12 dB at 10 KHz . BASS +12 to -12 dB at 100 Hz . Front panel: brushed aluminium with blackknobs and controls. Size: $66 \times 40 \times 207 \mathrm{~mm}$.

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## Project 60 Stereo F.M. Tuner




The phase lock loop principle was used for receiving signals from space craft because of its vastiy improved signal to noise ratio Now. Sinclair have applied the principle to an F.M tuner with fanlastically good results. Other advanced features influde varicap diode tuning. printed circuit coils, an I.C. In the speciaHy designed stero decoder and switchable squetch circult for silent tuning between stations. In terms of high fidelity this tuner has a lower level of distortion than any other tuner we know. Stereo broadcasts are received automatically, a panel indicator lighting up as the stereo signal is tuned in . This tuner can also be used to advantage with most other high fidelity systems
SPECIFICATIONS-Number of transistors: 16 plus 20 inI. C. Tuning range: 87.5 to 108 MHz . Sensitivity $7 \mu \mathrm{~V}$ for lock-In over full deviation. Squelch level: Typically $20 \mu \mathrm{~V}$. Signal to noise ratio: $>65 \mathrm{~dB}$. Audio frequency response: $10 \mathrm{~Hz}-15 \mathrm{KHz}$ ( $I$ 1dB). Total harmonic distortion: $0.15 \%$ for $30 \%$ modulation Stereo decoder operating level: $2 \mu \mathrm{~V}$. Cross talk: 40 dB . Output voltage: $2 \times 150 \mathrm{mV}$ R.M.S. maximum Operating voltege : $25-30 \mathrm{VDC}$. Indicetors: Stereo on ; tuning. Size: $93 \times 40 \times 207 \mathrm{~mm}$.

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sistor circuit contained within a 16 lead OIL package. and the finned heat sink is sufficient for all requirements. The Super IC 12 is compatible with Project 60 modules which would be used with the $Z .50$ and $Z .30$ amplifiers. Complete with free manual and printed circuit board.

## SPECIFICATIONS

Output power: 6 watts RMS continuous (12 watts peak) 6-8』. Frequency Response: 5 Hz to $100 \mathrm{KHz} \pm 10 \mathrm{~B}$. Total Harmonic Distortion Less than $1 \%$ (Typical $0.1 \%$ ) at all output powers and frequencies in the audio band ( 28 V ) Load Impedance: 3 to 15 ohms Input Im Load Impedance: 3 to 15 onms input Impedance: 250 Kohms nominal Power Gain:
90 dB (1,000.000,000 1imes) after feedback 90 dB ( $1,000,000,000$ 1imes) after feedback
Supply Voltage: 6 to 28 V . Quiescent curSupply Voltage: 6 to 28 V . Quiescent cur-
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## TWO-WAY LINK

COMMUNICATION is a fundamental function of electronics. It is particularly appropriate therefore to discuss certain aspects of communication relating to this magazine and its readers. First, one unquestionable fact: the link forged between a magazine and its readers is of inestimable value and significance. To this it can be confidently added that at magazine devoted to a creative hobby is especially favoured in that feedback from its readers is often likely to be in the form of examples of readers' own personal efforts and experiences. Such direct positive evidence of a live and actively committed readership is perhaps the best and most encouraging reward the editorial staff of such a publication could wish for.

Practical Electronics is proud of the two-way link established between individual readers and the magazine. We believe this is mutually stimulating. The existence of this link has long been evident, and has been demonstrated most effectively in our pages through the medium of Ingenuiry Unlimited. Reference to the popularity and importance of this feature is not out of place. It has of ten been commented upon that the circuit ideas published constitute a most valuable source of reference to designers and constructors alike. We agree, and feel sure that many a reader must have saved time and effort through studying the ideas in Ingenuity Unlimited.

The varied nature of these miscellaneous circuits is, in itself, quite instructive. It indicates a wide catholic range of interests and shows that the inventive spirit is as much alive as ever; a refreshing and reassuring antidote to the drab uniformity of thought and action which characterises many aspects of modern life.

The circuits published in I.U. are generally of modest proportions, but that is no true measure of their actual or potential worth. They represent the new outlook and approach to electronics which has emerged in the course of the last few years. The dramatic scaling down in physical size of components over the last decade has given electronics greater-almost total-freedom in the environmental sense, and in many instances it is released entirely from the shackles of the mains umbilical cord. One result of all this has been the creation of an enormous variety of small circuits, many owing their origination to the enterprising efforts of private individuals to solve some problem or meet some particular need through the agency of electronics, the very thought of which would have been preposterous a few years ago. Many of these are worth recording for the benefit of others.

We are glad to be able to present some of the fruits of this kind of private enterprise in Ingenuity Unlimited. And never let it be forgotten that such relatively simple circuits as these may sometimes prove to be the seeds from which bigger and grander concepts grow.-I.E.B.

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## Editorial

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The game of chess is more Machiavellian than military in its tactics and strategy and the circuit described here provides a version which is much more like the action of a military battle than court intrigue. Thus: Battle Chess.

No claim to originality is made by the author since Edgar Rice Burroughs--of Tarzan fame-once wrote a book called the "Chessmen of Mars" in which the idea was developed fully. The trouble with the Martian game was the requirement for real people, expected to fight to the death. The finding of 32 people willing to play chess in this way would undoubtedly prove difficult but the Battle Chess circuit is designed to simulate the duels which occur.

## THE GAME

In basic concept Battle Chess uses the moves of its namesake but introduces a variety of programmable factors which come into play each time there is a confrontation. Thus one piece does not merely "take" its opponent - they have to fight the situation out in an electronic simulation.

## PROGRAMMING

A number of factors have been programmed into the simulation circuit which parallel those found in real life military situations:

1. The strength of the various chessmen.
2. The addition of arms which increase the fighting strength of a chessman.
3. The reduction in fighting power due to supply problems (i.e ditsance between duelling chessmen and their respective King's squares).
4 . The proximity of supporting chessmen.
4. The effects of morale.

Combinations of resistors are used to determine factors 1 to 4 . Resistance values interact with each other to affect the frequency of multivibrators (see Fig. 1).

Morale, on the other hand, is time dependent, being at its highest immediately after a successfully completed duel. The charge remaining in a capacitor simulates the effects of morale; the charge leaks away to nothing and the probability of winning the next duel drops from exceptionally high for the previously winning player to the normal value over a period of about ten minutes.


Fig. 1. Block schematic of the Battle circuit showing how the basic units are interconnected

The factors which combine in the game of Battle Chess make it virtually impossible to predict the outcome of a simulated duel. Thus the tactics and strategy which have to be developed by the player are more dynamic, more subject to caprice, than those used in conventional chess and are designed to approximate the real life strategy of a series of military engagements.

## THE SIMULATION CIRCUIT

The circuit is shown in block form in Fig. 1 and in detail in Fig. 2.
Two conventional multivibrators A and B have their frequencies determined by external resistances connected between points 1 and 2 and the negative rail. The pulses of each multivibrator simulate the thrusts and parries of a duel and the resistances which combine to set the frequency of oscillation
freeze with the winning side indicated by the light which remains lit.

The opposing capacitor's charge is neutralised through diodes D1 or D2 so that the other Darlington remains unswitched. The charge in the winning capacitor remains at the switching voltage as long as the lamp remains lit and leaks away once the circuit is switched off. This slowly leaking charge represents the player-army's morale and if a fight is started before the charge has gone, it takes less time to build up to its switching value.

## CONSTRUCTION

The simulation circuit layout is not critical and the Veroboard method of construction is ideal.
The multivibrators may be constructed first and checked by temporarily conrecting $3.3 \mathrm{k} \Omega 2$ resistors


Fig. 2. Detailed circuit diagram of the equipment. When setting the timing of the various sections it may be necessary to select the electrolytic capacitors C1 to C8 if the variable resistors VR1 to VR4 are unable to provide sufficient adjustment to "pull" the periods into agreement
represent the value of the chessman, its distance from its King's square-representing G.H.Q.-and the number of friendly chessmen and their relative positions.

Pulses from each multivibrator are fed to a central flip-flop via capacitors C3 and C6. These pulses switch the flip-flop so that lamps LP1 and LP2 are switched on alternately with a continuously varying mark / space ratio. The lamps, as well as providing a visual analogue of the duel, eventually indicate which side has won.
Depending on which flip-flop transistor is on, a current flows in R7 or R10 providing a potential difference which charges capacitors C4 and C5 through resistors R6 and R11. Additional resistors may be inserted at points $\mathrm{XX}^{\prime}$ and $\mathrm{YY}^{\prime}$ to reduce the overall resistance and allow C4 or C5 to charge faster.

These additional resistances simulate weapons used by the battling chessmen. When either C4 or C5 reaches a voltage of about 0.7 volts, the associated score counter, a Darlington pair, switches on, causing two things to happen. The Darlington pair output biases the flip-flop via R5 or R12, causing it to
between points 1 and 2 and the negative rail. Connect the 9 volt battery and check oscillation with a voltmeter across R4 and R13.
The flip-flop should be built on next, together with its triggèr pulse feeds, C3 and C6. Connecting the battery should now cause the lamps to flash alternately in response to the multivibrators and clearly show the variation in mark/space ratio.

When constructing the score counters remember that diodes D1 and D2 must be good ones. A reverse leak on one or both of these, even if only four to five micro-amps, will upset the impartiality of the circuit. Pliers used as a heat shunt when connecting them will reduce the risk of damaging their characteristics.

To test the score counters, connect the battery once more and briefly touch a $10 \mathrm{k} \Omega$ resistor, connected to the positive rail, to the positive lead of C3. The lights should cease to flash, LP1 should stay lit and LP2 extinguish.
Leave the circuit in this state to check that it is a stable condition; if the situation changes it will almost certainly be one of the diodes at fault. Repeat the test with C5.


Fig. 3. Veroboard layout of the components on 0.1 inch pitch board. In the present case this was cut from a sheet edge to give a blank area which can be drilled for fixing as required

Finally, test that the flip-flop is charging the score capacitors properly. Temporarily connect a $1 \mathrm{M} \Omega$ resistor across the points $\mathrm{XX}^{\prime}$. Discharge the score capacitors by shorting their positive leads to the negative rail before switching on. The lamps should flash for 10 to 15 seconds and then LP1 will remain on and LP2 off. Repeat with the 1MS2 resistor across the points $Y Y^{\prime}$ and the opposite situation should occur. Failure at this stage could be due to poor solder joints, leaky diodes or slight tracking between the Veroboard copper strips.

## THE PLAYING BOARD

The playing board consists of a matrix of 644 -way sockets. Each side utilises two of the pin sockets and the connections to each side are similar and may be considered separately.

One of the pin sockets acts as a power feed and is connected to the negative line through a series resistor and a switch contact as in Fig. 4. Current is transferred to the other pin socket through the resistance of the chessman as shown in Fig. 5 and to the multivibrator through a second series resistor and switch contact. The two switches (1-pole. 8-way) act as Rank and File co-ordinates and locate the


## COMPONENTS

## BATTLE CIRCUIT

Resistors


## Miscellaneous

LP1, LP2 6V, 60 mA
S1 Single pole ON/OFF (pushbutton type)
B1 PP7, PP9 or similar
Veroboard
PLAYING BOARD
Resistors

| Resistors |  |
| :--- | :---: |
| R101, R115 | $4.4 \mathrm{k} \Omega$ |
| R102, R116 | $3 \cdot 3 \mathrm{k} \Omega$ |
| R103, R117 | $2 \cdot 7 \mathrm{k} \Omega$ |
| R104, R108, R118, R128 | $2 \cdot 2 \mathrm{k} \Omega$ |
| $\left.\begin{array}{l}\text { R105, R109, R114 } \\ \text { R119, R122, R127 } \\ \text { R106, R110, R113, R120, }\end{array}\right\}$ | $1.7 \mathrm{k} \Omega$ |
| R123, R126 <br> R107, R111, R112, R121, | $1.2 \mathrm{k} \Omega$ |
| R124, R125 | $1.0 \mathrm{k} \Omega$ |
| RX (28 off) | $10 \mathrm{k} \Omega$ |

## Miscellaneous

Four Phono sockets
64 four-way sockets
4 single-pole 8 -way wafer switches
Wire, hardboard, 2 in $\times \frac{1}{2}$ in section wood, glue, screws, panel pins.


Fig. 4. Wiring of the playing board and Rank \& File selection switches. Only the sockets at the periphery have been shown but, of course, there is a socket in each square, wired in a similar manner to all the others
chessmen which are to take part in the duel. The combined resistance of the two series resistors reduces the base biasing current in proportion to the chessman's distance from its King s square.

In addition to the series resistance and the chessman, each Rank and File is connected to its neighbouring Rank or File by a $10 \mathrm{k} \Omega$ resistor so that a parallel path exists through every other friendly chessman. Thus the number and closeness of the other chessmen modify the final value of the biasing resistance; in effect, they lend their support.

## BALANCING

Variations in the chessmen, the weapons, and the playing board resistors are quite in order and may be interpreted as the usual random variations to be found in any army. However, the battle simulator must be balanced so that should an identical situation occur, neither side has a better chance of winning. The four variable resistors VR1 to VR4 are used to balance the circuit.

Starting with the multivibrators temporarily solder a $4.7 \mathrm{k} \Omega$ resistor to each biasing network at 1 and 2 in turn, using 1 per cent or 5 per cent resistor if possible. Using a voltmeter across R4 or R13, count the number of oscillations in a ten second interval. Using the variable resistors, VRI or VR4 alter the frequencies until they are, as nearly as possible, equal.

To balance the score counters, temporarily solder two $2 \cdot 2 \mathrm{M} \Omega$ resistors across points $\mathrm{XX}^{\prime}$ and $\mathrm{YY}^{\prime}$. Switch on the simulator and note which lamp goes out permanently first. Alter VR2 and VR3 until only a small change in each preset changes the lamp which goes out. In between each try, it will be necessary to switch off and discharge C4 and C5.

## THE PLAYING BOARD

The playing board circuit is shown in Fig. 4 together with details of switch connections. The 64 sockets are arranged in an $8 \times 8$ array on a hardboard mounting which forms the top of the circuit cabinet and the playing board. The four single-pole, eight-way switches are mounted two at each end, the press on, press off mains switch, the lamps and phono sockets can be mounted in any convenient position. The line and interconnecting resistors are suspended in the wiring. The wiring can be tied into looms and attached to the inside of the wooden sides with wiring clips. The sockets can be attached with Araldite.

The sides and ends of the cabinet are fastened to the hardboard top with glue and panel pins. The bottom, another piece of hardboard, is secured to the sides with a few screws so that it can be removed for servicing. Dimensions have not been given since size and proportions will vary with the constructor.


Fig. 5. A suggested form of chessman and weapon

| CHESSMEN |  |
| :---: | :---: |
| Resistors |  |
| King | $0 \Omega$ (link) |
| Queen | $330 \Omega$ |
| Knight | $1.2 \mathrm{k} \Omega$ |
| Bishop | $2.7 \mathrm{k} \Omega$ |
| Rook (Castle) | $5.6 \mathrm{k} \Omega$ |
| Pawn | 8.2k $\Omega$ |
| Miscellaneous Holders | 32 four-way plug bases |
| WEAPONS |  |
| Resistors |  |
| Sword | 2.2M $\Omega$ |
| Dagger | 5 M S |
| Mace | $22 \mathrm{M} \Omega$ |
| Miscellaneous Plugs | Phono plugs in quantity to suit |

## CHESSMEN AND WEAPONS

In the present example the chessmen are constructed on the four-way plug bases by soldering the correct resistors between the requisite pins as in Fig. 5 and Aralditing a length of plastic tube onto the base. The tube length may be varied to indicate the power of the piece; say three inches for a King reducing to one inch for a pawn. The name of the chessmen should be marked on the outside of the tube. The weapons are constructed similarly, using the phono plugs. Remember the Queening rule for pawns which successfully reach the opposite side of the board and make two Queens for each side.

The more ambitious might wish to mount genuine chessmen on the bases or perhaps even make pieces of their own devising. There are plenty of techniques available today to do this.

## FINAL TESTING

When all the wiring is complete and the circuit board secured inside with small brackets, the complete unit may be tested. Switch both sets of coordinate switches to Rank 1, File 8 and place a pawn
from each side at this position. Switch on the simulator and observe the rate at which the lights flash. Switch off and move the pawns to square: Rank 8, File 1 and switch on; the lights should now flash more slowly. Try the same tests with Kings; the same thing should happen except that the rate of flashing will be higher in both cases. Sometime during these tests a win will be indicated; when this happens, switch off and wait for the score capacitors to discharge, or alternatively discharge by shorting while switched off.
Try adding other pieces to the board in different positions and note the different flashing frequencies of each combination. In some cases the difference will be hard to distinguish since the alteration will be small.

## RULES OF PLAY

The chessmen move as for conventional chess and for the same distances.

When one player wishes to take an opposing piece and gain the occupied square, the attacking player must verbally challenge the other. Attacking and defending chessmen should be indicated and the defender cannot retreat from the challenge.

Rank and File co-ordinates are set on the switches to indicate the squares holding each piece. Weapons may be selected at any time and plugged into the phono sockets. The battle simulator is now switched on and the two lamps will fiash alternately for, typically, 20 or 30 seconds. The light which remains on after the flashing has ceased indicates the winner who can now occupy the disputed square. The loser is removed from the game and the winner confiscates the loser's weapons for his own arsenal.

## TACTICS

The interaction of Player/Chessman/Position/ Weapons/Morale alters the - probability of any particular piece winning and if two roughly equal chessmen are opposed it is virtually impossible to predict a duel's outcome. The tactics used in Battle Chess can be effective in placing a player in a stronger position.

For example, wherever possible arrange to do battle as near as possible to your own King's square or try to arrange that your own combatant is close to a number of friendly pieces-the more powerful, the better.

Force battles to take place within 5 or 10 minutes of a previously successful battle or fight a delaying action for a similar time if a battle has just been lost. This avoids or uses the morale charge on the capacitor.

Of course there are the obvious comments like "don"t throw pieces away in needless battles, they may be useful for support if not for combat" and finally always remember, you may win a battle but lose the war.

## notes

Although the rules of play suggested follow the rules of conventional chess, the equipment can be used to simulate modern battle conditions quite successfully. The playing board may be marked out with a map, and infantry, tanks, missiles can be substituted for pawns, knights, rooks, etc. In this case, the constructor is left to formulate his own rules.

By R. A. Penfold

AN audio compressor is an audio amplifier which is designed to provide a constant output level, from a wide variety of input levels. Thus it is sometimes referred to as a constant volume amplifier. It merely consists of an audio amplifier which is fitted with some form of automatic gain control.

## AUDIO COMPRESSION

Reasons for using audio compression vary, as it can be used in several applications. It is often used in tape recording when something such as a debate is to be recorded, and only one microphone is to be used.

The use of compression obviates the need to re-adjust the recording level each time a different person speaks, as, once the level is set for one speaker, the correct modulation depth will be obtained for all the others. This is of course providing that all the speakers are close enough to the microphone, to provide a sufficient output to operate the compressor. This technique also removes the possibility of overmodulation at unexpectedly high volume levels.


Fig. 1. Drain current plotted against drain to source voltage for à typical $\boldsymbol{n}$-channel f.e.t.

Speech compression is used in some amateur transmitters in order to maintain a high average modulation level, without running the risk of overmodulating an a.m. transmitter, or exceeding the maximum power rating of the power amplifier of an S.S.B. transmitter.

Simple peak clipping circuits are sometimes used instead, but these introduce a comparatively high degreee of distortion, and are not as effective.

## USING AN F,E.T.

When subject to a low voltage between the drain, and source terminals, an f.e.t. exhibits the characteristic of an ordinary resistor. This is illustrated in Fig. 1, which shows typical transfer characteristics of an $n$-channel f.e.t., at various gate voltages.

It will be seen that the value of the resistor formed by the f.e.t. can be varied by altering the gate bias voltage. It can be varied from a few hundred ohms to many megohms.

## THE CIRCUIT

A circuit diagram of an audio compressor utilising an f.e.t. in a voltage controlled attenuator is shown in Fig. 2. The input impedance to the unit is high (typically 2.5 megohm), and is suitable for use with a crystal microphone. The output is at a low impedance, and will drive virtually any amplifier. For low level inputs (i.e. below the level at which compression begins) a voltage gain of about 275 is available with the gain control at maximum.

In order to obtain the required high input impedance, the input transistor, TR1, is operated in the emitter follower mode. This is direct coupled to TR2, which is a common emitter amplifier.

For TRI to produce a very high input impedance it must have a fairly high impedance in its emitter circuit. R4 is therefore used to raise the input impedance to TR2, in order to achieve this.

The bootstrapping technique has been employed in order to virtually eliminate the shunting effect the biasing resistors, R1, R2, and R3 would otherwise have on the input impedance. C3 is the bootstrapping capacitor.

Transistors TR1 and TR2 are used mainly as a buffer amplifier, and provide only a small voltage gain.

## VOLTAGE CONTROLLED ATTENUATOR

The output from TR2 is fed via C4 to the voltage controlled attenuator. R7 and R8 form a tap on the main supply rail, and produce a suitably low supply voltage for the f.e.t. TR3. The drain to source impedance of TR3, and R9 form an attenuator.

With no negative bias at TR3 gate, the drain to source impedance is very low, and the attenuation factor of the circuit is very low. By giving a negative bias at TR3 gate, the drain to source impedance can be greatly increased, and the attenuation factor of the circuit thus also greatly increased. A voltage controlled attenuator is thus formed.
The output from the attenuator is fed via C5 to the input of a very high gain common emitter amplifier, TR4, which is followed by an emitter follower stage,
tens of ohms. This will have a negligible effect upon the attenuation factor of the circuit.

Raising the input level slightly will increase the bias voltage, and due to the logarithmic relationship between bias voltage, and drain to source resistance, this will cause a much larger increase in this resistance, say a few hundred ohms. This will result in a noticeable, although still only small increase in the attenuation factor of TR3 and R9.

## INPUT LEVEL

It is at this point that raising the input level will begin to have a very noticeable effect on the voltage controlled attenuator, as only a very small change in bias is required to cause an increase of several kilohms in the drain to source resistance of TR3. Thus an increase in the input level causes the gain of the amplifier to drop considerably, and so reduce the output level. The output level will therefore tend to remain almost constant, even though the input level may vary considerably, providing the


Fig. 2. Circuit diagram of the complete Audio Compressor

TR5. From the emitter of TR5, some of the signal is fed via C8 to the volume control, VRI, and then to the output socket. The remainder of the signal is used to produce the biasing voltage for the attenuator.

## RECTIFYING CIRCUIT

It is fed via C6, and R14 to a rectifying circuit, consisting of D1 and D2. This arrangement is used as it provides a fast attack speed, but with a long decay. C7 smoothes the a.f. half cycles to a d.c. negative bias, which is then fed to the gate terminal of the f.e.t.

There is not a linear relationship between the gate bias voltage, and the drain to source resistance of the f.e.t. With a low level input, only a small bias voltage will be produced, and this will only alter the value of the resistor formed by TR3 by a few
input is above the level at which compression commences.
Even with quite high input levels (up to about 0.25 V r.m.s.) there will be only a small degree of distortion in the circuit. The use of modern silicon transistors in the input stage ensures a low noise level.

## TIME CONSTANT

The attack of the a.g.c. circuit is very fast, being virtually instantaneous, but the time constant capacitor, C7, produces a fairly long decay time ,(about two seconds). For most applications this is very desirable, as it prevents the gain from rising during brief pauses in the signal, and the noise which would subsequently accompany this.

However, the decay time can be altered to suit individual requirements by altering the value of C 7 , the larger its value, the longer the decay time.


Fig. 3. Layout of the components on the Veroboard panel and interconnections to the other components. Note breaks in copper strips

## GUMMAVME. .




## CONSTRUCTION

Constructional requirements will vary widely, as some constructors may wish to build the unit as an integral part of some piece of equipment, while others may wish to build it as a self-contained unit, as was the prototype. In either case the Veroboard layout shown in Fig. 3 can be used. 0.lin matrix board is used, and the copper strips run lengthwise. These are cut at a number of points as detailed in the diagram.

A 7 in $\times 4$ in $\times 1.5$ in aluminium chassis fitted with a base plate is used as a case for the prototype. The Veroboard panel is mounted on stand-off insulators in order to hold it a little way clear of the metal case. A PP6 battery is used to power the unit, this particular type being a good fit in the case, and has virtually its shelf life with normal use.

Phono sockets were use for SK1, and SK2 on the prototype, but almost any type of two way socket is of course suitable. Due to the high input impedance of the unit it is essential that the input lead is screened, in order to avoid unwanted noise pick up.


Fig. 4. Graph showing the relationship between the input and output voltages of the Audio Compressor. It can be seen that while the input changes from 2 to 50 millivolts the output only changes by 25 per cent

## RESULTS

A graph of the results obtained on the prototype compressor is shown in Fig. 4. This shows input voltage versus output voltage. With an input of 1 mV or less the gain is fairly constant at about 275 , or a little less. Above this the gain decreases slightly as the input voltage is raised, until it reaches about 2.6 mV , and increasing the input voltage above this level has very little effect upon the output.


A selection of readers' suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.
This is YOUR page and any idea published will be awarded payment according to its merits.

## LIGHT-OPERATED POWER CONTROLLER

THE circuit shown in Fig. I will control the a.c. power delivered to a mains load according to the level of light incident on a light-dependent resistor PCC1.|An advantage of this circuit is its extreme simplicity and consequent ease of construction.

As the mains voltage rises the capacitor Cl is charged via the potentiometer formed by R1 and PCCl . When the voltage across Cl rises sufficiently, the bi-directional trigger diode Dl breaks down and Cl is discharged into the gate of the triac CSR1, switching it into its low resistance state. The combination of a $0.1 \mu \mathrm{~F}$ capacitor and the breakover voltage of the diac of around $\pm 30 \mathrm{~V}$ gives a sufficient pulse to ensure the triggering of most commonly available triacs. The diac also ensures that the maximum voltage rating of the LDR is not exceeded. Since the triac and the diac are bi-directional this situation occurs on both positive and negative-going half-cycles of the mains supply, the triac returning to its high resistance state at each zero point in the cycle.
The point in each half-cycle at which the triac is fired is determined by the incident light on PCC1, full power being delivered to the load when this point occurs very early in the cycle-i.e. with the LDR in

## REED RELAY CURRENT TRIP

ASIMPLE over current protection device for a series regulated power suppply can be made using a thyristor and a reed relay. The circuit diagram is shown in Fig. 1.

When a fault current appears across VR1 and R1, R2, diode D1 conducts a positive going signal to the gate of the thyristor CSR 1 which triggers it on. The reed relay then closes applying a negative voltage to the base of TRI cutting it off and reducing the output voltage to zero.

The action of this cutout is much faster than a fuse thus protecting the sensitive semiconductors in the circuit being supplied. The lamp LP1 will show a fault condition until the SCR is shorted by SI. If the fault is still present the thyristor will fire cutting off the supply again.

The trip current is set by VR1. The reed relay is a miniature type about an inch long with 250 turns of 32 s.w.g. enamelled copper wire wound onto the reed relay body.
G. Daddy,

Hull.
darkness. The power that can be controlled by the circuit depends only on the ratings of the triac and on the adequacy of its heat sink.

Components L1, L2, C2, R2 provide suppression of the radio interference generated by the switching of the triac and also protect the triac against mainsborne transients to which these devices are highly susceptible. Note that R2 is necessary to limit the discharge current of C2 which might otherwise exceed the peak surge current of the triac when it fires, and destroy it.

Owing to the triac's low tolerance to over-voltage transients, this circuit can not be recommended for use with motor loads without the incorporation of additional protection. If circuit values are altered, to change the light threshold or the operating range of the unit for example, care should be taken not to exceed the current/power ratings of the LDR, the diac and the triac gate.

Circuit values are such that incident daylight will result in no power being applied to the load, while the level of light around dusk will give full power.
I. Page,

London, S.W. 13.



Many cars have reversing lights fitted which are operated by a switch on the dash; this must by law be illuminated, but I have found it quite easy to leave the switch on accidentally for periods during the daytime.
I therefore devised the circuit shown in Fig. 1. This is wired in parallel with the reversing light(s), and sounds a warning if left on for more than about a minute When power is applied, Cl slowly charges up through VR1 and TR1, the latter remaining switched on until C1 is almost fully charged. TR2 is a conventional oscillator, but cannot operate until TRI turns off, when it produces a fairly loud tone in the speaker.

Components are not critical. TR1 can be any silicon pnp transistor, and any of the OC72/75/81/ 82 range will work as TR2. Transformer T1 is a small audio output transformer out of an old transistor radio.

The circuit was made up on a small piece of tagboard, and mounted behind the dashboard.
D. L. Atkin.

Sheffield.


## SUN JC-TS THE EARTH

Soler stoems release an snormaus amonet of energy which can affect the spin ot the eath on its axis. Some ter years ag= the French astrenonxer. A. Danjon, announced that the very large soler fare of 1959, sigaificantly afiected he length of the cay. At tha tne re was not ve-g well supparted by colleagues.

Howewr. the very energetic sclar storm uhich occurred in August 1972 shewec an evea grzater effect on ite zarth. A change of more than 10 millieeconds was recorded This paticular solar s:orm is the greatest that ras been recordad during re 37 C years that observations have bean mate.

The forn. which kegan or Auguant e, started with very pro nounced sun spot acivity and con tinuec far a week. At that time there were frobes orbiting the eath and sun ard the Pioneer IC spazecraf. wa: ol is way towards Jupiter It was thus a time when an enormous amount of deta was io henc to record the solar azivity.

The rucorded cosmic rays and the cjected slasma measusements have enebicd an estimace to be madz of the manner in whech the magnetos shert is affezied. The storm abo gave a clue to the mechanism irvolved. There is always a short interval be.wzen the time of zorm and tre cl that the earth rezives The heory is that the partoles and te flasma dis turb the atmosphelic circulation. thereby affecting the zarth's spin.

## CHANDLER WOBBEE

Trat the lergth of day s increasing has been tnown or a long time since it can be deternined by obser*ation The reain effect is from the tidal action $b z w z e n$ the sun-zarth roen system. Another condition that affects the length of day is the Chandler nobble.

Namec after its =is iovener. the poles of the earth wobble agaiast the star bazl-ground in a regular way over a period of fourteer. months. The affect is by ne means well underslood but it is though: to be due to the fluidic centre core of the earti. The wobble effect on the slowing down of ite earth's rotation is superimposed on the main tidal valiation.

In adjition to the Chancler wobse tere is still another effece and that is the seasoral variation of the large scale atrospheriz movemen:s Here again ite actual mechanism s not fully understood though is is moted thet this effec: is on te ane scale as that of the solar ac. vity.


## CHINOOK WINDS

The stec-ng winds which blow on the castern slopes of the Rocky Mourtains are known as the Chinces winds and in some areas do much Jamage as there has been no wav in which io predict when these pincis will come. It is thought that the winds are related to the temperatuse inversion layer of the atmosphere on the windward side of the Continental Divide.

It is now theught that the Chincek wind of the Northern Plains. is produced because the weight of the air causes a compression which in turn raises the tempera:ure. While this is helpfully welectred in some places it is $70 t$ in ofzers.

## EAR:-I WARNING

To rovide some means of predicticn. audio methods are being tried out and powerful audio pu ses are jeirg directed at the atrosphere. These pulses transmitted verticaly upwards every twenty second: at a frequency of $1,000 \mathrm{~Hz}$. are refected by the inversion layars. High power is required because some of the temperature inversion layer: are as high as $7,000 \mathrm{ft}$.

By asing a doppler sounder is hoped to measure the profiles of the wincs. The very obvious advantage of this, in addition to warning. s the passible effects of polla-ion sarried by air. Siting lactories and other process plants could be fredshermined and a possible reduction of pollution effects controlled

## ASTEROIDS

The earth seems to have a wider influence in the solar system than was first thought There are from time to tme rejorts that sertain asteroids come close to the earth and but for the grace of cosmic law the home planet would be exposed to a catastrophe. It has now been established that certain of the asteroids are specifically involved.

The asteroids which have been the subject of special studs, are the Apollo group sonsisting at Toro and Geographos. which czoss the zarth's crbit. and three of the Amor zroup. Eros, Amor and Ivar. which cross Mars orti. These areroids all have orbits round the sun but. are perturbed in tese orbis oy the influence of the pianets.

Of the five asteroids named Geographos is oerturbed onlv at random. The others have regular zeriodical perturjations ir respect of the earth's ortital period The way in waich ths occurs is rather complex for there is libratior in he sense that the whole patiern of the orbit rocks about the earth-sun ine. Libration or rocking implies a periodic variation in the orbital period byt the peiod can oscillate without libration.

## ORBITAL PERIODS

The periods itat emerge are in he ratic five eath periods to eght Toro which wouc result in a period of 150 years were i: not complicated by a relation with Venus. This ratio of 5 Verius periccs to 13 Toro gives \& 180 year period. The Torc orbit is therefore an unetable one ard it E expectel tha: this may change $a$ fter 2200 A.D.
The complicated nature of these -ariations of ore body relative to enother is further indicatec by the act that the exth perturb; Eros once every sever years and also the asteroid Amor is involved. in the case of lvar twa ferturbations lake place in each 28 years. Th s has a libration of 300 years 1 Cd is the nost stable of the cribits.

## TAILPIECE

It seems that there is to be a turther revival of the Volkovsky theories. Claims are being made as 10 confirmation of such events as the Jupiter radiations and te magaetic remanence $n$ the olde- moon focks with a suggestion that the main craters an. 1 valleys are no more than 300 y yers old.

At any rate ne one can say that the astronomical world ane tha: of space is dull.

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ADI 62 pnd germanium medium power $32 p$ $42 p$ AFI39 pno germanium UHF 49p
NPN: BClO7 13p, BCl08 12p, BCl09 13p, BCl67 $11 \mathrm{p}, \mathrm{BCI} 68$ 10p, BCI69 IIp.
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| :--- | :--- | :---: | :---: |
| $C$ | $1 / 20 W$ | $5 \%$ | $82 \Omega-220 K \Omega$ |
| $C$ | $1 / 8 W$ | $5 \%$ | $4.7 \Omega-470 K \Omega$ |
| $C$ | $1 / 4 W$ | $5 \%$ | $4.7 \Omega-10 M \Omega$ |
| $C$ | $1 / 2 W$ | $5 \%$ | $4.7 \Omega-10 M \Omega$ |
| $C$ | $1 / 2 W$ | $5 \%$ | $4.7 \Omega-10 M \Omega$ |
| $M O$ | $1 W$ | $10 \%$ | $10 \Omega 1 / 20 \Omega$ |
| $W W$ | $0.22 \Omega-3.9 \Omega$ |  |  |
| $W W$ | $3 W$ | $5 \%$ | $1 \Omega-10 K \Omega$ |
| $W W$ | $7 W$ | $5 \%$ | $1 \Omega-10 K \Omega$ |

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| :--- | :---: | :---: | :---: |
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| E24 | 1 | 0.9 | 0.75 nett |
| E12 | 1 | 0.9 | 0.75 nett |
| E24 | 1.2 | 1 | 0.95 nett |
| E12 | 2.5 | 2 | 1.6 nett |
| E24 | 4 | 3 | 2 nett |
| E12 | 7 | 7 | 6 |
| E12 | 7 | 7 | 6 |
| E12 | 9 | 9 | 8 |

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## Strictly

## by K. Lenton-Smith

THE piano, without which no home was complete several decades ago, is now becoming a rarity because of lack of living room space and other home entertainment attractions. This could be one of the reasons that the guitar is often the choice of younger members of the household.

For those wishing to take up a keyboard instrument, the Electric Piano is ideal as it occupies very little room.

## ELECTRIC PIANO

The majority of these instruments suffer from only one disadvantage musically in that the keyboard is usually a standard five octave (organ) manual, against the seven octaves of a conventional piano. Nevertheless, five octaves are probably sufficient for performing many piano pieces. The cost of a compact electric piano may be well under $£ 200$ and, with preamplified output, it is small enough to be easily carried in one hand.

The percussive attack of a conventional piano will readily penetrate a semi-detached house's party wall: the volume of its electronic counterpart may be closely controlled. Earphones may be used for practising late in the evening. The electric piano allows changes of timbre, such as harpsicord, honky-tonk, etc.

Like the majority of commercial electronic organs, the generated waveform is square. The usual arrangement is a Hartley (sine wave) oscillator, followed by a series of Eccles-Jordan frequency dividers (square wave) for each of the 12 chromatic notes of the scale. The dividers are invariably integrated circuits as these are cheaper and smaller than discrete components and there is a saving in labour costs.
Piano tuners introduce small frequency variations to give an ordinary piano added brilliance: from middle " $C$ ', notes above are progressively sharpened slightly, those below flattened. A system of electronic frequency division-where tuning must be mathematically accurate-would thus not appear to be ideal. In practice, however, this discreparicy is not noticeable and, given a good amplification system (which could well be the domestic hi-fi), a most realistic piano results.

## KEYING METHODS

Diode-keying is normally used in electric pianos as this keying method can produce the attack/decay characteristics of a piano fairly closely. Keying transients are practically eliminated by diode-keying and precious metal keyswitches are not vital.

Some instruments employ a changeover keyswitch where a capacitor is kept charged while the key is at rest, depression of the key connecting the charged capacitor to the gating circuit. Filtering is required to round up the square waveform somewhat, usually in the form of a passive low pass filter for "piano" tone.
The keyboard may be split by a special control so that a "walking bass' may be played without drowning the melody line. The Italianmade "Instapiano" is an attractive example, retailing at about $£ 160$ before VAT. It measures approximately $48 \mathrm{in} \times 10 \mathrm{in} \times 4 \mathrm{in}$.

## PLAYING TECHNIQUE

The electric piano has a number of advantages, not least that there is plenty of published piano music available. Though it may take a little getting used to, pianists have no difficulty in accustoming themselves to the electric piano.

Where two manuals and a pedal clavier are concerned, printed music is not so readily available-unless the player particularly likes Bach and Handel.

The pianist who turns to the organ has some formidable problems: as long as a note is held it will sound,
unlike the automatic decay of the piano/electric piano. Thus, attempting to play piano scores on an organ results in disaster-and what does one do with the pedals except "double" the left hand part?

Any constructional project is a challenge to get the beast working! Those who build musical instruments have a further challenge: they have to master the keyboard, and organists who like lighter music will find threestave scores few and far between.

## CHORD SYMBOL BASIS

A number of readers will have built the P.E. Organ, or have access to an organ, and may have experienced difficulty buying music. The usual light music score includes the piano part, guitar symbols, vocal line and chord symbols; the last two are the important items for the organist. With practice, it is possible to play both manuals and pedals using this information only, on sight.

Memorising what each chord symbol involves might, at first, appear to be a mammoth task. Relatively few key signatures are used in popular music as the publishers have to bear in mind the transposing instruments (normally $B$ flat and $E$ flat) which may be involved. The same chords appear frequently and can be memorised as easily as the resistor colour code! But it must be admitted that the "bridge" often moves into a strange key and demands quick thinking.

The right hand part (melody) should be registered in a way that makes the tune fairly incisive; single notes are often sufficient and, where chords are used, the melody should still stand out.

The left hand part is the problem and should be considered along with the pedals; accompaniment registration should be quieter and less clear cut than the upper manual. The root of the chord symbol can be used for the pedal on the downbeats, at least for a start, alternating with the fifth (i.e., $C-G$ ) except where a diminished chord is indicated. The left hand plays the notes indicated by the chord symbol, according to the time signature and rhythm, using an "anchor note" if possible.

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This month EE features the construction of a Waa-Waa Pedal, a Slave Flash for photographers, and an Electronic Doorbell.
"It's all quite simple with Everyday Electronics. August issue on sale Friday, July 20. 15p.

# PE Sound Syutheriser 7  :ill mominioi reah level ilger 

## By G.D.SHAW

THis month the Ring Modulator, Peak Level Meter circuit and Reverberation Amplifier will be described.

## THE RING MODULATOR

With the ring modulator the combination of tones follows a complex inter-relationship in which each frequency is continuously compared with and modified by the other. The resultant output provides a tone which consists of the sum and difference of the two constituent frequencies appearing at the same time and irrespective of the phase angle relationship. A typical output waveform is illustrated in Fig. 7.1

A simplified version of a transistorised ring modulator manufactured in integrated circuit form by Silicon General, the SG3402N, is shown in Fig. 7.2 and it will be seen that the device consists essentially of a pair of cross-coupled differential pairs jointly controlled by a third. Two inputs-carrier and modulator-are required and it is important to differentiate between them since the input characteristics are dissimilar. Application of equal amplitude signals to both inputs will provide an output showing about 3 dB voltage gain over either input. Removal of the modulator with the carrier still applied will result in attenuation of the output signal by about 50 dB but if the input situation is reversed the output attenuation is only about 35 dB .

Although designed primarily for communications work the SG3402N is capable of working satisfactorily at quite low audio frequencies by the simple expedient of increasing the value of the input and decoupling capacitors.

The frequency response of the prototype Ring Modulator is shown in Fig. 7.3 and will be seen to be effectively flat over most of the audio frequency spectrum. The theoretical circuit is shown in Fig. 7.4.

The maximum input signal to the SG3402N should not normally exceed 50 mV and thus resistive attenuators are employed to raise the signal level, at the input sockets, to one more compatible with the signal level normally routed around the Synthesiser. With the value of resistors employed in the attenuators the maximum input signal at the sockets is thus 500 mV .

IC2 serves to amplify the output to about 1.5 V at the rated input levels and measured at an input frequency to both channels of 1 kHz . The ring modulator shares a circuit board with the peak level meter and the board layout is shown in Fig. 7.5.


Fig. 7.1. Typical output waveform of the Ring Modulator


Fig. 7.2. Schematic of a transistorised Ring Modulator

## RANGE OF SOUNDS PRODUCED

The type of modulation produced by the ring modulator is wholly unique and thus also is the range of sounds which can be achieved. If two pure tones are modulated together and one of them is reduced in frequency the resultant output would follow the pattern shown in the table below which relates the sum and difference output frequencies with the carrier and modulator input frequencies.

| Frequency (Hz) |  |  |  |  |  |  |  |  |
| :--- | ---: | ---: | ---: | ---: | ---: | ---: | ---: | :---: |
| Carrier | 700 | 600 | 500 | 400 | 300 | 200 | 100 |  |
| Modulator | 400 | 400 | 400 | 400 | 400 | 400 | 400 |  |
| Sum | 1100 | 1000 | 900 | 800 | 700 | 600 | 500 |  |
| Difference | 300 | 200 | 100 | 0 | 100 | 200 | 300 |  |

It can be seen that, whereas the resultant sum reduces in frequency at the same rate as the carrier, the frequency reduces until it reaches zero (carrier and modulator frequencies equal) and then, as the carrier continues to fall to a frequency lower than that of the modulator, the difference frequency begins to increase at a proportional rate.
When the inputs to the Ring Modulator carry harmonics in addition to the pure tones then further series of frequency relationships are established for each of the component harmonics relative to one another and to the respective fundamentals.
When the inputs to the Ring Modulator are of symmetrical triangular waveform, such as those generated by the v.c.o., an extremely complex set of frequency relationships is established due to the fact that, in common with the square wave, the triangular waveform consists of a long series of odd harmonics.

## SOUNDS PRODUCED

The Ring Modulator may be used in many fascinating ways from the creation of truly "out of this world" sounds, the transposition of tones, belllike sounds, Dalek voices and so on.
In the transposition of tones the only stipulation is that the modulating frequency should be higher than the carrier (this latter input consisting of the signal for treatment). For any range of carrier frequencies the modulator frequency has to be calculated or determined empirically, to provide the best overall effect.

An interesting experiment can be carried out by cascading two Ring Modulators. The first uses the v.c.o. output to drive carrier and modulator inputs so that the output is the octave, or second harmonic, of the v.c.o. frequency. The output of the first Ring Modulator is used to drive the modulator input of the second whilst the carrier input is derived direct from the v.c.o. Thus the difference frequency of the second Ring Modulator will follow, exactly, the performance of the v.c.o. while the sum frequency will approximate to a quarter-tone accompaniment about $1 \frac{1}{2}$ octaves higher. There are very wide possibilities for further experiment in this kind of mode.

A true bell tone is very complex and is difficult to imitate with exactitude. A fairly close approximation may be achieved by adjusting two v.c.o.s to a mid-range frequency, say 4 kHz , such that there is a slow beat between them. One v.c.o. then drives the carrier and the other v.c.o. the modulator input of the Ring Modulator. A very important characteristic of the bell-like sound lies in its envelope presentation and this will be dealt with in detail in next month's article.



Fig. 7.5. Simple resistive attenuator for use with the Ring Modulator


Modulator

## COMPONENTS . .

## PEAK LEVEL METER CIRCUIT

| Resistors |  |  |
| :--- | :--- | :---: |
| R1 | $91 \mathrm{k} \Omega$ |  |
| R2-R3 | $20 \mathrm{k} \Omega$ (2 off) |  |
| R4 | $91 \mathrm{k} \Omega$ |  |
| R5-R6 | $3.3 \mathrm{k} \Omega$ (2 off) |  |
| R7 | $91 \mathrm{k} \Omega$ |  |
| R8 | $110 \mathrm{k} \Omega$ |  |
| All $5 \%$ | $\frac{1}{2}$ watt carbon |  |

## Capacitors

C1-C2 1,500pF (2 off)
C3-C4 $22 \mu \mathrm{~F} 16 \mathrm{~V}$ tantalum
Potentiometers
VR1 $50 \mathrm{k} \Omega$ carbon preset

## Diodes

D1-D4 IN914 (4 off)
Integrated Circuits
IC1-IC2 741C (2 off)

## Miscellaneous

ME1 MR38P SEW panel meter (G. W. Smith Ltd.)
SK3 2 mm miniature socket

## RING MODULATOR

| Resistors |  |  |  |
| :---: | :--- | :--- | :--- |
| R1 | $1.8 \mathrm{k} \Omega$ | R5 | $10 \mathrm{k} \Omega$ |
| R2 | $200 \Omega$ | R6 | $10 \mathrm{k} \Omega$ |
| R3 | $1.8 \mathrm{k} \Omega$ | R7 | $200 \mathrm{k} \Omega$ |
| R4 | $200 \Omega$ | R8 | $10 \mathrm{k} \Omega$ |

All $5 \% \frac{1}{2}$ watt carbon

## Capacitors



All tantalum

## Potentiometers

VR1 100k $\Omega$ carbon preset
VR2 10kS2 miniature moulded carbon
Integrated Circuits
IC1 SG3402N
IC2 741C

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The unit is constructed on profes sional fibre-glass printed.circuit board material and uses latest fullwave triac circuitry. There is a master-level control, cogether with independent sensitivity controls for each channel. The original minimum been redesigned permitring have use as faders; allowing dimming from max. to zero at the turn of a knob. R.F.I. suppression is now incorporated as standard as well as provision for D.J. "Pulse-Flash" controls. The choice of two inputs enables operation from both high and low power amplifiers. Max. power 1.5 kW per channel at
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Dalek voices are produced by modulating a modified speech waveform at about $15-20 \mathrm{~Hz}$. Speech, and certain types of music waveforms, can present a very peaky characteristic. The peaks are multiplied and added to in the Ring Modulator and thus, if the signal is remodulated several times, or if the initial frequency is high enough, the final output contains a large proportion of sound which bears a remarkable resemblance to white noise. The cure for this problem is to limit the dynamic range of the signal.

Although exact details for such a procedure lie outside the scope of this series a passable method is to feed the offending signal to one of the input amplifiers and, observing the output on the oscilloscope, adjust the gain of the amplifier so that a large proportion of the peaks are suitably clipped. If insufficient gain is available to allow an adequate degree of clipping the input amplifiers may be cascaded. The achievement of clipping means, of course, that the amplifier output signals are swinging between the positive and negative saturation levels and it will be necessary to attenuate the signal quite considerably. Fig. 7.5 shows a simple resistive attenuator which will give a signal of about 500 mV from a 28 V source.

## CONSTRUCTION

Construction of the Ring Modulator is quite straightforward and the only critical requirement lies with the observation of polarity of the tantalum capacitors. Reversal of any of the capacitors will result in noisy operation and, in the case of the output capacitor, no operation at all. Tantalum capacitors have been specified in order to conserve space and there is no reason why 10 V electrolytics should not be used with an alternative layout.

## SETTING UP

Setting up the Ring Modulator consists only of providing a modulation balance. Set VR1 to its mid position and apply a common sine wave signal to both inputs. The output of the ring modulator will be a sine wave which is twice the frequency of the applied signal. If the modulation is out of balance alternate peaks of the output signal will be at different amplitudes. VR1 should be adjusted to bring the peaks into line at which point the modulation is balanced.

It is a wise precaution to repeat this measurement from time to time to adjust for settling down changes in the circuit.

## THE PEAK LEVEL METER

In the prototype Synthesiser the meter circuit was based on a precision rectifier built around a pair of operational amplifiers arranged in such a way as to eliminate the effect of the diode forward voltage drop. Although the circuit proved to be very responsive it was found, in practice, to present a number of disadvantages. In the case of a.c. signals the meter would read only the r.m.s. value and although it was possible to determine the actual peak-to-peak value by application of a form factor for known wave shapes the determination of peak-to-peak values for complex waveforms proved to be a very hit and miss affair.

In a similar manner, when endeavouring to set up reasonably accurate programming voltage levels, the rapid response of the meter frequently made it difficult to establish the peak value with any certainty.


In consequence it was decided to redesign the meter circuit to provide a peak reading facility which would be independent of waveform configuration and which would have a reasonably long decay time to ease the establishment of transient level readings. The final circuit is shown in Fig. 7.7. ICI and its associated circuitry is used to read the positive going peaks while IC2 deals with the negative side of the signal. The operation of the circuit is as follows.

## CIRCUIT OPERATION

The input sensitivity of the circuit at the i.c. is about 200 mV for full scale deflection of the meter. A positive going peak of this value appearing at the input of IC1 will swing the output positive to a level determined by the values of R4 and R5, about 6.2 V with the values shown, and capacitor C3 will charge at a rate determined essentially by the effective current output of the i.c. The charging time for C 3 is thus rather less than 2 mS .

If, after the capacitor is charged, the 200 mV peak is replaced by a lower amplitude peak the tendency would be for the i.c. to swing hard negative due to the effect of the positive voltage from the capacitor appearing at the inverting input via R4. This tendency is prevented by D2 which limits the negative excursion of the output to about 700 mV .

Capacitor C3 discharges through R4 +R 5 in parallel with $R 8+R$ (meter) and with the values shown takes about I second. Cl serves to decouple a.c. from the feedback loop and thus effectively extends the accurate range of the meter to about 15 kHz .

The negative reading side of the circuit around IC2 operates in the same way. The circuit is adjusted to give full scale deflection with inputs of 1.0 V and 0.5 V by means of the attenuator R1,2, 3 and VR1.

In using the meter it should be borne in mind that the peak values recorded represent only half the total peak to peak value of the signal being measured
and this applies whether the signal is symmetrical or assymmetrical about zero. When measuring low frequency programming signals of greater than 1 Hz the minimum reading of the meter between peaks does not represent the lowest level of programming signal.

This particular meter circuit can be used to measure the peak level of single transients of not less than 2 mS duration.

## ADVANTAGES

In tape recording the peak level meter scores heavily over the more conventionally employed v.u. meter. This latter meter will record what is essentially the mean value of signal presented to the recording amplifier and if, as is generally the practice, the mean level is kept to about -3 dB transient peaks are likely to be clipped or otherwise distorted. The use of a peak level meter, on the other hand, enables the peaks to be kept within the limits imposed by the recording amplifier and thus enhances the overall quality of the recording.

## COMPONENTS . . .



## THE REVERBERATION AMPLIFIER

Reverberation, or re-echo, in varying degrees is a characteristic observed in the majority of large halls, public buildings, cathedrals and so on. In a properly designed and proportioned hall the inherent reverberation characteristic can provide a high degree of enhancement to the sounds occurring therein.


Fig. 7.8. Block diagram of Reverberation Amplifier


There are a number of ways in which a reverberation characteristic may be simulated and for the Synthesiser the spring line has been adopted. The spring line consists essentially of a coiled wire, usually steel, which is supported at each end in a compliant mounting. At the supported ends of the wire are fitted electro-magnetic transducers. The line driving transducer is excited by an electrical signal and the varying field produced causes mechanical wave motion to be set up in the spring line. When the wave motion reaches the far end of the line it sets up an electrical disturbance in the line output transducer which is, in turn, amplified and added to the original signal.

Part of the original mechanical wave motion is reflected back down the spring line where it serves to modify further on-coming waves.

Because a mechanical wave motion travels much more slowly than its electrical counterpart the signals received by the line output transducer are delayed in relation to their source, such delay being a function of the length of wire used in the spring line. Thus
the mixing of the mechanically routed signal with the source signal constitutes the addition of an echo. However, since the wave motion, once initiated, travels back and forth along the line until its amplitude becomes negligible, multiple echoes are received and added to the original signal.

The spring provides a further useful feature having its origin in the fundamental resonance of the system. When the driving signal passes through the frequency at which the system resonates the output is characterised by a sudden increase in amplitude which can be as much as three times the value of the normal mean signal. Similarly when the input signal passes through any of the harmonics of the resonant frequency there is an increase in output signal amplitude, and this despite the fact that the useful range of the HR42 spring line, specified for this project, is limited at its upper end to about 4 kHz . In the prototype unit quite high resonant peaks were occurring at up to 25 kHz .

The combination of multiple echoes and varying amplitude imparts a very useful "singing" quality to an otherwise uninteresting sound.


Fig. 7.9. Circuit diagram of Reverberation Amplifier


The spring line unit is attached to the p.s.u. sub-frame

## DESIGN CONSIDERATIONS

In the prototype the line driving amplifier employed a single transistor operating in what was effectively Class A. The current consumption was thus quite high even in the quiescent state and small variations in the power supply rails gave rise to noise in the system which was apparent when the line was not being driven hard. For the modular version of the Synthesiser therefore the Reverberation Amplifier was redesigned to reduce current consumption, reduce hum and noise to negligible proportions and to enable a complete divorcing of the voltage controlled part of the system so that the amplifier may be built as a separate unit outside the Synthesiser project altogether.

A fortuitous advantage of the re-design provides sufficient power capability to drive two HR42 or one HR42 and one HR162 spring lines in series. It is also theoretically possible to drive up to four of the above spring lines in any combination although this latter method has not been tested.

The advantage in using more than one spring line in the system lies in the fact that it is rare for two units to have identical resonances and delays and thus two or more units can only improve the overall reverberation characteristic.

## CIRCUIT ACTION

The Reverberation Amplifier is shown in block form in Fig. 7.8 and the circuit diagram in Fig. 7.9. The input signal is led to a buffer stage, which has a gain of about six, and the output is divided to drive the line amplifier and output mixer. The line driving amplifier consists of a pre-amplifier built around a 741 and having a gain of about five which, in turn, provides drive to a complementary pair of output transistors having a current gain of about a hundred and arranged in what may be described as a modified form of Class B. The output from this latter stage provides drive direct to the spring line through a current limiting resistor.

The output from the spring line is amplified by another 741 having a gain of about nine and then led to the input of the voltage controlled amplifier based on the Motorola MFC6040. This latter device has a mraximum gain of 13 dB and a maximum attenuation of about 77 dB relative to the input signal which should not normally exceed 500 mV r.m.s. The overall gain of the spring line route is thus arranged so that when the line is being driven hard at a non-resonant frequency, and with the v.c.a. at maximum gain, the output of the v.c.a. is equal to the output of the buffer stage and thus the mixer is receiving equal components of reverberated and non-reverberated signal.

The choice of component values for C 10 and R26 may be arrived at by experiment on the basis of the measured response of individual spring lines. To limit the gain to 6 dB a value of 2.5 kilohms for R26 will suffice.

The value of ClO is calculated on the basis of the frequency at which the 6 dB gain is required. For a frequency of 15 kHz the value of C 10 is 1 nF .

The v.c.a. is controlled by a separate 741 arranged in the differential mode. The non-inverting



Fig. 7.10. Board assembly of Reverberation Amplifier
input is driven by a positive voltage derived from the divider R6, R8, and VR1. The high and low ends of VR1 are thus at 3.0 V and 1.75 V respectively. The inverting input of the 741 is driven by a control voltage which should have a swing of 2.5 V maximum.

With VRI at its minimum setting a control voltage swinging from zero to -2.5 V will have the effect of attenuating the output of the MFC6040. With VR1 at its maximum setting a control voltage swinging from zero to +2.5 V will have the effect of amplifying the output of the MFC6040 from -77 dB to +13 dB relative to its input signal. The inverting input of the 741 acting as control amplifier is prewired to a ramp generator which will, of course, provide the first mode of v.c.a. operation described due to its negative going output.

If external automatic control of reverberation is not required it is essential that a grounded jack plug be inserted into the control socket otherwise the output of the control amplifier will be insufficient to swing the MFC6040 through its full range.
The current sink at the control input of the MFC6040 is specified as being 2 mA but on several


Fig. 7.11. Front Panel Wiring


Fig.7.12. Two methods of providing external control using a potentiometer and voltage control input. The relevant response curves are located below each circuit. Here the 0 dB reference equals a 13 dB gain
tested in this mode of operation quite a wide variation in current sink was noted, the highest being 25 mA . Consequently it is prudent to provide a series transistor on the output of the control amplifier, with overall feedback, in order that the 741 is not overloaded. The effect of overload will not necessarily damage the 741 but it could result in a reduction of the output voltage swing which would, in turn, affect the operation of the 6040 .

## OMITTING THE V.C.A.

For some possible applications the use of voltage control will not be required and, in these instances, the MFC6040 and associated control amplifier may be omitted from the circuit entirely. In these circumstances the gain of the line output amplifier. IC5, will have to be increased by a factor of 0.33 if equal reverberated and non-reverberated components are required at the mixer. The output of IC5 is, of course. led direct to C6 on the mixer in these latter circumstances.

## CONSTRUCTION

Fig. 7.10 illustrates the recommended circuit board layout. Construction is quite straightforward and the only setting up required lies in checking'the signal levels at the outputs of the buffer, line driver and line output amplifiers to ensure that equal signal components from both sources are presented at the mixer when the line is being driven hard at a suitable non-resonant frequency.
Adjustment of the gain of the line output amplifier may be necessary and is dependent upon the mechanical attenuation of the line which may differ unit to unit.

Overall construction of the module should generally follow the pattern previously described and the wiring of the components on the front panel and McMurdo plug are shown in Fig. 7.11.
In this module the McMurdo plug has insufficient ways to carry all the necessary signals and two extra leads are required to carry the control and audio signals to the reverberation amplifier. Reference to the block diagram in the first part of the series will show that the control signal is derived from RG2 while the audio signal is derived from the right channel of the output amplifiers yet to be described. Suitable leads should be run from the respective McMurdo sockets on these latter modules to a point immediately adjacent the left hand Vero endplate and secured to the connector mounting rail by a tie of lacing cord. From this point they should run along the end plate and be trimmed so that they protrude about three inches beyond the front face of the mainframe. Terminated in 1 mm miniature plugs, they can be mated with their respective sockets on the Ring Modulator circuit board when the finished module is being inserted into the mainframe.

A fully comprehensive revision of all module interconnections will appear in part nine of the current series.

For the benefit of constructors who may wish to explore the possibilities of the MFC6040, Fig. 7.12 shows two possible methods of providing external control with the resistance attenuation curve and the control voltage attenuation curve of this very versatile device.
In Part 3, VRZ is 1002.
Next month : The Envelope Shaper will be described.


DESPITE the recent adoption of the title International London Electronic Components Show, the RECMF of old seems still to be so called both on the official catalogue and by many of the exhibitors and visitors. However, this year it bears little resemblance to the Radio Show of old. Perhaps the word "subdued" would describe the feeling best.
In size the 1973 event was not much more than half its previous size, a factor with very mixed blessings. Obviously less to see but, to the footsore visitor, the possibility of seeing most of what was there without total self destruction. And, with less stands, the opportunity to see around, particularly from the gallery which of recent years has tended to become rather crowded.
The last three years have left their mark on the industry very clearly, as the drop in the number of exhibitors showed and it is a shame that some of the more famous semiconductor manufacturers chose not to attend. In fact the usual bustle, not just of visitors, but of new. products looking for markets seemed to be muted almost out of existence.

Generally the atmosphere was one of trading rather than of displaying goods, many of the stands were totally bereft of components or equipment and, in this sense were a sad disappointment to the engineer looking for new "toys". However, the current world shortage situation in many component areas probably goes a long way to explaining this.

## RADIO RECEIVER I.C.s

Perhaps one thing which stands out from the show is the advances being made in integrated circuits. It is almost possible to build anything using i.c. techniques these days and several of the manufacturers are trying to prove just that.

Fairchild displayed a set of interesting chips which can make up an a.m. or f.m. radio with stereo facilities. These included their 720 single chip a.m. receiver, the 753 f.m. gain block, the 758 phase-
lock loop stereo decoder, the 3075 f.m. i.f. amplifier and limiter, detector and audio pre-amplifier, and the 706 audio power amplifier with a 5 W capacity.

Both mono and stereo were demonstrated using either one or two of the power amplifier chips and considering the nature of the halls at Olympia, reception was very impressive.

To an extent of course, this type of display is really more of an application demonstration than a display of new concepts since the basics of such systems have been around for some time. However, it does illustrate the way in which we can expect developments to go as more and more roles are taken over by the chip.

## FILTERS USING I.C.s

Take the case of Siliconix of Swansea, well known in the semiconductor market. They are investigating the ability of multi-amplifier chips to provide variable filters, of great value in audio control and generation applications.

They have already developed some prototype circuits using their L114 triple operational amplifier and have established that it is possible to obtain a tuning range from 0 to 10 kHz , that high Qs up to 400 can be obtained, that high and low bandpass is simultaneously possible and that both gain and $Q$ are easily programmable.
Indeed, they have even developed a digitally programmed filter using one of their own DG 507 chips to do the logic control.

With all this compression of componentry on to chips one almost wonders where the discrete component went. But one only has to look at any circuit board to see the still tremendous need for power rail droppers, couplers and so on.

## MULTI-PURPOSE INSTRUMENT

On the more constructional side there were a number of interesting items. For the portable instrument constructor there was the Pakit kit multi-purpose instrument. An analogue display and movement is available to fit a moulded plastic
case which has ample room for circuitry and components. Available from Elcometer Instruments Ltd., the Pakit can be bought complete with a rechargeable power supply, self-designed front panel, leather carrying case and printed circuit.

The basic unit with case will probably market for something under $£ 20$.

## CASES AND PACKAGING

For the inveterate casemaker there was a display of coated aluminium panel material from Bakelite Xylonite Ltd., which provides both strong covering. attractive colours and, as an added bonus fairly high electrical insulation by virtue of the plastic coating material.

For the man who is always losing components on the bench there was an interesting adaption of the plastic packaging market. Dunlavin Converters have developed their Ducon Carripallet system for packaging delicate materials to the point where it can be used for other applications.

Thus the Carripallet is a multicavity foam plastic pad measuring $400 \times 400 \times 50 \mathrm{~mm}$ and with 25 cells. Laid on its back. which is a cardboard support layer, it can be used as a multi-compartment tray. As the material is flexible foam plastic it will not damage delicate parts and indeed components can be stuck upright in it if required.

Normally items of this type are sold in large quantity only, but we understand that single sheets are available and will probably cost about fl each.

Weller were at the show with their latest low voltage temperaturecontrolled soldering pencils (no longer irons we see), the W-MCP available with a variety of tip shapes.

## PANEL METERS

On the meter front several socalled panel meters were on display including the Dinline 50, the first such product to come from the Avo stable. The name is based on the rectangular styling which follows the DIN (IEC 51) specification,

A digital panel meter is something that, only a short while ago, would have been regarded as an instrument rather than a component. By miniaturisation and recent price reductions in digital i.c.s, Analogic are able to offer a versatile 3 -digit digital panel meter at only $£ 25$ in quantity. Designated the 2530 , this unit can measure, display and transmit voltages and currents of either polarity. The 2530 has fully floating inputs just like any analogue meter so that measurements with respect to any arbitrary level in a circuit can be made.

A filament readout is used for good readability and low cost. This meter can be incorporated in many types of instrument giving a really competitive alternative to analogue measuring instruments.

West Hyde displayed their Contil digital panel meter card with three digit display in Atron tubes and costing less than $£ 25$.

Integrated Photomatrix showed their digital panel meter kit using a MOS LSI chip and l.e.d. display which is available for $£ 36.75$. Of course the IPL unit is considerably smaller than many other displays on show.

## LARGE SCALE INTEGRATION

One of the fastest-growing areas of semiconductor technology must be the MOS large scale integration (LSI) field. All the major companies appear to be competing to see who can cram the most MOSTS into a single 24 -pin package.

One of the latest contenders in this competition is the Motorola MCM6571L character general i.c. It contains a read only memory of 8,192 bits which can produce 128 different characters, including upper and lower case and Greek symbols, each character being formed by a matrix of seven horizontal and nine vertical dots. As well as the stored characters themselves there are 128 bits which are used to automatically control each character position so that the "tails" of letters such as $\mathrm{p}, \mathrm{q}$, and j come below the base line as in normal typewritten material.
Another piece of news in the MOS LSI field comes from General Instrument Microelectronics who announced that slashing price reductions have been made on their C500 calculator i.c. Its new price of $£ 13 \cdot 70$ ( 1 off) represents an $£ 18$ drop from its previous value. G.I. hope that this new low price will encourage designers to regard the calculator i.c. as just another component for use in many types of instrument where arithmetic operations are to be carried out. For instance, they could be used in a weighing scales which automatically displays the price of the goods after the price per pound is typed in.

## NEW LOW-PRICE FOR L.E.D. DISPLAYS

While MOS manufacturers are aiming at miniaturisation, the display device manufacturers are aiming at bigger and brighter components. One of the innovators of l.e.d. displays, Monsanto, announced new low prices for their displays.
Single l.e.d.s can now be bought for 5 p in large quantities making it possible for manufacturers to use them in such applications as diagnostic lamps on printed circuit boards, panel lamps, and battery "low" indicators.
On the numeric display side the MAN5, a green seven-segment device is down to $£ 3.99$ from $£ 9$ ( 1 off) and the MAN64A, a 0.4 in numeric display down to $£ 4.27$ from £7•85.

## NEW TIMER INTEGRATED CIRCUIT

A nother example of large scale integration, but this time combining digital and analogue functions was on show at the Elremco stand. Designated the LR171E, this i.c. has an enormous range of applications because of its inherent flexibility.
It uses a digital counter so that timing ranges of seconds, hours or even weeks can easily be obtained. A digital-to-analogue converter is used to give a current output which means that a cheap meter can be used to give an indication of time elapsed.
It has eight operational modes. delayed on, delay interval etc., and has three digital outputs which give indications at $\frac{1}{8}, \frac{1}{4}$ and $\frac{1}{2}$ of a preset time period.

The i.c. is TTL compatible and has integral output drivers for a triac or SCR. Price is $£ 12$ for one off.

## RUSSIAN COMPONENTS

Many manufacturers are finding difficulty obtaining such ubiquitous components as resistors and capacitors and so it is not really surprising to see more and more imported components creeping into instruments such as calculators.

Z \& I Aero Services are importers of Russian resistors and capacitors and supply both to industry and non-professional users. The components are cheap, clearly marked with their values (not colour coded) and readily obtainable.

## NOVEL BREADBOARDING AID

A novel breadboarding system which has great potential in the amateur field was shown by Critchley Bros. Ltd. Manufactured by the German firm of Christel Wainwright, the system is called "MiniMounts." Each Mini-Mount is a
small rigid board with a copper pattern on one side and adhesive on the other. Components are soldered onto the boards which are then positioned anywhere on a convenient baseboard (which could be copper-plated to give a good ground plane), and stuck in place.

This system has many advantages over other similar systems: no holes need be drilled; the adhesive holds the Mini-Mounts firmly in place yet they can be moved if the circuit requires; components and Mini, Mounts can be re-used if care is taken.
Mini-Mounts to take DIL i.c.s, and other such components are available so the system gives great scope to the designer.

## NEW SHAPE FOR LAMINATIONS

Though by no means a spectacular breakthrough, the new design for transformer laminations by Kent Insulations shows how old and tested designs can be improved with a little ingenuity. Instead of the usual " $E$ " shape the new type has a tapered centre arm so that excellent mechanical and magnetic contact is made when the two halves are fitted together.

## MULTI-MEMORY MACHINE

On the Advance Electronics stand and in fact using the same case as the Advance calculators was a new accounting aid from Phytron. Called the Analysis 14 this calculator incorporates 13 accumulating memories each of which can be debited or credited at any time simply by selecting the required store with one of thirteen keys. Only credit and debit (i.e. addition and subtraction) are available, the manufacturers suggesting that this instrument is to complement, rather than replace, the normal desk calculator which can multiply and divide.

At $£ 210$ this calculator cannot really be termed inexpensive by modern standards and one wonders whether a machine without a printout is really useful in accounting, where mistakes cost money.

## CONCLUSIONS

One wonders why a star attraction like the actual Apollo capsule "Charlie Brown" which circled the moon four years ago, which was on show on the Livinstone Hire stand together with a piece of moonrock. received so little publicity. This could have attracted great crowds, was this what the organisers were afraid of?
At a time when electronics is developing so fast, the show was not up to expectations and one can only hope that the companies will stop hiding their lights under bushels and give us some really interesting shows in the future.

## STARTING NEXT MONTH! PRACTICAL ELECTRONICS Proudly Presents the



The PE RONDO is a total system incorporating the very latest technology and it will be described in full with all constructional details in a series of articles starting next month.
The receiver incorporates a varicap f.m. tuner with integrated circuit i.f. amplifier, quadrature detector and phase-locked-loop stereodecoder; and a uniquei.c.phase-locked-loopsynchrodynea.m. medium-wave monotuner. Optional i.c. matrix decoders: CBS SQ quadraphonic decoder or CBS SQ logic-enhanced quadraphonic decoder. Additional decoders will be presented as further systems become viable.
Modular construction techniques give flexibility, whilst the use of staterof-the-art i.c. technology allows all electronics to be housed in one compact unit. The system is completed by four shelf-mounting speakers, construction of which will also be fully described in this series of articles.


# C. 2马 SEMICOMDETORS <br> PART 2 <br> By M. J. Rose (mullard ltd.) <br> <br> POWER DEVICES, PHOTO DEVICES AND INTEGRATED CIRCUITS 

 <br> <br> POWER DEVICES, PHOTO DEVICES AND INTEGRATED CIRCUITS}

The first article described the types of transistor and small-signal diode available. This article considers power devices, photo devices using, or emitting light, and the most revolutionary semiconductor device of all-the integrated circuit.

## POWER DIODES

The amount of power that can be handled by a semiconductor diode is limited by the junction temperature. Provided the heat dissipated within the device can be conducted away so that the maximum permissible junction temperature is not exceeded, the diode will operate satisfactorily. Therefore a power miode should have as large a junction area as possible, and a low thermal resistance to the case. The cooling area can be increased by mounting the diode on a suitably shaped heatsink.

Germanium power diodes were developed using these techniques, and could carry currents of approximately 10A and withstand peak inverse voltages of up to 600 V . The introduction of silicon, however, led to their replacement during the 1960's by silicon diodes with junctions alloyed or diffused with aluminium.

## AVALANCHE DIODES

As the reverse voltage across a junction diode is increased, a voltage is reached where avalanche breakdown occurs, marked by a sudden increase of current. Provided the diode can withstand the current at breakdown, it will recover when the reverse voltage is decreased below the breakdown value. Avalanche diodes are designed to withstand such breakdown currents, and so can be used safely in applications where voltage transients are likely to be encountered.

By the end of the 1960 's other protection devices such as high-speed fuses had been developed so that semiconductor-diode rectifier systems were firmly established in such applications as battery chargers, electroplating and electrolysis processes,

Fig. 8 High-voltage rectifier stack operating at 12 kV and 5 A with natural convection cooling (length approximately 10 in )

and electric furnace supplies. These semiconductor systems occupied less space than the existing systems, had a higher rectifier efficiency, and for the first time presented power engineers with a device that had no wear-out effects.

A high-voltage rectifier stack is shown in the photograph of Fig. 8. Diodes are mounted on heatsinks around a central fixing stud. Such stacks can be cooled by natural convection, or for higher currents by forced-air cooling or immersion in an oil bath.


Fig. 9 Structure and circuit symbol of thyristor


Fig. 10 Phase control using thyristor


## THYRISTORS

The thyristor or controlled silicon rectifier was developed for power control in parallel with the silicon rectifier diode. The rectifier action of the thyristor allows a current to flow in one direction only, but in addition current can only flow when the thyristor has been triggered.
In form, the thyristor is a four-layer pnpn device, as shown in Fig. 9. The circuit symbol is also shown in this figure. If the anode is positive with respect to the cathode, and a positive voltage is applied to the gate, the thyristor conducts. Once conduction has been established, the gate voltage can be removed.

Therefore the thyristor can be triggered by a pulse provided the duration is sufficient to allow the current to be established. The thyristor is made nonconducting by reducing the current to below a holding value.

The method of power control with thyristors is shown by the waveforms in Fig. 10. By varying the trigger angle within the half-cycle (x), the amplitude of the current pulses passed by the thyristor, and hence the power delivered to the load, can be varied. This control technique is called phase control.

A second method of control is burst triggering, used for loads with a high thermal inertia such as furnaces. In this method, complete half-cycles of the mains supply are passed by the thyristor, the ratio of half-cycles passed to those blocked determining the power to the load.

## TRIACS

Another device for power control similar to the thyristor is the triac or bidirectional thyristor. This device is equivalent to two thyristors connected in inverse-parallel with a common gate connection. The circuit symbol for a triac is shown in Fig. 11. A current will flow through the device when the gate is sufficiently positive or negative with respect to


Fig. 12 Thyristor stack for operation on 440 V three-phase mains to control 110A per phase

terminal mtl, the direction of current flow depending on the relative polarities of mtl and mt 2 .

The currents that could be handled by thyristors, and the inverse voltages they could withstand, increased during the 1960 's as the manufacturing techniques were improved. Present-day thyristors can handle currents up to 1000 A and withstand inverse voltages of over 2 kV . Protection devices have been developed as with rectifier diodes to ensure reliable operation under practical conditions.

A typical thyristor stack with thyristors connected in a bridge configuration for the control of power to a load is shown in Fig. 12. The thyristors are mounted on heatsinks.

## THE DIAC

The diac, or bidirectional diode thyristor, is a useful trigger device for thyristors and triacs. It uses avalanche breakdown, but as the characteristic in Fig. 13 shows, the voltage decreases after breakdown so that the gate circuit is not overloaded on triggering.

## SILICON CONTROLLED SWITCH

Another four-layer pnpn device is the silicon controlled switch or SCS. Unlike the thyristor and triac, both intermediate layers of the SCS are accessible making it a four-terminal device. The structure and circuit symbol are shown in Fig. 14.

The SCS (like the thyristor) has two stable states: conducting and non-conducting. The SCS can be used in two circuit configurations. In one, the load is connected in the anode gate circuit so that the SCS operates as a four-terminal device. In the other, the load is in the anode circuit and the anode gate is not connected. The SCS then acts as a lowpower thyristor or three-terminal device.

## PHOTOTRANSISTORS AND PHOTODIODES

Light falling on a junction in a semiconductor diode or transistor affects the current through the device. The energy of the light dislodges electrons and so increases the number of carriers available at the junction.

Constructive use of this effect is made in photodiodes and phototransistors where the change in current with light can be used in such applications as light meters and alarm systems.

Other semiconductor materials exhibit a change of resistance with light, and this effect is used in photoconductive cells (or light-dependent resistors). The choice of semiconductor material determines which part of the spectrum the device responds to, for example cadmium sulphide responds to visible light while lead sulphide is used for infrared detectors.

## LIGHT EMITTING DIODES

Another type of photodevice is the electroluminescent or light-emitting diode (Fig. 15). This device is made from gallium arsenide or gallium arsenide phosphide, and when a sufficiently high current (a few milliamperes) is passed through, light is emitted. Such diodes can be used as indicator lights directly coupled into, for example, computing systems.

## INTEGRATED CIRCUITS

Of all the semiconductor devices that followed the invention of the transistor, the most revolutionary both in reducing the size of equipment and improving reliability is the integrated circuit.

The problems of manufacturing different circuit elements on the same silicon chip were overcome so that integrated circuits that were both practicable and economic became available by the mid-1960's.

Today two types of integrated circuit (i.c.) are available, the bipolar and MOS, each with their advantages and disadvantages for particular applications.

## BIPOLAR I.C.

The bipolar i.c., as the name implies, uses bipolar transistors manufactured by the planar process. Diodes are formed by a single diffusion, capacitors by using a reverse-biased diode junction, and resistors by a single diffusion like a stretched-out diode with connections at both ends. The main problem with the manufacture of bipolar i.c.s is isolation between components.

## MOS INTEGRATED CIRCUITS

The transistor used in MOS i.c.s is a field-effect transistor, the MOSFET or MOST. Because MOS i.c.s are almost exclusively used in digital applications, an MOST can form the load for a nother MOST, the transistors are directly coupled, and the capacitances on which information is stored are formed by the gate capacitances of the MOSTs
themselves. Thus only transistors and connections need to be formed on the chip.

One advantage of MOS i.c.s over the bipolar type already mentioned, is the fact that no isolating diffusion is needed on the chip. In addition, an MOST is smaller than the equivalent bipolar transistor. Both these reasons lead to a higher packing density being achieved with MOS i.c.s. On the other hand, bipolar i.c.s have a higher operating speed, and can drive higher current and capacitive loads which MOS i.c.s cannot do without interface circuits.

Thus the choice of MOS or bipolar i.c. may well depend on the requirements of the application rather than any clear-cut advantage of a particular type.

In general, it can be said that small scale integration (SSI) is rarely economical with MOS i.c.s so that gate packs and flip-flops will use bipolar i.c.s.

Large scale integration (LSI) for such devices as random-access memories (RAMs) and read-only memories (ROMs) will use MOS i.c.s. The choice for medium scale integration (MSI) will depend on the application.

## COMPLEMENTARY MOS

A limitation on the use of MOS i.c.s occurs through the use of field-effect transistors. The current-carrying channel for the transistor is formed in the substrate, and so normally only $p$-channel or $n$-channel devices but not both can be formed on any one i.c.

To overcome this, a technique called complementary symmetry MOS or CMOS has been developed. Areas of $p$-material are diffused into an $n$-type substrate so that both $n$-channel and $p$-channel MOSTs can be formed.

More diffusions are required for CMOS than with normal MOS i.c.s, and a lower packing density results. On the other hand, there are considerable advantages for the user, particularly higher operating speeds and lower dissipation.


Fig. 16 Integrated circuit chip compared with ordinary sewing needle, the chip being $1.5 \times$ 3 mm


FAMILY TREE OF SEMICONDUCTOR DEVICES

## LINEAR I.C.s

The i.c.s described above are digital circuits. Later in the 1960's linear i.c.s were developed. In terms of the number of devices contained, these i.c.s are more complex than the equivalent discrete stages they replace, although cheaper and with better performance. Today a wide range of linear i.c.s is available covering r.f. and i.f. amplifiers, operational amplifiers, TV signal-processing circuits and audio amplifiers with output powers up to several watts.

The reduction in size possible with an i.c. is impressive, typified by such photographs as that in Fig. 16 showing a silicon chip containing over 120 devices passing through the eye of an ordinary sewing needle.

## COLLECTOR DIFFUSION ISOLATION

Another process which overcomes many of the disadvantages of conventional bipolar i.c.s has been developed by Ferranti from an American idea. Known as the collector diffusion isolation (CDI) process, it makes use of thin epitaxial layers but needs only five masking processes making it comparable to MOS technology in simplicity.

Fig. 17 shows the structure of a CDI transistor. The process uses a p-type substrate into which buried low resistivity $n+$ areas are diffused where each resistor, transistor or diode is to be formed. A thin epitaxial $p$-type layer is then diffused. The collector diffusion is then made producing low resistivity $n+$ channels round each component. This diffusion serves three purposes: to make contact with the buried $n+$ area which forms the collector; to provide isolation between components; and to define base and resistor areas.

A shallow $p$-type layer is diffused over the whole slice to define resistor values. A shallow emitter diffusion then follows.

The CDI process reduces transistor areas by up to a third and also enables digital and linear circuits to be combined on one slice. The main disadvantage is that there is no pnp transistor available, though


Fig. 17 Structure of a CDI transistor (Ferranti)
a $p$-channel f.e.t. under development should overcome this difficulty.

## CONCLUSIONS

From the original low-power low-frequency transistor have developed transistors capable of operating high in the radio frequencies, transistors handling powers of over 100 W , transistors capable of switching wave-forms with rise times of 1 ns. Signal diodes operating in the microwave frequencies have been developed, and diodes and thyristors capable of operating on high-voltage supplies controlling powers measured in megawatts. Devices reacting to and producing light are available, and devices containing a complete computer processing system on a chip only 3.5 mm square.

Although the transistor was the fore-runner, the most revolutionary device may well be the integrated circuit which has brought a new concept into electronic circuit design. The thermionic valve (apart from its more specialised forms such as klystrons and magnetrons) had a commercial life of about 35 years. It may well be that the discrete transistor apart from more specialised forms like photodevices will have a shorter life.


THE PAST few years have seen more and more houscholders tackling home wiring installation work. When the work is completed and before the mains can be permanently connected certain safety checks have to be made. This article and project deal with a very useful instrument that will measure the insulation resistance of the wiring.

Poor insulation causes current to flow between the line and earth or neutral. This current in turn will generate heat in the wiring, which if of sufficient temperature could cause fuse blowing or, more seriously, a fire.

Unfortunately, most do-it-yourself wiring enthusiasts do not possess the means for measuring leakage currents at high voltage. Commercially available instruments are expensive and secondhand units are not cheap either. The traditional instrument consists of a hand driven generator that causes a high voltage to appear across a lead terminating in a couple of croc clips, a meter registers the insulation resistance. More recently, electronic testers have tended to replace the hand driven types.


Fig. 1. Circuit diagram of the Megohmmeter

## DESIGN CONSIDERATIONS

The a.c. voltage in the United Kingdom is 240 volts r.m.s. Since we are interested in peak voltage, and $V_{\text {peak }}=V_{\text {r.m.s. }} \times 1.414$ the test voltage generator will need to be at least 340 volts. In practice, however, regulations require that a minimum of 500 volts be used.

The 550 volt megohmmeter here described satisfies the twin requirements of the home electrican; it is easy to build and the price is low compared to commercially available units. Push button operation is employed as this prevents inadvertent battery run down due to a switch being left on. All of the components are readily available.

As to the choice of a suitable transistor high voltage generator; the demands made by the Megohmmeter dictate that a sine wave oscillator type d.c. convertor be used. There are three broad types of d.c. convertor that could be used, namely: ringing choke, multivibrator and sine wave.

Each of these circuits produce an oscillating voltage that is stepped up to a higher voltage by means of a transformer and then rectified to give a d.c. voltage at the required level.

Although the least efficient of the three, the sine wave oscillator is the first choice in favour of the ringing choke method when it comes to ease of starting. In economy of components it beats the multivibrator circuit. Since the Megohmmeter does not need to produce more than about 170 microamps under short circuit conditions, a low efficiency high voltage generator does not matter in the least.

## CIRCUIT OPERATION

Transformer TI, TRI and the associated circuitry form a sine wave oscillator whose period of oscillation is determined by the inductance and self capacitance of TI (Fig. 1).

The feedback winding of T t sustains the oscillation and is connected to the junction of R1 and R2 which together with R3 and VR1 set the d.c. bias for TRI. Cl prevents a.c. degeneration. VR1 allows for variation in voltage developed across the collector winding of TI .

The diodes, D1, D2, D3 and D4 plus the capacitors, C2, C3, C4 and C5 comprise a voltage quadrupler circuit. This circuit rectifies the a.c. voltage appearing across L3 and multiplies by four its equivalent d.c. value.

By varying VRI over its whole range the open circuit output at the test leads can be varied from approximately 300 to 650 volts. In practice however, the voltage will be set at 550 for normal use. The wide range does allow for increased versatility and the constructor will be able to make use of this from time to time.

## CONSTRUCTION

Dismantle the Ferroxcube core and carefully mount the bobbin on a suitable arbor - the author used a wheelbrace which had a gear ratio of $3.75: 1$ clamped in a bench vice and a 2 BA screw as the arbor.

Strip a 2 in length of thin pliable plastic sleeve from some spare wire and thread this onto some 41 s.w.g. enamelled copper wire (L3).

Carefully wind 400 turns and finish off L3 by insulating with one turn of Scotch Tape.

Next wind 40 turns of 41 s.w.g. for L2 and similarly insulate. Finally wind 40 turns of 28 s.w.g. for LI and complete the transformer with a layer of tape.

The rest of the construction is quite straightforward (Fig. 2). The only caution is that building the circuit on inferior leaky tagboard is bound to cause difficulty in getting an infinity reading since leakage current will cause a standing error. The ideal case is plastic although metal can be used if care is taken with insulation.

## SETTING UP

Once the circuit has been built, it is first of all necessary to establish the working of the oscillator.


Fig. 2. Layout of the components on the tagboard and interconnections to the transformer, potentiometer, pushbutton and meter.

## componexis

## Resistors

R1 $39 \mathrm{k} \Omega$
R2 $10 \mathrm{k} \Omega$
R3 $100 \Omega$
R4 $2.2 \mathrm{M} \Omega$
All $\frac{1}{4}$ W $10 \%$ carbon

## Potentiometers

VR1 $1 \mathrm{k} \Omega$
VR2 $5 \mathrm{k} \Omega$ preset
Capacitors
C1 $25 \mu \mathrm{~F} 6.4 \mathrm{~V}$ electrolytic
$\mathrm{C} 2,3,4,5 \quad 0.047 \mu \mathrm{~F} 400 \mathrm{~V}$ polyester (4 off)

## Semiconductors

TR1 2N3053
D1, 2, 3, 4 1N4004 or any 400 p.i.v. low current diode (4 off)

## Miscellaneous

S1 Miniature pushbutton
ME1 $\quad 100 \mu$ A f.s.d.
T1 Ferroxcube core type LA1 or equivalent 9 way Tagboard
Crocodile Clips (2 off)
9 V Battery. PP7 or similar
Metal case $4 \mathrm{in} \times 2 \frac{1}{2} \mathrm{in} \times 2 \mathrm{in}$
41 and 28 s.w.g. enamelled copper wire

Short the test clips together and the meter should indicate some value. If no movement is seen the oscillator feedback winding (L.2) has to be reversed. Once the circuit is working, the output should be set to 550 volts.

Connect a high resistance voltmeter between points A and B then adjust VRI for 550 volts. During this operation the test clips must of course be open circuited. The meter can now be calibrated in terms of megohms.

Remove the meter from its case and place it on the working surface having previously cleaned up and dusted down. Carefully remove the scaleplate and paint the reverse side matt white with emulsion paint.

When dry place the scaleplate - with its original markings uppermost-on a piece of cardboard. With a compass find the radius of the scale line and the maximum and minimum positions. Transfer these lines to the blank side. Screw the scaleplate back on the meter.

Short out the test clips and mark the scale with a soft pencil at this point. Now connect 1, 3, 10 and 50 megohm resistors and mark these equivalent points in. A professional job can be done by using plastic film ink for the scale lines and Letraset for the letters and numbers.

## CONCLUSION

The 550 volt insulation tester that has been described should prove to be a valuable addition to the electricians tool kit. There are of course other uses to which it can be put, insulation tests for the electronic engineer being one example.

By making a simple twin pointed probe, relative dampness in wood and plaster can be measured.


## UPTURN

Yesteryear's sobs and groans from industry salesmen were conspicuously absent at the London Electronic Components Show. Nobody seemed to care that the show was smaller than before and, from first reports, less well attended. Those who came were buyers and that was the important thing.

But it wasn't all smiles and sunshine. The spectre of empty order books may have retreated but only to make way for another spectre to give top management some sleepless nights. What is haunting the industry to-day is how to get components made fast enough to meet the demand. Those maior companies which had spare capacity have none today. An even worse fear looming up is a shortage of raw materials.

But, on the whole, things look pretty good. Quipped one exhibitor, "If you can't make a few bucks in these conditions you never will."

This year's Chairman of RECMF, Ronald Bulgin, wore one of the biggest smiles at the show. His publicly quoted family business is still a profit leader in the industry and he will preside as the component industry Chairman in a boom period. What could be better?

Wearing his chairman's hat rather than his company's, he put out a public plea for rationalisation of the exhibition calendar. And he was quite right to do so. In the past six months we have had Electronica in Munich, the Paris Salon and the British show at Olympia. What the industry needs is one big international show a year in Europe with London, Paris and Munich in rotation so that each of the three electronics "capitals" puts on a show every three years.

A sensible idea but unlikely to be accepted by exhibition organisers while they still make huge profits from the present spate of shows. The only hope is that the economic forces of the market place will do what exhortation won't. If enough would-be exhibitors opted out, the exhibition organisers would have to adjust their ideas.

## ROYAL SPOKESMAN

His Royal Highness The Duke of Kent is tipped to succeed Admiral of the Fleet Earl Mountbatten of Burma as an important independent commentator on electronic industry affairs.

As yet, The Duke admits to being "' a complete amateur, hardly knowing the difference between $R$ and C.: But having heard him speak on the subject I find him wellinformed. Naturally, he is briefed in advance of public utterances but I am reliably told that his interest in electronics borders on the enthusiastic and that he is eager to expand his knowledge of the industry not only in its technology but also in its commercial and social impact.

If knowledge is born of experience he should be learning fast. He is a member of the National Electronics Council and opened the London Electronic Components Show and, more recently, Microwave ' 73 at Brighton.

## THE OLD AND THE NEW

This is a good year for anniversaries. Sperry Gyroscope has just celebrated 60 years in the U.K. with an exhibition opened by Prince Philip, Duke of Edinburgh, at the company's Bracknell HQ. Then there's AVO Ltd., celebrating fifty years in coil winding equipment and instruments (the millionth Avometer left the works as lona ago as 1965!).

But what about companies yet unborn? There are plenty on the way to swell the ranks of industry. Expect announcements soon that Jim Griffith, boss of Plastronics, is setting up a manufacturing plant in Germany and that Maurice Hatter, a co-founder of Keyswitch Relays (subsequently sold to Thorn). is moving back into relay manufacture in a plant in Italy. Two new British Euro-babies for the Common Market.

## HI-FI

Cosmocord is looking to hi-fi as a logical expansion based on its long history in pick-up cartridges, audio instruments and, more recently.
head sets and ear protectors. For a start, the company has won the sole U.K. franchise for quality speakers built by Martin in the USA. There is a complete range from about $£ 30$ per pair up to $£ 200$ for a single 100W multiple speaker unit.

Cosmocord tell me that there is a possibility of making some of the Martin range in the U.K. but following the recent introduction of the ACOS "Lustre" pick-up arm (reported to be doing well), it is more likelv that manufacture will be concentrated on tuners and amplifiers to complete the audio chain. Cosmocord marketing men are scanning the market with a keen eye before committing themselves, but if the Martin speakers are to come as part of a Cosmocord hi-fi package my guess is that they are lookina at the top end of the market.

The company has now absorbed the Birch-Stolec thumbwheel switch business - renamed Cosmo--cord-Stolec-which has a turnover of some $£ 250,000$ a vear. In all Cosmocord is targeted for $£ 1.85 \mathrm{mil}$ lion turnover in the fiscal vear just started.

## NOT ALL HONEY

Although the electronics capital goods sector has a more healthy looking order book than last vear. things are still sluggish according to the Electronic Enaineering Association. Sales at $£ 684 \cdot 5$ million. although marginally higher in total than in the previous year, were in real terms, showina a decline said retiring President Dr B. J. O'Kane in his "state of the industry" address at the EEA annual luncheon. Best areas in the vear under review were communications and marine radar, both of which showed gains of better than 20 per cent.

Disturbing and perhaps an uncomfortable experience for RankXerox, brightest and most profitable jewel in the Rank crown, is the impending Monopolies Commission probe. The British inquiry follows the threat to the U.S. parent company of anti-trust action by the U.S. Trade Commission. Curtailing of Rank-Xerox activities could have quite an effect on many of the smaller electronic companies who are engaged in sub-contract work for the company.

Even prosperous Mullard has its black spots. Forecast losses on integrated circuit manufacture this year is $£ 700,000$, following losses of nearly $£ 3$ million in the dast two years. Break-even is forecast by the end of the year and profit by the end of '74, or perhaps a little earlier. Meantime, the really hot lines like colour TV tubes more than make up for the deficiency.


## 3/ CARRY OUT OVER 40 EXPERIMENTS ON BASIC ELECTRONIC CIRCUITS \& SEE HOW THEY WORK, including :



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# ELECTRONORAMA 

## STEPS FORWAŔD IN TIME ....

UNTIL 1970 electronic watches seemed to make very little impact on the market, probably because the manufacturers themselves seemed mostly reluctant to invest in this new technology. However three years ago the major Swiss manufacturers unveiled new electronic models incorporating quartz oscillators and it became clear that the whole industry was actively concerned with "the watch of the future".

## THREE TYPES

Since that time electronics have become the major topic of interest and excitement throughout the watch world. Today there are three types of electronic watches driven by batteries. These are tuning fork, quartz and solid state.

Tuning fork watches employ an acoustic resonator (tuning fork) as a timing device and a motor driven by a transistor circuit. Because of this they do not tick but emit a slight hum. The accuracy of these watches is about one minute a month.

Quartz systems with mechanical display use a quartz crystal which oscillates under the effect of an alternating electric field. These oscillations, through a system of circuits and a motor, control the hands of the watch and provide accuracy within 5 seconds a month.

Solid state electronic models show the time in figures with a digital display instead of hands and have no moving parts. They use a quartz crystal oscillator as a timekeeper and activate a liquid crystal or diode display on the face of the watch. Again these have an accuracy of around 5 seconds a month. In addition to these, there are watches which are better described as electric rather than electronic. They employ a conventional hair spring and a balance wheel as the timing mechanism, but are powered by a battery as opposed to a main spring. Their accuracy is no more than that of a comparable conventional watch.

## SIMPLE SERVICING

The advent of electronic watches offers considerable benefits as substantial improvements in accuracy and reliability can be achieved. In addition, problems of aftersales service will be reduced at a time when servicing is increasing in cost and there are fewer skilled watchmakers, because the regular cleaning and maintenance necessary for conventional watches is either considerably reduced or eliminated for electronic models.

## NEW DESIGN CONCEPTS

Omega, part of SSIH (Société Suisse pour I'Industrie Horlogère SA)-the world's third largest watch manu-facturer-have announced new design concepts for electronic watches, which made their international debut last month at the Swiss Industries Fair in Basle.

The three new watches are the Megaquartz 2400, the Time Computer and the Megasonic 720. The Megaquartz is a quartz watch with a mechanical display. It has a precision reaching $\pm$ one second a month in normal wearthe world's most accurate watch.
The Time Computer is a solid state electronic watch with no moving parts, which shows the time digitally. This model has an accuracy within five seconds a month.

The third new model, the Megasonic 720 , is an original and entirely new development of the basic tuning fork watch, and has an accuracy reaching 10 seconds a month in normal wear.

## THE MEGAQUARTZ 2400

The Megaquartz 2400 heralds a new generation of quartz watches. Announced as the world's most accurate watch it only varies within one second a month. This remarkable increase in precision over earlier quartz watches (about 1 minute a year) is achieved with a crystal vibration of $2,359,296$ times a second.

The first generation of quartz watches used frequencies of up to $65,536 \mathrm{~Hz}$. In order to achieve any further technical progress beyond this point it was necessary to increase frequency up to at least $1,000,000 \mathrm{~Hz}$, but microelectronic technology was not available to make this practical.

Now with the Megaquartz 2400 Omega have developed a unique electronic microcircuit. This has been achieved by using an analogue circuit dividing the quartz frequency, and by the most advanced CMOS technology in the area of integrated circuits of micropower.

One characteristic of the watch woild be important to the "Jet Set", who travel between time zones. In order to maintain the accuracy of the minute and second hands the watch can be immediately adjusted by moving the hour hand alone.

Megaquartz 2400 is now in production, and it is expected that it will be available in the U.K. in 1974, at prices starting from $£ 425$.


Photograph showing the movement of the Megaquartz 2400, claimed to be the world's most accurate watch

Movement of the Megasonic 720 - the first original development on the basic tuning fork watch


The Time Computer. A solid state digital watch with no moving parts

## THE TIME COMPUTER

The Time Computer is a completely solid state digital watch with no moving parts. The display is by red light emitting diodes arranged for four digits.
Time is shown "on demand" by pushing a command button which illuminates the light emitting diodes covered by a synthetic ruby face, chosen for its hardness and filtering qualities. When the command button is pressed the time in hours and minutes is shown for 1.25 seconds and is then replaced by the seconds for as long as the button remains depressed.
Another advance in this timepiece is its unique system for resetting time that allows hours or minutes to be changed independently. This is an obvious advantage for international travellers constantly flying from one time zone to another. Time is changed by inserting a tiny magnetic key into one of the two timeset recesses in the back of the watch, one linked to the minute digits, the other to the hours. To change the hour only, the magnet is placed into the hour recess, when the face lights up to show the hour digits moving forward.

The Time Computer has the equivalent of 1,238 transistors in a surface area of only $3.8 \times 3.8 \mathrm{~mm}$. Its brain is an electronically operated quartz crystal vibrating 32,768 times a second.
The vibrations are counted and the results are fed to the driver decoder circuit, which activates the time display on derpand. The watch is powered by two tiny batteries.

Because the watch has no moving parts there is no need for the oiling and cleaning advised for conventional movements.
The Omega Time Computer will be available in the shops within a few months at around £300.

## MEGASONIC 720

Electronic tuning fork (acoustic resonator) watches are well established and proven on the market. Almost all the major manufacturers offer models which are basically derived from the original invention in the 1950's by Max Hetzel.
The Megasonic 720 has distinct advantages over earlier tuning fork watches. It gives a considerable improvement in accuracy to within $\pm 10$ seconds a month in normal wear, compared with 60 seconds in similar watches. This is achieved by increasing the frequency at which the resonator vibrates to 720 hertz compared to $300 / 360$.
The acoustic resonator is stimulated by an integrated circuit. The movement is transmitted to the train by an original micromotor. The train is driven by magnetic gear, and conventional display shows seconds, minutes, hours, day and date. A mercury battery powers the movement and guarantees it for one year's running.
Its launch is scheduled for 1974 in a new range of models styled Omega Megasonic 720. Retail prices have not yet been finalised, but are expected to be only slightly above existing models.

## TRANSFORMERS

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|  |  | AILABLE |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
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| 100 | 80 | 38 | $8.9 \times 8.0 \times 7.7$ | 18236 | N014 |
| 61 | 100 | 5121 | $10.2 \times 8.9 \times 8.3$ | $2 \cdot 88 \quad 52$ |  |
| 30 | 200 | 981 | $12.0 \times 103 \times 10.0$ | 48352 |  |
| 62 | 250 | 124 | $9.5 \times 12.7 \times 11.4$ | 6.3867 |  |
| 55 | 350 | 1501 | $14.0 \times 10.8 \times 12.4$ | 8.5582 |  |
| 63 | 500 | 2701 | $17.1 \times 11.4 \times 159$ | 1232 |  |
| 92 | 1000 | 400 | $17.8 \times 17.1 \times 21.6$ | 2270 |  |
| 128 | 2000 | 6302 | $24.1 \times 21.6 \times 15.2$ | 3750 |  |
| Ref. | VA | AUTO Weight | SERIES (NOT size cm. | ISOLATED) Auto Tops |  |
|  | (wotts) | ) 16 oz |  |  |  |
| 113 | 20 | 11 | $7.3 \times 4.3 \times 4.4$ | 0-115-210-240 | 0.9322 |
| 64 | 75 | 114 | $7.0 \times 6.4 \times 6.0$ | 0-115-210-240 | 1.8230 |
| 4 | 150 | 30 | $8.9 \times 64 \times 76$ | 0-115-200-220-240 | $2.20 \quad 36$ |
| 66 | 300 | 60 | $10.2 \times 10.2 \times 9.5$ | .. .. | 4.2852 |
| 67 | 500 | 128 | $14.0 \times 10.2 \times 11.4$ | " ${ }^{\text {" }}$ | 6.3567 |
| 84 | 1000 | 160 | $11.4 \times 140 \times 140$ | . . . | 11.5482 |
| 93 | 1500 | 289 | $13.5 \times 14.9 \times 165$ | .. .. | 16.72 |
| 95 | 2000 | $40 \quad 0$ | $178 \times 165 \times 21.6$ | . .. | 21.82 |
| 73 | 3000 | 458 | $17.4 \times 18.1 \times 21.3$ | ,. | 29.70 |

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$\begin{array}{cc}\& & 0 \\ 0.93 & 22\end{array}$
$\begin{array}{ll}0.93 \\ 1.11 & 2\end{array}$
$1.46 \quad 22$ $\begin{array}{ll}1.46 \\ 2.04 & 36 \\ 2.46 & 42\end{array}$ $\begin{array}{ll}2.46 & 42 \\ 2.73 & 52\end{array}$ $\begin{array}{ll}2.73 & 52 \\ 3.23 & 52\end{array}$ $\begin{array}{ll}4.99 & 52 \\ 6.35 & 67\end{array}$ 6.3567
1.7382 Ref. Amps. Weighe Sizecm. 30 VOLT RANGE $\begin{array}{lrr}p & \& & p \\ \& & p \\ 1 & 11 & 22 \\ 1.48 & 36 \\ 2.21 & 36 \\ 2.72 & 42 \\ 3.23 & 52 \\ 4.02 & 52 \\ 4.80 & 52 \\ 6.20 & 67 \\ 7.85 & 67\end{array}$
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N The June issue of Practical Electronics the introduction of a new integrated circuit, the 555 , was discussed in detail from both the theoretical and practical point of view. Only one application as a timer suitable for photography or event control was considered as a constructional project.

Thus we now consider the application of this i.c. to the control of such items as the lights on the Christmas tree or in a window display so as to provide a flashing effect. At the instant the one set of lights is switched off, another set can be switched on if this is desired. The two sets are then illuminated alternately.

## THE CIRCUIT

The type NE555V i.c. which has a dual-in-line encapsulation was used by the writer, but the NE555T in the circular TO-99 encapsulation is equally suitable. Both types have eight connecting leads and contain the equivalent of 23 transistors, two diodes and 16 resistors in a small package.

The 555 is used in the astable mode in this application. In the circuit shown in Fig. 1, the capacitor C1 alternately charges and discharges so that the potential across it varies between $\mathrm{V}_{\mathrm{cc}} / 3$ and $2 \mathrm{~V}_{\mathrm{c}} / 3$. Each time the voltage across Cl falls to $\mathrm{V}_{\mathrm{cc}} / 3$, the 555 is automatically re-triggered by means of the connection to the trigger pin 2 ; the capacitor then commences to charge again.

## TIMING

The charging current flows through both R1 and $R 2$. It can be shown that the time for charging from $\mathrm{V}_{\mathrm{cc}} / 3$ to $2 \mathrm{~V}_{\mathrm{cc}} / 3$ is $0.693\left(R_{1}+R_{2}\right) C_{1}$ seconds where $R_{1}$ and $R_{2}$ are expressed in ohms and $C_{1}$ is expressed in farads. During discharge the current from CI flows through R2 only; thus discharging lakes the shorter time $0.693 R_{2} C_{1}$. The frequency of oscillation is $1.44 /\left(R_{1}+2 R_{2}\right) C_{1}$. The charging time cannot be made shorter than the discharging time.

If one uses the values for $\mathrm{R} 1, \mathrm{R} 2$ and C 1 shown in the circuit, one can calculate that the charging time is 4.8 seconds and the discharging time 3.3
seconds. This should be suitable for the automatic switching of lights in a shop window or on a Christmas tree.

In practice the times will not be exactly equal to the calculated values since the values of the three components will differ somewhat from their marked values. In particular, electrolytic capacitors have very wide tolerances.

The component values can be altered to obtain the desired switching times. However, for applications of this type one does not need to adjust the values critically. One may require shorter times for use in a flashing toy; for example, Cl may be reduced to $2 \mu \mathrm{~F}$ of $1 \mu \mathrm{~F}$. The value of the power supply voltage, $\mathrm{V}_{\mathrm{cc}}$, does not affect the switching times appreciably.

## THE RELAY

The relay remains open whilst the capacitor is charging, but closes during the discharging time. A diode must be placed in parallel with the relay in order to suppress the transient back e.m.f.; the latter is generated across the inductive relay coil when the current ceases to flow through it. If the transient voltage is not suppressed with a diode, it could damage the integrated circuit.


Fig..1. Circuit diagram for the simple i.c. flasher

The writer used an economical microswitch relay type MS1B designed for printed circuit board mounting. It is readily available (through retailers) from Keyswitch Relays Ltd. The value of the power supply voltage, $\mathrm{V}_{\mathrm{cc}}$, used must match the recommended relay operating voltage to within about 20 per cent. If $\mathrm{V}_{\mathrm{cc}}$ is between five and seven volts, an MS1B with a $6 \mathrm{~V}, 50 \mathrm{~mA}$ coil rating should be employed. Alternatively $\mathrm{V}_{\mathrm{cc}}$ may be between 9.5 and $15 \cdot 5 \mathrm{~V}$, in which case the MS1B employed should have a $12 \mathrm{~V}, 26 \mathrm{~mA}$ coil rating.

The MS1B relay has a single group of change-over. contacts which can switch 250 V at up to 5 A in a.c. circuits. This maximum power of 1.25 kW is more than is likely to be required in any shop window of moderate size.

Nevertheless, a larger relay can be used in this circuit if necessary provided that it does not require a current of over 200 mA to operate it. Another type of microswitch relay, the Keyswitch Relay type MS2B, has two pairs of change-over contacts each of which can control a current of up to 2 A in a 250 V a.c. circuit. In d.c. circuits the current ratings of relay contacts are lower $(0.2 \mathrm{~A}$ at 2.50 V and 0.25 A at 100 V for both the MS1B and MS2B).


Fig. 2. Circuit diagram for a suitable power supply

## POWER SUPPLY

The circuit of Fig. 1 can be operated from a small battery. Indeed, this is the most sensible source of power to use in a toy for children. The integrated circuit itself requires a current of about 3 mA (maximum 6 mA ) when $\mathrm{V}_{\mathrm{cc}}$ is 5 V , but the current rises to about 10 mA (maximum 15 mA ) when $\mathrm{V}_{\text {ce }}$ is 15 V . The relay coil current is additional to these values.

When the circuit is used to switch 250 V lamps on and off, it is normally more convenient to employ a small power pack which operates from the mains.


Fig. 3. Board layout for the flasher circuit

## Components

## Resistors

R1 $=220 \mathrm{k} \Omega, 1 / 10 \mathrm{~W}, 10 \%$
R2* 470k $\Omega, 1 / 10 \mathrm{~W}, 10 \%$

## Capacitors

C1* $10 \mu \mathrm{~F}, 15 \mathrm{~V}$ electrolytic

## Miscellaneous

NE555V (or NE555T) Signetics integrated circuit OA47 gold bonded germanium diode. MS1B relay, 6 V or 12 V coil (see text).
Mains three-way input plug if needed.
One or two two-way output connectors (depending on whether one or two sets of lights are to be switched).
Eight pin dual-in-line socket (if NE555V used).
*Values may be altered to obtain desired switching times.

## POWER PACK

T1 Small mains transformer with an output of either about 5 V or about 10 V r.m.s., depending on the relay used (see text).
D1-4 1N4001 or similar or alternatively one bridge rectifier such as REC 41A (RS Components Ltd.).
$250 \mu \mathrm{~F}$ capacitor, 15 V , electrolytic.

The whole system can then be operated from a supply which is switched off at a preset time at night by the normal type of time switch used for controlling the lighting in many shop windows.

A suitable power supply circuit is shown in Fig. 2. The output of the secondary winding of the transformer Tl should be chosen so that it is suitable for the coil operating voltage of the relay used. A 5 V r.m.s. transformer winding is suitable for a relay with a 6 V coil rating, whilst a 10 V r.m.s. winding may be used with a 12 V relay. However, these voltages are not very critical.

The output voltage of the transformer may be rectified by four separate diodes (D1 to D4), such as type 1 N 4001 . Alternatively a single bridge rectifier (such as the RS Components type REC 41A) may be used instead of the four diodes.

## CONSTRUCTION

The whole unit, including the mains power pack, may be placed in a small die-cast metal box. One suitable box of approximate external dimensions $114 \times 89 \times 55 \mathrm{~mm}$ is available from RS Components Ltd., whilst another type of approximate dimensions $119 \times 93 \times 52 \mathrm{~mm}$ is available from Eddystone Radio Ltd.

The metal box should be connected to the mains earth when used with mains equipment for safety reasons. The small mains transformer can be mounted directly on the box, but the remaining components are conveniently mounted on a small circuit board. One can solder directly to the contacts of the 555 integrated circuit, but it is generally more convenient to employ an eight-pin dual-in-line socket if the NE555V is used.

In view of the simplicity of the circuitry and mechanics involved it is deemed unnecessary to discuss construction in greater depth on this project.

# LOGIC TUTOR <br>  

DE AIORGAAN'S THEOREM

FIRSTLY the answer to last month's question. There are various ways of getting the six input AND but they all use the same principle. There is an Associative rule in Boolean algebra which says that if you have a number of variables coupled by the same logical functions then the variables can be grouped together in sub-groups and combined by their function independently; the independent groups can then be coupled together with like functions to produce the final desired effect.
Thus if we want to AND together inputs A, B, C, D, E and F we can carry out the operation in three stages; firstly we AND A with $B$ and $C$ (as a sub-group) then $D$ with $E$ and $F$ as a separate sub-group. Finally we take the outputs of each of the sub-groups and AND them together in a two input gate to give the total effect.

One form of six input AND gate using NANDS is shown in Fig. 4.1. Notice that it is necessary to use a lot of gates to carry out what is basically a very simple function. It would be much more economical-in space and cost-to use a six input NAND followed by an inverter or alternatively convert a four input NAND into a six input version using an expander before inverting.

## DE MORGAN'S THEOREM

Referring to the truth table for the NAND we could say that the output is 1 when $A$ is $O$ OR $B$ is 0 . Remember we are describing the same function as last month but are using a different point of view. Using the Boolean nomenclature that $\bar{A}$ represents "when $A$ is nought" we can say that the output $Q$ is given by $\bar{A}$ or $\bar{B}$

$$
Q=\bar{A}+\bar{B}
$$

But from a different view point-last month we saw that

$$
\mathrm{Q}=\overline{\mathrm{A} \cdot \mathrm{~B}}
$$

Therefore by normal algebraic argument we can say that
This proves the first of De Morgan's Theorems which-in very simple terms-says that an inverted AND is identical to a sort of inverted OR.
There is a second theorem which is very similar (it is worth you thinking how to argue it out) which says:-

$$
\bar{A} \cdot \bar{B}=\bar{A}+\bar{B}
$$

Again in simple terms an inverted $O R$ is the same as a sort of inverted AND

Before moving on, take note of a catch that beginners sometimes fall into. $\bar{A} \cdot \bar{B}$ is not the same as saying $A . B$. This means that when writing Boolean expressions you have to be careful with the length and breaks of the negate bar over the top of the alphabetical characters. Sometimes brackets are used to make the distinction clear in complex expressions.

## OR FROM NAND

De Morgan's Theorem is one of the most used in Boolean algebra because it gives a NAND gate a duality of purpose. Depending on how we want to think we can say that the output is either $\bar{A} \cdot \bar{B}$ or $\bar{A}-\bar{B}$. We have already utilised the former to give us AND from NAND. Now we can use the latter to give us basic OR.


Fig. 4.1. A solution to last month's problem.

Fig. 4.2 shows the way of producing a fundamental two input OR function. The inputs $A$ with $B$ become $A$ with $B$ at the outputs of their respective inverters. The output of the final NAND can be considered to be an OR function coupling the inverted form of its inputs. Therefore the output in this case is $\bar{A}+\bar{B}$. The double negates over each variable cancel and we are left with $A \rightarrow B$.
Use the toggle switches on the Logic Tutor to provide inputs to this circuit and monitor the logic levels at the various nodes on the lamps and check these against the truth table for the circuit shown in Fig. 4.2.

As an exercise try and use the knowledge you now have of the Associative rule and the gates available on Logic Tutor to produce a six input OR. (One answer to be given next month).
by M. Hughes
Next month we shall deal with the EXCLUSIVE OR function.

| $A$ | 8 | $\overparen{A} \cdot \boldsymbol{B}$ |
| :---: | :---: | :---: | :---: |
| This column adso |  |  |
| describes $\hat{A}+\bar{B}$ |  |  |
| 0 | 0 | 1 |
| 1 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 1 | 0 |


| $A$ | $B$ | $\bar{A}$ | $\bar{B}$ | $A+B$ |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 1 |



Fig. 4.2. OR from NAND logic.


## I.C. TRIAC CONTROL

In this section we present a selection of both new devices and applications, with news of applications developed for existing devices.

Generally only basic circuit details will be given sufficient for the experimenter to create his own equipment.

The triac is, without a doubt, one of the most convenient forms of a.c. power control yet devised. Provided one supplies it with correctly timed trigger pulses, it will control large amounts of power reliably without taking up too much space.

A variety of methods of supplying the trigger pulse have been developed but thus far these have involved discrete components. Now a new integrated circuit has been developed by Plessey specifically for triac control circuitry. Of necessity it is internally fairly complex as can be seen from the block schematic of Fig. 1.

It includes an amplifier, capacitor charge and discharge control circuitry, a comparator, a voltage stabilizer, a zero crossing detector and a triac firing circuit.

The basic function performed by this SL440 as the device is identified is in phase control circuits in which the power in the load depends on the phase angle or point on the a.c. cycle, at which the triac is fired. The extra facilities offered by the SL440 enable various circuits to be implemented with the bare minimum of external discrete components.


Fig. 1 Block schematic diagram of the SL440

## timing circuit

The SL440 contains a timing circuit with a period determined by external control voltages. It is the timing circuit's period which determines exactly when in each half cycle of the mains power supply the triac is fired.
This is illustrated diagrammatically in Fig. 2. Whenever the mains power supply crosses through zero volts,


Fig. 2 (a) When the voltage across the timing capacitor falls below the on-chip reference valtage a triac firing pulse is generated. (b) When the capacitor is discharged more quickly, more power is applied to the load
either in the positive or negative direction, a capacitor external to the i.c., but connected to it, is rapidly charged.

During the following half cycle the capacitor discharges at a rate determined by an external control voltage until the voltage across the capacitor falls to a level set within the i.c. When this occurs a pulse is generated which fires the triac.

In Fig. 2a the effect of quite a long timing period is illustrated. As the mains power supply crosses through zero the timing capacitor connected to pin 14 is charged. During each half cycle the capacitor discharges at a constant rate until the voltage across it reaches the same value as the reference when a triac firing pulse is generated.

In the illustration the dotted sinewave represents the mains supply while the solid line indicates the voltage across the load.


Fig. 3 Showing triac conduction time for different values of timing capacitor and different voltages at pin 13

## DISCHARGE RATE

The external control voltage setting the discharge rate would normally range from 3 to $8 V$, this being sufficient to alter the power in the load from zero to full power. An inverse law applies in that the lower the control voltage the higher the power in the load. The actual power in the load for a given control voltage is also dependent on the value chosen for the timing capacitor.

The larger the capacitor's value the greater the power applied to the load for a given control voltage. These relationships can be seen in Fig. 3, which plots the control voltage against triac conduction time for three different values of timing capacitors.

## ON-CHIP AMPLIFIER

The SL440 integrated circuit incorporates an amplifier, called a servo amplifier by Plessey, which can be used to produce the control voltage. It can really be looked at as a grounded emitter amplifier with a beta of about 2,000 and a 2 kS resistor in the emitter. It is necessary to connect a suitable external load resistor.

The amplifier is useful in motor speed control as an error voltage amplifier or it can be connected as an
integrator in automatic lamp dimmer circuits and the like.

## VOLTAGE STABILIZER

The SL440 contains a voltage stabilizer which performs three main functions. Firstly, it provides a stabilized 11.3 V supply for the rest of the chip; it also provides the reference voltage against which the voltage across the timing capacitor is compared and, finally, it provides a stabilized 11.3 V supply for circuitry external to the chip. However, the current available for off-chip circuits is extremely limited and nust not be allowed to exceed 3 mA .

## MAINS INPUT

The SL440 does not require a d.c. power supply in the conventional sense. Power input is obtained directly from the mains via a diode and series dropping resistor. With


Fig. 4 A circuit to replace the heat dissipating mains dropper resistor
the values recommended by Plessey, the half-wave rectified input has a peak value of about 60 or 70 V . The series resistor ( $6.8 \mathrm{k} \Omega 2,5 \mathrm{~W}$ ) can be eliminated with the circuit shown in Fig. 4.

The half-wave rectified input, as well as supplying power, also allows the chip to detect the zero crossings of the mains. Each time a zero crossing is detected the circuifry on the chip charges the external timing capacitor.

## OUTPUT AND INHIBIT

When the voltage across the timing capacitor falls below that of the reference voltage supplied to the comparator from the voltage stabilizer, an output triac firing pulse is generated. This pulse is $50 \mu \mathrm{~S}$ wide and must not be allowed to exceed 60 mA . However, this will be more than sufficient for most applications.

An inhibit input is provided which, when connected to the common line (or less than 5 V ), prevents any firing pulses from being generated. This input could be used in conjunction with current sensing circuits for protection purposes.

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| ADi61 | 33p | BC148 | 13 p | BF197 | 15p | 2N2926R 9p | 40360 | 35p |
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| AF114 | 20p | BC157 | 14p | BFY50 | 20p |  | 40362 | 40p |
| $\begin{aligned} & A F I \mid 5 \\ & A F I 16 \end{aligned}$ | 20p | BC158 | 14 p | BFYSI | 20p | $\begin{aligned} & \text { 2N2926Y 9p } \\ & \text { 2N2926G } \end{aligned}$ | 40408 | 40p |
| AFII 7 | 20p | BC159 | 14p | BFY52 | 20p | $10 p$ | ZT×302 | 15p |
| AFlil | 38p | BC187 | 22p | BU105 | 225p | 2N3054 58p | ZTX500 | 15p |
| AFI24 | 22p | BD131 | 75p | OC26 | 45p | 2N3055 60p | ZTX502 | 20p |

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 $0 \cdot 22 \mu \mathrm{~F}$, $5 \mathrm{p} .0 .33 \mu \mathrm{~F}, 6 \mathrm{p}, 0.47 \mu \mathrm{~F}, 7 \frac{1}{1} \mathrm{p}$. $066 \mu \mathrm{~F}$, $11 \mathrm{p} .1 .0 \mu \mathrm{~F}$, 13 p
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## Typical <br> APPLICATIONS

LIGHT DIMMER


## SPEED CONTROL

## APPLICATIONS

Numerous applications for the device will have probably already suggested themselves to the reader. Pleisey have developed several circuits.

Fig. 5 shows the $\$ 1.440$ used in a basic lamp dimmer circuit with the minimum of external components. Resistor RI provides the triac firing circuit with its load.

The potentiometer forms a potential divider across the 11.3 V supply and varies the light output by vary. ing the voltage at pin 13. The input imperlance at this point is very high and current flow can be measured in $n A$. The internal amplifier is not employed in this circuit.

The automatic lamp fader circuit of Fig. 6 is a variation on the same theme, the main difference being that the internal amplifier can be switched to perform an integration of the control voltage.

When SI is open. the brilliance of the lamp is set by RVI. When SI is closed, the positive potential derived from the divider R2, R3 is presented to the input causing the lamp to give full output.

If now S2 is closed and S1 is opered. the lamp will gradually reduce in brightness until it reaches the brightness level corresponding to the setting of RVI. The time taken for the fade to occur will depend on the value of the integration capacitor CI. With the value shown. this will be between 20 and 35 minutes.

A motor speed control circuit is shown in Fig. 7 This is basically similar to the previous circuits except that one leg of the potential divider is formed by a tachometer generator driven by the motor.
The device is available from SDS Components Lid.. Hilsea Trading Estate, Portsmouth. Hiants, at $£ 2.08 p$ for one-off. $£ 1.82$ for 25 off.
D. TROTMAN (SDS COMPONENT.S)

# Ridadout <br> A SELECTION FROM OUR POSTBAG 

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## THE TOPICAL SYNTHESISER

Sir-In answer to Mr. Baily's letter (last month)-he admits that the building of a stable, accurate log law circuit is difficult. I would tend to go further and say that for the average amateur constructor the task is almost impossible. This statement is in no way intended as a slight to the amateur but serves to highlight a situation which seems to be occurring all too often these days. Although there are many more avenues for the amateur to explore than there were a few years ago it is frequently found that exploitation of a particular circuit or function requires the possession of a bewildering array of test equipment and the technical expertise to interpret their respective readings.

I his situation is very much in evidence in the case of designing and building a log law v.c.o. as Mr. Baily has testined. There is littje point in publishing designs for such circuits since the problems arise, not so much in the building, but in the final setting-up and matching. Thus, from this point of view alone, there is considerable justification in the presentation of a linear v.c.o. which can be built with a minimum of test equipment and which behaves, with few limitations, in an exactly determinable manner. I would like to underline the fact that the P.E. Synthesiser is very much an experimental project which has been designed, as far as possible, to allow the widest possible licence to the individual constructor for the incorporation of modifications and/or additions to the system. In the case of the v.c.o. the overall response is determined by the response of the "front-end" as it is in most commercially available units. Thus by changing the response of the front end only the circuit can be made to obey a variety of laws to suit the whim of the constructor and this without disturbing the settings of the oscillator section at all.

With regard to Mr. Baily's specific criticisms of the linear v.c.o. the following observations apply :

1. No chording facility. This is a perfectly valid criticisn and without doubt is the major disadvantage of the linear v.c.o. Octave chords can be played however and the keyboard incorporates a harmony switch for this purpose. If the Synthesiser is to be used for musique concrete purposes the lack of chording facility is no disadvantage since a full range of four note chords may be set up and recorded as discrete sounds.
2. No variable keyboard pitch. Not strictly true since the keyboard oscillators have their own manual frequency control with which it is possible to vary the "spread" by about an octave either way. Since the keyboard oscillators are tuned an octave apart this effectively gives a register of seven octaves although the upper and lower ends are not tightly in tune due to the "non-logarity" of the oscillators.
3. Difficulties in tuning . . . Again not entirely valid. The variation in programming voltage from end of the keyboard to the other is only about 65 millivolts (see Fig. 3.4.) consequently there are only two values of fixed resistor and one value of preset per key. The
values of fixed resistor are chosen such that the majority of presets are operating within the middle 60 per cent of their rotation. There is thus not such a vast range of adjustment as Mr. Baily anticipates. Furthermore, and to the advantage of this particular system, the resistive value set-up during tuning affects only one note whereas in the series chain system used with log v.c.o.s any one resistor going unstable will affiect all the others downstream in the chain.
4. Poor frequency stability. Voltage and resistance wander can scarcely be cited as being problematical in this respect since they are relatively easy to correct. The use of a well regulated power supply together with adequate decoupling and the use of high stability resistors in the frequency determining networks will largely dispose of the problem. Mr. Baily correctly pinpoints the problem of thermal drift with the linear v.c.o. circuit as published.

On test at an ambient temperature of $18^{\circ} \mathrm{C}$ and with the oscillator running at 256 Hz rapid cycling between about $0^{\circ} \mathrm{C}$ (induced with a freezer aerosol) and $25^{\circ} \mathrm{C}$ (close proximity 100 watt lamp) resulted in a variation in frequency of between 222 Hz at $0^{\circ} \mathrm{C}$ and 312 Hz at $25^{\circ} \mathrm{C}$. This approximates to -10 per cent, +20 per cent on the basic frequency. On the other hand the oscillator in its case was monitored over a 48 hour period with a digital frequency meter and normal changes in day/night ambient temperatures resulted in frequency changes within $\pm 2$ per cent. Extreme changes in ambient temperatures are unlikely under what might be termed normal operating conditions and such light changes as do occur can be adequately compensated for by adjustment of the manual frequency bias on the oscillators.
5. Uneven swing in pitch control. There is no real answer to this one since Mr. Baily is correctly citing the characteristic of the linear circuit, i.e. the requirement for a progressively increasing programming voltage in order to maintain the same rate of change of frequency as frequency increases. I feel bound to say, however, that I have never noticed this as a problem.
Two final points are worth mentioning. Mr. Baily cites the increase in power required from the use of integrated circuits as opposed to discrete components. This is undoubtedly true and while there is probably a hard core of amateur constructors who enjoy the challenge of design there is a far larger group who build for the sake of building and whose principal requirement from their hobby is results. To this latter group the integrated circuit, in its many forms, represents a release from many of the problems associated with design and allows them to enjoy their hobby more fully as a result. The increase in power required is thus surely a small price to pay?

Finally, on the subject of price, I would be very interested to have full details of the range of Dewtron modules offering the same specification as the P.E. Sound Synthesiser at under $£ 200$.
D. Shaw.

## Variable voltage tannsformers



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| $6-12$ | $4 \mathrm{c} / 0$ |
| $8-12$ | 6 M |
| $10-18$ | $4 \mathrm{c} / 0$ |
| $9-18$ | $2 \mathrm{c} / 0$ |
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 p．p．25p．
MAMS TRANSFORMER，Pri： $100-2500^{\circ}$ ，Sec $\because 2: 0: 22 \quad 200 \mathrm{~m} / \mathrm{A}$ ．$\because 2: 0: 2 \cdot 2 \quad 100 \mathrm{~m} / \mathrm{A} \quad 0: 24 \mathrm{~V}$ 20m／A， 80p，p．p．20pp．
TRANBISTOR OUTPUT TRANSFORMER，Ratio $8: 1,1 \div 0 \mathrm{mH}$ ．Gentre tap 2 watts．output，20p， р．р．јp．
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20p，p．p．7p．
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Remily drilled．80p，p－p．7p
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SUB－MIR．CROC．CLIPS．Red or Black，insulated 4p．
Min，quantity， $\mathbf{6}$ ，p．p． 4 p ． Min．quantity，6，1．p．4p．
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Lock 4－pole changeover，15p，p．p．4p．Ex equip．
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gOLENOIDS 18 VOLT PULL ACTION
$\operatorname{lin} \times \ln \times$ inn，40p，p．p．$x p$
SOLENOMS $1 \because 24 V$ dic．pull action 1 in $\times 1$ in $\times$ 1引in，40p，p．p．ip．
 $11 \mathrm{in}, 50 \mathrm{p}, \mathrm{p} . \mathrm{p} .9 \mathrm{p}$ ．
SARGAMO WESTON TIME LAPSED METER
Maina operated． $1!\mathrm{in} \times 1$ in $\times 2$ in，$\$ 1.50$ p．p． 7 p ．
ARROW RELAX， 240 V a．c．coll，double pole change over， 1 make contact＇s $10 \mathrm{~A}, ~ 240 \mathrm{~V}$ a．c．， $25 \mathrm{p}, \mathrm{p} . \mathrm{p} .8$ p．
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POTTER BRUMFIELD 1 IV d．c． 3 pole changeover
with bage，contacts rated 7 A il．e． f 1 ，p．p． 10 p ． with base，contacts rated 7A 1i．e．，el，p．p．10p．
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MULLARD 4 DM 160 DNDICATORS in playtic holder／eocer．ex equip．，nize approx． 1 in $\times 1$ in $\times$ $\frac{1}{2}$ in， 36 p，p．p． 51
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\& TRANSISTORS \& 40316 <br>
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203602 \& 0.15 \& $2 N 3415$ \& 0.10 \& 40361

 

20331 \& 0.16 \& $2 N 3414$ \& 0.10 \& 40360 <br>
20302 \& 0.15 \& $2 N 3415$ \& 0.10 \& 40361 <br>
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2 N 29 ${ }_{2}{ }_{2} \mathrm{~N}_{2} 2907 \mathrm{Cl}$ $\begin{array}{ll}2 \mathrm{~N} 2907 \mathrm{~A} \\ 2 \mathrm{~N} 2923 & 0 . \\ 0 .\end{array}$ $\begin{array}{ll}2 \mathrm{~N} 2924 & 0.12 \\ 2 \mathrm{~N} 2925 & 0.15\end{array}$ $\stackrel{2}{2} 2$

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| :---: | :---: | :---: | :---: |
| Green | 0.12 | ${ }_{3 N 142}$ | ${ }_{0} 0.58$ |
| Yellow | 0.10 | 3N143 | 0.75 |
| Orange | $0 \cdot 10$ | 3N152 | 0.92 |
| 2N3053 | 0.81 | 3N153 | 0.81 |
| 2N3054 | 0.80 | 3N154 | 0.84 |
| 2 N 305 J | 0.80 | 3N159 | 1.17 |
| 2N3990 | 0.20 | 3N187 | 1.55 |
| 2 N 3391 | 0.20 | 3 N 200 | 8.49 |
| 2N3391A | 0.22 | 3N201 | 1.05 |
| 2N3392 | 0.18 |  |  |
| 2N 3393 | 0.12 |  |  |
| 2N3394 | 0.17 | 40050 | 0.78 |
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20 p
28 p
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ANODE AND CATHODE STUD


 $\begin{array}{rr}\text { 1N34A } & 10 \mathrm{p} \\ \text { IN914 } & 7 \mathrm{p} \\ \text { IN916 } & 7 \mathrm{p} \\ \text { IN }\end{array}$ $\begin{array}{lr}\text { IN916 } & 7 \mathrm{p} \\ \text { AA119 } & 7 \mathrm{p} \\ \text { AA129 } & 15 \mathrm{p}\end{array}$

| AA129 | $15 p$ |
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gain aelec.
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covering A M:
$33 b-1+i 05 \mathrm{kzz}$
L. W. $\quad 150-$
380 Hz 380 kHz : M. 8.0 MHz
8 W :
3.W.3: $16-24 \mathrm{MHz}$ : P.S.B. $1: 30-$
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35 mmi mike ingut aockets, ete., 3.5 mml mike ingut aockets, , te.,
etc. Frequency repponse $100-$ $\begin{array}{llll}8 \mathrm{kHz} & (100-12 \mathrm{kHz} & \mathrm{CrO}) \\ 8\end{array} \quad \mathrm{~g} / \mathrm{N}$
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 4 ch control ( $\left.4 \frac{1}{\text { in }} \times 10 \mathrm{lin}\right)$ Mk. 2-also holds
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staced "as published". PCBs are designed by Phonostated as published. PCBs are designed by Phon
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HI-FI TAPE LINK
(PE Mar./Apr. 73 ). S/c's, i,c.'s, Rs, Cs, Relay and pc-base, Pot Cores and pc-bases, Sw's, Pots,

A.F. SIGNAL GENERATOR (PE Nov. 72 ). S/c's. Rs, Cs, Pors,
Sw's. PCB $\left(2 \frac{1}{2}\right.$ in $\times 4$ in) also holds Sw's, \&3.15.
(PE AUDIO MIXER
(PE Jan 72) Rs, Cs, Pots, PCB
DOOR BELL YODELLER
(PE Apr. 71), $5 / c^{\prime}$ 's, Rs. Cs, Pors,
 L/spkr $\mathbf{E l} \cdot \mathbf{3 0}$.

BIOLOGICAL AMPLIFIER (PE Jan./Feb. 73) P/A Sec-S/c's, i.c.'s, Rs. Cs, Pots, PCB ( 1 tin $\times 3 \neq 1 \mathrm{n}$ ),
E 3.70 . O/P' Stages (S/c's. Rs. Cs, Pors and Sw's as read.). Alphaphone 60 p , Cardiophone 75 p . Freq. Meter E1.90. Vis-Feedback 60p. Audio Amp PC7 avail. co order 44.75.

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(PE Sed. 72/Jan. 73)-Details in lists
GEMINI STEREO AMPLIFIER
(PE Nov. 70/Mar. 71) Stereo Sets and PCBs. Pre-amp-S/c's 61.85. Rs. Cs.
 rotary or slider pots and Maka-Sw's, 62.10. Main Amp-Rs Cs, Pots 5540. PCB ( 3 i⿻ in $\times 5 \operatorname{in}$ ), 41.40 . PSU-Rs, Cs, Pot, 43.70 . PCB ( $2 \mathrm{in} \times 4 \mathrm{in}$ ). 75 p .

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(PW Nov.jDec. 72). Pre-amp-S/c's, Rs, Cs, Pots, Sw-Mono, ©2.50;
Stereo, 5.20 PCB M Stereo, $\mathbf{~ 5 ~} \mathbf{2 0}$. PCB ( $3 \frac{1}{4}$ in $\times 7 \frac{1}{2 i n)}$
(Stereo) also holds rotry (Stereo) also holds rotary or slider pots and Sw, \&1.50. Main AmpS/c's. Rs, Cs, Pot-Mono, 43.90; Stereo,
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# TRANNIES <br> I DOCKYARD, STATION ROAD, OLD HARLOW, ESSEX Phone Harlow 37739 <br> P/P 10p. Price list S.A.E. (Saturday callers welcome) all prices include vat <br> <br> ?19.50 ELECTRONIC DIGITAL CLOCK <br> <br> ?19.50 ELECTRONIC DIGITAL CLOCK <br> <br> (For complete kit of parts including case.) <br> <br> (For complete kit of parts including case.) <br>  <br> This $\mathbf{4}$ digit 24 hour clock is available to readers at this special price for I month only. Parts would normally cost over $\mathbf{4 2 5}$. Kit of parts ineludes twelve IC's, indicators, and a smart whiteplastic case. 

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| SNit00 | 16p | 15p | 8 BN 723 | 55p | 50p | SN7450 | $18 p$ | 15p | SN7489 | 6. 05p | b-85p |
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| 8 N 7402 | 18 p | 15p | SN7407 | 49p | 48p | 8N7453 | 18p | 159 | 8 N 743 L | $1 \cdot 10 p$ | $1 \cdot 04 p$ |
| $6 \times 7403$ | 16p | 15p | 8N7428 | 77p | 72p | SN 7454 | 16p | 15 p | 8N7402 | 74 p | 72 p |
| $8 \mathrm{SN7404}$ | 16p | 15p | SN7430 | 16p | 15p | SN7460 | 16 | 15p | SN7493 | 74 p | 72 p |
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| SNT411 | 27p | 25p | SN 7442 | 74p | 70p | 8S7480 | 73p | 70p | $8 \mathrm{~N} 7+107$ | 44p | 42p |
| SN742 | 38p | 35p | 8N7443 | 1.43p | 1-370 | $8 \times 7481$ | 1-32p | 1-26p | - ENTil 10 | 81 p | 50p |
| 8N7413 | 32p | 29p | 8 N 744 | 1.43 p | $1 \cdot 37 \mathrm{p}$ | 8N748: | 97 p | 95p | SNT+111 | 1-37\% | 27p |
| SN74!6 | 47p | 43p | 8N74.5 | 2.00 p | 1-92p | 857403 | 1-20p | 15p | - 5 (74118 | 1.10p | 1.05p |
| SN7417 | 47 p | 43p | $8 \times 7415$ | 1.07p | $1-02 \mathrm{p}$ | $8 \times 7484$ | $1 \cdot 10 \mathrm{p}$ | $1-05 p$ | $8 \mathrm{NT}+119$ | 1.47 p | 1.37 p |
| 8N7420 | 16p | 15] | 8N7447 | 1.10p | 1-03p | 8N7485 | $3 \cdot 96 \mathrm{p}$ | 3.85p | SNT+121 | 44p | 41p |
| SN-422 | 55 p | 50p | 8N7448 | 1 10p | 1-03p | SNT48i | 36p | 35p | 8N742\% | .54p | 43p |

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\hline IN2I \& $$
\begin{aligned}
& \text { ip } \\
& 0.17
\end{aligned}
$$ \&  \&  \& 0A7．11 \& $$
\log _{0}
$$ \& \& 頻 <br>
\hline 1 N 23 \& 0.20 \& AsY 260.25 \& BYZ11 0.32 \& OAZ222 \& 0.45 \& 78271 \& <br>
\hline 1N85 \& 0.88 \& $\begin{array}{ll}\text { AsY } 27 & 0.32\end{array}$ \& BYZ1． 0.38 \& 0 OR223 \& 0.45 \& 28271 \& 0.18 <br>
\hline $1 \mathrm{~N}^{253}$ \& 0.50 \& ASY：28 0．25 \& BYZ12 0－80 \& 0 OR224 \& 0.45 \& 7T：1， \& 0.28 <br>
\hline 1N256 \& 0.80 \& ASY29 0．80 \& BYZ13 0.2 \& OAZ241 \& 0.22 \& ZT43 \& 0.25 <br>
\hline 1N645 \& 0.25 \& A8Y36 0.26 \& $3 \mathrm{BZ15} 1.00$ \& 0azide \& 0.23 \& 7TX 107 \& 0.15 <br>
\hline 1N725A \& 0.20 \& ABY50 0．17 \& BYZ16 0.62 \& 9，AZ944 \& 0.22 \& ZTX108 \& 0.12 <br>
\hline 1 N 914 \& 0.07 \& ASYOL 0．40 \& By zRRCJW 3 \& 0A724 6 \& 0.28 \& ZTX 300 \& 0.12 <br>
\hline IN4007 \& 0.80 \& AAYY3 0.20 \& －111 0.15 \& 0 AZ 290 \& 0.38 \& \& 0.25 <br>
\hline $$
18113
$$
$$
18130
$$ \& $$
\begin{aligned}
& 0.16 \\
& 0.18
\end{aligned}
$$ \& $\begin{array}{ll}\text { ASYG5 } & 0.20 \\ \text { ASY } & 0.25\end{array}$ \& $\begin{array}{lll}\text { C111 } & 0.65 \\ \text { CR8105 } & 0.25\end{array}$ \& ${ }_{0}^{0 \mathrm{Cl} 16}$ \& 0.50
0.88 \&  \& 0.26
0.16 <br>
\hline 18131 \& 0.18 \& ASY86 0.88 \& CRSI／40 0.45 \& OC19 \& 0.37 \& ZTX503 \& 0.17 <br>
\hline 18202 \& 0.28 \& ASZ 210.42 \& C84B $\quad 2.50$ \& $0 \mathrm{Cl}^{2}$ \& 0.85 \& ZTX031 \& 0.25 <br>
\hline 20371 \& 0.22 \& AsZas 00．76 \& CS10］3．18 \& 0 C 22 \& 0.50 \& \& <br>
\hline 20381 \& 0.26 \& Al＇Y10 0.98 \& DD000 0.16 \& $\mathrm{OCL}^{2}$ \& 0.80 \& \multicolumn{2}{|l|}{\multirow[t]{2}{*}{InTEGRATED
CIRCUITS}} <br>
\hline 20414 \& 0.80 \& AL＇101 1.60 \& DD003 0.15 \& 0 Cl 4 \& 0.60 \& \& <br>
\hline 2 G 417 \& 0.22 \& BC

0.10 \& $\begin{array}{ll}\text { 0D006 } & 0.18\end{array}$ \& $0{ }^{0} 5$ \& 0.87 \& \& <br>
\hline ${ }^{2} \mathrm{~N} 40.4$ \& 0.20 \& BClios
BClos 0.10 \& DD007 0.40 \& ${ }^{0 c 25}$ \& 0.87 \& － 7401 \& 0．20 <br>
\hline －${ }^{2} \mathrm{~N} 697$ \& 0.15
0.40 \& $\begin{array}{ll}\mathrm{BC} 109 & 0.10 \\ \mathrm{BCl13} & 0.15\end{array}$ \&  \& $0 \times 26$
OC28 \& 0.26 \& － \& 0.20 <br>
\hline 2 N 706 \& 0.10 \& $13 \mathrm{Cl15} 0$ \& ${ }^{1} \mathrm{DD} 4{ }^{\text {d }}$ \& 0 C 29 \& 0.80 \& －403 \& 0.20 <br>
\hline 2N706A \& 0.12 \& BCili 00.25 \& （D）${ }^{\text {d }}$ \& 0 C 30 \& 0.40 \& T404 \& 0.20 <br>
\hline 2N708 \& 0.15 \& BC116． 0.80 \& 9D8 0.25 \& 0 C 35 \& 0.40 \& T40j \& 0.20 <br>
\hline 2 N 709 \& 0.68 \& $1 \mathrm{Cl18} 80.25$ \& GD12 0.06 \& $0 \mathrm{C35}$ \& 0.60 \& 74148
7407 \& 0.30
0.30 <br>
\hline 2N1091 \& 0.88 \& $\begin{array}{ll}\mathrm{BC121} & 0.20\end{array}$ \& GET10：0－80 \& OC36 \& 0.60 \& ${ }_{7}^{7} 407$ \& 0.30
0.20 <br>
\hline 2N1131 \& 0.25 \& BC122 $\quad 0.20$ \& （EET103 0.28 \& 0 C 41 \& 0.25 \& 7418 \& 0.20 <br>
\hline 2 N 1132 \& 0.25 \& BC125 0．68 \& GET113 0.20 \& OCt2 \& 0.80 \& 7410 \& 0．46 <br>
\hline 2 N 1302 \& 0.18 \& BC12ar 0．05 \& GFT114 0．16 \& OC43 \& 0.40 \& 417 \& 0.20 <br>
\hline $2 \mathrm{~N}^{2} 303$ \& 0.18 \& BC140 0.55 \& GET115 0 0．45 \& $0 \mathrm{C4} 4$ \& 0.17 \& 7411 \& 0.23
0.42 <br>
\hline 2N1304 \& 0.22 \& $\mathrm{BCl}^{147} 00.16$ \& GET116 0．50 \& OC44M \& 0.17 \& 7413 \& 0.42
0.30 <br>
\hline 2 N 1305 \& 0.22 \& 13C148 0.13 \& $\begin{array}{ll}\text { GET120 } & 0.25\end{array}$ \& 0 C 45 \& 0.12 \& ${ }^{7} 113$ \& 0.30
0.30 <br>
\hline ${ }^{2} \mathrm{~N} 130 \mathrm{O}$ \& 0.25 \& $1 \mathrm{BC149} 00.15$ \& GET872
0.30 \& OC4ism \& 0.18 \& 7413 \& 0.30 <br>
\hline $2 \mathrm{~N} 130^{\circ}$ \& 0.25 \& ${ }^{13 C 157} 00.16$ \& GET876 0－25 \& OC46 \& 0.27 \& 7417 \& 0.80
0.20 <br>
\hline ${ }^{2} \mathrm{~N} 1308$ \& 0.25 \& BC15 0.12 \& $\begin{array}{ll}\text { GET } 880 & 0.37\end{array}$ \& 0C57 \& 0.60 \& 7－ 100 \& 0.20 <br>
\hline 2 N 2147 \& 0.75 \& BCi60
0.69 \& GET881 0－25 \& （）C5y \& 0.80 \& 「＋20 \& 0.48 <br>
\hline 2 N 2148 \& 0.80 \& 3C169
0.13 \& GET882 00.25 \& ${ }^{0} \mathrm{Cay}$ \& 0.65 \& 7423 \& 0.48
0.48 <br>
\hline 2N2160 \& 0.80 \& 13C＇31 0.85 \& GET885 0.25 \& $0 \mathrm{C66}$ \& 0.50 \& － \& 0.48 <br>
\hline ${ }_{2}^{2 N} 2218$ \& 0.20 \& BCY3： 0.55 \& （EEX44 0.08 \& OC70 \& 0.12 \& － \& ${ }_{0}^{0.50}$ <br>
\hline  \& 0.20
0.15 \& $\begin{array}{ll}\text { Bry32 } & 0.50 \\ \text { He＇33 } & 0.25\end{array}$ \& $\begin{array}{lll}\text {（EXX4511 } & 0.10 \\ \text { GEX } & \\ \text { Cil }\end{array}$ \& $0 \mathrm{OC71}$ \& 0.12 \& $7+2 \times$
74.30 \& 0.50
0.20 <br>

\hline $$
\begin{aligned}
& 2 \mathrm{~N} 2369 \mathrm{~A} \\
& 2 \mathrm{~N} 2444
\end{aligned}
$$ \& 0.15

1.98 \& $\begin{array}{ll}\text { He＇y33 } & 0.25 \\ \text { BCY } 34 & 0.80\end{array}$ \&  \& 0 Cl
0 C 73
0 \& 0.20
0.80 \& 7430
743
7 \& 0.20
0.48 <br>
\hline ${ }_{2} \mathrm{~N}^{2813}$ \& 0.88 \& BCY 380 \& $\begin{array}{ll}\text {（1，54M } & 0.88\end{array}$ \& $00^{0} 4$ \& 0.30 \& ${ }^{7} 433$ \& 0.70 <br>
\hline 2 N 2046 \& 0.45 \& 13CY39 1.00 \& （iJ5M 0．26 \& 0075 \& 0.25 \& 7437 \& 0.68 <br>
\hline 2 N 2904 \& 0.20 \& BCY40 0.60 \& $\begin{array}{ll}\text { gJ7M } & 0.87\end{array}$ \& 0 C 76 \& 0.25 \& －13．4 \& 0.65 <br>
\hline 2 N 2904 A \& 0.25 \& BCY4： 0.25 \& HG11000 0.50 \& 0 C 77 \& 0.40 \& i440 \& 0.20 <br>
\hline 2N2906 \& 0.20 \& 13 CY 700 \& HS100A 0.20 \& OCim \& 0.20 \& 7411 \& 0.78 <br>
\hline 2 N 2907 \& 0.28 \& BCY71 0.20 \& MAT100 0.25 \& OC79 \& 0.22 \& 744： \& 0.78 <br>
\hline $2 \mathrm{~N}^{2924}$ \& 0.28 \& BCZ10 0.85 \& Matiol 0.80 \& OC81 \& 0.20 \& － 450 \& 0.20 <br>
\hline 2 N 2925 \& 0.15 \& BCZ11 0.50 \& Mat120 0.25 \& OC811， \& 0.20 \& －451 \& 0.20 <br>
\hline 2 N 2926 \& 0.10 \& BD12］0．65 \& $\begin{array}{ll}\text { MaTlel } & 0.80\end{array}$ \& OС81M \& 0.20 \& －43\％ \& 0.20 <br>
\hline 2 N 3054 \& 0.50 \& BD123 00.80 \& MJE520 0．87 \& ос81D \& 0.18 \& 34.4 \& 0.20 <br>
\hline 2 N 3055 \& 0.75 \& B1124 0.75 \& MJ E295；1．87 \& OC818． \& 0.40 \& 7460 \& 0.20 <br>
\hline 2N370：2 \& 0.10 \& BDY11 1.68 \& MJ E3055 0.87 \& OCP2 \& 0.25 \& 7470 \& 0.80 <br>
\hline 2 N 3705 \& 0.10 \& BFils 0.26 \& NKT128 0 0．36 \& OC8211 \& 0.20 \& 74： \& 0.80 <br>
\hline 2 N 3708 \& 0.23 \& BF117 0.50 \& NKT129 0．30 \& 0 C 83 \& 0.25 \& 1473 \& 0.40 <br>
\hline 1N3707 \& 0.12 \& BF167 0．25 \& $\begin{array}{ll}\text { NKT211 } & 0.25\end{array}$ \& OC84 \& 0.25 \& 7474 \& 0.40 <br>
\hline 2N3709 \& 0.10 \& BF173 0．25 \& NKT213 0．25 \& OCll \& 0.38 \& 7475 \& 0.56 <br>
\hline 2N3710 \& 0.10 \& BF181 0.85 \& $\begin{array}{ll}\text { NKT214 } & 0.16\end{array}$ \& OC122 \& 0.80 \& 7474 \& 0.45 <br>
\hline 2N3711 \& 0.10 \& BF184 0.20 \& NKT216 0 0．37 \& $0 \mathrm{Cl23}$ \& 0.68 \& T480 \& 0.80 <br>
\hline 2 N 3819 \& 0.85 \& BF185 0．20 \& $\begin{array}{lll}\text { NKT217 } & 0.35\end{array}$ \& OC139 \& 0.25 \& 748：3 \& 0.87 <br>

\hline 2N5027 \& 0.58 \& BF194 0．17 \& | NKT218 |
| :--- | :--- |
| 1.18 | \& 0 Cl 40 \& 0.85 \& 7143 \& 1.00 <br>

\hline 2N5088 \& 0.33 \& BF195 0．15 \& NKT219 0.33 \& OClid \& 0.60 \& T484 \& 0.80 <br>
\hline 28301 \& 0.50 \& BF198 0.15 \& NKT222 0－20 \& OC169 \& 0.20 \& i48t \& 0.45 <br>
\hline 28304 \& 0.75 \& BF197 0．15 \& NKT224 0.22 \& 0 Clito \& 0.25 \& 7490 \& 0.75 <br>
\hline 28501 \& 0.87 \& HFS61 0．28 \& NKT251 0.24 \& 0 Cl 11 \& 0.30 \& 790．．N \& 1.00 <br>
\hline 28703 \& 0.62 \& BFS98 0.28 \& NKT271 0.25 \& OC200 \& 0.40 \& － 492 \& 0.78 <br>
\hline AA129 \& 0.20 \& BFX1： 0.20 \&  \& OC201 \& 0.70 \& －193 \& 0.75 <br>
\hline AAZ12 \& 0.80 \& BFX13 0．25 \& NKT273 0.16 \& OC20： \& 0.80 \& 7494 \& 0.80 <br>
\hline AAZ13 \& 0.12 \& $\begin{array}{ll}\text { BFX29 } & 0.85\end{array}$ \& NKT274
1 \& Oc203 \& 0.40 \& T493 \& 0.80 <br>
\hline ACl07 \& 0.37 \& BFX30 0．25 \& $\begin{array}{lll}\text { NKT275 } & \mathbf{0 . 2 5}\end{array}$ \& OC204 \& 0.40 \& 7496 \& 1.00 <br>
\hline AC126 \& 0.20 \& BFX35 0．88 \& NKT274 0.20 \& OC205 \& 0.76 \& T497 \& 8.25 <br>
\hline ACl27 \& 0.25 \& BFX63 0.50 \& NKT278 0 0．25 \& OC206 \& 0.90 \& T＋100 \& 2.50 <br>
\hline AC128 \& 0.20 \& BFX84 0.25 \& NKT301 0.40 \& 0 C 207 \& 0.90 \& 74107 \& 0.50 <br>
\hline AC187 \& 0.25 \& BFX85 0.30 \& NKT304 0.75 \& 0 C 460 \& 0.20 \& T4110 \& 0.80 <br>
\hline AC188 \& 0.25 \& BFX88 0.85 \& NKT403 0.76 \& OC470 \& 0.80 \& it111 \& 1.45 <br>
\hline ACY17 \& 0.80 \& ${ }^{\text {BFX } 87} 0$ \& NKT404 0.55 \& OCP71 \& 0.97 \& － 4118 \& 1.00 <br>
\hline ACY18 \& 0.26 \& BFX84 0.20 \& $\begin{array}{lll}\text { NKT678 } & 0.30\end{array}$ \& ORP12 \& 0.50 \& $7+119$ \& 1.90 <br>
\hline ACY19 \& 0.25 \& BFY10 1．00 \& NKT713
0.25 \& ORP60 \& 0.40 \& 7419］ \& 0.60 <br>
\hline ACY20 \& 0.20 \& BFY11 1．25 \& $\begin{array}{lll}\text { NKT733 } & 0.25\end{array}$ \& ORP61 \& 0.42 \& 74129 \& 1.35 <br>
\hline ACY21 \& 0.20 \& BFY17 0．25 \& NKTTTT 0.88 \& B19T \& 0.80 \& 74123 \& 2.70 <br>
\hline ACY2 2 \& 0.10 \& BYF18 0.25 \& 078B 0.88 \& SAC40 \& 0.25 \& 74141 \& 1.00 <br>
\hline ACY27 \& 0.25 \& BFY19． 0.25 \& \& SFT308 \& 0.88 \& 7414 \& 1.50 <br>
\hline ACY28 \& 0.17 \& BFY24 0.45 \& $\begin{array}{ll}\text { OAB } & 0.12\end{array}$ \& ST ${ }^{\text {2 } 22}$ \& 0.38 \& 74150 \& 3.35 <br>
\hline $\mathrm{ACY}^{\text {C9 }}$ \& 0.50 \& BFY44 1.00 \& OA47 0.10 \& ST7231 \& 0.68 \& 74151 \& 1.10 <br>
\hline ACY40 \& 0.15 \& Bryso 0.22 \& OA70 0.10 \& 9X68 \& 0.20 \& 7415－4 \& 2.00 <br>
\hline ACY41 \& 0.15 \& BFY51 0．20 \& OA71 0．10 \& 5X631 \& 0.80 \& 7 $71 . \overline{0}$ \& 1.65 <br>
\hline ACY44 \& 0.26 \& BFY62 0.22 \& OA73 00.10 \& 8X635 \& 0.40 \& 741，56 \& 1.55 <br>
\hline AD140 \& 0.50 \& BFY53 0.17 \& $\begin{array}{ll}\text { OA74 } & 0.10\end{array}$ \& 8X640 \& 0.50 \& 7＋157 \& 1.80 <br>
\hline AD149 \& 0.60 \& $\begin{array}{ll}\text { BFY } 64 & 0.42\end{array}$ \& $\begin{array}{ll}0.79 & 0.10\end{array}$ \& 9X641 \& 0.55 \& 74170 \& 4.10 <br>
\hline AD161 \& 0.87 \& BFY90 0.65 \& $0.481 \quad 0.08$ \& 8×642 \& 0.60 \& 7 4174 \& 2.00 <br>
\hline AD16\％ \& 0.37 \& BSX27 0.80 \& 0 A 850.12 \& SX644 \& 0.76 \& $\bigcirc$ \& 1.36 <br>
\hline A F106 \& 0.80 \& BSX 6000.98 \& OA86 0．15 \& 8×646 \& 0.75 \& － 4176 \& $1 \cdot 60$ <br>
\hline AF114 \& 0.25 \& B8x76 0.16 ， \& $\begin{array}{ll}0.990 & 0.08\end{array}$ \& T15／30P \& 0.60 \& －+190 \& 1.95 <br>

\hline AF115 \& 0.25 \& B9Y26 0.18 \& | 0.491 | 0.07 |
| :--- | :--- |
| 0495 |  | \& －30／201P \& 0.75 \& $\stackrel{+191}{ }$ \& 1.95 <br>

\hline AF116 \& 0.26 \& BSY2 ${ }^{\text {a }} 0.17$ \& OA95 0.07 \& 「60／201 \& 0.80 \& －+192 \& 2．00 <br>
\hline AF117 \& 0.85 \& B8Y61 0.50 \& OA200 0.07 \& ${ }^{*} 60 / 2011{ }^{\text {P }}$ \& 0.76 \& 74193 \& 2.00 <br>
\hline AF118 \& 0.68 \& BSY95．A 0.12 \& $\mathrm{OAP202}^{0.2010}$ \& XA101 \& $0 \cdot 10$ \& 74194 \& $2 \cdot 60$ <br>
\hline AF119 \& 0.20 \& B8Y95 0.12 \& ${ }^{012210} 00.25$ \& X 4102 \& 0.18 \& － 410 \％ \& 1.85 <br>
\hline AFl24 \& 0.25 \& $3 \mathrm{~T} 102 / 500 \mathrm{H}$ \& 0 A 21100.30 \& X A 151 \& 0.18 \& －+100 \& 1.50 <br>
\hline ${ }_{4} \mathrm{~F} 125$ \& 0.20 \& 13Y4．${ }^{0.75}$ \& \& X A 152 \& 0.15 \& \％+197 \& 1.50 <br>
\hline ${ }_{\text {AF126 }}$ \& 0.17
0.17 \& \multirow[t]{2}{*}{BTY79／100R} \& $\begin{array}{ll}\text { OAZ201 } & 0.50 \\ \text { OAZ202 } & 0.42\end{array}$ \& X X 161 \& 0.25 \& ${ }_{7} 119 \%$ \& 4.60 <br>
\hline AF127
AF139 \& 0.17
0.80 \& \& $\begin{array}{ll}\text { OAZ204 } & 0.42 \\ \text { OAZ203 } & 0.42\end{array}$ \& XA162
X （162 \& 0.25 \& 74194 \& 4.60 <br>

\hline AF178 \& 0.55 \& $1 \mathrm{BTY}^{\text {Y }} / 4 / 400 \mathrm{R}$ \& 0Az204 0.30 \& X B 101 \& 0.48 \& \multicolumn{2}{|l|}{\multirow[t]{6}{*}{| Plus in surket 4 low profle |
| :--- |
| It bin DIL 0.15 |
| Iti！nin DIL |}} <br>

\hline AF179 \& 0.65 \& 1.25 \& OAZ205 0.42 \& XB102 \& 0.10 \& \& <br>
\hline AF180 \& 0.68 \& 13 y 10400.15 \& ${ }_{0}^{0 A Z 206} 00.42$ \& X B103 \& 0.25 \& \& <br>
\hline ${ }_{4} \mathrm{~F} 181$ \& 0.42 \& $\begin{array}{ll}\text { BY128 } & 0.15 \\ \text { BY127 } & 0.17\end{array}$ \& $\begin{array}{ll}0 A Z 207 & 0.47 \\ 0 \text { AZ208 } & 0.32\end{array}$ \& X X 113 \& 0.12 \& \& <br>
\hline AF186 \& 0.40 \& $\begin{array}{ll}\text { BY } 127 & 0.17\end{array}$ \& OAZ208 0－32 \& ${ }^{\text {X }} \mathrm{Bl21}$ \& 0.12 \& \& <br>
\hline AFY19
AFZ1I \& 1.18
0.60 \& $\begin{array}{ll}\text { BY182 } & 0.85 \\ \text { BY213 } & 0.25\end{array}$ \& $\begin{array}{ll}0 \text { AZ209 } & 0.82 \\ 0 \text { AZ210 } & 0.82\end{array}$ \& \& 0.43 \& \& <br>
\hline
\end{tabular}

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## VARICAP STEREO TUNER F．M．$\varnothing$

（ACTUAL BEECHING UNITS）

MODULES LPII85／86<br>REGULATOR MFC4060<br>DECODER MCI3IOP<br>${ }_{69} 9$ pair<br>60.78 each<br>63.15 each<br>（ 100 ＇s in stock）<br>PRINTED CIRCUIT BOARD＿－GLASS FIBRE $£ 1.87$ each<br>READY BUILT AND TESTED BOARDS<br>623 each<br>COMPLETED TUNERS IN TEAK CABINETS $£ 34$ each

（BRUSH ALU．FRONT PANEL wish 6 BUTTONS，MAINS＋ 5 STATIONS）
（ACD 25p POSTAGE TO ABOVE）
SUPER SPECIAL OFFER WHILE STOCKS LAST
25 W．R．M．S．DISCO AMPLIFIERS ONLY EIO each ！

WHAT A LINE UP：
$0.2 \%$ DISTORTION， $25 W$ RMS INTO $8 \Omega$ ，FLAT RESPONSE，S／C PROTECTION！AND USING 40361／40362／MJ481／MJ491 TRANSISTORS．
WE ARE MAD！EACH ONE TESTED AND WORKING AND YET ONLY $£ 10$ Ready to use．

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BAKER LOUDSPEAKER CO．，BENSHAM MANOR PASSAGE thornton heath，surrey tel． 01 －684 1665 loudspeakers catalogue and enclosure plan 5p

## NSS <br> D0－IT－YOURSELF ELECTRICAL EQUIPMENT TRANSFORMERS • RECTIFIERS INDICATOR LAMPS • RELAYS CONTACTS－KEY SWITCHES BURGLAR ALARM EQUIPMENT SOLDERING IRONS－COMPONENT CASES HOLE CUTTERS－TERMINAL BOXES <br> plus numerous other electrical components． Send IOp for full price list and illustrated catalogue to： <br> NORTHERN SECURITY SUPPLIES（DEPT．PE） 104 KENT CRESCENT，KENTROAD，PUDSEY，YORKSHIRE



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Sensational 'Once in Life-time Ofter cleared. The amplifier is made by Mullard. Carries maker's Guarantee. In nea case. May be used for
Mono or Stereo. Music or speech. Works off dry battery. car batt or mains power pack. FREE-al who purchase we send copy of Mullard booklet DIY stereo. 1160


## MIGHTY MIDGET

Probably the tiniess possible radio. as described in Practical Wireless,
parts $£ 2.20$ post paid.


## TIME SWITCH

Smith's mains driven clock with 15 amp switch. also notes showing how you can wake up with music playing kelle boiling or come home to a warm house. Warn of burglars keep pets warm.
bill. etc. $\& 195$.

## 1 CHIP RADIO

Ferranti's latest device ZN4/4-gives results better than superhet. Supplied complete with technica notes and circuils. $\mathbf{~ 1 / 3 8}$ each. I0 for $\mathbf{5 1 1 \cdot 1 1}$.

HI-Q TUNER COMPONENTS
For experimenting with the $2 N 414$. KTI NO. 1. Plesscy Miniature Tuning Condenser
with built in LW switch and $3^{\prime \prime}$ ferrit slab and litz wound MW coil, 72p
KIT NO. 2. Air spaced tuning condenser $6^{\circ}$ ferrite rod litz wound MW and IW coils. \$p
KIT NO. 3. Air spaced TC with slow motion drive
$\mathbf{8}^{*}$ ferrite fod. with litz wound LW and MW coils. si IU. $8^{*}$ ferrite rod. withlitz wound LW and MW coils. I 1 IU.
KIT NO. 4. Permeability tuner with fast and slow KIT NO. 4. Permeability tuner with fat
motion drive and LW loading coils, 45p.


12 VOLT $1 \frac{1}{2}$ AMP POWER PACK
This comprises double-
wound
$230 / 240 \mathrm{~V}$
mains transformer with full wave rectifier and 2000 mF smoothing. Price $2 \mathbf{2} 20$, plus 20 p pos: and packing.
Heavy Duty Mains Power Pack. Output voltage adjustable from $15-40 \mathrm{~V}$ in steps-maximum load
250 W -that is from 6 amp at 40 V to 15 mmp at 15 V . This reatly is a high power heavy duty unit with dozens of workshop uses. Output voltage adjustmen is very quick-simply interchange push on leads Silicon rectifiers and smoothing by 3.000 mF . Price 56.33 plus 65 p post.

## MICRO SWITCH

5 amp changeover contacts. 11p each
10 for 99 p . 15 amp Model 15p Changeover 15p each.


MAINS RELAY BARGAIN

.Special this month are some single. double and treble pole changeove relays. Contacts rated at 15 amps Good British Make. Unused. Size approx. $1 t^{\prime \prime} \times 1^{\prime \prime}$. Open construction Single pole 28p each 10 for $£ 248$.

MAINS OPERATED SOLENOIDS
 Model 772-small but powerful ${ }^{1}$
pull-approx. size it $\times 1+\times 1 t^{n}$ 66p. Model $400 / 1-\AA^{\prime \prime}$ pull. Size $24^{\prime \prime} \times$
 20 p post and insurance

## MAINS TRANSISTOR POWER PACK

Designed to operale Adjustable output 6 V . 9 V , 12 volts for up to 500 mA (class B working). Takes the place of any of the following batteries: PP1. PP3. PP4. PP8. PP7,
PP9, nod others. Kit comprises: mains transformer rectifier. smoot hing and load resistor cons 20 postage

## DESK TELEPHONES

Ex G.P.O. Black standard model with dialing dial but no internal bell. Supplied with connection diagram
sap each-post etc. 4 p for single then 6rp per pair. Ditto. with bell plus 40 p post for single then 65 p per pair.

PAPST MOTORS
Est. $1 / 40$ th h.p. Made for $110-120 \mathrm{~V}$ working, but two of A really bearifl mains reversible. 1165 each. Postage one 23p. $1 w 0$ 230 V modet $£ 3.30$.

## 10 AMP DIMMER CONTROL



For the control of lighting on stage or in a studio or for control of portabic equipment in workshops, eic. This has two 13 amp socket outtets each is controlled by a 5 amp solid state regulator. The overall length is 17 in ., width 3 tin. and depth 1 fin . In the end is fitted a master OrvOff switch indicator, lamp and fuse. Price $\mathbf{5 8} \cdot \mathbf{2 5}$.

## STANDARD WAFER SWITCHES

Standard size is wafer-silver-piated 5-amp contact

| No. | Standard size $1{ }^{\prime \prime}$ wafer-silver-plated 5 -amp contact. standard $\frac{4}{}^{*}$ spindle $2^{\prime \prime}$ long-with locking washer and nut. |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| of Poles | 2 way | 3 way | 4 way | 5 way | 6 way | 8 way | 9 way | 10 way | 12 way |
| 1 pole | 44p | 44 p | 44p | 44 p | 44 p | 44p | 44 p | 44 p | 44 p |
| 2 poles | 44p | 44p | 44p | $4 \mathrm{4p}$ | 44p | 44P | 44p | 77 | 77p |
| 3 poles | 44p | 44 p | 44p | 44 p | 77 p | 77 p | 77 | ¢104 | 5104 |
| 4 poles | 44p | 44p | 44 p | 77 p | 77 p | 77p | 77p | 51.32 | ¢1.32 |
| 5 poles | 44 p | 44 p | 77 P | 77 p | ¢104 | 51.04 | 5104 | ¢160 | 1160 |
| 6 poles | 44p | 77 p | 77p | $77 p$ | ¢104 | 5104 | ¢ 11.04 | ¢187 | 41.87 |
| 7 poles | 77 p | 77 p | $77 p$ | 5104 | ¢1.32 | ¢1.32 | ¢1.32 | £2-15 | ¢2.15 |
| 8 poles | 77 p | 77p | 77 | ¢1.04 | ¢1.32 | ¢1.32 | E1.32 | £2 42 | ¢2. 42 |
| 9 poles | 77p | 77p | ¢104 | 51.04 | ¢160 | \&140 | ¢ 11.60 | ¢2.70 | ¢ 52.70 |
| 10 poles | 77p | 77 p | 5104 | ${ }_{6} 1.32$ | ¢1 60 | £160 | 11.70 | E3.00 | 53.25 |
| 11 poles | 77p | 1104 | f104 | ¢1.32 | £1.87 | ¢1.87 | \$1.87 | 53.25 | 53.25 |
| 12 poles | 77p | 1104 | £104 | ¢1.32 | £1.87 | £1 \% | 11.87 | f3. 5.2 | E3.52 |

## CAPACITOR DISCHARGE CAR IGNITION

## ELECTRONIC IGNITION

 This system which has proved to be amazingly efficient and reliable was first described in the Wireless World about a year ago. We can supply kit of parts for an improved and even more efficient version (Practical Wireless. June). Price 65.55 plus 20 p post. When ordenng please state whether for posiDe-luxe model including printed circuit board. etc. $£ 7$. 9 .

## MULLARD UNILEX

This D.I.Y. Stereo Amplifier is still available complete at 57.00 for the four Mullard Modules, or Modules can be bought separately as follows:- 4 wat amplifies module
Pre amp module Mullard Ref. No. E.P. $9001-\varepsilon 198$ each.
P0
Power Module-Mullard Ref. No. E.P. $9002-$ E2.53 each
In addition and made to Mullard specification knebs- $\mathbf{3 3} 30$.
Knobs-Set of 4-50p.
Special offer the complete Unilex with control panel at PTe VAT priceSpecial offer the
$\mathbf{1 0 0 0 0}$ post paid.


## ZPM MODULATION MOTOR

Could also be used to open ventilators. doors. valve. damper ete. particularly suitable for remote controf. Made by Saichwell. Essentially a reversible geared motor fitted with internal limit swithes to stop it at the end of its travel. Size approx. bin. $x$ sin. $x$. m . and weighing approx. powerful and would lift a heavy door or open a long line of
ventilators. To operate this motor you put the so cycle supply ventilators. To operate this motor you put the so cycle supply through a changeover switch. For instance a her growing house. chicken hatchery. could automatically regulate the remperature in awo the slate of open or close. Also etc. An indicator on the motor graduatede from this to a volt meter would give a remore indication of the open or close position. A very expensive motor if both direct from Satchwell., our price complete with step down Transformer is $\mathbf{\varepsilon 1 6} \cdot 50$.

## CENTRIFUGAL FAN

Mains operated. turbo blower type. Pressed steel housing contains motor and impeller. Motor is $1 / 10$ th h.p. giving considerable air flow but virtually no noise. Approx. dimensions 104 in . wide $\times 12 \mathrm{in}$. dia. outlet into trunking $104 \times 4 \ddagger$ in. 66.55 plus 51 post and insurance

## THIS MONTH'S SNIP

## TAPE PLAYBACK UNITS

Mains operated. Made by Reditune the famous "music in background people". These are complete units ready to work and we understand that they are in good going order. We have not lested them but would exchange any That do not work properly. These have a supenor motor nd also an even equally useful valve amplifier with FI
 and also an even equally usefur valve amplifier with EL 84 outpur. in a steel case

3 hour cassettes. already recorded light music $51 \cdot 10$ each extra

## 24-HOUR TIME SWITCH

Made by Smiths, these are A.C. mains operated. NOT CLOCKWORK. Ideal for mounting on rack or shelf or can be built into box with 13A socket. Two completely adjustable circuit on or off during these periods. $\mathbf{2 9 5}$ post and ins. 25 p. Additional time contacts 55 p pair.


MULLARD AUDIO AMPLIFIERS
All in module form, each ready buitt complete with heat sinks and connection tags, data supplied. Model 1153500 mW power output 72 p . Model EP9000 4 watt power output $\$ 160$ EP9001 twin channel or stereo pre amp. 11 \$9. $10 \%$ discount if 10 or more ordered.

NEW ITEMS THIS MONTH
MINI SEALED RELAY
American made. Our Ref: REL A1. Measures only $3^{n}$ wide $\times \ddagger^{\prime \prime}$ thick and $\ddagger^{\prime \prime}$ high and it's a double changeover. we don't know the contact rating but estimate this at $3 / 5$ amps. The coil resistance is 600 ohms
and $9-12$ volt will close it. Ideal for models and miniaturised equipment. It's a plug in relay but we supply compicte with base. Price zap including base SUB-MINIATURE MICROSWITCH
Made by Burgess. their Ref V476-our Ref MS. Al.
 over contacis
or 10 for $\$ 144$.

## 3-CORE MAINS FLEX

Metric size. 5 mm which is approx. equivalent to the old $14 / 36$ rating. Suitable therefore for mower or imilar portable tools. Cores are colour coded to the new European standard. Brown-live ... Bheneutral . Yellow/Green-earth. Grey P.VC
covered overall. 100 mm coils $\mathrm{E5}, 50 \mathrm{~m}$ coils B and covered overall. 100 mm cols $65,50 \mathrm{~m}$ coils 53 and
25 m coils EI .75 . Post 40 p on 100 m . 25 p for 50 m coils 25 m coils EI 75 . Post
and 20 p for 25 m coils.

## PANIC SWITCHES

Tough enough to foot switches and elegan enough to be panel switches. Bakelite construction with white push rod. Price 40 p less quantity discounts Switch is rated $250 \mathrm{~V}-2 \mathrm{amp}$

## 20 WATT CAMPING LIGHT

Also makes good car emergency light. This uses a car battery drawing approx. 1A. This gives illumi nation per amp hour of battery life far in excess to filament lamps and in fact to the miniature 8-13 watt camping lights often offered. Complete unit ready to operate, in strong white ename lied metal frame These would normally sell at $\mathrm{f6}$ are unused bu slightly soiled and

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