PRACTICAL ECTRONIC APRIL 1972 20.

PEF STEREO TUNER

OFF ON

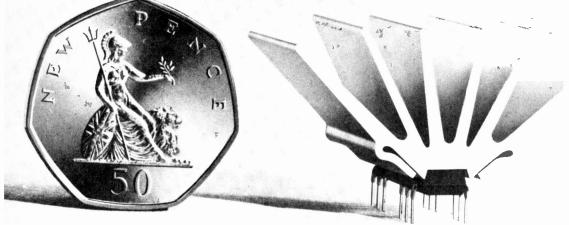
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TUNING

GEMINI STERED TUNER



Super IC-12



High fidelity Monolithic Integrated Circuit Amplifier

Two years ago Sinclair Radionics announced the World's first monolithic integrated circuit Hi-Fi amplifier, the IC.10. Now we are delighted to be able to introduce its successor, the Super IC.12. This 22 transistor unit has all the virtues of the original IC.10 plus the following advantages:

- 1. Higher power.
- 2. Fewer external components.
- 3. Lower quiescent consumption.
- 4. Compatible with Project 60 modules.
- Specially designed built-in heat sink. No other heat sink needed.
- 6. Full output into 3, 4, 5 or 8 ohms.
- 7. Works on any voltage from 6 to 28 volts without adjustment.
- 8. NEW 22 transistor circuit.

SINCLAIR GENERAL GUARANTEE Should you not be completely satisfied with your purchase when you receive it from us, return the goods without delay and your money will be refunded in full, including cost of return postage, at once and without question. Full service facilities are available to all Sinclair customers

- Output power 6 watts RMS continuous (12 watts peak).
- Frequency Response 5 Hz to 100KHz \pm 1 dB

Total Harmonic Distortion Less than 1%. (Typical 0.1%) at all output powers and all frequencies in the audio band.

Load Impedance 3 to 15 ohms.

Input Impedance 250 Kohrns nominal.

- Power Gain 90dB (1,000,000,000 times) after feedback.
- Supply Voltage 6 to 28 volts (Sinclair PZ-5 or PZ-6 power supplies idea!).

Quiescent current 8mA at 28 volts; low enough to make the IC.12 ideal also for battery operation.

Size 22 x 45 x 28 mm including pins and heat sink.

With the addition of only a very few external resistors and capacitors the Super IC.12 makes a complete high fidelity audio amplifier suitable for use with pick-up, F.M. tuner etc. Alternatively, for more elaborate systems, modules in the Project-60 range such as the Stereo 60 and A.F.U. may be added



FREE 44 page instruction manual now included with all units. Available free on request to present IC.12 users. Gives full circuit and

wiring diagrams for many applications including car-radios, oscillators, etc.



Price, Inc. FREE printed circuit board for mounting and manual. **£2.98** Post free

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Sinclair Project 60

The World's leading range of high fidelity modules







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All you need for a superb 30 watt high fidelity stereo amplifier.

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Project 60 offers more advantage to the constructor and user of high fidelity equipment than any other system in the world.

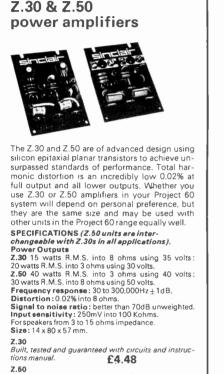
Performance characteristics are so good they hold their own with any other available system irrespective of price or size.

Project 60 modules are more versatile – using them you can have anything from a simple record player or car radio amplifier to a sophisticated and powerful stereo tuner-amplifier. Either power amplifier can be used in a wide variety of applications as well as high fidelity. The Stereo 60 pre-amplifier control unit may also be used with any other power amplifier system, as can the AFU filter unit. The stereo FM tuner operates on the unique phase lock loop principle to provide the best ever standards of sensitivity and audio quality. Project 60 modules are very easily connected together by following the 48 page manual supplied free with all Project 60 equipment. The modules are great space savers too and are sold individually boxed in distinctive white and black cartons. With all these wonderful advantages, there remains the most attractive of all – price. When you choose Project 60 you know you are going to get the best high fidelity in the world, yet thanks to Sinclair's vast manufacturing resources (the largest in Europe) prices are.fantastically low and everything you buy is covered by the famous Sinclair guarantee of reliability and satisfaction.

Typical Project 60 applications

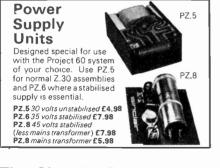
System	The Units to use	together with	Cost of Units
Simple battery record player	Z.30	Crystal P.U., 12V battery volume control	£4.48
Mains powered record player	Z.30, PZ.5	Crystal or ceramic P.U. volume control etc.	£9.45
20 + 20 W. stereo amplifier for most needs	2 x Z.30s, Stereo 60, PZ.5	Crystal, ceramic or mag. P.U., F.M. Tuner, etc.	£23.90
20 + 20 W. stereo amplifier with high performance spkrs.	2 x Z.30s, Stereo 60, PZ.6	High quality ceramic or magnetic P.U., F.M. Tuner, Tape Deck, etc.	£26.90
40 + 40 W. R.M.S. de-luxe stereo amplifier	2 x Z.50s, Stereo 60 PZ.8, mains trsfrmr	As above	£34.88
Indoor P.A.	Z.50, PZ.8, mains transformer	Mic., guitar, speakers, etc., controls	£19.43
F.M. Stereo Tuner (£25) 8	& A.F.U. Filter Unit (£5.98)	may be added as required.	

from a simple amplifier to a complete stereo tuner amplifier with Project 60 modules



4

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The Sinclair Guarantee

If within 3 months of purchasing Project 60 modules directly from us, you are dissatisfied with them, we will refund your money at once. Each module is guaranteed to work perfectly and should any defect arise in normal use we will service it at once and without any cost you whatsoever provided that it is returned to us within 2 years of the purchase date. There will be a small_charge for service threafter. No charge for postage by surface mail. Air-mail charged at cost.

Project 60 Stereo F.M. Tuner



First in the world to use the phase lock loop principle

The phase lock loop principle was used for receiving signals from space craft because of its vastly improved signal to noise ratio. Now, Sinclair have applied the principle to an F.M. tuner with fantastically good results. Other original features include varicap diode tuning, printed circuit coils, an I.C. in the specially designed stereo decoder and squelch circuit for silent tuning between stations. Good reception is possible in difficult areas, and often a few inches of wire are enough for an aerial. In terms of a high fidelity this tuner has a lower level of distortion than any other tuner we know. Stereo broadcasts are received automatically as the tuning control is rotated, a panel indicator lighting up as the stereo signal is tuned in. This tuner can also be used to advantage with any other righ fidelity system.

SPECIFICATIONS—Number of transistors: 16 plus 20 in I.C. Tuning range: 87.5 to 108 MHz. Cepture ratio: 1.5d8. Sensitivity: $2\mu V$ for 30dB quieting: $7\mu V$ for lock-in over full deviation. Squelch level: $20\mu V$. A.F.C. range: ± 200 KHz. Signal to noise ratio: > 65dB. Audio frequency response: 10 Hz = 15 KHz ($\pm 1dB$). Total harmonic distortion: 0.15% for 30% modulation. Stereo decoder operating level: $2\mu V$. Cross talk: 40dB. Output voltage: 2 x 150mV R.M.S. Operating voltage: 25-30 VDC. Indicators: Power on/tuning/stereo. Size: 93 x 40 x 207 mm.

Built and tested. Post free.

£25

Stereo 60 Pre-amp/control unit



Designed for Project 60 range but suitable for use with any high quality power amplifier. Again silicon epitaxial planar transistors are used throughout, achieving a really high signal-to-noise ratio and excellent tracking between channels. Input selection is by means of push buttons and accurate equalisation is provided for all the usual inputs. **SPECIFICATIONS—Input sensitivities:** Radio – up to 3mV. Mag. p.u. 3mV: correct to R.I.A.A curve $\pm 1dB.20$ to 25,000 Hz. Ceramic p.u. – up to 3mV: Aux – up to 3mV. **Output:** 250mV. **Signal to noise ratio:** better than 70dB. **Channel matching:** within 1dB. **Tone controls:** TREBLE + 15 to --15dB at 10 KHz: BASS + 15 to --15dB at 100Hz. **Front panel:** brushed aluminium with black knobs and controls. **Size:** 66x 40x 207mm. **£9.98**

A.F.U. High & Low Pass Filter Unit



For use between Stereo 60 unit and two Z.30s or Z.50s, and is easily mounted. It is unique in that the cut-off frequencies are continuously variable, and as attenuation in the rejected band is rapid (12dB/octave), there is less

loss of the wanted signal than has previously been possible. Amplitude and phase distortion are negligible. The A.F.U. is suitable for use with any other amplifier system. Two filter stages – rumble (high pass) and scratch (low pass). Supply voltage – 15 to 35V. Current – 3mA. H.F. cut-off (–3dB) variable from 28KHz to 5KHz. L.F. cut-off (–3dB) variable from 25Hz to 100Hz. Distortion at 1KHz (35V. supply (0.02% at rated output. Size: 66 x 40 x 90 mm. Built tested and guaranteed.

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vorr mouth-organ type reed, etc. Fully tran-sistorised. SELF-CONTAINED LOUD-SPRAKEE, Filten sprate keys span too full octave--play the "Yellow Rose of Texa", etc. etc. You have the thrill and excitement of building it together with the pleasure of playing a real, live, portable electronic organ. **AO PERFYOUS KNOWLEDGE OF ELECTRONICS INFERDED**. No soldering necessary, simple is adE0 to make. Argone over nine years can build it easily in one extended by this instructions. **ONLY 20**,76 + 23 p & p for kit, including case, nuts, screws, simple instructions, etc. Uses standard battery (parts available separately). Have all the pleasure of making it yourself, fulls with an exciting gift for someone. **EFADY RIJILT AND TESTED**

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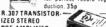
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R.152 STERED CAR

SPEAKERS

you'll find it hard to peat. 0.7 mil diamond stylus. Dut-put: GmV per channel. Frequency range in como My Channel balance: ±15dB. put: 5mV per channel. rrequency renge 30-18,000 Hz. Channel balance: 1 5dB, Channel separation: 20dB. Recommended stylus pressure: 2-4 grams. Compliance: 9 10-6 cm/dyne. ROC PRICE C4 75

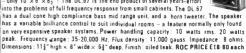


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AM-395 SYSTEM Olson AM-395 Stereo Amplifier, Garrard SP25 Mk III Record Player with Eagle LC07 Stereo Magnetic Cartridge, plinth and cover and a pair of Eagle OL.67 Speakers. Matching RA.310 Stereo Tuner £39-95 extra if required. Normal Price £106-65 ROC PRICE £92-60

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REALISTIC SA-500 SYSTEN Realistic SA-500 Stereo Amplifier Garrard SP25 Mk III Single Record Player with Eagle LC.07 Stereo Magnetic Cartridge, plinth and cover and a pair of Crysler CE-5b Speakers, Normal Price £157-95 ROC PRICE F174 nn

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Practical Electronics April 1972



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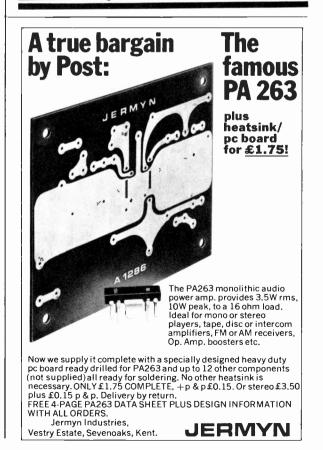
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BRUSHED ALUMINIUM PANELS I $2in \times 6in = 25p;$ $12in \times 2\frac{1}{2}in = 10p;$ $9in \times 2in = 7p,$	Prin insertion tool 52p Spot face cutter 42p Pkt. 50 pins 20p 20p 9V mains power supply. Same size as PP9 battery.

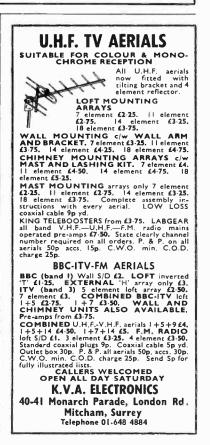
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12V x 2 5A x 2 MT 72				
30 volts. All tapped a	t 0-12-10-20-24-30V			
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	Amps. £ 4p 3A MT 20 AT 2.64 30p			
1A MT 79 AT: 1.55 2	8p 4A MT 21 AT 3 10 40p			
	8p 5A MT 51 AT 4-31 38p			
50 volts. All tapped a				
500 mA MT 102 AT: 1 45 22	2p 3A MT 105 AT 8-91 39p			
1A MT 103 AT 2:00 30	Dp 4A MT 106 AT 5.06 39p Dp 6A MT 107 AT 7.47 50p			
2A MT 104 AT 8-10 30	op 6A MT 107 AT 7.47 50p			
60 Voits. All tapped a	t 0-24-30-40-48-60V			
500 mA MT 124 AT: 1.46 21	p 2A MT 127 AT 3 16 39p			
IA MT 126 AT 2-24 28	Bp 3A MT 125 AT 4-59 39p			
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Power Winding tapped at	Ref. No. Price P. & P.			
output				
20 VA 0-115-210-240	MT 113 CT 20.89 14p			
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0-15-27×2 500 mA×2	MT 203 AT* \$2.45 27p MT 204 AT* \$8.42 28p			
10-12-0-12-20 700 mA (d.c.)	MT 221 AT* 21.11 24p			
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JARGON

IN common usage, "jargon" appears to have become relegated to a term of disparagement. Complaints concerning the use of jargon are generally intended as motions of censure for some supposed grammatical or lingual offence. True the Concise Oxford Dictionary gives some rather unflattering definitions for jargon: "Unintelligible words . . . barbarous or debased language . . ." Other authorities assert that the much abused word jargon is a perfectly correct and proper synonym for a private or technical language. It follows, of course, that a technical language is likely to be unintelligible to those unfamiliar with the subject concerned. But who can with reason argue that technical language is improper or out of place in a technical journal, for example?

Every profession and trade, just like every branch of science and art, has its own private language, or jargon. Contrary to some beliefs jargon is not always an invention to bar or confuse the uninitiated. Usually it serves a very practical purpose: it facilitates verbal and written communication and, since it is generally succinct and incapable of misinterpretation, allows for economy in speech no less than in print.

Electronics, being a fast growing technology, has, not surprisingly, built up its own large vocabulary of technical terms. Some of these have a very homely ring about them, since they have been "borrowed" from the common language. They are well used to describe unequivocally the prevailing state of some given circuit or its mode of operation. Many terms have been specially coined, and they often create a vivid mental picture of the circuit action they describe. Yet other well known technical terms are formed from abbreviations or from the initial letters of complicated descriptions relating to materials or manufacturing processes. A custom that is quite widespread in ordinary everyday affairs, as well.

Without the use of this convenient terminology detailed explanations of circuit operation would become rambling, verbose discourses, tiresome to the reader and a strain upon his mental digestive system. This is, perhaps, nowhere better demonstrated than in the case of logic design where complex systems can be built up from a large number of basic circuit blocks interconnected in various configurations, but with much pattern repetition.

A knowledge and understanding of this special vocabulary (or call it jargon, if you will) is an indispensable part of training and education in electronics. It is the common language used and recognised by all those involved professionally, in trade and industry; it should be equally familiar to private constructors and students. It certainly should not be disparaged. F.E.B.

THIS MONTH

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Our May issue will be published on Friday, April[']14.

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BY C.J. MILLS

DOUBLE

Lights two 8W lamps from a 12V car battery. Ideal for campers or for emergency lighting

N this article designs for both a push-pull inverter to supply two 12in, 8 watt fluorescent lamps and a simpler unit to drive an 8 watt lamp will be given. Both units will operate from a 12V d.c. supply.

The use of a high frequency of oscillation for the inverters (20–30kHz) provides several advantages. It gives a more efficient operation of the lamp and it permits the use of smaller and more convenient types of transformer such as the Ferroxcube pot cores. Also, as this frequency is above the audio range, there are no annoying whistles from the units.

PUSH-PULL INVERTER

Now that high frequency planar power transistors are available at reasonable prices, the construction of high frequency power inverters becomes attractive because of the small size of transformer required.

As planar transistors are more prone to failure under reverse bias conditions than others, inverter switching circuits have to be modified to prevent this by the inclusion of high speed diodes in the bias circuit.

In Fig. 1 is shown the circuit of a 20 watt inverter suitable for driving two 8 watt fluorescent lamps or one 13 watt lamp. There is also a 230V d.c. outlet which can be used for powering a shaver.

To understand the circuit action, assume that TR1 is off and TR2 on and ignore the effect of the starting bias network. TR2 obtains its base current

from the feedback winding L1 via C6 and will stay on until this capacitor is discharged.

When the discharge current drops below the holding base current of TR2 its collector current drops and it is switched off while TR1 is switched on by transformer action. The reversed feedback voltage now holds TR1 on until the capacitor is again discharged and the cycle is completed. The timing of the on periods depends on the exponential discharge of the capacitors and therefore the CR time constant controls the frequency of operation. With this circuit a good square wave output can be obtained at frequencies up to 50kHz and the low saturation voltage of the transistors result in low dissipation and high efficiency operation.

The secondary circuit has to provide three separate heater supplies and sufficient volts to strike the fluorescent lamps. The current for the lamp LP2 is limited by the choke L6 which integrates the square wave input voltage to give a triangular wave. LP2 is supplied through a capacitor giving a differentiated wave form.

The result of these wave changes is a more efficient loading of T1.

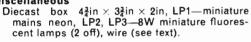
SHAVER SUPPLY

To obtain a 250V d.c. supply for an a.c./d.c. shaver, the voltage from L5 is switched via S2 to a bridge rectifier. Small capacitors are added at the output to boost this voltage.

DOUBLE LAMP INVERTER

Resistors

R1 1kΩ R2 220Ω R3 22 Ω R4 **18** Ω R5 **220** Ω 22 Ω **R**6 R7 1k Ω All #W, 10% carbon Capacitors C1 0.047μF 1,000pF 300 V C2 C3 4,700pF 300 V C4 0.68µF 300 V C5 4,700pF 300V C6 0.18µF Transistors TR1, TR2 BD123 (2 off) Diodes D1, D2 IN916 (2 off) D3-D6 Silicon Bridge Rectifier 400V 2A Switches S1 5A single pole on/off toggle S2 Single pole changeover Transformer T1 35mm Ferroxcube type FX2242 Choke L6 25mm Ferroxcube type FX2240 **Miscellaneous**



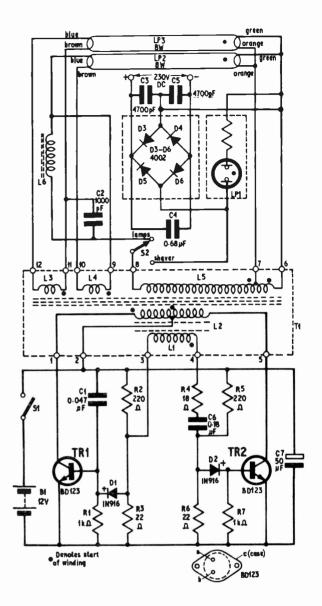


Fig. 1. Circuit diagram of 20 watt inverter

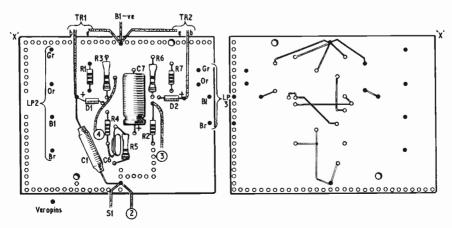


Fig. 2. Assembly and wiring details of component board

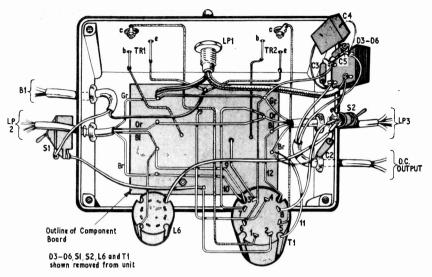


Fig. 3. Interwiring details of case mounted components and the board of Fig. 2

COIL WINDING

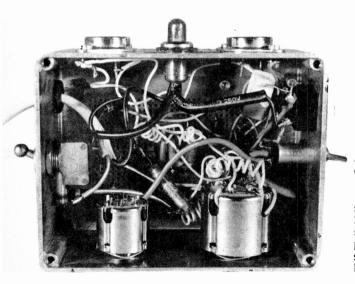
The transformer consists of a 35mm Ferroxcube pot core with the bobbin wound as follows.

First, about 10ft of 24 s.w.g. enamelled copper wire is doubled and wound on to the bobbin evenly until a double winding for L2 of 14 turns for each half is obtained. Next, the 15 turn feedback winding L1 is added using 30 s.w.g. wire followed by a layer of plastic insulating tape.

The main secondary winding L5 is now added, this being tapped after 10 turns and another 220 turns then put on and insulated with a layer of tape. Finally, two 10 turn heater windings (L3, L4) are put on.

Each winding should be identified by different coloured sleeving before fitting the bobbin into the pot core.

To operate a 13 watt fluorescent lamp the turns on L5 should be increased to 270 and one of the insulated heater windings omitted.



For the choke L6 a 25mm Ferroxcube FX2240 is used with 200 turns of 36 s.w.g. enamelled copper wire wound on the bobbin.

CONSTRUCTION

A suitable case for the inverter is a $2in \times 3\frac{3}{4}in \times 4\frac{3}{4}in$ diecast box which can be used as a heatsink for the transistors if these are mounted with mica washers and plastic bushes.

Three holes are drilled in the case and fitted with grommets for the input and output leads. Ideally all output leads should be screened to reduce capacity coupling and radio interference to a minimum.

Assembly and wiring details for the inverter are given in Figs 2 and 3. Here the circuit board should be trimmed to fit inside the case and two holes drilled to take supporting screws.

TESTS AND ADJUSTMENTS

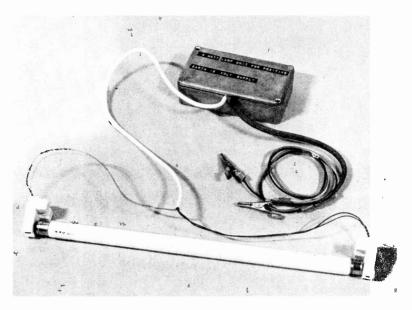
The first test of the inverter should include some means of current limiting to prevent transistor damage under fault conditions. A series 10 watt wire wound resistor of 2 to 3 ohms is suitable and should be included in the battery line. An ammeter, if available, should be used to check the current.

With no loads connected and S2 switched to "Shaver" the unit can be switched on for a short period. If the neon lights this proves that the inverter is oscillating; with no overheating apparent the limiting resistor can be removed and the lamps connected.

The relative brightness of the lamps can be adjusted by trimming C2 or by adjusting the frequency by trimming C6. Increasing this will reduce the frequency which will reduce the voltage drop across L6 and increase the voltage drop across C2 with both lamps alight the current drawn should be about 1.8A.

A test load resistor of between 5 and 10 kilohms should be connected across the bridge output when S2 is switched to "Shaver". The switch does not stop heater current being taken by the lamp, so to prevent excessive current being drawn the inverter should not be operated in the d.c. mode without a load (shaver) connected.

8 WATT Fluorescent LAMP Inverter



SINGLE LAMP INVERTER

A circuit for driving a single 8 watt fluorescent lamp is given in Fig. 4. As can be seen this is less complex than the push-pull inverter, its working principles being based on the well known Hartley oscillator.

This particular configuration has the advantage that the collector of TR1 can be connected to the case which forms the heat sink. A disadvantage is

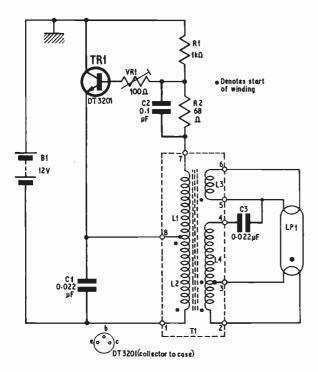
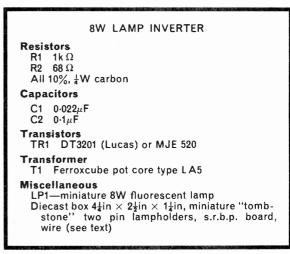


Fig. 4. Circuit diagram of single lamp inverter

Practical Liectronics April 1972

COMPONENTS . . .



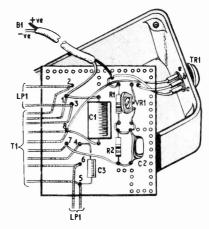
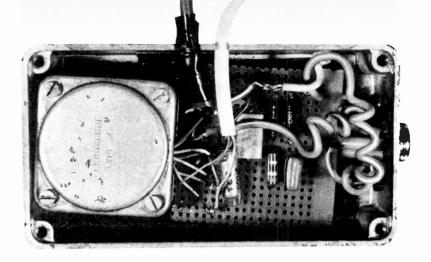


Fig. 5. Assembly and wiring details of the single lamp inverter $% \left({{{\rm{AS}}} \right)$



Interior of single lamp inverter. The components board should be insulated from the case

the risk of an excessive voltage appearing across the transistor if the unit is operated off load. Whilst there are a number of protective techniques available these were not found necessary providing the particular silicon transistor specified is used.

It is possible to strike the fluorescent lamp without the heater supply if sufficient volts are applied but this is not recommended as it shortens the life of the lamp.

No attempt was made to switch out the heaters when the lamp has struck or to switch the heaters across the battery prior to starting as the additional power consumed by the heaters is small and the extra size and complexity of the switching required was not considered worthwhile.

CURRENT LIMITING

The secondary voltage developed by the inverter must be high enough to strike the lamp and this voltage is higher than the normal running voltage. The lamp provides a negative resistance load on the inverter and a series impedance is required to limit the lamp current and drop the running voltage to the correct value.

The series impedance normally consists of a choke for higher power lamps; a small value capacitor (C3) is adequate.

TRANSFORMER

The transformer T1 consists of an LA5 pot core wound as follows. The primary winding L2 is wound on first using 26 turns of 26 s.w.g. enamelled wire. The feedback winding (L1) is then added using 10 turns of 32 s.w.g. wire and in the same direction as the primary. After identifying the ends with coloured sleeves, wrap round a layer of plastic insulating tape.

The secondary winding (L4) consisting of 350 turns of 36 s.w.g. wire is put on next, with a tapping at 18 turns for a lamp heater supply. The ends of this are identified and a layer of tape added. A final heater winding, in the same direction as before, of 20 turns of 32 s.w.g. wire (L3) completes the transformer.

UNIT CONSTRUCTION

The next stage is to mount TR1 on a $4\frac{1}{4}$ in $\times 2\frac{1}{4}$ in \times lin diecast box as in Fig. 5. The Ferroxcube pot core is attached to a piece of s.r.b.p. board which

approximately fits the box area. One method of doing this is to fit 6B.A. soldering tags under two adjacent screws on the pot core and to solder these tags to pins fitted on the board.

After completing the component assembly, unit operation should be checked. An a.c. meter or neon can me used to check that the unit is oscillating and producing a secondary voltage. Failure of the circuit to operate is indicated by a low input current and no secondary volts and can be due to a reversed or shorted winding or a low supply voltage.

CONNECTING THE LAMP

When the fluorescent lamp is connected and the normal supply voltage is applied, an initial glow at the tube ends will indicate that the heaters are working and the tube should strike and light up almost immediately.

To assist in starting during preliminary tests a fine wire can be stretched between the metal end caps and connected to one of the heater pins. This may be dispensed with in normal use if the tube is mounted close to a metal mounting bracket which is connected to the metal end caps and one heater winding.

When the lamp lights, small adjustments of bias and frequency can be made to obtain the best performance. The variable resistor VR1 should be set for optimum light output with satisfactory starting under the worst conditions (a cold lamp and a low voltage battery).

The oscillator frequency can be adjusted by varying the capacity across the primary or secondary coil until it is above the audio range. If measuring facilities are available a frequency of 18 to 25kHz is suitable remembering that the capacity of the cable connecting the lamp to the inverter will have some effect.

The final power consumption of the unit should be about 0.6A at 12V. \bigstar

P.E. BINDERS =

Binders for P.E. are available from Post Sales Department, IPC Magazines Ltd., 68, Great Queen Street, London, W.C.2. Price including postage, 85p State Volume Number required



RADIO TELESCOPE TO REACH LIMITS OF THE UNIVERSE?

A new radio telescope of very large aperture (VLA) is to be set up by the National Science Foundation under Dr W. McElroy. This is an outstanding project which will make this aerial system the most sensitive and greatest collector of radio energy from outside the earth.

No final details are at present available but the general design will be in the form of multiple dishes in a pattern 25 miles long. Situated somewhere in the mountainous area of south western America, it will take four to six years to build at a total estimated cost of 60 million dollars.

The limit of range is claimed to be between 13 and 16 thousand million light years. Hitherto, the limit of any array has been thought to be 13 thousand million light years because this is approximately the limiting factor set by the speed of light. Dr Edward David, who is science adviser to the President, has said that the project will put America in the forefront of space exploration by radio for many years to come.

Carl Sagan, one of the pioneers of listening for signals that might emanate from other civilisations, stated recently that an instrument of such gathering power would be ideal for seeking evidence of external intelligences.

GRAND TOURS CANCELLED

The proposed grand tours of space have been dropped from the new U.S. space budget funding as being too expensive when set against the scientific value of data recovered. This will be a disappointment to many astronomers engaged on studies of the solar system.

SKYLAB

There are three scientists among the crew for *Skylab* space laboratory. This space station will go into orbit for seven months next year.

The date for launch is April 30, 1973. A day later the first threeman crew will be launched in an *Apollo* spacecraft. They will meet and lock on to the space station, the crew will then transfer aboard and work for 28 days in *Skylub* before returning to earth.

If this first mission is successful then about a month later the second crew will journey to the space station where they will stay for 56 days. After their return and another waiting period the third crew will go to *Skylab* for another 56 days.

The work undertaken in Skylah will be extensive and will include observations of the sun with a large telescope attached to the station; observations of earth resources and the weather; and experiments in chemical, physical, and medical effects of weightlessness.

The nine crew members together with the six back-up crew will represent more than a third of the 44 active astronauts. Those not assigned to *Skylab* missions will be undergoing training for the space shuttle programme with the exception of the crews for *Apollo 16* and *Apollo 17* due for launch in April and December 1972.

MARINER 9

Mariner 9 has been raised in orbit a second time to compensate for Mars effect on the spacecraft. This effect is twofold; the direct effect is the gravitational field of the planet and the perigee point has been raised from 1.388km (862 miles) to 1,650km (1,025 miles). This increases the mean orbital period by some 79 seconds to 11hr, 59min. 32sec.

Originally three 20-day cycles were allocated to complete the revised mapping of the surface with a fourth period in hand as reserve. The great dust storm which raged for so long has made it necessary to complete this mapping in two 20-day cycles with a third period to cover gaps. The raising of the orbit will facilitate this. The other reason for changing the craft's orbit is that the distance between Mars and earth is now rapidly increasing. This requires a reduction in data transmission time. The present rate is 16,200 bits/sec. Another important item is that the Golstone antenna will have to be assigned to the Apollo 16 mission. The new orbit places the transmissions in the middle of the Golstone viewing period ensuring maximum transfer of data.

NEW VIEW OF MARS

The surface changes on Mars have always excited the imagination from the time of Schiaparelli in 1877. With the fading hopes of life on Venus it was natural to turn to Mars. Markings on Mars were translated as being due to vegetation of an elementary kind which extended with the release of moisture from melting polar caps.

Even this interpretation is now suspect. Again it is the pioneer Carl Sagan together with Peter Gierasch and Joseph Veverka who have been responsible for directing attention to other explanations. This team have shown that the Viking landings on. Mars will face difficulties as a result of the dust storms and atmospheric disturbances.

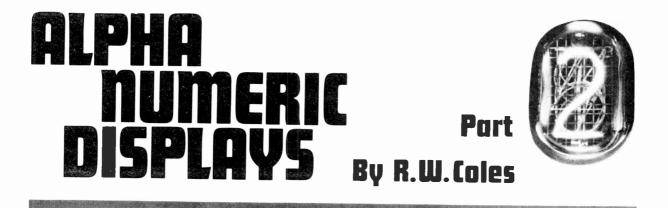
There is a special difference between the generation of wind on the earth and on Mars. Essentially, the difference is that on Mars the vertical relief is comparable with the scale height. This is the vertical distance over which the pressure falls by half.

In the mountainous areas it means that the isotherms follow the contours of the relief. The result is that if a horizontal wind strikes a slope it runs into a steep thermal gradient. It is possible that convective relief winds could attain velocities of 250mph.

There are also global systems which are seen to be dependent on the seasonal heating and cooling of the polar regions. If local and global winds should reinforce one another then speeds nearing 400mph could arise.

The highest velocities are found over large basins and large plateaux which are bordered by steep relief. The velocities are many times greater than the minimum required to transport the dust.

Maximum activity should come when Mars is at perihelion since the effects of uneven solar heating would ensue. It would appear then that the growth of the dark areas are due to wind activity. Radar measurements have already shown that the dark areas are zones of high elevation. An extensive study of the Syrtis Major area confirms that the changes, seasonal and secular, are in conformity with the observation of windborne dust.— Exit little green men?



Decoding & Driving circuits for cold cathode tubes

WHEN it is necessary to display the contents of a memory or counter a special circuit is needed to decode the B.C.D. into a one out of ten output in order to drive a cold cathode tube. The outputs of this circuit must be able to withstand the high voltages present when driving these tubes. In this month's article some of the methods of decoding and driving are described.

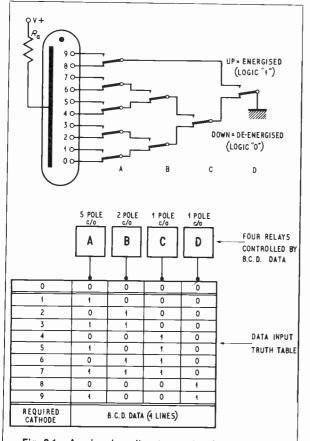


Fig. 2.1 A relay decoding tree using four relays to switch the inputs of a cold cathode tube

DECODING AND DRIVING CIRCUITS

The simplest way of energising a numicator tube is to select the required cathode by means of a multi way switch, the pole of which is earthed. This is fine if such a simple system is compatible with the display inputs, but in most cases data will be presented to the display in coded form, requiring some method of decoding before application to the tube.

The code most used is the simple four bit (bit means binary digit) code called B.C.D. (binary coded decimal). A four bit binary group has 16 possible combinations, which by convention are equivalent to the decimal numbers 0 to 15. In the B.C.D. code only the first ten combinations are utilised to represent the numbers 0 to 9, and if the other six combinations are allowed to appear, they represent error states. With this in mind we can now look at the simplest way of decoding the B.C.D. code, and using the decoded form to drive a cold cathode tube.

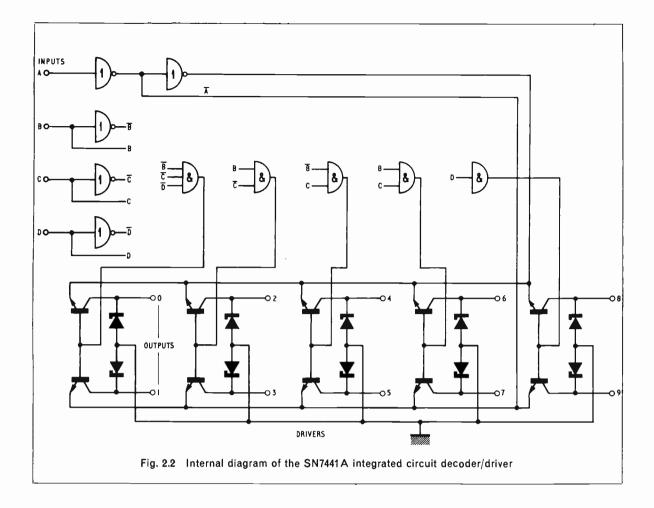
Fig. 2.1 shows a circuit arrangement known as a relay decoding tree, which consists of four relays each controlled (energised or de-energised) by a particular digit in the B.C.D. group. If a digit is a logic "1" then its associated relay will be energised, and if it is a logic "0" its relay will be de-energised, each B.C.D. combination giving a unique path through the relay contacts to the appropriate cathode.

Decoding trees of this kind can be very useful in some applications, but have a number of disadvantages common to all circuits which use relays, including slow operation and short life. A solid state decoder driver circuit overcomes all of the disadvantages of the relay tree, and today tiny i.c.s are available which will decode the B.C.D. and drive the numicator cathodes directly, all for less than £1.

I.C. DECODERS

The availability of decoder/driver i.c.s at such low prices makes it most unlikely that anyone would want to go to all the trouble and expense of building this sort of circuitry with discrete diodes and transistors, and there seems little need to discuss these possibilities.

The first i.c.s produced to drive cold cathode displays were the now very common SN7441As. These i.c.s form part of the TTL range of logic types, and



because of the relatively large number of components integrated in each device, come under the subheading of "Medium Scale Integration" (M.S.I.).

The 7441 has four TTL logic compatible inputs, marked A, B, C, and D, and has ten outputs which will withstand at least 55 volts in the off state, and sink currents high enough to drive even large tubes.

INTERNAL CIRCUIT

The circuit of the 7441 is shown in Fig. 2.2. The inputs to the circuit are first buffered and then inverted to make available both true and complement data to the following decoding gates. Input A is used to switch on either the emitters of the odd number driver transistors, or the emitters of the even number drive transistors, thus simplifying the gating, which drives the bases of the output transistors a pair at a time.

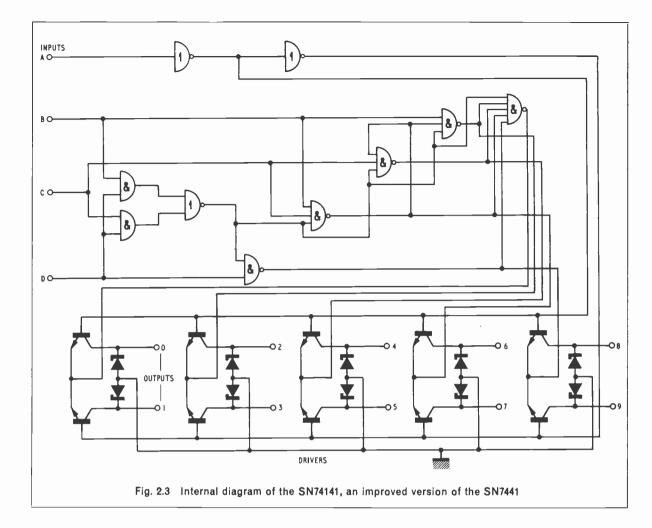
There are five pairs of output transistors, and these are controlled by one three input gate (to decode 0 and 1), three two input gates (2 and 3, 4 and 5, 6 and 7) and the last pair by the ungated D input. This decoding uses the minimal logic, and assumes that the error states in the B.C.D. code (the binary equivalents of 10 to 15) will never occur; if they do occur, more than one of the ten outputs will be enabled. Binary 15 for example, 1111, will drive cathodes 7 and 9 at the same time, and this state of affairs must not be allowed to occur.

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ZENER DIODES

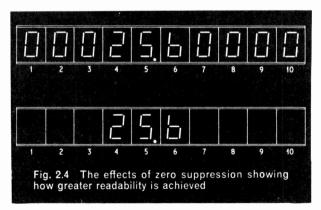
Note the Zener diodes between the collectors of the drive transistors and ground, these are a modification to the original 7441 design, and account for the A suffix in the full SN7441A type number. Their purpose is to prevent voltages appearing at the collectors of the drive transistors which would exceed their collector breakdown limit, and thus force the transistors themselves to behave like Zeners. Voltage spikes of up to the full positive supply voltage can occur at the cathodes of tubes driven by transistors, and it is clearly better to limit these spikes with a Zener than to let the transistor breakdown, even temporarily.

The fact that these i.c.s will withstand only a minimum of 55 volts on their outputs may be puzzling some readers, this value seeming to bear little relation to the already discussed supply voltage range used by "Nixie" tubes. The point here is that, unlike a relay tree, the transistor merely switches a differential voltage (of at least 55V) which is sufficient to reduce the voltage across the tube to a level below its striking potential, and thus extinguish it. The 55V differential is quite small, and does mean that the lower supply voltages specified for the tubes must be used in order to be sure of extinction of "off" cathodes. The voltage usually employed with these i.c.s is of the order of 180V, 55V being a big enough proportion of this to ensure correct operation.



IMPROVED PERFORMANCE

It may have become apparent during discussion of the 7441 device that there is room for improvement in some of its characteristics; having all cathodes off for error inputs would come in very handy as will be seen and an increase in the 55V level would give more freedom in supply voltage choice. These points have not escaped the i.c. manufacturers, and a new device which incorporates the improvements mentioned above and a few more besides, has recently been introduced, the SN74141.



This new device is already available through suppliers advertising in this magazine, and at the same price as the 7441. The block diagram of the 74141 is shown in Fig. 2.3 and, as can be seen, the basic principles are the same, the only difference being that the 74141 has a full decode of the input data, so that error codes will not drive any of the cathodes, and the tube will remain off.

The differential voltage switching capability has been raised to at least 65V with this device, and other circuit improvements have been incorporated to reduce switching transients and power consumption.

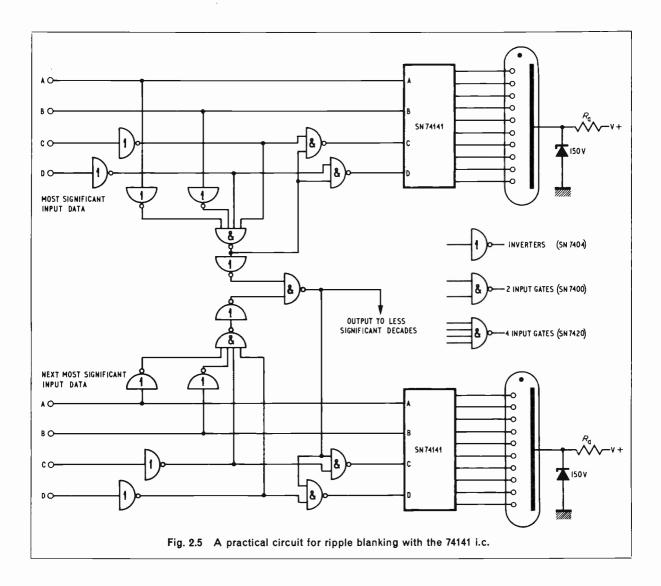
SUPPRESSION OF ZEROS

The invalid, or error code blanking, is useful when leading or trailing edge suppression of zeros is required in long displays. The effect of zero suppression is to blank the display tubes in a system which would normally show insignificant zeros, giving a resultant display which is very easy to read. For an example of the effects of combined leading and trailing edge zero suppression see Fig. 2.4.

The algorithm for producing the ripple blanking effect is quite simple.

FOR LEADING EDGE:

If digit one is a zero—blank it



If digit two and the digit before it are zeros—blank it

If digit three and the digits before it are zeros blank it, and so on down the display from the left until the first integer is encountered, after which no more zero blanking is allowed.

If a decimal point is used in the display it is usual to display the zero immediately preceeding the point if no integer is present, and numbers after the point are blanked if necessary by the same algorithm as used above, but this time starting from the righthand side.

FOR TRAILING EDGE:

If digit ten is a zero-blank it

If digit nine and the digit after it are zero—blank it If digit eight and the digits after it are zeros blank it also.

This process continues until an integer is encountered, or until the digit after the decimal point is reached, which is displayed regardless.

If a display holds all zeros the ripple blanking will give

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A PRACTICAL CIRCUIT

Blanking with the 74141 device is achieved by forcing its inputs into one of the invalid states when required, and part of a ripple blanking scheme with this device is shown in Fig. 2.5.

The operation of this circuit is simply that the 7420 4 input gate detects a zero in the most significant position and blanks it by forcing the 74141 inputs to binary 12. If the next most significant position also contains a zero its 7420 output is gated with the previous one to force binary 12 into its inputs also. This system can be expanded to handle any number of digits required, it can also be reversed of course, to give trailing edge blanking.

There is one point to note. It may be necessary to incorporate a Zener diode of 150V rating at the anode of each tube, as shown; this has the effect of clamping the anode voltage to 150V and is desirable to prevent partial striking of some of the cathodes when no current is being drawn by the tube during blanking.

Next month: Incandescent filament display devices.

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A selection of readers' suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought. This is YOUR page and any idea published will be awarded

payment according to its merits.

SIMPLE LAMP STROBE

The circuit diagram in Fig. 1 may be of interest to some of your readers. It was built especially for a school production, where it was used for special stage lighting effects.

The circuit consists of a six volt power supply, using a mains transformer and a full-wave rectifier (note: no smoothing capacitors). This powers a simple multivibrator, whose frequency is governed by potentiometer VR1. This feeds the gate of thyristor SCR1 via a small isolating transformer T2, which in this case was a discarded transistor radio push-pull type.

A $1\frac{1}{2}A$ 400 p.i.v. SCR, fitted to a heatsink, provided excellent control for a 150 watt reflector lamp. However, if the strobe is to be used for high speed work, then low wattage bulbs (or a string of them in parallel) should be used, as thermal inertia sets up in the filaments of high wattage bulbs and light is still emitted after the current has been turned off. This may continue until the next pulse reaches the filament, therefore not producing a true strobe effect.

> I. Hunt, Lower Hutt, New Zealand.

UJT STEP-FREQUENCY OSCILLATOR

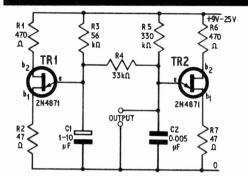


Fig. 2. Circuit diagram of unijunction oscillator

THE circuit in Fig. 2 will produce a sound of increasing frequencies until the highest one is reached and then the cycle will repeat again starting from the lowest.

The circuit is basically two unijunction transistor relaxation oscillators cross-coupled by resistor R4. When the circuit is connected to the supply, capacitors C1 and C2 start to charge up via resistors R5 and R3.

Because R5, C2 is the shorter time constant, TR2 will fire first and C2 discharge. When C2 again starts to charge there is a voltage on C1 and C2 now charges via R5, R3 and R4. This will shorten the time constant for TR2, due to C1 charging. The unijunction TR2 now fires quicker and quicker until TR1 fires. The cycle will then repeat.

The cycle length is controlled by R3 and C1 and the lowest frequency by resistor R5 and capacitor C2. A wide variation of values for these components is possible and those given in Fig. 2 are only a guide.

This circuit would make an addition to the sound effects board used in the War Games Computer. Other possible uses include door bells and audio alarms.

> M. K. Cook, Newcastle-upon-Tyne 1.

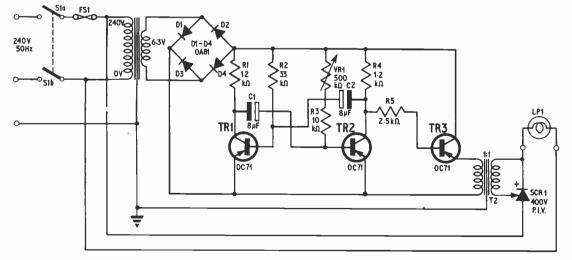


Fig. 1. Simple mains lamp strobe circuit

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TAPE SPEED CONTROL

PERHAPS the following circuit (see Fig. 3) would be of interest to your readers, and hence suitable for inclusion in the Ingenuity Unlimited section.

The circuit was designed to regulate the speed of the motor of a small capstan driven battery tape deck, a Garrard two-speed two-track transport, now out of production. The system should also prove suitable for the regulation of speed of other portable deck motors, provided that they have nonferrous flywheels.

The pulse signal, consisting of "bursts" of sine wave from the flywheel velocity detector, is fed via C1 to the base of TR1, which is biased by potentiometer VR1 adjusted to give a collector-emitter voltage of six or seven volts. The output from TR1 passes through capacitor C2 to the base of the Schmitt trigger TR2, which is biased to near threshold by resistor R2. The output from the trigger at TR3 collector is passed to the input of the monostable TR4 and TR5 via C3. The leading edge of the monostable output waveform is sharpened by the "speed-up" capacitor C6 to give a fast switching pulse at TR5 output.

The values of C5 and R8 were chosen to give a pulse duration of about ten milliseconds, as the basic pulse repetition frequency is roughly 30Hz at a tape speed of $3\frac{1}{4}$ in per second.

The monostable square wave output (of constant amplitude and duration, despite variable frequency) is fed to an integrating network consisting of R12, D1, C8 and VR2, where varying frequency causes a proportional varying d.c. voltage to appear across VR2. This potentiometer is set for the final motor speed and acts as a potential divider providing base voltage for the common collector d.c. amplifier TR6.

When the voltage at the base of TR6 exceeds the base-emitter voltage (approx. 0.6V) the transistor starts to conduct, making the anode of diode D2 less negative. When the anode-cathode voltage of D2 falls below the Zener voltage, the Darlington pair TR7 and TR8 switch off. Hence the motor power is reduced, the flywheel speed and pulse frequency fall. The centrifugal governor contacts should be connected together.



When the system is switched on, near full voltage appears across the motor, which promptly runs up to a speed where the integrating circuit voltage output causes TR6 to conduct heavily enough to decrease motor power. The motor speed then holds satisfactorily stable, although there is slight "hunting" about the mean flywheel speed set by VR2. This may be reduced by placing a $1,000\mu$ F capacitor across the motor terminals. There is also some possibility of the logic circuitry being falsely triggered by armature noise from the motor. This may be cured by decoupling the supply line and/or stabilising the power supply.

The sensor could consist of a small "button" permanent magnet ("Eclipse" type) with as much 38 s.w.g. enamelled copper wire as will fill the magnet limbs and wound over paper insulation. However, this sensor has not been tried in practice as the prototype used the magnet and coil assembly of a small magnetic earpiece. As the input voltage is not critical there is no reason why the aforementioned system should not operate.

The flywheel markers were four short pieces of 18 s.w.g. soft *iron* wire stuck to the flywheel circumference at equally spaced distances. The sensor was mounted close to the flywheel periphery (less than 2mm air gap) and provided triggering down to speeds of 30 r.p.m.

If dual speed operation is required potentiometer VR2 should be duplicated in parallel, with a changeover switch provided between the base of TR6 and the respective wipers of the additional potentiometers. The required speeds may then be separately set on the potentiometers, calibration being carried out using a measured length of tape and a stopwatch.

The power transistor TR8 must be mounted on a heatsink approximately 15 square inches of either copper or aluminium sheet.

J. D. Watson, West Worthing.

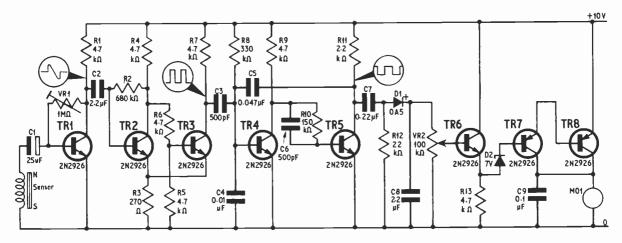


Fig. 3. Circuit diagram of the tape speed controller

CARAVAN FLASHER UNIT

was recently amazed when I discovered the price of heavy duty motor car flasher units, used when a trailer or caravan is being towed. These units simply accommodate the extra indicator lamps without upsetting the timing of the indicator, and to my mind, this simple task does not justify its cost.

The circuit shown in Fig. 4 is an extremely simple modification to a car's existing indicator circuit, the only extra components being two 12V relays (singlepole change-over) which may be purchased fairly cheaply.

A further modification, at a slightly extra cost, would be to make RLA a double-pole change-over relay and wire as the other contact and a switch shown dotted in Fig. 4. If S1 is depressed and the

UNISELECTOR PAPER FEEDER

THE IDEA to use a uniselector as a capstan drive for a paper recorder or seismograph was originally thought of as an alternative to the revolving disc system which had a somewhat restricted recording period. The circuit Fig. 5 provides variable paper tape speed from 2mm to 25cm per minute.

The unijunction TR1 generates positive going pulses, which are controlled by VR1. These are conveyed to TR2 by C2 which inverts the pulses and switches on TR3 for the duration of each pulse. Normally no heatsink is required for the power transistor TR3.

The uniselector used was a small 50Ω type and was marked T30338C. When all the switchgear is removed a 1.8cm shaft is revealed which can be

CAR ALARM TIMER

AVING purchased a car alarm quite recently I was amazed how many times I forgot it was there and released a fearful blast from the car horns.

The circuit diagram, Fig. 6, shows a simple 4 to 12 seconds timer. The switch S1 is the trigger, it can be either the "tremor" switch supplied with burglar alarm kits or, as I have used, a pressure switch underneath the driver's seat cover.

When you leave the car you set the alarm by closing the toggle switch S2. When you get back in the car and sit down LP1 lights, this tells you that you've got about 6 seconds before the horns blast, so you open S2 and all is well.

The capacitor C2 across the relay holds the horn on if a real theft is going on. The transistors TR1 and TR2 can be any germanium general purpose types, i.e. OC71 or OC81. The lamp LP1 can be a l.e.s. 12V type (or 6V for motor cycles).

I. Davey, Hinckley, Leicester.

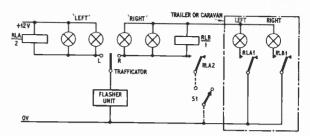


Fig. 4. Caravan or trailer flasher unit

trafficator switched to the "LEFT" position, ALL the six indicator lamps will flash on and off, constituting an emergency warning system in the event of an accident or breakdown.

> L. Musworth, Lancs.

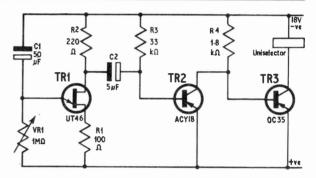


Fig. 5. Circuit diagram for paper feed controller

extended by a sleeve. This is used together with a sprung jockey wheel to feed the paper from a roll. B. Darnton, Sherfield English, Hampshire.

Fig. 6. Circuit diagram for a simple 4 to 12 second delay timer for a car burglar alarm







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BC134 20p MJ421 80p ZTX 5500 15p 2573 BC138 15p MJ2801 ZTX 5502 20p 4270 BC136 20p MJ2801 ZTX 5502 20p 4275 BC137 20p MJ2801 ZTX 5502 20p 4275 BC137 20p MJ2801 ZTX 5502 20p 4276 BC137 20p MJ2801 ZTX 5502 20p 32826 BC147 12p MJE370 50p TX 514 40p 33826 50p BC148 10p MJE370 50p 1N914 7p 2N3824 50p BC143 20p MJE520 75p 1N4146 7p 2N3806 280p BC143 20p MJE520 75p 1N4146 7p 2N4041 12p BC167 15p 4110 18922 7p 2N4041 12p BC172 20p MF82056 203303 32p	MC1304P E2 25 14 wait 9 SL403D £1.50 ZL series 25p 23p 30p 17p 15p 9C300 A 100 15 amps £1.35 741C(D1L) 75p 3 watt 9C302 A 100 15 amps £1.35 9C302 A 100 15 amps £1.35 914(T05) 40p 37Z series 30p 27p 25p 35p 20p 9C302 A 100 15 amps £1.35 923(T05) 40p 10 watt 22 series + 40p 37p 35p 30p 25p 8C40E 500 6 amps £1.35 20 watt amp. 24 47 All types arc 5%, Wire Ended + these 8C46E 500 10 amps £1.45 708HIBA 8Lud, in voltager 3.3-100 voit in 8 8C46E 500 15 amps £1.45	25 + 8p 100 + 7p 500 + 6p 1000 + 5p 00028 Mullard 60p 25 + 55p 100 + 30p 500 + 45p 1000 + 45p 213819 Texas 35p 25 + 30p 100 + 25p 500 + 25p 100 + 17p 1000 + 17p 1000 + 17p
BCY30 85p NKT404 55p 2N930 20p 40430 8100 BCY31 40p 0A5 85p 2N947 40p 40434 64p BCY32 60p 0A10 85p 2N917 40p 40543 64p BCY32 60p 0A47 10p 2N1302 18p 40636 81.06 BCY33 40p 0A47 10p 2N1302 18p 40636 81.10 BCY34 45p 0A70 10p 2N1302 18p 40636 81.10 BCY39 0A79 0A79 10p 2N1302 18p 40636 81.10 BCY30 0A90 7p Ist available 50p 6000	1×4002 100 2p 0p 0p 0p 0p 1002 100 2a 60p 4004 400 4a 70p 1×4003 200 8p 7p 6p 5p 4ip 2002 2002 2a 60p 6004 600 4a 80p 1×4004 400 8p 7p 6p 5p 4ip 4002 400 2a 75p 1006 100 6a 70p 1×4005 600 10p 9p 8p 7p 7p 6p 6002 600 2a 80p 2006 200 6a 70p 1×4005 800 12p 10p 9p 8p 7p 1004 100 4a 85p 4006 400 6a \$1.00 1×4007 1000 15p 12p 10p 9p 8p 2004 200 4a 70p 6006 600 6a \$1.10	25 + 8p 100 + 7p 500 + 6p 1000 + 5p AD161 AD162 35p each 25 + 32p 100 + 28p 500 + 25p 1000 + 22p
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SPECIFICATION . . .

Circuit

17 transistors, nine diodes and two integrated circuits with ceramic filters to give good selectivity and ease of alignment. Phase lock stereo decoder

Tuning Range 87-108MHz f.m. only

Sensitivity $2\mu V$ for 20dB quieting and $8\mu V$ for 40dB

Signal to noise ratio 60dB for inputs above $50\mu V$

Distortion Tuner 0.5% and stereo decoder approx. 0.3% at 1kHz, 100% modulation

Output 200mV at 100% modulation

Selectivity 6dB down at 235kHz bandwidth and 80dB down at 900kHz bandwidth Image rejection 45dB I.F. rejection

80dB A.M. rejection

50dB

Stereo separation Better than 30dB at 1kHz

Discriminator Differential peak detector with single easily adjusted coil

Stereo decoder Phase lock loop type with only one easily adjusted coil

Construction

Built in case matching the P.E. Gemini preamplifier, with its own built-in power supply to enable it to be used with any high quality amplifier



PART ONE

BY D.S.GIBBS & I.M.SHAW

Practical Electronics April 1972

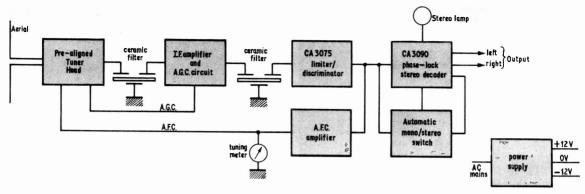


Fig. 1. Block diagram of the complete stereo tuner

THE "P.E. Gemini" Stereo Tuner is designed to give a very high standard of performance with the simplest possible alignment. In the past a high quality f.m. tuner has been a difficult project for the home constructor because of the problem of alignment, which in most cases could only be accomplished satisfactorily with the aid of a sweep generator. Pulse counter designs overcame the alignment problem, but only at the expense of poor selectivity, and spurious whistles when used with a stereo decoder.

Now all this has changed. By the use of advanced integrated circuits, ceramic filters and a pre-aligned tuner head, it is possible to build an f.m. tuner which can easily be set up by the home constructor.

The circuit can be conveniently broken down into five parts: the tuner head, the i.f. amplifier and filter, the limiter and discriminator using the CA3075 integrated circuit, the phase locked stereo decoder using the CA3090 integrated circuit, and the power supply. A block diagram is shown in Fig. 1.

TUNER HEAD

It is difficult for the home constructor to build a satisfactory tuner head since not only must lead lengths be kept to an absolute minimum and the individual stages carefully screened from each other, but the finished tuner head must be aligned and tracked to give consistent performance over the whole f.m. band. Consequently, this design makes use of a ready made and pre-aligned tuner head. The internal circuitry is shown in Fig. 2 for purely academic interest.

The tuner head used is a three transistor design with a three-gang tuning capacitor to give good selectivity. The whole tuner head is enclosed in a metal case to give good screening.

The tuner has facilities for a.f.c. (automatic frequency control) and a.g.c. (automatic gain control). The a.f.c. action, with the help of the a.f.c. amplifier, is very powerful and the tuner shows no tendency to drift. Unfortunately the strong a.f.c. causes

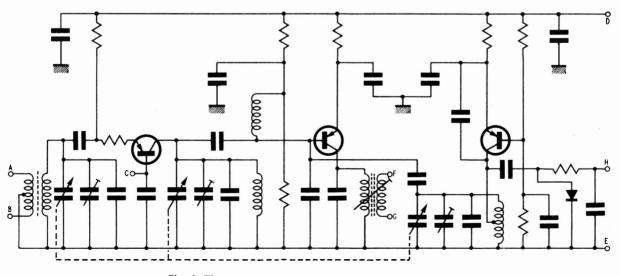


Fig. 2. Theoretical diagram of the pre-aligned f.m. tuner head

-

COMPONENTS . . .

Resistors

R1	1·8kΩ	R25	470kΩ
R2	330 Ω	R26	390Ω
R3	330 Ω	R27	150Ω
R4	47Ω	R28	4·7kΩ
R5	330Ω	R29	10kΩ
R6	680Ω	R30	10kΩ
R7	100kΩ	R31	2·2kΩ
R8	22kΩ	R32	100Ω
R9	3-3kΩ	R33	3-3kΩ
R10	330Ω	R34	22kΩ
R11	82Ω	R35	6-8kΩ
R12	22Ω	R36	22kΩ
R13	1MΩ	R37	6·8kΩ
R14	100Ω	R38	22kΩ
R15	10kΩ	R39	10kΩ
R16	390 Ω	R40	10kΩ
R17	12kΩ	R41	10kΩ
R18	6·2kΩ	R42	10kΩ
R19	10kΩ	R43	820Ω
R20	4·7kΩ	R44	2·7kΩ
R21	47kΩ	R45	820Ω
R22	2·2kΩ	R46	820 Ω
R23	10kΩ	R47	820 Ω
R24	10kΩ	R48	820 Ω

All \pm 5%, $\frac{1}{4}$ W carbon.

Potentiometer

VR1 4.7k Ω (or 5k Ω) linear miniature moulded track preset

Capacitors

- 4µF elect. 40V C1
- C2
- 150pF polystyrene 125V 680pF polystyrene 125V Č3
- C4 0.1µF disc ceramic 30V
- Ċ5 1,000pF disc ceramic 1,000pF disc ceramic
- C6
- Č7 0.022µF disc ceramic 18V
- C8 33pF polystyrene 125V
- C9 1,000pF disc ceramic C10 0.01µF disc ceramic 18V
- C11 10µF elect. 16V
- C12 1,000pF disc ceramic
- C13 0.01 µF disc ceramic 18V
- 0.01µF disc ceramic 18V C14
- 10pF polystyrene 125V 47pF polystyrene 125V C15
- C16
- 0.01 µF disc ceramic 18V C17
- C18 C19 1,000pF disc ceramic
- 220pF polystyrene 125V
- C20 25µF elect. 25 V C21 0-22µF polyester
- C22 25µF elect. 25V
- C23 3,300pF polystyrene 125V
- C24 1µF polyester 100 V
- C25 0.47µF polyester 100V
- 1µF polyester 100V C26
- C27 2.5µF elect. 16V
- C28 6,800pF polyester C29 6,800pF polyester
- C30 4µF elect. 40V
- C31 4µF elect. 40V
- C32 470pF polystyrene 125V
- C33 470pF polystyrene 125V
- C34 0.015µF polyester
- C35 32µF elect. 40V
- C36 125µF elect. 16V
- C37 C38 250μF elect. 25V 250μF elect. 25V
- C39
- 0.0047µF polyester 2,000µF elect. 25V C40
- C41 0·1µF 1,000V

Transistors

TR1	Z I X311
TR2	ZTX311
TR3	ZTX311
TR4	ZT X311
TR5	ZTX108
TR6	ZTX500
TR7	ZTX500
TR8	ZTX108
TR9	ZTX108
TR10	ZTX108
TR11	ZTX108
TR12	ZTX550
TR13	ZTX500
TR14	ZTX108

Integrated Circuits

IC1 CA3075 (R.C.A.) IC2 CA3090Q (R.C.A.) quad in-line or CA3090E dual in-line (see text)

Diodes

D1 ZS170 D2 ZS170 D3 ZS170 D4 ZS170 D5 ZS170 D6 ZS170 D7 **KS110A** D8 KS120A

Filters

F1-2 Vernitron FM4 ceramic filter (2 off)

- Meter
 - 50-0-50µA centre zero f.m. tuning meter, ME1 220-250Ω

Transformer

T1 Mains transformer, secondary 12V-0-12V 0.5A

Inductors

- L1 17 turns of 30 s.w.g. enamel on Neosid NS/E3 former
- L2 2mH adjustable coil type 87135 BX 1 3mH to 2.3mH or type QL10 1.5mH to 3mH (details next month)

Lamps

- LP1 6V 60mA l.e.s. indicator lamp (stereo pilot) LP2 12V 120mA l.e.s. scale lamp
- LP3 12V 120mA l.e.s. scale lamp

Switches

- S1 Double pole rotary mains switch S2 Optional single pole on-off switch for a.f.c.

Sockets and Plug

- SK1 Coaxial aerial socket
- SK2 DIN output socket 5-way 180
- PL1 Panel mounting miniature mains connector 3-way type P429 with socket P430

Miscellaneous

- Case Contil MOD-2 size G (West Hyde Developments Ltd., Ryefield Crescent, Northwood Hills, Northwood, Middlesex)
- F.M. Tuner head (details next month)
- FS1 Fuse 1A slow blow and fuseholder
- Printed circuit board (fibreglass) 10in × 4.5in
- Satin finished brushed aluminium front panel $12\frac{3}{2}$ in $\times 2\frac{3}{2}$ in
- Jackson tuning dial and drive to be described in Part 2
- Aluminium panel 18 s.w.g. $10in \times 2in$

P.E. GEMINI STEREO F.M. TUNER CIRCUIT

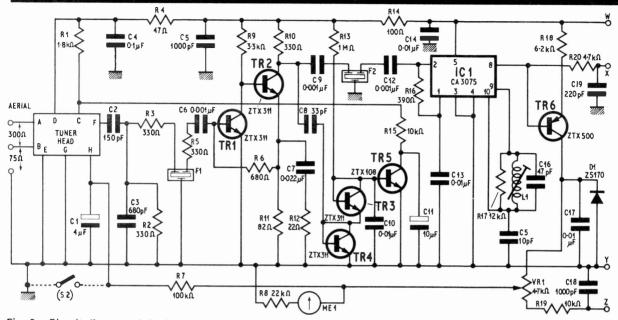


Fig. 3a. Circuit diagram of the i.f. amplifier with tuner head connections

the tuner to "jump" from one station to another as the tuning knob is turned. Commercial designs usually overcome this by providing a switch to cut out the a.f.c. whilst tuning, but in this design, such an arrangement was omitted so as not to spoil the simple and functional appearance of the front panel. However, the constructor can include an a.f.c. switch if he desires and the correct position for it is marked on the circuit diagram (Fig. 3a) as S2.

A.g.c. is a facility that is frequently omitted on f.m. tuners, on the philosophy that the signal is going to be limited anyway so why bother to control its level? If the input stages are allowed to overload, spurious sidebands can be produced and "pulling" of the logical oscillator may occur. This is clearly undesirable so an a.g.c. system has been included which effectively prevents overloading in any stage of the tuner.

I.F. AMPLIFIER

Between the tuner head and the CA3075 limiter/ discriminator there is a ceramic filter i.f. section (shown in Fig. 3). TR1 and TR2 form the actual i.f. amplifier, a simple two transistor feedback pair which provides a modest amount of gain and acts as a buffer between the two ceramic filters. These filters must be operated with resistive source and load impedances of 330 ohms, which is easily provided by making the collector load resistor of TR2 330 ohms and putting a 330 ohm resistor in series with the input (R5). The input impedance of the amplifier itself is very low and can be ignored. The total gain of the i.f. amplifier is about 20dB but there is a loss of about 3dB in each of the filters, giving an overall gain for the stage of 14dB.

CERAMIC FILTERS

The ceramic tuned filters are Vernitron type FM-4. These filters give a remarkably high standard of selectivity, each filter being approximately equivalent to two double-tuned critically coupled transformers. No adjustment is required of couse so that there are no alignment problems. However, because of production tolerances the filters are graded into five frequency groups at 37.5kHz intervals around 10.7MHz and are identified by coloured dots. There is no advantage in using any particular frequency group, but you should ensure that both filters carry dots of the same colour.

The frequency response of two filters is typically 6dB down at 235kHz bandwidth and not less than 80dB down at 900kHz bandwidth. This is shown in Fig. 4. This is actually somewhat on the narrow side, since a bandwidth of 300kHz is usually considered advisable for multiplex reception.

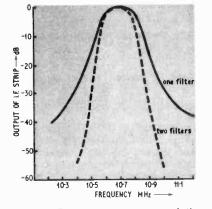
The reason for this is that for good stereo separation a tuner must have a linear phase/frequency relationship for modulation frequencies up to 53kHz. In effect this means that the time delay through the whole tuner must be the same at all frequencies, so that all the harmonic components of a complex waveform will come out with exactly the same phase relationships as when they went in. This is important because the correct phase relationship between the pilot tone and the multiplex sidebands is essential for good stereo separation.

In fact the phase/frequency response of the ceramic filters is perfectly linear up to 53kHz, as Fig. 5 shows, and there should be no problems on this score.

But this is not the end of the story. An f.m. signal generates a very complex series of sidebands, which is unfortunately beyond the scope of this article to discuss in detail. At low modulation frequencies all the significant sidebands are contained within the ± 75 kHz deviation, and no problem arises. How ever, at high modulation frequencies there are significant sidebands beyond the ± 75 kHz limits and these are attenuated to some extent by the filters.

+12V ð <R29 R22 **C**R30 C20r LPI 6 Z22kΩ Ziokn Ziokn 25µF1 C27 2-5µF R39 10kΩ R.H. CHANNEL R28 47kf X 3000 R 40 R32 < 10kΩ C.2. 100 A AUDIO R25 470 kΩ 1 🕫 Ç 30 OUTPUT 4μF 0-015 µF TR7 C 31 JOKA R42 R 2 3 >10kΩ 6 0 L.H **IC2** 10 kΩ CHANNEL Z TX 500 4µF C 21 CA 3090Q 15 16 0.22 µF 6-8KA TR10 R 37 R33 R 31 L 2 C33 2-2KA TR8 C32 6-8 KA TRI 3-3kΩ < TR9 470pF 000 470pF 17k1 R214 R24 C 2 8 10 KQ 6800pF C29 6800pF C22 آبر25 C23 3300pF ZTX108 ZT X108 ZTX108 ZTX108 0 V R34 R 36-R 38 4 22kΩ 22kΩ. 2**2**kΩ -121

Fig. 3b. Circuit diagram of the stereo decoder



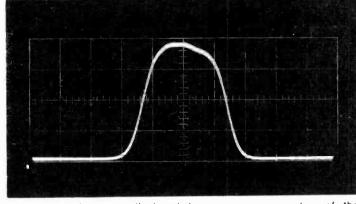


Fig. 4b. Oscillogram, displayed by a sweep generator, of the response shown in Fig. 4a. Horizontal scale is 100kHz per division

Fig. 4a. Frequency response of the i.f. amplifier and filters

This appears as slight distortion on the output waveform.

This distortion is not of great importance since it only appears on the L-R difference signal, which is generally of low amplitude. It is in any case a small price to pay for the convenience of the ceramic filters and the majority of new commercial designs use these filters.

A.G.C. AMPLIFIER

The output of the i.f. amplifier is peak-peak rectified by transistors TR3 and TR4, which are connected as "super diodes" and do actually cost less than conventional diodes. The rectified signal voltage is applied to the base of TR5 but, as the signal level rises, TR5 base is driven more negative, turning off TR5. This reduces the bias on the first stage of the tuner head so the gain of the tuner head falls, preventing overloading.

SIGNAL FREQUENCY kHz 50 0 10 20 30 40 60 CA 3075 only 4(DEGREES 5075 one fiter 80 PHASE SHIFT, C 3075 two filters 160 200

Fig. 5. Phase vs frequency characteristic of the detector and i.f. amplifier, showing very good linearity

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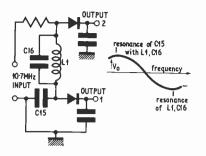


Fig. 6. Basic circuit and output waveform of the differential peak detector V_{2} = output 1 - output 2

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LIMITER AND DISCRIMINATOR

The R.C.A. type CA3075 limiter/discriminator contains three cascaded differential amplifiers which provide 60dB gain and excellent limiting. These are followed by a discriminator using what R.C.A. call a "differential peak detection circuit". This requires only a single coil and is consequently very simple to align.

The differential peak detector is novel but very simple. When the input frequency is slightly below the resonant frequency of L1 and C16 the net impedance of the L1–C16 combination is inductive. This forms a series tuned circuit with C15 and a large signal voltage is developed across C15 at this frequency. As the frequency is increased the voltage across C15 falls as it moves away from resonance but the voltage across L1–C16 increases, reaching a peak at their own resonant frequency. If we now rectify these two signals and measure the difference between them we obtain the familiar S curve (see Fig. 6).

In the CA3075 the rectification is done by four transistors which charge up two capacitors also within the i.c. The difference between these two voltages is amplified by a differential amplifier and appears at the output.

The output of the CA3075 for ± 75 kHz deviation is approximately 500mV r.m.s. At this level the distortion is about 0.5 per cent. The distortion generated by the CA3075 actually remains virtually constant up to 50kHz due to the very wide bandwidth of the discriminator, but as already discussed the sideband cutting introduced by the filters increases the distortion at high frequencies.

When comparing the specification of this tuner with commercial models, remember that many manufacturers specify the distortion at 30% modulation, i.e. only ± 25 kHz deviation. The distortion at 75kHz deviation is usually considerably greater. This is analogous to specifying the distortion of a 10 watt amplifier at 1 watt.

A.F.C. AMPLIFIER

The a.f.c. input of the tuner head requires a control voltage of about ± 1 volt. However, the output of the CA3075 is superimposed on a d.c. level of 5.4 volts. To overcome this difficulty TR6 acts as a level shifter, giving an output centred on zero,

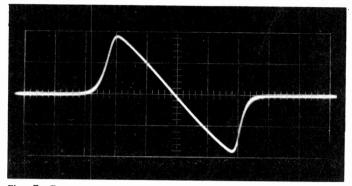


Fig. 7. Frequency response oscillogram of the CA3075 and i.f. strip together. Horizontal scale 100kHz per division; vertical scale IV per division

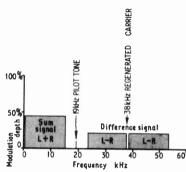


Fig. 8. Spectrum of stereo signal with equal but different signals in the left and right hand channels. The relative amplitudes of the sum and difference signals can vary according to the signals present in the left and right hand channels

and also inverts the output of the CA3075 to give the correct phase of feedback. If the feedback were of the wrong phase the a.f.c. voltage would send the tuning in the opposite direction to that required with the result that the tuner would refuse to lock onto a station.

The output of the a.f.c. amplifier is also used to operate the tuning meter.

STEREO SIGNAL

The stereo signal at the output of the discriminator has three main parts. These are shown in Fig. 8. Extending from 30Hz up to 15kHz is the normal audio signal, which in the case of a stereo transmission is the sum of the left and right hand channels (L + R). With a mono tuner this is all you ever hear. Above this there is a low level 19kHz pilot tone, and above that, extending from 23 kHz to 53kHz there are a pair of sidebands which are modulated with the difference between the left and right hand channels (L-R).

The 38kHz carrier is eliminated to economise on bandwidth and improve the signal to noise ratio and so it has to be regenerated in the receiver from the 19kHz pilot tone. This enables the (L-R) difference signal to be obtained and the left and right hand channels can be obtained by adding and subtracting. i.e. (L + R) + (L - R) = 2L(L + R) - (L - R) = 2R

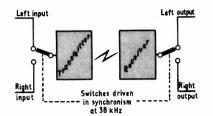


Fig. 9. Basic principles of stereo transmission system

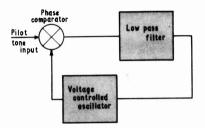


Fig. 10. Basic block diagram of the phase lock loop

A stereo decoder has a difficult job to do. It has to sort out the 19kHz pilot tone from the signals on either side of it, which may be up to nine times larger. This requires high-Q tuned circuits. It then has to double this frequency to 38kHz to replace the missing carrier in exactly the same phase as the original. A phase error between the regenerated carrier and the difference sidebands drastically reduces the stereo separation and the higher the Qof the tuned circuits the more sensitive to errors of tuning the decoder will be.

The amateur may find it difficult to understand why a phase error in the regenerated 38kHz carrier should upset the separation. One way of looking at it is to imagine a switch at the transmitter looking alternately, at 38kHz, at the left and right hand channels (see Fig. 9).

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If we have a similar switch in the receiver, exactly in synchronism with the switch at the transmitter, we can reconstitute exactly the original left and right hand signals. But suppose that the receiver switch is not exactly in synchronism with the transmitter switch. When the receiver switches to the right hand channel the transmitter might still be looking at the left hand channel. Thus the left and right hand signals get mixed up and the separation is greatly reduced.

There are slight differences between the actual modulation process and the simple description above, but it is nevertheless valid and most stereo decoders work on exactly this principle.

Most commercial stereo decoders compromise between the conflicting requirements of good selectivity (i.e. high Q) and good phase stability by using three tuned circuits of moderate Q. However, such decoders are difficult for the home constructor to adjust, and may not be particularly stable once adjusted. Up to the present time one had little choice, but now integrated circuits have made it possible to use phase lock loop techniques to overcome this problem.

PHASE LOCK LOOPS

All phase lock systems contain the same basic components, and the bare bones are shown in Fig. 10. There is a voltage controlled oscillator, a phase comparator which gives an output voltage related to the phase difference between this oscillator and the input signal, and a low pass filter which removes switching transients and high frequency components from the output of the phase comparator and also determines the bandwidth of the system.

When the input signal is first applied the v.c.o may not be running at exactly the right frequency. If this is the case the difference frequency appears at the output of the phase comparator, and if sufficiently low it passes through the low pass filter and frequency modulates the v.c.o. If the v.c.o. is now able to reach the signal frequency it will lock on to the signal. Once locked, any drift in the v.c.o. causes a phase error. This changes the output voltage of the comparator which pulls the v.c.o. back into the correct phase, enabling the v.c.o. to maintain the correct phase to within a few degrees, even when grossly misadjusted.

The v.c.o. can only lock on to signals close to its own frequency; signals beyond these limits have no effect. This gives the system good selectivity without the phase problems normally associated with high Q.

Phase lock loops are not new; they have been used in sophisticated instrumentation systems for years, but up to the present their cost and complexity has made them impracticable for consumer equipment.

STEREO DECODER

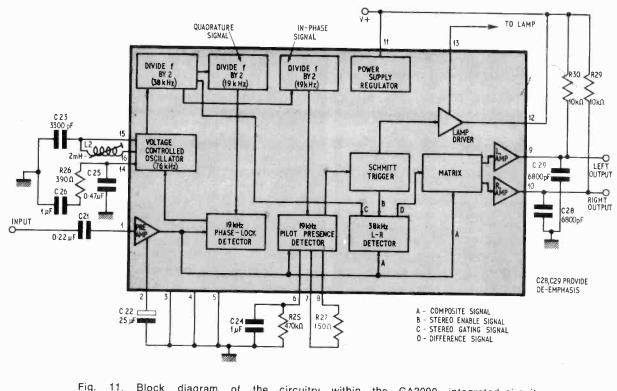
The CA3090Q stereo decoder integrated circuit is a remarkable device, containing no less than 128 transistors. It is shown in diagrammatic form in Fig. 11 but space restrictions prevent us publishing the full circuit diagram.

To describe the operation of this circuit in detail would take an article all by itself, but the following brief explanation may be of interest to the constructor.

A double balanced phase lock detector provides a d.c. output according to the phase difference between the pilot tone and the 19kHz signal generated in the circuit. This output is filtered by C25 and C26-R26 (outside the i.c.) and applied to a reactance circuit, which controls the frequency of the tuned circuit L2/C23 connected to pins 15 and 16. The actual oscillator is of the negative resistance type, the negative resistance cancels out the (positive) loss resistance of the tuned circuit and enables it to oscillate at 76kHz. This frequency varies according to the phase lock detector output voltage. The v.c.o. output is fed to a bistable, the 38kHz output driving the L-R detector.

The reason for using a bistable to generate the 38kHz waveform rather than running the oscillator at 38kHz is to ensure that the waveform is perfectly symmetrical. This assists in obtaining good stereo separation. This bistable drives two others that provide two pairs of 19kHz outputs differing in phase by 90 degrees. The first bistable drives the phase lock detector discussed above whilst the second drives the pilot presence detector which operates the pilot tone indicator.

STEREO DECODER



11. Block diagram of the circuitry within the CA3090 integrated circuit

POWER SUPPLY

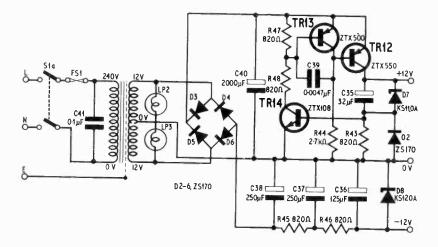


Fig. 12. Circuit diagram of the stabilised power supply

A third phase sensitive detector, driven by the 38kHz bistable, separates the two channels. The left and right hand signals are mixed with a proportion of the input signal to balance out the crosstalk. which is inevitably produced by square wave demodulation, and the result is fed through the output amplifiers within the i.c.

Most of the other transistors in the i.c. perform mono/stereo switching or provide bias potentials.

The CA3090O has guad in-line lead-out pins, i.e. alternate pins are staggered to give greater separation. The CA3090E is identical to the CA3090Q except that the pins are dual in-line. Allowance in the printed circuit pattern must be made according to which type is used.

AUTOMATIC STEREO SWITCHING

One unfortunate feature of phase lock loops is that the oscillator has to run continuously. This would not matter if the B.B.C. did not have the unfortunate habit of putting a 23kHz pilot tone on top of mono programmes. The result is that, due to stray coupling within the i.c., a low amplitude 4kHz tone appears at the output. The level is very low, but nevertheless is just sufficiently loud to be annoying, so we were obliged to include an automatic stereo/ mono switch. This cuts out the decoder when a mono programme is being received and completely eliminates the whistle.

The mono/stereo switching is performed by TR9, TR10 and TR11. These three transistors are worked in inverted mode (i.e. the emitter is used as the collector) to give a very low saturation voltage.

When a mono programme is being received the output from pin 13 of the CA3075 is low, so that the lamp driver transistor TR8, and TR9 are turned off. The collector voltage of TR8 is high and this turns TR10 and TR11 hard on. These two transistors short out the left and right hand channel outputs of the i.c. TR9 however is turned off and allows the signal from the emitter follower TR7 to pass straight through to the outputs.

When a stereo programme is being received the output from pin 13 rises and turns on the lamp driver and TR9. This closes the "through path" but TR10 and TR11 have now been turned off and the two outputs of the stereo decoder are now coupled to the output.

POWER SUPPLY

The tuner requires +12 volts at about 160mA and -12 volts at only 2.5mA. The -12V is provided by a simple zener diode stabiliser (Fig. 12) although extensive decoupling is necessary to prevent hum appearing on the a.f.c. line.

The $\pm 12V$ is provided by a three transistor series regulator. Any tendency for the output voltage to rise causes TR14 to conduct more heavily. This increases the current in TR13, which reduces the current flowing in the regulator transistor TR12 and hence the output voltage. The purpose of C35 is to ensure that when the power supply is first switched on the +12V supply rises at the same rate as the -12V supply. This prevents the a.f.c. circuit from locking on to the wrong station.

Next month: Part 2 will give details of construction

NEWS BRIEFS

LIGHT TOOTH

By USING Fibre Optics your dentist may soon be able to see through your teeth without using X-rays.

This concept, known as transillumination, has not been widely used before because lights bright enough to shine through teeth usually have been too large for use inside the mouth, as well as being too hot for the patient's comfort.

The fibre optics tool inserted in the mouth is about the size of a small screwdriver and can easily be manœuvred inside the mouth. When illuminated a healthy tooth seems to glow white, tooth decay shows up as a darkened shadow. Fillings appear as black spots or lines and hardened, crust-like accumulations of food residues and mouth secretions are clearly distinguishable.

Developed in America this new technique will soon be ready for distribution.

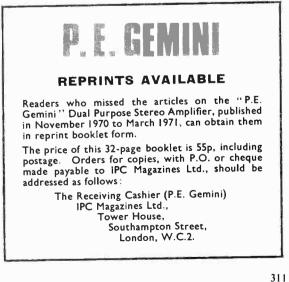
One possible result of transillumination could be the mass screening of children by trained dental assistants who would refer those needing treatment to dentists.

BBC COLOUR

UNDER a substantial re-equipping programme to bring full colour-broadcasting facilities to its regional tele-vision studios, the BBC has ordered further colour television broadcast cameras worth more than £100,000 from EMI Electronics Ltd.

Approximately £2 million has already been spent on EMI 2001 cameras by the BBC over the past three years and the addition of the new cameras will bring the Corporation's total to some 116 cameras. This represents over 80 per cent of the colour cameras used for studio productions and for outside broadcasting by the BBC

The additional colour cameras will be used in BBC studios located in major cities throughout Britain including Bristol, Southampton, Leeds, Newcastle and Man-chester. Several studios will be able to televise local programmes in colour for the first time, while at other locations, the EMI cameras will extend existing colour facilities. The BBC has already re-equipped a number of studios including Glasgow and Birmingham under its scheme which is expected to take about 18 months to complete.



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ELECTRONORAMA

RIDING THE MILLIMETRIC WAVES

T • ASSIST in meeting the explosive growth expected in telecommunications in the next 20 years the Post Office and the Science Research Council have begun what is probably the largest microwave-propagation study yet. This project could have a major influence in opening up the 10-100GHz range of the radio spectrum (wavelengths down to 3mm) to new microwave telecommunication system use.

The propagation study is based at the Post Office Research Station at Martlesham Heath, near Ipswich. The SRC Radio and Space Research Station is carrying out supporting research at Slough.

The Post Office needs to study the problems of transmitting radio waves at these super-high frequencies because its existing radio-relay network operating at frequencies of 2, 4 and 6GHz, is now approaching congestion and the higher frequencies offer greater message carrying capacity.

As frequencies rise above 10GHz, transmissions are increasingly affected by the weather: heavy rainfall can cause serious signal fading; and the temperature stratification that occurs on clear evenings in still air can occasionally produce multiple transmission paths that could adversely affect microwaves carrying high-speed digital signals.

A network of experimental radio transmitters has been set up in East Anglia between Martlesham and Mendlesham to find out just how radio transmissions at these millimetric wavelengths are affected by the weather and what can be done to avoid disruption of service.

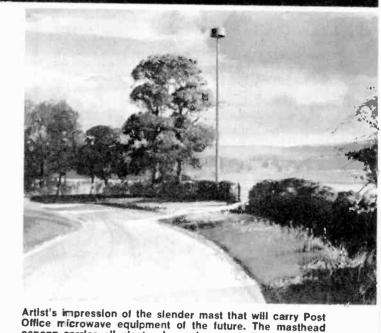


Experimental microwave station in East Anglia set up to study the effect of the weather on superhigh-frequency radio transmissions

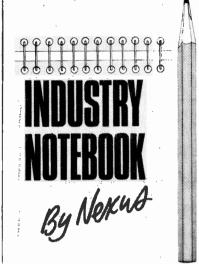
antennas

Standard lattice steel microwave tower in the Post Office radio-relay network near Plymouth. The lowest dish antenna on the lefthand leg, with a protective radome, is installed for the new 11GHz link to Caradon Hill





canopy carries all electronic equipment and the two dish



FLY-CATCHERS

When you visit a manufacturer of high voltage power supplies you expect to see equipment for general industry. I was not disappointed when visiting Brandenburg Ltd., Thornton Heath, Surrey, who specialise in high voltage work. As expected, there were units in production for use with scanning electron microscopes, electron beam machining equipment, photo-multipliers, and neat little solid state e.h.t. power packs for CRT units, the big new market for these being in computer video terminals.

But what were those peculiar devices with concentric cylindrical metal grids and a light in the middle? They were traps for flying insects. An ultra-violet light source is used as the lure to attract insects between the grids. The air is ionised between the grids and the flies, moths, gnats, bite the dust in a handy disposal tray at the bottom.

Development of this novel application for high voltage was done jointly by Brandenburg and Rentokil and the equipment is supplied by Rentokil under the trade name "Rentoflash". So far, Brandenburg has built 7,000 of these units which, I gather, are particularly effective in cow houses.

MOBILE IDEAS

To find out what's new in the mobile radio world I called on Jim Finke who has been setting up a Motorola production unit in Wiesbaden, W. Germany, to serve the European market. Motorola pioneered mobile radio in the United States, starting with a car radio as early as 1928. The name Motorola came to

The name Motorola came to company founder Paul Galvin as a flash of inspiration while shaving. It is now world famous in both communication equipment and solid state devices. Big breakthrough for Motorola was development of first the "Handie-Talkie" and, later, the "Walkie-Talkie".

"Wasn't mobile radio getting a little 'old hat'", I asked Finke. That was enough to trigger him off. "Not a bit of it", he flashed back. "Do you realise that when a guy has a heart attack the first half hour is critical? Why not fit the ambulance with a special radio unit for transmitting the patient's electrocardiograph through to the hospital. Let the doctor diagnose the heart condition and decide the treatment while the patient is still on his way."

Well, that is only one of the Motorola ideas for expanding the market for mobile radio beyond basic two-way voice communications. Another, already in production and selling to police forces in the USA, is a tiny dashboardmounted teleprinter which prints out messages in hard copy. Transmission from the base station is by coded audio tones over the usual f.m. mobile frequencies and eavesdropping criminals have little chance of deciphering messages unless they can find out the tone codes and get hold of the right receiving equipment. Another big advantage of this system is that police officers can leave their car unattended and find a hard printout of all messages waiting for them when they return.

Yet another idea is a bus location system, one of which has already been installed in Chicago. With microelectronics it's amazing how many extra facilities can be squeezed into vehicles without excessive penalties on space. Digital communications between vehicles is also a growing field.

But Europe, however closely integrated economically, is completely fragmented for the mobile radio man. Each country has its own technical specifications and licence regulations. Motorola says it's a real nightmare dealing with 14 countries, each with different requirements. Which explains why those huge transcontinental lorries have no mobile radio. An installation legal in the country of origin is often illegal as soon as it crosses the border!

THE BRITISH ENGINEER

The Survey of Professional Engineers conducted by the Council of Engineering Institutions brings good news for electronic engineers. Incomes below £1,200 have all but disappeared and the great middle body of engineers' salaries now come in the range £2,400-2,599. Unemployment, at one per cent, was well below the national average of $3\cdot 3$ per cent when the survey was taken.

But there were areas of disappointment. The report, which covers 1971 and was completed last December, showed that only 19 per cent of all engineers took any courses during the period and, of these, 50 per cent were on management courses while only 33 per cent took technical updating courses. This amounts to only 7 engineers in every 100 who had technical refresher courses, hardly enough, one would think, in these days of fast moving technology.

It is debatable, too, despite improved salaries, whether the engineer is adequately paid. Lavatory attendants at British-Leyland are reported as getting £1,500 a year and I have heard of loaders at London Airport (Heathrow) getting £70 a week take-home pay. The trouble with engineers is that they are generally enthusiasts. Good for them to enjoy their work, of course, but no reason why they should undervalue their services.

A MOVE INTO THICK FILMS

Coutant Electronics, a major manufacturer of power supplies, has decided to expand into the custom-built thick film market.

This is not quite such a revolutionary decision as might at first appear because the company has had an in-house thick film capability for some years. Now, it is being made available to all.

Coutant has had an interesting history. It was the first company in the Unitech group and every previous diversification has resulted in new companies being formed. Celdis, the well-known component distributors was an early example, the name Celdis being derived from Coutant Electronics Ltd., DIStributors. Coutant's pressure transducer interests were also hived off separately as Transducers (C.E.L.) Ltd.

ducers (C.E.L.) Ltd. If the thick-film venture goes well, will it, too, be hived off into a separate concern?

BLUE STREAK — ALL SYSTEMS WERE GO!!

My recent comments on the *Europa 11* launcher failure brought a sharp reminder from a member of the Hawker Siddeley Dynamics design team that the British *Blue Streak* first stage performed perfectly and the failure was due to the inertial guidance system in the third stage when *Europa 11* was at an altitude of 17 miles.

Blue Streak, says my correspondent, maintained its 100 per cent success rate after its previous ten successful launches.

WIDE BAND SIGNAL INJECTOR

Can be used for servicing

- Audio equipment
- Radio sets

V.H.F. and U.H.F. television receivers

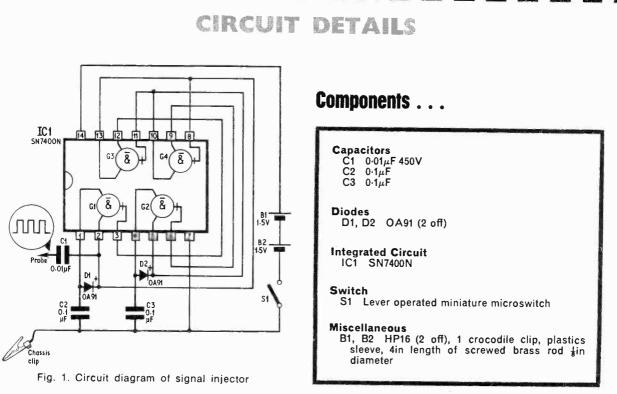
O^{NE} of the most useful aids for servicing audio and high frequency equipments is undoubtedly a device which will provide a modulated signal to enable tracing the signal path through the circuit.

This injector uses one of the most common TTL integrated circuits, the SN7400N, which consists of four two input NAND gates.

Although the total component count of the circuit is 40, all but five of these are within the i.c. package so that construction is very easy.

MULTIVIBRATOR

By suitably connecting the four gates (G1-G4) in the i.c. as in Fig. 1, a multivibrator square wave generator is produced with a fundamental frequency in the audio range. Since the waveshapes produced have very short rise and fall times the harmonics generated extend up to the u.h.f. television bands, thus the generator may be used to inject signals into anything from an audio amplifier to a television set.



CONSTRUCTION

Despite the apparent complication of the circuit, it is no more expensive to build than a conventional multivibrator using two transistors and is certainly a lot neater.

The majority of small components are built up on a piece of 0-1in matrix Veroboard measuring 14in \times 1in as in Fig. 2. The only point to watch is the correct location of the i.c. before soldering as it is very difficult to remove all 14 pins at once if it has to be removed.

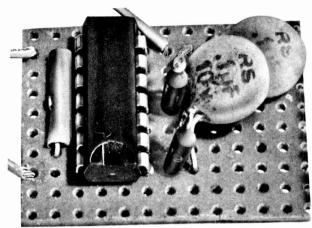
UNIT HOUSING

The actual housing was a $2\frac{1}{4}$ in \times 1in dia. tin with a snap-on plastics lid. As it was impossible to obtain a battery holder small enough to fit this, it was necessary to solder the battery leads directly to the battery terminals. If the constructor wishes to use a battery holder a larger tin will be found necessary.

A small microswitch is used for S1 with the lever bent as shown to protrude slightly from the lid. Adjacent to this is the probe, a 4in length of $\frac{1}{8}$ in diameter brass rod threaded at one end and covered with plastic sleeving so that only the tip is bared.

TESTING

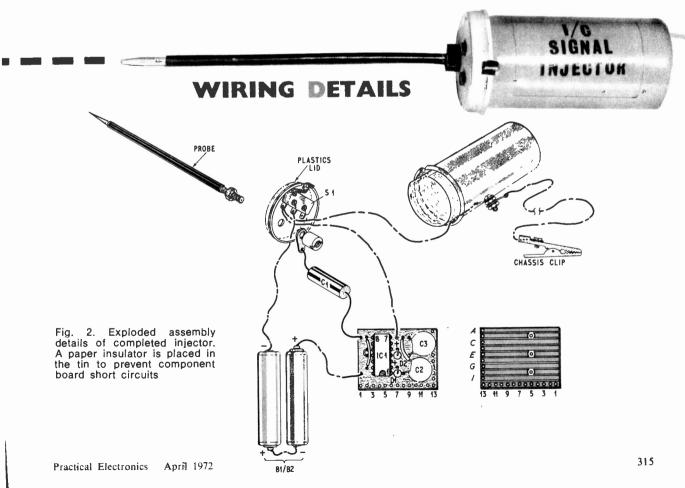
The finished unit can be tested by connecting a pair of phones between the probe tip and chassis clip. If all is well a tone of about 3kHz should be heard.



Small component assembly on 0-1in matrix Veroboard

To check the u.h.f. properties of the generated tone, connect the output to a television receiver aerial socket which should produce an audible output from the receiver speaker.

The earth clip is not essential for use at radio frequencies but a higher output will be obtained if it is used.





Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned.

PRESSURE SENSITIVE RESISTOR

A product that should prove both versatile and also tax readers' inventive instincts is ResilistoR, a special pressure sensitive resistor announced by Logic Applications Ltd.

The "resistors" are made from cubes, slices or discs of polyurethane foam impregnated with a special graphite conductive substance. According to the amount of pressure applied the graphite granules are brought closer to each other causing a gradual reduction in resistance across the device. As soon as the applied pressure is released, the ResilistoR reverts back to its unloaded resistance and shape.

Resistances from 50 ohms to 500 kilohms are available in varying ranges. The voltage range of the device may vary from 5V to 40V a.c./d.c. according to resistance range required. Devices capable of handling 100V d.c. at 60mA and 240V a.c. at 500mA have been produced to special order.

An experimenter's pack, containing one cube, one disc and one slice is available, price £2.50 including postage and packing. The uses of ResilistoR is obviously very numerous and some

The uses of ResilistoR is obviously very numerous and some of the applications which incorporate the devices are: intruder alarm pads; motor controllers; limit switches and volume controls.

In the electronic music field they have been used in keyboards and make excellent foot controls for Waa-Waa Pedals. Aids for the disabled is one field where experiments have been carried out with some success.

Further information, experimenting kit, and resistance ranges can be obtained from Logic Applications Ltd, 47 Victoria Street, London, S.W.1.

LIGHT SOURCES

The forecast by many experts that light sources will be one of the biggest growth sections in the electronics industry this year seems to be underway. Already, devices using varying methods of producing a light source are appearing on the market.

The solid state Ga As light indicator from West Hyde Developments uses a gallium arsenide light source and can be driven directly by a T.T.L. i.c.

The indicator will operate at 15mA at 1.6V or more. The recommended operating conditions are 40mA for brightest display (approximately 750 foot Lamberts). The device will continue to emit an adequate light down to 5mA.

Full details are available from West Hyde Developments Ltd, Ryefield Crescent, Northwood Hills, Northwood, Middx.

The use of liquid crystal cells for a light source is the latest development from **RCA**.

Not available generally, at the present time, the TA8032 and the TA8034 are single digit, seven segment numeric display devices.

The TA8032 is a transmissive type in which both conductive surfaces are transparent. This device is particularly suitable in applications where back or edge-lighted readouts are desirable.

The TA8034 is a reflective type and utilises a mirrored area on the inner surface of the back plate. This type is particularly useful in those applications where available front lighting is of primary importance.

These devices operate from 8V to 50V and are primarily intended for a.c. operation at frequencies from 30 to 60Hz although operation at other frequencies (400Hz) is possible. D.C. operation is not recommended.

Technical specifications and information is available from RCA, Solid State Europe, Sunbury-on-Thames, Middlesex, TW16 7HW.

Guest International have just introduced the Litronix Data-Kit 62

seven segment numeric display using gallium arsenide red light emitting diodes. The display contains approximately 56 diodes in one 14pin dual-in-line package and the luminescence is typically 500 foot Lamberts at 20mA.

Back to the more realistic light sources, filament and neon types, Guest have also released a range of miniature neon and filament lamps with either wire-ended terminations or flanged bases.

Particulars of the Data-Kit 62 and the miniature lamps are available from Guest International Ltd, Nicholas House, Brigstock Road, Thornton Heath, Surrey, CR4 7JA.

PRINTED CIRCUIT AIDS

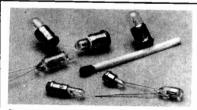
Of special interest to readers who once having proved a bench "lashup" circuit have difficulty in translating the circuit to a printed circuit layout. **Technomark** are now marketing a kit for making printed circuits easily.

The Technomark printed circuit pack is known as the Ambitrack system and costs £5.35 with s.r.b.p. boards or £6.25 with epoxy based glass fibre boards.

The pack contains three $8\frac{1}{2}$ in \times $6\frac{1}{2}$ in copper laminated sheets, with a 0·1 matrix scored into the copper surface; a bottle of etchant and a bottle of cleaning fluid; a pair of tweezers; magnifying glass; special grid patterned tracing sheets; and a special etchant resist pen. The case of the pack acts as a suitable etchant bath.

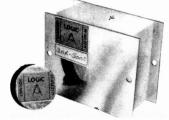
The circuit pattern is drawn onto the copper surface with the special resist pen and then immersed in the etching fluid. Alpha-numeric graduations around the edges of the boards allow for easy checking once the etching process is completed and the board cleaned.

Kits are available from Technomark, Borough Green, Sevenoaks, Kent.



Range of miniature indicators marketed by Guest International

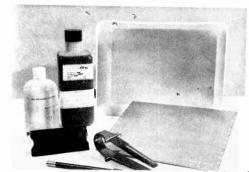
Two types of ResilistoR from Logic Applications





Ga As light source from West Hyde Developments

Technomark Ambitrack printed circuit kit



1 the MAY issue Lists the most commonly available types of pnp, npn, field effect and uniiunction transistors: thyristors and triacs: power, small signal. Zener. Lists the most commonly available types of pnp, npn, field effect and unijunction transistors; thyristors and triacs; power, small signal, Zener, and tunnel diodes. and tunnel diodes. Provides rapid lead, tag, or terminal identification for these popular solid state devices. state devices. This Wall Chart, measuring 21in×15½in, has been specially designed for This constructor's workshop and gives vital information at a glance. This Wall Chart, measuring 21in×15½in, has been specially designed the constructor's workshop and gives vital information at a glance. and tunnel diodes. the constructor's workshop and gives vital mormanu No more doubts when wiring up these devices! state devices.

Ultrasonic Transmitter-Receiver

With a range extendible up to 100 feet, this equipment combines ease of construction with high sensitivity. Its applications range from "invisible beam" burglar alarms to remote control and signalling, and it doesn't need a transmitting licence.







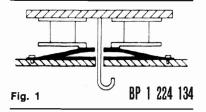
Add the wild sound of "fuzz" to your music. This is the third in the trio of sound effects units for pop group guitars and musical experimenters.





PATENTS REVIEW...

PRESSURE TRANSDUCER ALARMS



T HEFT detection devices, for instance for use in museums, are legion. Usually ultrasonic or electromagnetic barriers are used although sometimes light or infrared waves are relied on. In each case movement of a foreign object in a space saturated with the radiation is sensed in one way or another.

Cerberus AG of Switzerland have patented (BP 1 244 134) what looks like a novel approach to theft detection. The object to be protected is supported on a force transducer. The transducer produces an output signal and this signal is fed to an amplifier and alarm system. When the transducer is loaded with a constant force the signal is stable, but a change in load means a change in the output signal and the alarm is triggered.

To take a simple example, a painting could be suspended by two cables, each attached to a force sensitive sensor. When the force exerted on the sensors due to the weight of the painting is changed, if an attempt is made to steal the painting, the signal from at least one sensor changes to sound the alarm. The patent also gives suggestions for suitable constructions for the sensors and circuits for converting a change of output into an alarm.

A convenient type of sensor was piezo-electric elements arranged in a cavity mounted on a vibration absorbent base (to prevent building vibrations setting off the alarm). Ideally, two elements are mounted with their axes parallel and the cord between them so as to sense not only vertical forces (due to a downward tug) but also lateral forces, such as might be caused by moving an object by sliding it sideways.

A particularly sensitive construction is a ring-shaped piezo-electric transducer with the point of application of the force situated at the ring centre.

In one example given by the inventors, sensitivity is further in-

creased by suspending the picture or the like from a hook which is mounted on a dished spring with non-linear deflection characteristics, i.e. which will flatten suddenly at a certain loading, see Fig. 1. In a normal state of loading the spring is flattened, but if only a slight change of loading takes place it will cause a dramatic arching of the spring and a sudden force to be exerted on (or taken off) the transducer. This prevents "tricking" of the alarm by relieving the load in a slow manner.

Although not suggested, devices of this type could presumably also be used in other fields, e.g. by fishermen to sense "bites" at night.

COMPANDER CIRCUIT FOR DISC Recording

N the audio field it is common practice to compress sounds together in amplitude before recording them Among other considerations this lessens the risk of overloading the disc cutter and ensures that the recorded groove does not encroach into the adiacent groove space. In sophisticated systems the audio signal is expanded over the lower range of amplitudes and compressed over the higher range. Unfortunately socalled "hush" noise is often heard when a loud noise decays rapidly. First there is a rise in background noise as the compressor gain rises and then there is a fall in background noise as the expander gain falls.

The problem is complicated and EMI Limited have patented (BP 1 245 394) a correspondingly complicated answer.

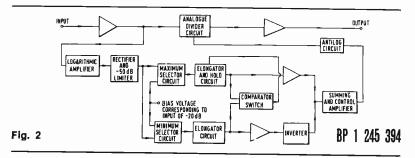
As the block diagram in Fig. 2 shows, an audio input signal is amplified and fed in parallel to both an analog divider circuit and a logarithmic amplifier. The output of the log amplifier is rectified to provide an output signal which corresponds to the envelope of the audio signal fed from the log amplifier. The rectifier circuit also functions as a limiter and its output is fed in parallel to both a minimum selector circuit and a maximum selector circuit. Both these selector circuits are also supplied with a limiting bias voltage.

The minimum selector circuit produces an output signal which corresponds to whichever is the less—the envelope signal or the bias voltage. The output signal from this minimum selector circuit is fed to an elongator circuit which forms an expansion control signal.

The maximum selector circuit produces an output signal which corresponds to whichever is the greater—the envelope signal or the bias voltage. The output signal from this maximum selector circuit is applied to an elongator and hold circuit which produces a compression control signal.

These compression and expansion control signals are fed to a comparator, the output of which is connected to the hold circuit in the elongator of the compression control signal path. Both the compression and expansion control signals are then summed and the combined signal is applied (via an anti-log circuit) to the analog divider circuit to control the transfer factor applied to the audio signal which is passing through the analog divider.

By suitable choice of circuit parameters, the combined amplified input signal and the processed signal is held constant as the compression and expansion components decay together. As a result of this process the gain of the divider circuit remains substantially constant. Background noise thus remains equally constant with a consequent avoidance of "hush" effects.





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NPN-PNP PLASTIC AND TRANSISTORS Clearance of manufacturers' seconds, types and guaranteed no open or t

types and guaranteed no open or short circuit units. Ideal cheap transistors for radio enthusiasts, manufacturers, schools and colleges.

 TYPE
 STN18,
 Silicon
 Planar
 Transistors
 npn

 TO-10
 Metal
 Can.
 Types
 similar
 to:
 2N706,

 2N2220,
 BSY27-95A,
 BSX44-76-77.
 Price:
 500 £9;
 1,000 £15

TYPE STP18. Silicon Planar Transistors pnp TO-18 Metal Can. Types similar to: BCY0-72, 2N2906-7, 2N2411 and BC186-7. Also used as complementary to the above npn type device type STN18.

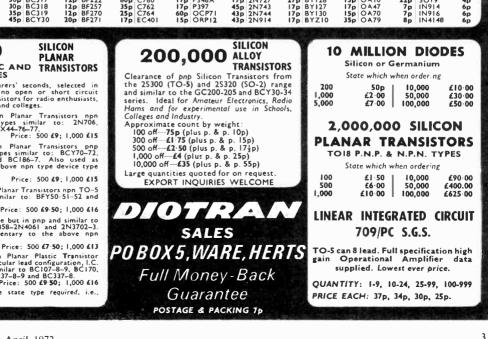
TYPESTN5. Silicon Planar Transistors npn TO-5 Metal Can. Types similar to: BFY50-51-52 and 2N2192-92.

Price: 500 £9-50: 1,000 £16

TYPE STPL. As above but in pnp and similar to types 2N5354-56, 2N4058-2N4061 and 2N3702-3. Also used as complementary to the above npn devices type STNL.

TYPE STNK. Silicon Planar Plastic Transistor npm with TO-18 pin circular lead configuration, I.C. 200mA, 300mW and similar to 8C1078-8-9, 8C170, BC173, BC182-184, BC237-8-9 and BC337-8. Price: 500 (49:50; 1,000 (16

When ordering, please state type required, i.e., 5TNK or STN18, etc.





By A. Russell

A SIMPLE but effective unit for heightening the higher frequency harmonics of electric guitar sounds is a Treble Booster.

The actual sounds produced are akin to those made by the early "Rock in Roll" guitarists, particularly when played near to the instrument bridge.

In addition to use as an effects unit the Booster will act as a straightforward pre-amplifier if required.

HOW IT WORKS

Basically the circuit consists of a simple preamplifier, using a low noise, high gain transistor. In shunt with the input (Fig. 1) is an inductor L1.

The impedance of this is less to low frequency

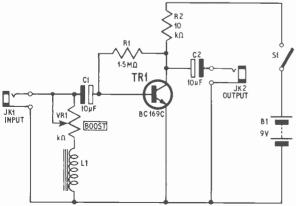


Fig. 1. Circuit diagram of Treble Booster

signals and so the bass notes tend to be shunted to earth leaving the higher frequencies to be amplified and passed to the output socket JK2.

The "Boost" control VR1 is a 5 kilohm potentiometer in series with L1 and it controls the amount of bass cut applied to the incoming signal. When the wiper is rotated for maximum resistance there is almost no bass loss and all frequencies are amplified equally.

CONSTRUCTION

The prototype was built on a piece of 0.15in matrix Veroboard $1\frac{1}{2}$ in $\times 2\frac{1}{2}$ in as in Fig. 2. No breaks are required in the copper strips.

The primary of a small transistor output transformer is used for L1. The secondary winding and centre tap is not used and the leads from these should be cut short.

Control panel and Veroboard interwiring is straightforward (Fig. 2) and should present no difficulties. To prevent hum pick-up the input and output leads should be screened.

TESTING

With the unit completed, the wiring should be given a final check. With the battery connected you should find that the circuit will work first time since it is so simple.

A point to watch is the siting of the Booster. If it is placed near to the mains transformer of the amplifier, hum will be picked up by L1 so this should be avoided.

COMPONENTS . . .

Resistors R1 1.5MΩ R2 10kΩ ‡W 10% carbon

Capacitors C1, C2 10µF elect. 25V (2 off)

Potentiometer

VR1 5kΩ lin. carbon

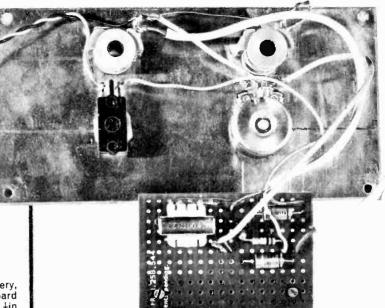
Inductor

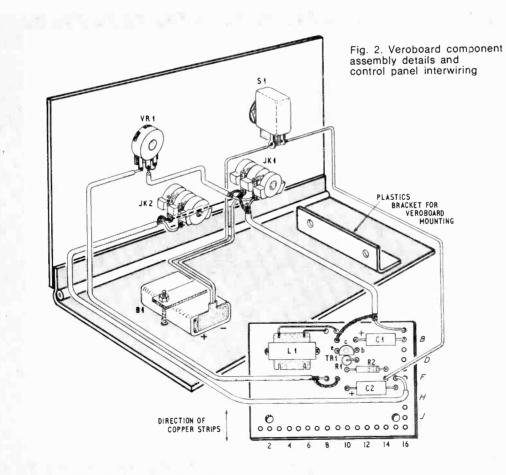
L1 Eagle L.T700 miniature output transformer

Transistor TR1 BC169C

Miscellaneous

JK1, JK2 Standard jack sockets (2 off) S1 On/off toggle switch, B1-PP3 9V battery, Battery connectors, Control knob, Veroboard 1¼in × 2¼in 0.15in matrix 2½in length of ½in × ½in plastics angle.





Silicon Controlled Switch

By D. Burn, Ph.D.

THE purpose of this article is to introduce a relatively little known semiconductor device, the silicon controlled switch (SCS), and to show how quite simple and inexpensive counting circuits may be constructed with it.

THE SILICON CONTROLLED SWITCH

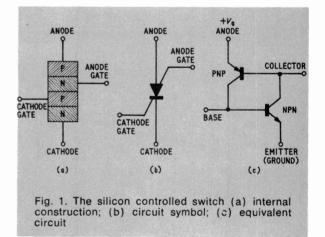
The SCS is a four layer device, very similar to the thyristor, in which all four layers are accessible as electrodes (Fig. 1a). Unlike most thyristors, it is designed to handle only fairly small currents (up to 500mA), and may be regarded as a low power transistor combined with a "holding" circuit.

The usual symbol for the SCS is shown in Fig. 1b, which corresponds closely to the structure of Fig. 1a, and emphasises its similarity with the thyristor. On the other hand, it is much easier to understand the operation of the SCS if it is thought of as a pair of *pnp-npn* transistors connected as shown in Fig. 1c, the electrodes being named accordingly.

Suppose that a voltage is applied to the device as indicated in Fig. Ic and that the base is reverse biased by means of a resistor connected between the base and emitter. Provided that the applied voltage is less than the transistor breakdown voltage, neither transistor will conduct, since neither can obtain any base current. The device will therefore be in a stable "OFF" state.

If a positive pulse is now applied to the base, the npn transistor will begin to conduct and thus supply base current to the pnp transistor. This in turn will begin to conduct and supply more base current to the npn transistor, augmenting that derived from the input pulse.

The process is now self-sustaining and the current through the device rapidly increases until both the transistors are saturated. Since each transistor is providing the base current required by the other, there is no further need for the input pulse, and current will continue to flow after the input has ceased. The device is now in a stable "ON" state, with the value of the anode current being determined by the external circuitry.



TURNING OFF

To turn the device off again, it may be thought that it is merely necessary to reverse the turn-on procedure and apply a negative pulse to the base. Unfortunately it is not quite as simple as that.

Due to the very high gain of the device, the magnitude of such a negative pulse would almost certainly exceed the maximum permitted reverse base-emitter junction potential and thus destroy the SCS. Instead, a technique is employed analogous to that used with thyristors: reduction of the anode current to a low value.

There is a certain minimum current, known as the holding current $i_{\rm H}$, necessary to maintain the device in the "ON" state. If the anode current is reduced below $i_{\rm H}$, neither transistor can obtain sufficient base current to keep it conducting and so the combination rapidly turns off. This may be achieved either by applying a negative pulse to the anode, or a positive pulse to the emitter; both methods will be illustrated in the circuit to follow. Once the device has turned off, the anode potential may be restored for, as we have already seen, it can only be turned on by a pulse at its base.

GARLA DEPTFORD BR		BROS. L Y, LONDON,	Device Exception .
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	SOOV S/M ISp 500V Cer. Sp 500V Poly. Sp 500V Poly. Sp 500V S/M ISp 500V Poly. Sp 500V Poly. Sp 500V S/M ISp 500V V/M ISp 500V V/M ISp 500V S/M ISp 125V P.S. IOIp 125V P.S. IOip 125V P.S. IOip 500V S/M ISp 500V S/M ISp 500V S/M ISp 500V S/M ISp 500V <td< td=""><td>RESISTORS CARBON All 5%, high-stability. El2 values. $\frac{1}{2}$ watt-$\frac{1}{2}$ watt-$\frac{1}{2}$ watt-$\frac{1}{2}$ watt-$\frac{1}{2}$ watt-$\frac{1}{2}$. WIREWOUND $\frac{1}{2}$ watt-$\frac{1}{10}$; 10 watt-$\frac{1}{2}$. LOW-OHM RESISTORS $\frac{1}{2}$ watt wire-wound. 10, 1.8Ω, 27 Ω, 33 Ω, 39 Ω, 47Ω, 5-6 Ω, 68 Ω, 82 Ω. CONTROLS, Log. or Lin. Single, less switch, 15p Single, D.P. switch, 12p Tandem, less switch, 40p Sk Ω, 10k Ω, 25k Ω, 50k Ω, 100k Ω, 250k Ω, 500k Ω, 10k Ω, 25k Ω,</td><td>$\begin{array}{c c} \textbf{ELECTROLYTICS} \\ \hline 10\mu^F & 64V & 6p \\ 25\mu F & 50V & 6p \\ 50\mu F & 50V & 10p \\ 100\mu F & 25V & 10p \\ 100\mu F & 50V & 10p \\ 250\mu F & 25V & 12p \\ 250\mu F & 25V & 13p \\ 500\mu F & 25V & 18p \\ 500\mu F & 25V & 18p \\ 500\mu F & 25V & 18p \\ 2,000\mu F & 25V & 35p \\ 2,000\mu F & 25V & 36p \\ 2,000\mu F & 25V & 35p \\ 2,500\mu F & 50V & 51p \\ 2,500\mu F & 50V & 51p \\ 2,500\mu F & 50V & 60p \\ 3,000\mu F & 25V & 45p \\ 5,000\mu F & 25V & 45p \\ 5,000\mu F & 25V & 55p \\ 5,000\mu F & 50V & 98p \\ \end{array}$</td></td<>	RESISTORS CARBON All 5%, high-stability. El2 values. $\frac{1}{2}$ watt- $\frac{1}{2}$ watt- $\frac{1}{2}$ watt- $\frac{1}{2}$ watt- $\frac{1}{2}$ watt- $\frac{1}{2}$. WIREWOUND $\frac{1}{2}$ watt- $\frac{1}{10}$; 10 watt- $\frac{1}{2}$. LOW-OHM RESISTORS $\frac{1}{2}$ watt wire-wound. 10, 1.8Ω , 27 Ω , 33 Ω , 39 Ω , 47Ω , 5-6 Ω , 68 Ω , 82 Ω . CONTROLS, Log. or Lin. Single, less switch, 15p Single, D.P. switch, 12p Tandem, less switch, 40p Sk Ω , 10k Ω , 25k Ω , 50k Ω , 100k Ω , 250k Ω , 500k Ω , 10k Ω , 25k Ω ,	$ \begin{array}{c c} \textbf{ELECTROLYTICS} \\ \hline 10\mu^F & 64V & 6p \\ 25\mu F & 50V & 6p \\ 50\mu F & 50V & 10p \\ 100\mu F & 25V & 10p \\ 100\mu F & 50V & 10p \\ 250\mu F & 25V & 12p \\ 250\mu F & 25V & 13p \\ 500\mu F & 25V & 18p \\ 500\mu F & 25V & 18p \\ 500\mu F & 25V & 18p \\ 2,000\mu F & 25V & 35p \\ 2,000\mu F & 25V & 36p \\ 2,000\mu F & 25V & 35p \\ 2,500\mu F & 50V & 51p \\ 2,500\mu F & 50V & 51p \\ 2,500\mu F & 50V & 60p \\ 3,000\mu F & 25V & 45p \\ 5,000\mu F & 25V & 45p \\ 5,000\mu F & 25V & 55p \\ 5,000\mu F & 50V & 98p \\ \end{array} $
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	100V Mylar 3p 18V Disc 5p 250V M.F. 3p 600V Poly. 3p 600V MDC 7p 000V MDC 7p 250V MDC 7p 250V MDC 9p 250V M.F. 4p 12V Disc 6p 120V Poly. 4p 250V M.F. 3p 30V Poly. 4p 250V M.F. 4p 400V Poly. 5p 30V Disc 6p 30V M.F. 4p 250V M.F. 5p 400V Poly. 5p 250V M.F. 5p 400V Poly. 5p 250V M.F. 5p 250V M.F. 8p 250V M.F. 8p 250V M.F.	FUSES I jin glass—2jp 60, 100, 150, 250, 500, 750mA; I, 1-25, 1-5, 2, 2-5, 3, 5, 7-5, 10, 15 amp. jin glass—2jp 100, 250, 500mA; I, 2-5 amp. Anti-surge Ijin—8p 250, 500, 750, 850mA; I, 1-5, 2, 3 amp. Anti-surge 20mm—5p 80, 125, 200, 315, 400, 500, 630, 800mA; I, 2 amp.	SILICON BRIDGE RECTIFIERS 40 P.I.V., 1'5 amp. 5000 200 P.I.V., 2'0 amp. PANEL FUSE-HOLDERS For 1 jin fuses 18p For 20mm fuses 18p For 20mm fuses 15p EENER DIODES From 2 to 33 volts. 400mVV, 15p; 1:5 watt, 22jp
0.001µF 1,000V MDC 6p 0.47µF 0.0015µF 400V Poly. 3p 0.47µF 1 0.0015µF 500V S/M 10p 1.0µF 0.0015µF 500V Cer. 5p 0.0018µF 500V S/M 10p 0.002µF 500V Cer. 5p P.S. = 0.002µF 500V Cer. 5p P.S. = 0.002µF 125V P.S. 6p MDC 0.0022µF 125V P.S. 6p MDC	400Y Foil 15p 000Y MDC 20p 250Y M.F. 15p = silver mica 1% tol. = polystyrene 21% tol. - a.c. rating = 300V. = Mullard min. foil. = dullard min. foil. = ceramic.	PLUGS 14p Co-axial 8p D.I.N. 2 pin (speaker) 10p D.I.N. 3 pin 13p D.I.N. 5 pin, 180° 13p D.I.N. 5 pin, 240 13p D.I.N. 5 pin, 240 13p	TITLE CAR
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Thus, in the SCS we have an inherently bistable device which, with the minimum of additional components, will function in the same way as a bistable circuit requiring two transistors, two diodes and several resistors and capacitors. Hence it is possible to construct counting circuits which are very much simpler, and more eonomical, than their transistor counterparts.

ANODE GATE

So far in this discussion, the collector, or anode gate, has not been mentioned. In fact, it is quite possible to design SCS counting circuits in which the collector is not used at all. However, where it is desired to provide a visual indication of the count, the collector forms a very convenient output electrode. Although at first sight it may appear somewhat paradoxical that the anode is used as a control electrode and the anode gate forms the output electrode, the *npn-pnp* concept of Fig. 1c does help to provide a logical explanation.

Reference to Fig. Ic will show that in the "ON" state, the collector will be very close to 0 volts (i.e. V_{CEsat} of the *npn* transistor). In the "OFF" state, the collector will be at some positive potential determined by the external circuit.

Hence one form of read-out would be a low voltage, low current filament bulb included in the collector circuit as indicated in Fig. 2a. If, on the other hand, numerical indicator tubes are required, then another valuable feature of the SCS comes in useful, that is, it is designed to withstand high voltages. This means that the collector may be directly

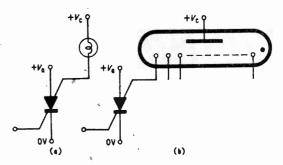


Fig. 2. (a) SCS used to drive low voltage bulb; (b) SCS driving a cold cathode tube

connected to an indicator tube cathode without the need for an intermediate, high-voltage buffer transistor (Fig. 2b).

RING COUNTER

Having discussed the SCS in some detail, the basic circuit for a ring counter with an indicator tube read-out can be introduced (Fig. 3). While it may appear rather formidable, it is, in fact, far simpler than a corresponding transistor circuit. Its operation may be explained in the following way.

To begin, assume that the \overrightarrow{RESET} line is connected to the 0 volt line, and that the first SCS, CSR0, is on. Cathode 0 of the indicator tube V1 thus has direct path to 0 volt and is therefore lit.

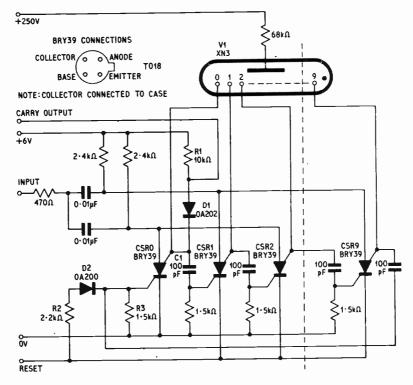


Fig. 3. An SCS ring counter

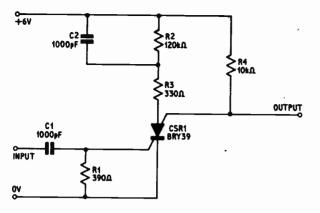


Fig. 4. An SCS pulse generator

The anode of CSR0, and of all other even numbered SCSs, will be close to 0 volt, whilst the collectors of all the other SCSs will be at potentials ranging from about ± 15 to ± 120 volts, determined mainly by the characteristics of V1.

If a negative pulse of about 6 volts amplitude is applied to the input, all SCSs will lose their anode potential so that CSR0 will turn off and cathode 0 will be extinguished. The collector of CSR0 will go positive and this positive pulse will be passed to the base of CSR1 via C1. CSR1 will therefore turn on, its collector will fall to 0 volt and cathode 1 of V1 will light. Athough the negative input pulse is applied to the anodes of all SCSs, this will not prevent CSR1 from turning on. The design of the circuit is such while the anode of CSR0 goes to about -2 volts, the anode of CSR1 only falls to about +2 volts, which is just enough to allow it to turn on.

Thus the use of two anode lines is essential to the correct operation of this type of counter, and ensures that only the correct SCS can turn on with each successive input pulse. It should perhaps be emphasised that this feature means that the circuit can only be used for an even numbered count, since an odd number of stages would require two consecutive anodes to be connected to the same line.

This then is the basic ring counter circuit, requiring only a few additional refinements to turn it into a fully operational unit.

PULSE GENERATOR

First of all, we need to generate suitable input pulses, and although there are several pulse generator circuits that would do the job, a particularly simple one results from the use of another SCS, as shown in Fig. 4. The SCS is normally held off by the bias resistor R1. A positive pulse of any shape and duration applied to its base will turn it on so that the anode drops to close to 0 volts. C2 rapidly charges to 6 volts through R3 and the SCS, its charging current keeping the SCS turned on.

Once C2 is charged, this current falls to zero so that the only current that can flow becomes determined by R2. The value of this resistor is chosen so that this current is less than $i_{\rm H}$, hence the SCS turns off, and C2 discharges through R2. The result of all this is that a negative pulse having very short rise and fall times, and the desired amplitude and duration, appears at the collector.

CARRY AND RESET

If one wishes to set up a chain of ring counters, the circuit must be arranged such that as each one completes its count, it automatically provides an output pulse suitable for stepping the next counter in the chain. This is achieved by means of diode D1 and resistor R1 in the collector circuit of CSR0 Fig. 3). When CSR0 is off, D1 will be reverse biased so that the output line is clamped to +6volts.

As soon as CSR0 turns on, D1 becomes forward biased and the output falls to 0 volts, thus providing the required input to the next counter.

This very simple way of obtaining a logic output from CSR0 can, of course, be applied to every SCS in the counter, thus making it possible to add, for example, pre-determining and alarm circuits to the basic counter.

The last function to be considered is the RESET facility. It may be seen that the emitters of all SCSs, except CSR0, are connected to the reset line (Fig. 3), and it was previously assumed that this line was connected to 0 volt. Suppose that the reset line is momentarily taken to +6 volts. All SCSs other than CSR0 lose their supply voltage, and the one that was on will turn off.

At the same time, this positive pulse is coupled into the base of CSR0 via R2 and D2, and since CSR0 has not lost its supply it will be turned on. The reset line also fulfils another important function. When power is first applied to the circuit, it is probable that none of SCSs will turn on. Furthermore, since the input pulses merely turn off any SCS that was conducting, there is no way of initially turning on any particular SCS. The reset line allows the circuit to start operating correctly by turning CSR0 on.

OTHER APPLICATIONS

So far, this discussion has been limited to counting circuits in which a read-out is required. However, many applications do not need a read-out and the SCS may be used just as efficiently to construct such circuits.

A ring counter having n stages, where n is an even number, will deliver one output pulse for every n input pulses, and is a very simple method of frequency division. A suitable circuit is illustrated in Fig. 5. It is virtually identical with that already described and operates in exactly the same way.

The indicator tube has been replaced by resistors which are returned to a voltage preferably higher than the anode supply. The reason for this is that under certain circumstances the collector of an SCS could go less positive than its anode. If this should happen, the SCS would be turned on since effectively the base of a *pnp* transistor is going negative with respect to its emitter (see Fig. 1c).

An output pulse, limited in amplitude to about 6 volts, may be obtained from the collector of any stage by means of a diode-resistor combination as shown for CSR0. Suitable input pulses may be obtained from the same type of pulse generator that was used in the counter circuit.

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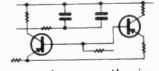


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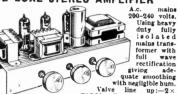
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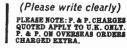
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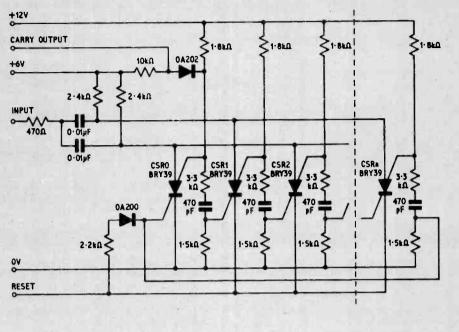


Fig. 5. An SCS frequency divider

The usual problem will arise if it is desired to produce an odd-numbered count. However, it will generally be possible to devise a gating circuit which will detect the required count and apply a reset input to the counter.

RADIO CONTROL APPLICATIONS

Some very sophisticated circuitry has been developed for modern model radio-control gear, and the final example of a counting circuit is typical of those used in the so called digital proportional equipment.

In this system, the position adopted by a servo mechanism is proportional to the duration of an input pulse supplied to it. This duration is made variable, typically between 1 and 2 milliseconds, for travel between one extreme of servo position and the other. The transmitter is therefore required to generate a string of pulses, one for each function to be controlled, whose individual durations may be varied at will, generally by means of "joy-stick" type of controls.

The appearance of such a pulse train is shown in Fig. 6, where the four pulses might control the movement of the rudder, elevator, ailerons and engine throttle. The task of the receiver is to detect these pulses and to route each one to the correct servo.

It is not intended to describe in detail the circuits of a complete system, for which the reader is referred to the numerous publications specialising in this subject. Instead, the description is limited to one aspect of the system—how does the receiver sort out the incoming pulses and know which pulse to send to which servo? This is a vitally important task, for the result of the receiver interpreting, for example, a "left rudder" command as a "down elevator" command could well be disastrous! The part of the

Practical Electronics April 1972

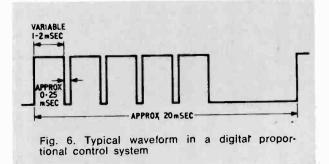
receiver that carries out this function is known as the decoder, and may take the form of a particularly simple SCS counter (Fig. 7).

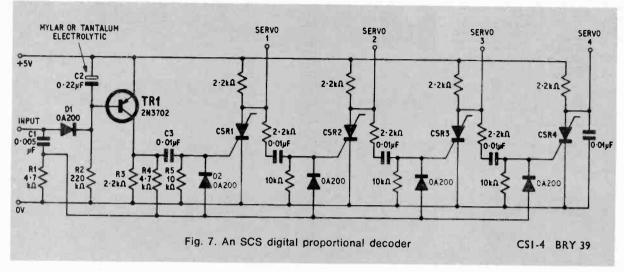
There are several important differences between this circuit and those described earlier. In the first place, the collector (anode gate) is not used at all, the output being taken from the anode. The SCS is in fact functioning as a low current, sensitive thyristor. Secondly, the SCS is turned both on and off by means of positive and negative pulses respectively applied to its base. While this may appear to conflict with what was said earlier in this article, it is permissible here because this is a low-current, lowvoltage circuit.

Finally, the circuit is not a ring counter, that is, there is no feedback from the output of the last stage to the input of the first. For reasons that will become evident later, the counter has to be told when to go back to its initial state.

DECODER OPERATION

The operation of the circuit may be followed with the aid of the waveforms shown in Fig. 8. The



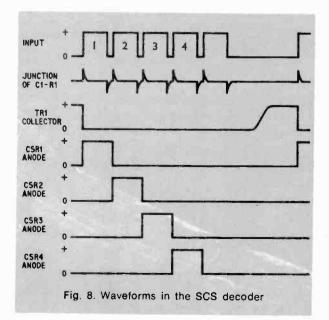


input pulses are applied to the differentiating network CI-RI and the resulting positive spikes are passed, via diodes D2, to the bases of all the SCSs and turning them all on. (The "resting" state of the counter is that in which all the SCSs are on, so that all the outputs are at 0 volts.)

At the same time, the first positive going transition of the input will discharge C2, previously charged through R2, and thus turn TR1 off. Its collector will go negative and apply a negative-going spike, via a second differentiating network C3–R5, to the base of CSR1 and so turn it off.

There is an apparent problem here in that CSR1 has both a positive and a negative pulse applied to it at the same time. However, it will be seen from the component values that the time constant of C3–R5 is much longer than that of C1-R1 so that the negative pulse wins and CSR1 turns off. This negative pulse is prevented from reaching the bases of the other SCSs by the diodes D2.

The next input pulse will turn CSR1 on again via D2 so that its anode returns to 0 volts and terminates



the first output to servo 1. At the same time, this negative transition is passed to the base of CSR2, and once again the time constants are such that CSR2 is turned off and initiates the input to servo 2. This process continues until the final SCS has been turned off and then on again, and one pulse has been applied to each servo in turn.

RESET PERIOD

Up till now, however, there has been no way of ensuring that the correct pulse has reached the proper servo, and this is where the long gap in the train of pulses comes in. There is a third and very important time constant built into the circuit, that of C2 and R2. This is a very long one, as long as the longest possible duration of two consecutive input pulses.

The result is that once TR1 has been turned off by the first pulse in a sequence, C2 does not have time to charge up sufficiently to turn it back on again until the last pulse in that sequence has been decoded. When it finally does turn on, the resulting positive transition at its collector ensures that all the SCSs are turned on, via R4 and the diodes D2, ready for the next sequence. This explains why the counter is not a closed ring, for if the transmitter and decoder should get out of step, there will be a maximum period of less than 20ms before the counter is reset to its initial state and the correct sequence is restored.

It is hoped that these examples have given some indication of the versatility of the silicon controlled switch and may perhaps prompt some further experimentation.



LOGICAL RADIO CONTROL (January 1972)

Page 51, Fig. 17a. IC1, IC2 and IC3 should read IC2, IC3 and IC4. Fig. 20. Connection pads to pins 8 and 9 on IC2 should be reversed.

I.C. AUDIO MIXER (January 1972)

Page 44. Fig. 2. An additional break should be made at 10A on the component board.

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NEW IKW MODEL Electronically changes speed from approxi-mately 10 revs. to maximum. Full power at all speeds by finger-tip control. Kit includes all parts, case, everything and full instructions. 81:50 plus 13p post and insurance. Made up model also avaii-able, \$2:25 plus 13p post and p

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THE FULL-FI STEREO SIX

THE AMPLIFIER SENSATION OF THE YEAR

EXAMPLE A CONTRACT OF THE VERSE THE STATE OF THE VERSE OF THE VERSE THE STATE OF THE VERSE OF THE VERSE THE STATE OF THE VERSE OF THE VERSE OF THE VERSE THE STATE OF THE VERSE OF TH

DISTRIBUTION PANELS



Just what you need for work bench or lab. 4 x 13A sockets in metal box bo takestandard 13A fused plugs and on/off switch with neon warning light. Supplied complete with 6ft of fex cable. Wired up ready to work. \$25.26 with fitted 13A plug, plug 23p P. & I.

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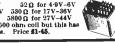


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			1 normally closed	75p
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Correspondents wishing to have a reply must enclose a stamped addressed envelope. We regret we are unable to guarantee a reply on matters not relating to articles published in the magazine. Technical queries cannot be dealt with on the telephone.

Who pays?

Sir-I have just read your February Editorial and may I, with respect, be permitted to ask a question?

Who is going to pay for all these fantastically expensive schemes you are proposing? As a taxpayer, I, for one, strongly object. After all, it only needs a little less speed (i.e. to the prevailing conditions) and a little more courtesy and considera-tion for other road users by a very small fraction of motorists, and your problem of "Motorway Super-vision" in large measures disappears.

The considerations of safety for the air do not compare with those for the road. I cannot imagine BOAC allowing a "provenallowing a dangerous - complete - with - en-dorsements" driver even to enter the flight deck, let alone fly,

No, Sir, do not suggest pampering them at our expense, but sock'em hard by removing their licences at their OWN expense. I regret that this sentiment does not promote the electronic trade, as we know that is your inclination of course, but would you say me nay? J. G. Newbury,

Ashby Parva,

Rugby.

Paradoxical stage

Sir-I am a retired teacher, having spent 41 years teaching "low level" physics mathematics and in secondary schools, and I naturally compare the new maths with the traditional. I did have some contact with the new maths and Nuffield Science before I retired, and I followed the new maths pro-gramme on BBC schools television and have close contacts by observing my grandchildrens' textbooks and their methods.

This comparison causes me much worry. I believe a blend of the old and new is the best solution, but so far as I can see, the balance is being tipped too much in favour of the new maths, and the reason put forward is that we must teach our students about computers and computer programming.

This was the reason why one of my grandsons went through junior school without learning the multiplication tables! The Headmaster said that computers do all that! Needless to say, the boy was seriously handicapped at the Comprehensive School.

But it does not end there. A careful study of his textbooks shows that the old traditional methods take about five per cent of the space.

From my own observations I know that those old fashioned initials H.C.F. and L.C.M. have key rolls in computer mathematics, and I know from experience that they teach far more about numbers than any other sysem, but they have absolutely no mention in the modern text book, and even comparing the value of fractions by bringing them to a common denominator has only one paragraph in a whole chapter given to that subject.

And how does this affect traditional physics? Part of the purpose of going to school is to learn to think in a logical way.

The Wheatstone Bridge is a first-class example of logical thinking yet I can find no help in the maths text book for working out

$$R_{\rm X} = \frac{{
m R}3}{{
m R}4} \times \frac{{
m R}2}{{
m R}1}$$

It looks as though we are coming to the paradoxical stage when the physics department will have to teach "old fashioned" mathematics in order that the students will understand the "computer" mathematics departments who are doing all this to help in the teaching of electronics?

> G. A. Cozens. Southampton.

"Switched on"

Sir,-It may interest you to know that I am 76 years old and I became hooked on your magazine about three years ago. Your magazine has revived my old interest in these subjects of fifty or more years ago -"the cat's whisker days.

All my working life I have never been able to find the time to follow up these interests but I have now found my feet, so to speak. I like the methods used by your technical contributors in explaining the functions of the new apparatus and components. These descriptions have given me a new lease of life. They are, for the most part, brief and precise and full of technical interest

I have been an electrical and mechanical engineer all my working life, in the heavy steel and iron industries, and felt very "switched off" when I had to retire. I can recommend your magazine to any retired gentleman who has some knowledge of these subjects and now requires a real interest in life.

H. O. Guest, Worral, Sheffield.

BACK NUMBERS Anyone who can supply the undermentioned communicate directly with the reader. WANTED are asked

November 1969 Mr. J. Fenn, 33 Tinwell Lodge Cottages,

Tinwell, Stamford, Lincolnshire.

May, June 1970

D. J. Parfitt, c/o The Duke of Richmond Hotel, Cambridge Park, St Peter Port, Guernsey, Channel Islands.

July 1970 to March 1971

Mr. S. January, 18 Hassan Assem, Zamalek, Cairo, A.R.E.

September, October, November 1971 Mr. I. M. Johnstone, 8 Hillpark Crescent, Blackhall, Edinburgh, EH4 7BG.

November 1971

Mr. J. L. Concannon, C.P.O. Mess, H.M.S. Osprey, Portland, Dorset.

February 1971 Mr P. J. Arden, 1, Hallam Grange Rise, Fulwood, Sheffield S10 4BE

May 1969 to March 1970 D1922536 Cpl. R. Larkman, "B" Flight (SAM), RAF North Luffenham, Oakham, Rutland.

January 1969

Mr A. J. Campbell, "Donegal", 9, Medina Gardens, East Oakley, Basingstoke, Hants.

We regret that back numbers of Practical Electronics can no longer be supplied. We will try to publish announcements of readers' requirements (without a guaranteed date) free of charge.

October 1970

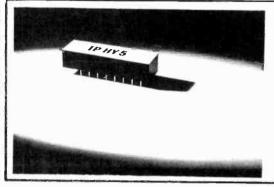
Mr A. P. Allid, 15740, Sherman Way, Van Nuys, California, USA.

January 1967 Mr R. D. Frank, Post Office Box 88, Mt. Lawley 6050, Western Australia.

August to December 1968 Mr B. M. Oddy, 27, Gimble Way, Pembury, Kent.

July to September 1999 Mr. M. McPhie, Lowfields, 6, Enterpen, Hutton-Rudby, Yarm-on-Tees, Yorks.





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C SC DEVICE DEC. C	SE								ES
	Transistors 12N341a	22p.2N5455	- 35p, B(1)4 - 1	15p BFW90 - 22p.NF	T219 30p	Integrated [FJ11101 25p.8874	.30 20p		the water the second second
	26301 20	 27.9 (28.46.3) 27.9 (28.10.2) 37.9 (28.10.2) 37.9 (28.10.2) 37.9 (28.10.2) 37.9 (28.10.2) 37.9 (28.10.2) 37.9 (28.10.2) 25.9 (28.30.2) 25.9 (28.30.2) 25.9 (28.30.2) 27.9 (28.30.2)<td>400 BC115 B 400 BC116 1 25p BC118 1 25p BC118 1 25p BC118 1 25p BC121 22 80p BC121 22 80p BC121 22 80p BC125 1 75p BC126 2 80p BC125 1 75p BC136 1 75p BC136 1 77p BC138 2 801417 3 8059 BC149 1 559 BC149 1 559 BC149 1 559 BC1668 1 870 BC167 1 870 BC1677 1 870 B</td><td>15p BFW91 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX37 30p N+ 15p BFX44 37p N+ 15p BFX46 67p N+ 15p BFX46 67p N+ 15p BFX46 62p N+ 15p BFX46 62p N+ 15p BFX48 62p N+ 15p BFX48 62p N+ 15p BFX48 62p N+ 15p BFY18 42p N+ 16p BFY18 42p N+ 16p BFY18 42p N+ 15p BFY36 45p N+ 16p BFY36 45p N+ 16p BFY36 45p</td><td>T1223 25p T1224 227 T1225 220 T1225 220 T1225 220 T1225 25p T1225 25p T1225 200 T1225 200 T1225 200 T1225 200 T1225 200 T1226 200 T12276 200 T12277 200 T1277 200 T1277 200 T1277 200 S75 200 S75 200 S75 200 S75 200 S75 200 S75</td><td>$\begin{array}{ c c c c c c c c c c c c c c c c c c c$</td><td>40 20p 40 20p 40 20p 40 20p 41 NN 75p 41 NN 75p 42 75p 42 75p 46 100p 44 12Sp 50 20p 51 20p 53 20p 54 20p 55 220p 154 20p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 10 150p 10 150p 55 147p 22 360p 70p 70p 75p 60p 70p 70p 75p 55 227 50 272 30 9 55 427 50 272 30 9 55 427 50 272 30 9 55 147p 22 360p 70p 70p 75p 55 277 50 272 30 9 55 427p 50 272 50 27</td><td>0A2 38p 2524 0B2 45p 2523 0B2 45p 2523 1L4 20p 300117 185 30p 300117 185 30p 300117 185 30p 300117 114 20p 300117 114 30p 300117 114 30p 300117 384 35p 30117 384 45p 30113 573 30113 573 6407 40p 3523 6407 30p 3524 6A07 30p 3524 6A07 30p 3523 6A12 30p 3523 6A12 30p 3523 6A1</td><td>SOLP ELSO SOLP SALP EMAGU SOLP SALP EXAL SOLP SOLP EXAL SOLP SOLP EXAL SOLP SOLP EXAL SOLP <!--</td--></td>	400 BC115 B 400 BC116 1 25p BC118 1 25p BC118 1 25p BC118 1 25p BC121 22 80p BC121 22 80p BC121 22 80p BC125 1 75p BC126 2 80p BC125 1 75p BC136 1 75p BC136 1 77p BC138 2 801417 3 8059 BC149 1 559 BC149 1 559 BC149 1 559 BC1668 1 870 BC167 1 870 BC1677 1 870 B	15p BFW91 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX12 22p N+ 15p BFX37 30p N+ 15p BFX44 37p N+ 15p BFX46 67p N+ 15p BFX46 67p N+ 15p BFX46 62p N+ 15p BFX46 62p N+ 15p BFX48 62p N+ 15p BFX48 62p N+ 15p BFX48 62p N+ 15p BFY18 42p N+ 16p BFY18 42p N+ 16p BFY18 42p N+ 15p BFY36 45p N+ 16p BFY36 45p N+ 16p BFY36 45p	T1223 25p T1224 227 T1225 220 T1225 220 T1225 220 T1225 25p T1225 25p T1225 200 T1225 200 T1225 200 T1225 200 T1225 200 T1226 200 T12276 200 T12277 200 T1277 200 T1277 200 T1277 200 S75 200 S75 200 S75 200 S75 200 S75 200 S75	$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	40 20p 40 20p 40 20p 40 20p 41 NN 75p 41 NN 75p 42 75p 42 75p 46 100p 44 12Sp 50 20p 51 20p 53 20p 54 20p 55 220p 154 20p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 16 200p 10 150p 10 150p 55 147p 22 360p 70p 70p 75p 60p 70p 70p 75p 55 227 50 272 30 9 55 427 50 272 30 9 55 427 50 272 30 9 55 147p 22 360p 70p 70p 75p 55 277 50 272 30 9 55 427p 50 272 50 27	0A2 38p 2524 0B2 45p 2523 0B2 45p 2523 1L4 20p 300117 185 30p 300117 185 30p 300117 185 30p 300117 114 20p 300117 114 30p 300117 114 30p 300117 384 35p 30117 384 45p 30113 573 30113 573 6407 40p 3523 6407 30p 3524 6A07 30p 3524 6A07 30p 3523 6A12 30p 3523 6A12 30p 3523 6A1	SOLP ELSO SOLP SALP EMAGU SOLP SALP EXAL SOLP SOLP EXAL SOLP SOLP EXAL SOLP SOLP EXAL SOLP </td



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2G309 80p 2N3570 £1-25 40324 2G371 15p 2N3572 971p 40395	471p BC212L 13p BSX27 471p NKT244	624) 1A, 21.60; 5A, 21.60; 10V, 21.60; 20V, 21.60; 50V, 31.60; 300' 749) 21.60; 500V, 21.60. 209
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N1090 221p 2N3854 271p AC126 N1091 221p 2N3854A 271p AC127	20p BD116 21-124 BSY37 221p NKT603F	32419 IS113 ISp BA100 ISp BY103 22419 OA73 14 32419 IS120 ISp BA102 22419 BY122 37419 OA79 1
N1131 25p 2N3855 271p AC128 N1132 25p 2N3855A 80m AC154	20p BD123 821p BSY51 321p NKT677F	30p 18121 14p BA110 32p BY126 15p OA81 4 30p 18130 8p BA115 7p BY127 17p OA85 1
2N1302 171p 2N3856 30p AC176 2N1303 171p 2N3856A 35p AC187	25p BD131 75p BS152 325p NKT781 621p BD132 85p BSY53 371p NKT781	25p 18133 10p BA141 32±p BY164 52±p OA80 30p 18132 12p BA142 32±p BYX10 22±p OA91 30p 18920 7p BA144 12±p BYX10 35p OA95
N1304 2219 2N3858 25p AC188 N1305 2219 2N3858A 30p ACY17 N1306 25p 2N3859 271p ACY17	274p BDY11 21-62 BSY56 90p NKT10439	80p 18920 7p BA144 124p BYZ10 35p OA95 ′ 87†p 88923 8p BA145 20p BYZ11 32‡p OA200 10 82†p 8923 12p BA154 12‡p BYZ12 30p TTV307 51
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N 1613 25p 2N 3877 A 40p ACY 28 N 1631 85p 2N 9900 871p	20p BDY60 21-25 C111 75p NKT80113	1 amp (Douglas) MT103 Sec. tappings from 6V to 50V
N1637 80p 2N3901 971p ACY41	25p BF115 25p C426 40n NKT80212	9210 Post and packing 25p. 9219 Post and packing 25p. 9219 Is amp (Douglas) MT107 Sec. tappings from 6V to 50V 27-5 1
N1638 27tp 2N3903 35p AC144 N1671B 41 2N3904 35p AD140 N1711 25p 2N3905 87tp AD149	5219 BF163 8719 C428 8719 NKT80214 5719 BF167 189 C744 809 NKT80215	9219 Post and packing 3710. 9219 Various other Transformers ranging from 1A to 5A in stock.
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N2220 25p 2N4289 171p AF125 N2221 25p 2N4290 171p AF126	20p BF197 42;p GET120 52;p OC41 20p BF198 42;p GET873 12;p OC42	22 p TC4/20 (Pressfit) 4 amp 200 PIV
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£59-00 1 Total

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	£22.00 + 90p p&p £32.00 + £3 p&p	£23-00+1	d bé pě
SYSTEM 2	Viscount R101 amplifier 2 × Duo Type III speakers	cartridge, plinth and cover	Total

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14 watts per channel into 3 to 4 ohms. Total distor-tion of 10W of 1 kHz 0.9%. P.U.J. (for ceramic cartridges) 150mV into 3 Meg. P.U.2 (for magnetic cartridges) 4 mV or 14Hz into 47K equalised within - 1dB R.I.A.A. Rodio 150mV into 220K. (Sensitivi-ties given at full power). Tape out facilities: head.

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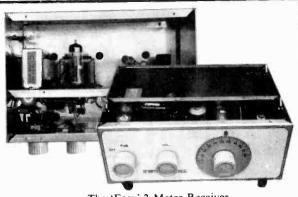
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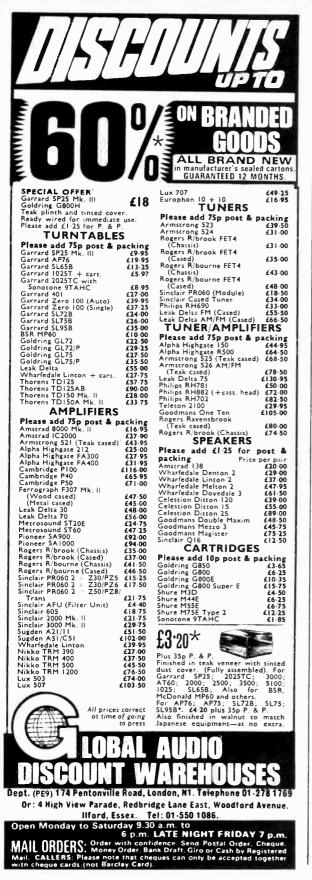
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7401	Quadruple 2-input NAND gate with open collector output	201	20-	16.0	14-		
7402			20p	16p	14p		
7402	Quadruple 2-input NOR gate Quadruple 2-input NAND gate	191	20p	l6p	l4p		
/403	Quadruple 2-input NAND gate with open collector output	291	20p	16p	14p		
7404	Hex inverter	211	25p	21p	18p		
7405	Hex inverter with open collector		тэр	∡rp	rop		
	output	271	25p	21p	18p		
7408	Quad 2-input positive AND gate						
	Totem pole output	381	25p	21p	18p		
7409	Quad 2-input positive AND gate						
	open collector	391	25p	21 p	18p		
7410	Triple 3-input NAND gate	111	20p	16p	I4p		
7413	Schmitt Trigger	351	35 p	29p	25p		
7420	Dual 4-input NAND gate	121	20p	16p	14p		
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7451	Dual 2-wide 2-input AND-OR-				_		
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7453	Expandable 4-wide 2-input AND-	171					
7454	OR-INVERT gate	171	20p	lóp	14p		
7434	4-wide 2-input AND-OR-INVERT gate	181	20-	lóp	14-		
3	gatt	FLY	20p	rep	14p		
7460	Dual 4-input expander	101	20p	lép	I4p		
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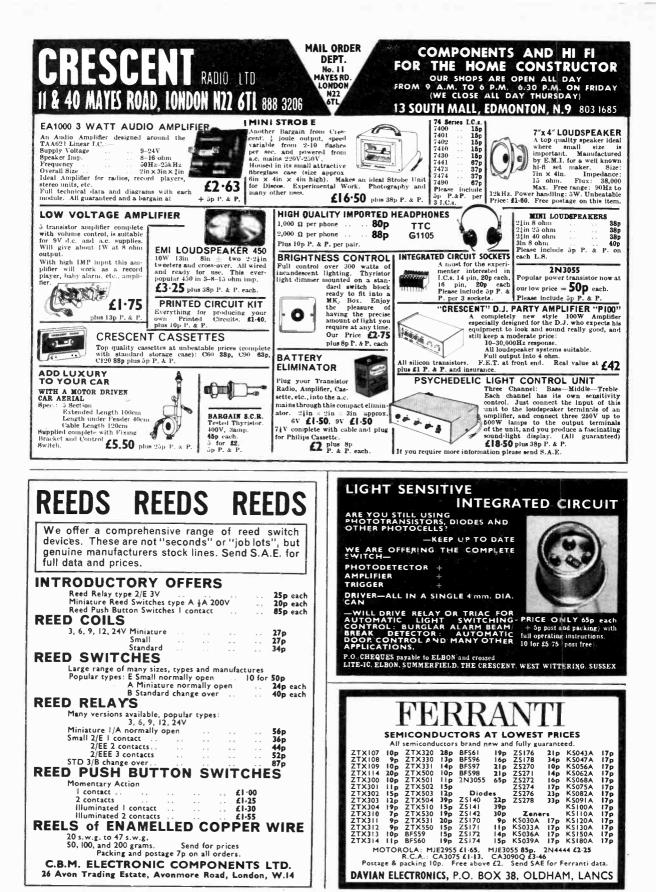
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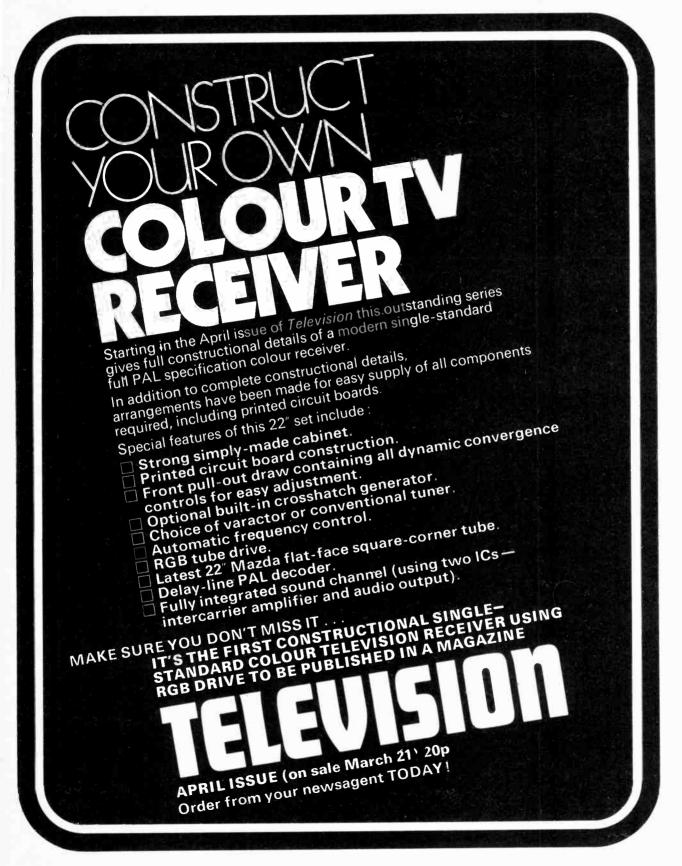
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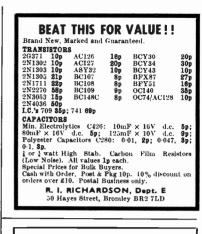
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18021 0-80 ACY 18113 0-15 ACY	21 0.20 22 0.10	BF898	0-28 0-28 0-20	MJE520 MJE2955	0.87 1.87	OC59 OC66	0-50
18130 0-18 ACY	27 0.85	BFX12 BFX13	0.52	MJE3055	0.87	0C70 0C71 0C72	0·12 0·12
16131 0-18 ACY: 16202 0-98 ACY:	28 0-17 39 0-50	BFX29 BFX30	0-25 0-25	NKT128 NKT129	0-85	0C73	0-20
18131 0-18 ACY 18202 0-38 ACY 99340 1-97 ACY 26301 0-30 ACY	40 0-15 41 0-15	BFX35 BFX63	0-98 0-50	NKT211 NKT213 NKT214	0-25	OC74 OC75	0-20 0-20 0-20 0-25
20302 9-33 AUY	14 0.25	BFX84	0.52	NKT214	0.15	0076	0-25
2G371 0-22 AD14	9 0-50	BFX85 BFX86 BFX87	0-80 0-25	NKT216 NKT217	0-87 0-85	0C77 0C78	0-40 0-20
2G381 0-25 AD10 2G414 0-80 AD10	1 0.87 2 0.87	BFX87 BFX88	0-25 0-20	NKT218 NKT219	1·18 0·38	0C79	0-22 0-20
2G417 0.22 AF10 2N214 0.48 AF11	6 0.80	BFY10 BFY11	1.00	NKT219 NKT222 NKT224	0.20	0C81 0C81D	0.20
2N247 0-35 AF11	5 0.25	BFY17 BYF18	1·25 0·25	NK 1224 NK T251 NK T271	0-22 0-24	OC81M OC81DM	0-20 0-18 0-40
2N247 0-25 AF11 2N250 0-50 AF11 2N404 0-20 AF11	7 0-25		0-25 0-25	NKT979	0-25	0C81Z 0C82	0-40 0-85
2N697 0.15 AF11 2N698 0.40 AF11	8 0.62 9 0.20	BFY24 BFY44 BFY50	0.45	NKT273 NKT274 NKT275	0-15 0-20	OC82D OC83	0-20
2N706 0-10 AF12	4 0.25	BFY50	0.22	NKT275	0-25	0084	0-25
2N706A 0-12 AF12 2N708 0-15 AF12	6 0.17	BFY51 BFY52 BFY53	0-20 0-22	NKT277 NKT278	0-20	OC114 OC122 OC123	0-88 0-60 0-65
2N709 0-63 AF12 2N711 0-87 AF13	7 0-17 9 0-80	BFY53 BFY64	0-17 0-42	NKT301	0-40	OC123 OC139	0-65 0-25
2N711 0-87 AF13 2N987 0-58 AF17 2N1090 0-80 AF17	8 0-55 9 0-65	BFY64 BFY90 BSX27	0.65	NKT304 NKT403 NKT404	0.75 0.75 0.55	OC139 OC140 OC141	0-85
2N1091 0.88 AF18	0 0-52	BSX60 BSX76	0-98	NKT678 NKT713	0.80	OC169 OC170	0-20
2N1132 0.25 AF18	6 0.40	B8Y26	0·15 0·18	NKT773	0-25 0-25		0-25 0-80
2N1302 0-18 AFY 2N1303 0-18 AFZ	19 1.18 1 0.60 2 1.00	BSY27 BSY51	0-18 0-17 0-50	NKT777 078B	0-88	OC200 OC201 OC202	0-40
2N1304 0-22 AFZ]	2 1.00	BSY95A	0.12	OA5	0.20	OC202	0-80
2N1305 0-89 ASY2 2N1306 0-85 ASY2	7 0-82	BSY95 BT102/50	0.12 0 R	0A6 0A47	0.12 0.10	OC203 OC204	0-40
2N1307 0-85 ASY2 2N1308 0-86 ASY2	8 0-25 9 0-80	BTY42	0-75 0-92	0A70 0A71	0.10	0.02905	0.75
2N1308 0-35 ASY2 2N1309 0-35 ASY2 2N1420 0-98 ASY2	6 0·25	BTY79/1	00R	OA73	0.10	OC206 OC207 OC460	0-90
2N1507 0-28 ASY	1 0.40	BTY79/4	0.75 00 R	0A74 0A79	0.10 0.10	0C470	0-20
2N1526 0-38 ASY2 2N1909 8-25 ASY2 2N2147 8-76 ASY6	5 0-20	BY100	1.25 0.15	OA81 OA85	0-08 0-12	OCP71 ORP12	0-80 0-97 0-80
2N2147 0.75 ASY6 2N2148 0.60 ASY8	2 0.25	BY126	0.15	OA86 OA90	0-15 0-08	ORP60 ORP61	0-40
2N2160 0-00 ASZ2	1 0.42	BY127 BY182	0.17 0.85	O A 91	0.07	910T	0-80
2N2218 0-20 ASZ2 2N2219 0-20 AUY	10 0-98	BY213	0.25	OA95 OA200	0.07	SAC40 SFT308 ST722	0-25 0-38 0-38
2N2287 1-08 AU10 2N2297 9-20 BC10	7 0-10	BYZ10 BYZ11	0.85	OA202 OA210	0.10	8T722 8T7231	0.62
2N2369A 0-15 BC10	8 0.10	BYZ12	0.80	OA211 OAZ200	0-80	SX68 SX631	0-20
2N2613 0-28 BC11	3 0.15	BYZ13	0.25	OAZ201	0.50	8X635	0-40
2N2646 0-45 BC11 2N2712 0-55 BC11	5 0-20 6 0-25	BYZ15 BYZ16	1.00	OAZ202 OAZ203	0.42	SX640 SX641	0-50 0-55
2N2712 0-35 BC11 2N2784 0-50 BC11 2N2846 0-75 BC11	6A 0-80 6 0-25	BYZ88C3	V3 0-15	OAZ204 OAZ205	0.80	SX 642 SX 644	0-60
2N2848 -43 BC12	1 0-20	C111	0-65	O A Z 206	0-42	SX645	0.75
03100044 8 84 10(10	5 0-68	CR81/05 CR81/40	0-25	OAZ207 OAZ208	0-47	V15/30P V30/201P	0-50 0-75
2N2906 0-20 BC12 2N2906 0-20 BC12 2N2907 0-23 BC14 2N2924 0-33 BC14	6 O+65	CS4B CS10B	2.50 8.18	OAZ209 OAZ210	0-82	V60/201 V60/201P	0-50 0-75
2N2907 0-28 BC14 2N2924 0-33 BC14 2N2925 0-15 BC14	7 0-15 8 0-18	DD000 DD003	0.15	OAZ211	0-82	X A 101 X A 102	0-10 0-18
2N2926 0-10 BC14	9 0-15	DD006	0.15 0.18	OAZ222 OAZ223	0-45	X A102 X A151	0.15
2N3055 0.75 BC15	7 0-15 8 0-12	DD007 DD008	0-40	OAZ224 OAZ241	0-45	XA152 XA161	0-15 0- 25
2N3702 0-10 BC16 2N3705 0-10 BC16 BC16		GD3 GD4	0-83	OAZ242 OAZ244	0.28	XA161 XA162	0-25
2N3706 0-23 DOV		GD5	0.88	OAZ246	0-28	XB101	0-48
2N3709 0.10 BCYS	2 0.55	GD8 GD12	0-25 0-05	OAZ290 OC16	0-38	X B102 X B103	0-10 0-85
2N3710 0-10 BCY3 2N3711 0-10 BCY3	3 0-25 4 0-80	GET102 GET103	0.80	OC16T OC19	0-38	XB113	0-19
2N3710 0-10 BCY3 2N3711 0-10 BCY3 2N3819 0-85 BCY3 2N3820 0-66 BCY3 2N3823 0-75 BCY4 2N5027 0-83 BCY4 2N5088 0-83 BCY3 28006 1-60 BCY3 28178 0-60 BCY3	8 0-40 9 1-00	GET113 GET114	0.20	OC20 OC22 OC23	0-85	X B121 ZR24	0-48
2N3823 0.75 BCY4 2N5027 0.58 BCY4	0 0.50	GET115	0-45	0C23	0-60	Z8170	0-10
2N5027 0-58 BCY4 2N5088 0-88 BCY7	2 0-25 0 0-15	GET116 GET120	0-50 0-25	OC24 OC25	0-60	Z8271 ZT21	0-18 0-25
28005 1.00 BCY7 28178 0.40 BCZ1	1 0-20 0 0-85	GET872 GET875	0-80		0-25	ZT43	0-25
28301 9-50 BCZ1	1 0-50	GET880	0-87	OC28	0.60	ZTX107	0-15
28501 9-87 BD12	3 0·80	GET882	0-25	OC29 OC30	0-60 0-40	ZTX108 ZTX300	0-12 0-12
28703 0-62 BD12 AA129 0-90 BDY AAZ12 0-80 BF11	1 1.62	GET885 GEX44	0-25	OC35	0-50	ZTX 304	0-25
AAZ12 0-30 BF11 AAZ13 0-12 BF11	5 0-25 7 0-50	GEX45/1 GEX941	0·10 0·15	OC36 OC41	0-60	ZTX 500 ZTX 503	0-16 0-17
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