## PRACTICAL

## DECEMBER 1971



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| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| GP25* | 20 V | InA | 10 V | 20 mA | - | TO72 |
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$\begin{array}{lll}0.16 & 0.14 & 0.18 \\ 0.16 & 0.14 & 0.18\end{array}$ $\begin{array}{lll}0.15 & 0.14 & 0.12 \\ 0.18 & 0.14 & 0.12 \\ 0.09 & 0.98 & 0.91\end{array}$ $\begin{array}{lll}0.29 & 0.28 & 0.84 \\ 0.15 & 0.14 & 0.18\end{array}$ $\begin{array}{lll}0.18 & 0.14 & 0.12 \\ 0.15 & 0.14 & 0.12\end{array}$ $0.87 \quad 0.84 \quad 0.88$ $\begin{array}{lll}0.97 & 0.84 & 0.88\end{array}$ $\begin{array}{lll}0.15 & 0.14 & 0.12\end{array}$ $\begin{array}{lll}0.15 & 0.14 & 0.12\end{array}$ $\begin{array}{lll}0.15 & 0.14 & 0.12 \\ 0.15 & 0.14 & 0.12\end{array}$ $\begin{array}{lll}0.16 & 0.14 & 0.12 \\ 0.15 & 0.14 & 0.12 \\ 0.29 & 0.86 & 0.94\end{array}$ $\begin{array}{lll}0.29 & 0.26 & 0.24 \\ 0.29 & 0.26 & 0.24 \\ 0.37 & 0.35 & 0.38\end{array}$

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| $=7410$ |

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$B P 30=7430$
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BP41 $=7441$
BP42 $=7442$
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BP47 = 7447
$\mathrm{BP} 4=7448$
$\mathrm{BP} 50=7450$
HP51 $=\mathbf{7 4 5 1}$
BP53 $=7453$
$\mathrm{BPG4}=74.34$
$B P 60=7460$
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$\mathrm{HP} 83=7483$
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BP 92
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Br 93
$=7493$
$\mathrm{BP94}=7494$
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$\mathrm{HF96}=7496$
$\mathrm{BP100}=74100$ BPIO4 $=74104$
$\mathrm{BP105}=74105$
BP107 $=74107$
$\mathrm{BP110}=74110$
BPI11 = 74111
BP118 $=74114$
BP119 = 74119
$\mathrm{BP121}=74121$
BP145 $=74141$
BP150 $=74150$
BP151 $=74151$
BP153 $=74153$
BPI54 $=74154$
BP155 $=74105$
BP156 $=74156$
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BP191 $=74191$
$B P 192=74193$
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$\mathrm{BP} 196=74196$ BP197 $=74197$ BP198 $=74198$
BP190 BP199 $=74199$ epiced to application (TTL 74 Series only)
$\begin{array}{ll}\text { BP197 }=74197 & \text { Presctable } 0 \text { 0MHz binary counter } \\ \text { BP198 }=74198 & \text { 8-bit parallel L-R Ehilt register } \\ \text { BP199 }=74109 & \text { 8-bit parallel access shift register }\end{array}$
 (15V outputs) decmer/arive (15V outputs) Expandable dual decoder/drive invert .. -input and-or Dual 2 -wide $\underline{0}$-input and-or-invert Quader 2 -input. expandable and-orinvert -wide 2 -input and-or-invert gates Single-phase J.K flip.flo Master slave J-K fivnop Dual Master slave J-K flipDual D type flip-flop Qusd lateh fated full adders
l6-bit read/urite memory 2 bit bloary full aiders Quad full adder Quail 2 -input exclusive NOR gates 8-bit shift registers
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Dual entry 4 -bit shift
4-1,it un-down ahift register -bit parallel in parallel out whift 8 -bit bistable latches Single J•K Hip-flop equivalent 9000 single
Single J.K flip flop equivalent, 900 Dual Master slave flip-flopa Hates master-slave tlip-flops Dual data lock-out flip frop Hex ret-reset tatehes Hex set-reset latehes. 24 -pin Monostable multivibrators BCD-to-decimal decoder/driver 6-bit data selector
-bit data selectors (with strole) ulal 4-line-to-l-line tata - to 2 . to 4.line deco Dual 2 - to 4 -line decoder 0 Sync. lecade counter ync. Nync. binary uperlow counter clock line) (apiow counter (single lock mp-do
syc. binary up dowe counte counter (tow Pre-setable 50 MHz slecade counter -bit parallel access shift register
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| BP 711-HA711 | TO-5 | 10 | Dual comparator | 58p | 800 | 450 |
| BP $741-72741$ | D.I.L. | 14 | High Gain OP Amp (P'rotected) | 78 p | 60D | 50p |
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# VOL. 7 <br> No. 12 <br> December 

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## TODAY'S ICs AND TOMORROW'S

THE profusion of integrated circuit devices on the retail market offers great opportunities for equipment designing and building. Yet the formidable lists of type numbers must be daunting to many a reader as he peruses those advertisements featuring i.c.s. Clearly there is a requirement for a guide that identifies the more common types, indicates function and provides basic application data. This need we have attempted to meet in the Linear and Logic IC Survey which is the subject of this month's special supplement.

It will be appreciated that this Survey is not, and indeed could not be, exhaustive. Only those types of integrated circuit known currently to be available via retailers are included. This in fact makes the Survey all the more valuable, since no reader is likely to be sent on a wild goose chase after some rare device that cannot be obtained, unless one has special connections within the industry.

$$
\star \quad \star
$$

Having (hopefully) clarified the current i.c. situation to some extent and produced two separate lists, one for linear, the other for logic devices, we learn that this orderly and simple segregation into just two clearly defined categories may not always be possible in the years to come. By strange chance, just as our Survey was completed news was released of what is claimed to be a revolutionary development in i.c. design and manufacture by a British firm. The Collector Diffusion Isolation (CDI) method now perfected by Ferranti permits both digital and linear circuits to be formed on the same monolithic chip; it combines the linear high performance and digital high speed capability of the bipolar method with the high circuit density capability offered by the MOS technique.

The developers of CDI have suggested that this new bipolar process will cause a general widening of MSI and LSI applications particularly in areas involving analogue-digital techniques; they have mentioned, specifically, desk calculators, fuel injection systems, washing machine controls, and model control as examples where CDI will have great impact.

It is good to hear of a UK firm making an outstanding contribution to the development of new i.c. techniques. Just how important and significant the CDI method actually is, time will tell. But there are already two American contenders in the field. Both Fairchild ard Raytheon have developed their own methods for increasing the component density on a single chip. Are we about to witness another price war? With mammoth production runs, technical considerations tend to play a minor role-the more economical process usually wins the day.
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SPACEWATCH ..... 1029
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## SPECIAL SUPPLEMENT

LINEAR AND LOGIC IC SURVEY

## Our January issue will be published on

 Friday, December 10.[^1]

The vast number of integrated circuits now on the market has lowered the cost to levels whereby they can be used for many applications Although the circuitry for the system described would be possible with discrete components, the complexity would be such as to deter many from contemplating a sophisticated system, whereas with integrated circuits, the modules are simple and easy to build.

This article deals with the coder and decoder sections only; a block diagram of a typical system is shown in Fig. la. These may be built and tested against each other (Fig. 1b) the interface or coupling units being simple stages allowing, for example, a transmitter with a -12 V supply to be used with the coder which requires $+5 \cdot 2$ to $5 \cdot 7 \mathrm{~V}$.

The system is described for six channels although it can easily be varied for three to nine channels. It is not economic to have less than three channels; for more than nine channels some compromise is necessary with regard to proportionality and the time rate of control.

Printed circuit boards are necessary for this project and care should be taken to make these of a high standard since one interconnection short can cause complete malfunction and may be hard to trace; time taken wiring up is amply repaid.

Alternative forms of coder are described and it is not advisable to attempt construction without considering the corresponding decoder. Positive logic (output high $=1$, low $=0$ ) is used throughout

PULSE CONTROL TECHNIQUES
The control system involves the production of a train of (positive going) pulses, each pulse corresponding to a channel and being independently and proportionally variable in length from about 0.5 to 4 ms .

As shown in Fig. 2a, the pulses occur sequentially but their position relevant to the start of the train is determined by the length of the other pulses and the interval time between pulses $-\frac{1}{3} \mathrm{~ms}$.

The pulse train is repeated at intervals $\tau$, of 25 to 50 ms or 20 to 40 times a second. This is the rate at which the pulse lengths are measured and changes detected. Allowing five measurements (but preferably ten) for the detection of a small change and servo initiation, this gives a system response time of about $\frac{1}{8}$ to $\frac{1}{4}$ second.

Due to the inertia of the motor and load, the servo has a response time which must be added to the above; a model aircraft flying at 60 m.p.h. would move 22 ft in $\frac{1}{4}$ second before, say, the rudder servo started operating.

PULSE LENGTH
The pulse train repetition time $\tau$, for $n$ channels is made up of $(n-1) \times \frac{1}{3} \mathrm{~ms}$ intervals, $n$ pulses of maximum length $\tau_{a} \mathrm{~ms}$ and a synchronising period $\tau_{2}$ used in the decoder to determine the start of the pulse train. This synchronising period $\tau_{2}$ must be $1 \frac{1}{2} \times \tau_{3}$ or preferably $3 \times \tau_{3}$.

A four-channel system with a maximum pulse length $\left(\tau_{3}\right)$ of say 4 ms would therefore give as a minimum $(4 \times 4)+\left(3 \times \frac{1}{3}\right)+\left(1 \frac{1}{2} \times 4\right)=23$ milliseconds.

The sharp-edged pulses used in the coder and decoder are "softened" to approximately sine wave shape, before actual transmission and reception (as shown in Fig. 3), the pulse interval period being then approximately 0.5 ms wide corresponding to a 2 kHz tone. This allows a factor of approximately two for the bandwidth performance of any existing receiver, it being equally convenient to use a tone modulator transmitter or to use the i.c.w. mode and to detect the 27 MHz carrier only in the receiver.

The maximum pulse length $\left(\tau_{3}\right)$ is determined as a reasonable relationship with regard to the interval time, being restricted to contain $\tau_{1}$ to less than 50 ms . If a slower system response time can be tolerated, as in the case of a scale model boat, the maximum pulse length could be increased to 10 ms or alternatively more channels could be used.



Fig. I. Block diagram of a typical radio control installation


Fig. 2. Variable pulse lengths for proportional control


Fig. 3. The coder pulses are "softened" to almost sine wave shape for transmission to avoid troubles with short propagation times of square wave pulses

If only one channel, e.g. rudder, was required for maximum sensitivity, the corresponding maximum pulse length could be increased provided that the other channels in use were restricted operationally to prevent all the pulses being at maximum length, hence encroaching into the sync period $-\ldots$.

## CODER VARIANTS

Three variants of coder are described: ceders 1 and 2A are similar in form providing alternative construction dependent upon the use of components that may be to hand. The cost of the timing capacitors becomes comparable with that of the "active" components. In these cases, the period $\tau_{1}$ is fixed by using a multivibrator.

The coder 2B simply uses a longer positive-going pulse to provide the synchronising period shown as $\tau_{\mathrm{n}}$ in Fig. 2b; the channels are no longer being completely independent, $\tau_{1}$ not being fixed. In this case the end of the synchronising pulse $\tau_{5}$ is used to initiate the next pulse train.

The particular advantage is that, with some of the channel controls at minimum, a shorter system response time is possible. The disadvantage is that. operating any control, the corresponding pulse length is changed, in turn changing the total cycle time, and hence changing the measurement rate of the other pulses and their corresponding output.

Considering the case of a six-channel coder with all controls in the mid-position a pulse length of 2 ms cycle time $=(6 \times 2)+\left(6 \times \frac{1}{2}\right)+6$ (synchro pulse) $=20 \mathrm{~ms}$. If one control is changed to maximum and its corresponding pulse length to 4 ms , then the cycle time becomes 22 ms .
The output signal derived from these pulses changes in the ratio $\frac{2}{20}: \frac{4}{22}$ not $2: 4$ and the other channels outputs change by $20: 22$.

The greater the number of channels used, the less marked the loss of complete channel independence, and the greater the gain with respect to cycle and system response time.

Table I: MAKERS LOGIC I.C. TYPE NUMBERS

| Description | Logic <br> type | Ferranti | Motorola | Mullard | SGS | Signetics | S.T.C.-I.T.T. |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Quad 2-input | TTL | ZN7400E | MC7400P | FJHI3I | 6900259 |  |  |
| NAND gate | DTL | ZN346E | MC846 |  |  |  |  |
| 8-input NAND | TTL | ZN7430E | MC7430P | FJHIOI |  | MIC9495D |  |
| gate |  |  |  |  | N7430A |  |  |
| Dual JK flip-flop | TTL | ZN7473E | MC7473P | FJJI21 |  | N7473A |  |
| Monostable | TTL | ZN7470E |  | FJIOI | 9601 | N7470A |  |
| Retriggerable | TTL |  |  |  | TII8 |  |  |
| Monostable | DTL |  |  | 693059 |  | MIC9305D |  |
| Dual 4-input | DTL | ZN330E | MC830P |  |  |  |  |
| NAND gate |  |  |  |  |  |  |  |
| with node |  |  |  |  |  |  |  |

## DECODER

The decoder consists of two parts, the sync separator using a retriggerable monostable, and a shift register formed from a chain of JK flip-flops.

The received positive-going pulses repeatedly trigger the special monostable which has a full back-off time of at least $1 \frac{1}{2} \times$ the maximum signal pulse length. It can change state therefore only during the sync period, this change of state being used to produce a special pulse which is applied to all the "clear" inputs of the JK flip-flops.

During the sync period the outputs of the flipflops are set to "0", awaiting the train of signal pulses which are applied to all the "clock" inputs. One flip-flop is used to store a "l" which is successively moved down the chain (shift register) in steps coincident with the signal pulses.

The final individual output pulses from each flipflop output is equal to the period of the signal pulse together with the following interval ( $\frac{1}{2} \mathrm{~ms}$ ). Either one more flip-flop than the number of channels is used (to store a "l") or the last output is gated out as shown, the gating being associated with the decoder or one servo unit as desired.


Fig. 4. The pulse lengths are determined and set by the potentiometers in the timing circuits. NAND gates are used to determine the timed sequence of pulses being fed to the master control gate GI3


Fig. 5. Triggering is dependent on coincidence of an incoming clock pulse from this multivibrator and trigger circuit

COMPONENTS . . .
CODER I (Figs. 4, 5, and 8)
Resistors


## Potentiometers

VRI to VR6 $2.5 \mathrm{k} \Omega$ for pre-selection of values, then replacement by fixed resistors (see text)

Capacitors
$\mathrm{Cl}, \mathrm{C} 3, \mathrm{C} 5, \mathrm{C} 7, \mathrm{C} 9, \mathrm{ClI} 10 \mu \mathrm{~F}$ tantalum or elect. 20 V (6 off)
$\mathrm{C} 2, \mathrm{C} 4, \mathrm{C} 6, \mathrm{C} 8, \mathrm{C} 102 \mu \mathrm{~F}$ polarised tantalum or elect. 25 V ( 5 off)


Fig. 6. The waveforms appearing at (a) TRI collector, (b) TR2 collector, (c) TR3 base, (d) TR3 collector


Fig. 7. The whole of "Coder I'' and clock pulse generator is mode up on fibreglass printed circuit board; the pattern here is reproduced full size
$\mathrm{Cl} 2, \mathrm{Cl} 3 \quad 1.5 \mu \mathrm{~F}$ elect.
Cl $4 \quad 0.01 \mu \mathrm{~F}$ disc ceramic
$\mathrm{Cl} 5 \quad 50 \mu \mathrm{~F}$ elect. 15 V
$\mathrm{C} 16, \mathrm{Cl} \quad 0.1 \mu \mathrm{~F}$ disc
ceramic (2 off)
Intergrated circuits
IC1, IC2, IC3 SN7400 quad 2-input NAND gate (3 off) IC4 SN7430 8-input NAND gate

Transistors
TRI, TR2 2N37II
npn silicon
TR3 2N3702 pnp silicon

Diode
DI OA8I or any 100 mA signal diode

Miscellaneous
Fibreglass printed circuit board (see Fig. 7)


## PATENTE PEWIEMO

## IWFLAMMABLE LIOUID LEVEL MONITOR

AN interesting new use of fibre loptics occurs in BP 1223769 which is in the name of Maria and Giuseppe Panerai. Their Patent Specification is predominantly concerned with gauging the level of liquids in tanks.

Reading between the lines their main interest is in sensing the level of highly inflammable liquids such as petroleum where it is obviously far from ideal to use any electrical system in contact with the liquid. Of course, simple mechanical systems tend to be unreliable, especially if they are gravity dependent

What these Italian inventors suggest is to arrange a sequence of prisms or equivalents in a vertical line, up from the base of the liquid tank, so that as the tank fills more and more prisms are submerged.

The prisms protrude from a removable pipe, each one backing onto a photosensitive cell. A lamp is fixed to the top of the tank so as to illuminate the interior. It is quite easy to see that, as the liquid level goes down, so more and more prisms will be exposed and illuminated and more and more photocells activated. The cells can be linked to any simple electronic circuitry outside the tank by wires with a consequent minimal risk of sparking and explosion.

- But none of this concerns fibre optics so far. It turns up almost as an afterthought in one of the


## BP 1223769


examples given in the specification where it is suggested that instead of putting the photocells directly behind the prisms, i.e. in the pipe which is submerged in the liquid, the photocells can be arranged elsewhere and the light from the prisms taken to them by light pipes or optic fibres.

## AUTOMATIC FIMGERPRINT IDENTITY

THE North American Rockwell Corporation have a new British Patent BP 1225083 which applies to the automation of fingerprint
straightforward "off-the-shelf" electronics to provide means for scanning the fingerprint area. The major scan pattern has a minor scan pattern repeatedly superimposed over it.
The major scan pattern is a pre-determined linear pattern, which scans through a succession of relatively small area portions, and the minor pattern scans over each of these portions in a polar mode. This minor scan produces signals indicative of the pattern at that portion and these are stored in a bank of storage elements. The states of selected signals in these

## BP 1225083


checking. Rockwell start their specification with some sobering tacts.

For instance, the FBJ in America has a fingerprint file of over 182 million fingerprint cards, each having ten prints on it. So, if checking a suspect's prints against the records is to be finished in his lifetime, sophisticated automation is obviously necessary. What's more, with lost or stolen credit cards being an escalating problem, it may well be necessary one day to identify a legitimate card-holder by his fingerprints at the time of purchase.

Rockwell remind us that recognising the minutiae of a fingerprint is basically a problem in pattern recognition, which is complicated by the obvious fact that the minutiae occur at arbitrary orientations.

Whereas others have tended to concentrate on new techniques such as holography (matching prints with known masks), Rockwell propose a system which uses
storage elements are then sensed.
In more detail, Rockwell suggest the use of a flying spot scanner to derive an electrical analogue signal indicative of the fingerprint pattern at each small portion. This analogue signal is converted into digital form for storage and there is constant circulation of the signal in the memory through each of the storage elements so as to help the recognition of minutiae regardless of angular orientation.
An automatic contrast control circuit adjusts the detection process as a function of the general overall fingerprint image so that characteristics like ridge endings will produce distinctive light and dark areas to be encountered by the flying spot scanner moving in the orbits suggested.
Rockwell give plenty of details on suitable scanning techniques giving a total of 300,000 individual locations scanned on a single print. For present purposes, their block diagram gives the general picture.


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ability in the field of sace communications. Yes, 8 separate wavebands, inct
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## By K. J. Matthews <br> M.Sc.

LUMINESCENCE is the general term which is applied to the production of light, either visible or infrared, by the direct conversion of some other form of energy. The general term is then subdivided to denote the particular energy conversion involved.

Therefore, the production of light from heat, an effect which has been known for some considerable time, is designated thermoluminescence. A more modern, but very popular effect, is cathodoluminescence. This is light from kinetic energy or particle bombardment and keeps millions of T.V. screens active.

## ELECTROLUMINESCENCE

Other forms include chemiluminescence, and bioluminescence, which is a division of chemiluminescence and occurs in some deep sea fish and in glowworms. Photoluminescence is a special case as it involves a light to light conversion, the emitted light differing from the stimulating radiation in frequency.

Finally, electroluminescence is the emission of light by direct conversion of electrical energy. The standard abbreviation for the effect is written EL. This process is not to be confused with the ordinary tungsten filament bulb, where there is an intermediate stage of heat making the process thermoluminescent.

A few EL devices are now avalable to the public. Unfortunately these are as yet only infra-red emitters, but visible light devices do exist and are certain to become available in the near future.

## USES

Uses for these devices are self evident. In most cases they can be substituted wherever small panel lamps are employed, and their higher efficiency will
make them suitable for use with battery powered equipment. Numerical displays have been fabricated and because conventional read-out tubes require high voltages, EL devices of this type are likely to become very popular.

At the present stage in the development of EL devices, their use for general lighting seems a little remote. They cannot compete with fluorescent lighting for efficiency and only give monochromatic light.

It is not the aim of this article to describe practical applications but to give some insight into the mechanism of EL and of trends in present day research.

## SEMICONDUCTOR BAND STRUCTURE

Some knowledge of semiconductor band theory is necessary before any understanding of electroluminescence is possible. Unfortunately this can be very complex and it is only possible to seratch the surface of the topic.

Fig. I depicts a semiconductor in energy band form. It consists of two bands, the conduction band and the valence band with an energy gap separating them. Electrons cannot exist with energies within this gap, and for this reason it is known as the forbidden gap or region.

For an electron to move from a valence band to a conduction band, it must "jump" the energy gap. A gradual transition is not allowed.

If the semiconductor has no energy, the conduction band will be void of electrons and the valence band full. In this state it is electrically inert-an insulator. However, a semiconductor always has some energy by virtue of its temperature, and this causes electrons to be propelled across the gap into the conduction band.


Fig. I. Simple energy band diagram of a semiconductor


Fig. 2. The excitation process


Fig. 3. Radiative recombination

To do this it is necessary that an electron receives an amount of energy at least equal to the energy represented by the width of the forbidden gap to enable it to make the jump. If it receives less it will not be able to do so. As we will see later, the size of the gap is constant for a particular semiconductor and is measured in terms of energy, in elec-tron-volts (eV).

## EXCITATION

When an electron receives sufficient energy to jump the gap to the conduction band, it leaves behind in the valence band a vacancy known as a "hole." See Fig. 2. It is convenient to think of these holes as positive electrons, but with reduced mobility.


Fig. 4. Wavelength to energy conversion chart
If energy is now injected into the semiconductor for a short time and then removed, the internal equilibrium of the semiconductor is upset. There will be too many electrons in the conduction band and the semiconductor is said to be in an excited state.

There are many ways in which equilibrium may be restored. Here are three of the more important processes:
(1) the slectrons may be ejected from the material entirely-similar to thermionic emission.
(2) the electrons may impart their energy to the crystal lattice of the semiconductor causing its atoms to vibrate. These vibrations are known as phonons.
(3) the electrons may fall back across the gap to occupy holes in the valence band, emitting as it does so, quanta of electromagnetic radiation, the frequency of which is determined by the width of the forbidden gap. This process, known as elec-tron-hole recombination, is illustrated in Fig. 3.

These three processes, and others unmentioned, all occur in any semiconducting material simultaneously competing with each other. It is obviously the latter radiative process which is of interest to

EL, and it is desired that this be the main process if good light emitting efficiencies are to be obtained.

## CRITERIA FOR ELECTROLUMINESCENT MATERIAL

In some semiconducting materials a radiative transition can only be achieved after a process of the phonon type has occurred. This is a characteristic of the material and cannot be altered. As this process is a two stage one, the energy gap of the semiconductor is known as indirect, or the material is called an indirect semiconductor.

In other materials, known consequently as direct semiconductors, the radiative transition occurs singly. Other processes still compete but these are minor compared to the serious drawback of an indirect energy gap. They can in any case often be minimised by reducing the temperature of operation of the semiconductor if super-efficiency is required.

Clearly then, the first criterion for an electroluminescent material is that it be direct. The second is the width of the energy gap, as this determines the frequency of the emitted light.

According to quantum physics, energy and frequency of light are related by the equation.

$$
\text { Energy }=\mathrm{h} \times \text { frequency }
$$

where $h$ is Planks constant $=6.62 \times 10^{-24}$

$$
\text { Since frequency }=\frac{\text { velocity }}{\text { wavelength }}
$$

the energy equation becomes

$$
\text { Energy }=\frac{h \times \text { velocity of light }}{\text { wavelength of light }}
$$

Substituting for $h$ and velocity of light and converting to convenient units yields

Wavelength in angstrom units $(\AA)=12,400$
Energy in electron volts ( eV )
The visible spectrum runs from $8,000 \AA$ (red) to $4,000 \AA$ (blue) which on conversion gives red as equivalent to 1.55 eV and blue to 3.0 eV .

To recapitulate, the semiconductor must have a direct gap, and this must lie within the range, 1.55 to 3.00 eV for visible light emission. If the gap is narrower than 1.55 eV , infra-red emission will be obtained. See Fig. 4.

## DEVICES

Before investigating the available EL materials consideration must be given to the method of its use in the production of a useful device.

As we have seen the trick lies in the method by which non-equilibrium conditions are set up in the semiconducting material. This can be done by any form of energy input, but for EL it must obviously be brought about by electrical energy. There are two known ways of doing this, and this causes a split in the research into EL devices as the resulting products are so unalike.

The older method is the electroluminescent panel in which excitation is brought about by a high electric field although the full theory is not understood.

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$4 / 350 \mathrm{~V}$ \& 14 p \& $500 / 25 \mathrm{~V}$ \& 20 p \& $50+50 / 350 \mathrm{~V}$ \& 35 p <br>
$8 / 450 \mathrm{~V}$ \& 14 p \& $1000 / 25 \mathrm{~V}$ \& 35 p \& $60+100 / 350 \mathrm{~V}$ \& 58 p

 

$16 / 450 \mathrm{~V}$ \& 15 p \& $1000 / 50 \mathrm{~V}$ \& 47 p \& $32+32 / 250 \mathrm{~V} .$. \& 18 p <br>
$32 / 450 \mathrm{~V}$ \& 20 p \& $8+8 / 450 \mathrm{~V}$ \& 18 p \& $32+32 / 450 \mathrm{~V}$. \& 33 p

 

$25 / 25 \mathrm{~V}$ \& \& 10 p \& $8+18 / 450 \mathrm{~V}$ \& 20 p \& $350+50 / 325 \mathrm{~V}$ \& $\mathbf{3 0 p}$ <br>
$50 / 50 \mathrm{~V}$ \& $\cdots$ \& 10 p \& $16+16 / 450 \mathrm{~V}$ \& 25 p \& $32+32+32 / 350 \mathrm{~V}$ \& 43 p

 

$50 / 5 / 25 \mathrm{~V} .$. \& 10 p \& $16+16 / 450 \mathrm{~V} 25 \mathrm{p}$ \& $32+32+32 / 350 \mathrm{~V} 43 \mathrm{p}$ <br>
$100 / 32 / 350 \mathrm{~V} 25 \mathrm{p}$ \& $100+50+50 / 350 \mathrm{~V} 48 \mathrm{p}$
\end{tabular} SUB-MIN. ELECTROLYTICS. 1, 2, 4, 5, 8, 16, 25, 30, 50, 100 . $200 \mathrm{mF} 15 \mathrm{~V} 10 \mathrm{p} ; 500,1000 \mathrm{mF} 12 \mathrm{~V} 18 \mathrm{p} ; 2000 \mathrm{mF} 25 \mathrm{~V} 85 \mathrm{p}$. CERAMIC, 1 pF to 0.01 mF , 4 p . Silver Mica 2 to $5000 \mathrm{pF}, 4 \mathrm{p}$. PAPER 350V-0 $14 \mathrm{p}, 0.513 \mathrm{p} ; 1 \mathrm{mF} 15 \mathrm{p}$; 2 mF 150 V 15 p . $500 \mathrm{~V}-0.001$ to $0.054 \mathrm{p} ; 0.15 \mathrm{p} ; 0.258 \mathrm{p} ; 0.4725 \mathrm{p}$. SILVER MICA. Close tolersnce 1 . $2 \cdot 2-500 \mathrm{pF} 8 \mathrm{p}$; $560-$ 2,200pF 10p;2,700-5,600pF 20p; $6,800 \mathrm{pF}-0 \cdot 01, \mathrm{mld} 30 \mathrm{p} \mathrm{each}$. drive $365 \mathrm{pF}+365 \mathrm{pF}$ with $25 \mathrm{pF}+25 \mathrm{pF}$, 50 p : Slow motion drive $365 \mathrm{pF}+365 \mathrm{pF}$ with $25 \mathrm{pF}+25 \mathrm{pF}, 50 \mathrm{p} ; 500 \mathrm{pF}$ slow SHORT WAVE SINGLE. $25 \mathrm{pF}, 50 \mathrm{pF}, 55 \mathrm{p}$

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This method has been known for some time but modern materials research has improved the EL panel vastly, making d.c. operation feasible where before only a.c. was possible.

## STRUCTURE OF PANEL

The basic construction of a panel is simple, see Fig. 5, although more complicated, and more efficient geometries have been produced. In a d.c. display, the backing electrode which is the cathode is made of aluminium.

The anode, through which the light must pass, is of glass treated in such a way as to make it conductive. This is accomplished by placing a very thin coating of tin oxide on its surface, by vacuum evaporation or by a chemical method, so that it remains transparent.


Fig. 5. Structure for a simple type of EL panel
The electroluminscent material is then mixed with a binding agent and spread very thinly between these electrodes, so that a high field is produced by a comparatively low voltage, typically 50 microns for 20 to 30 volt operation.

A newer method of stimulating EL and one which has been causing a great deal of interest, is based on the pn junction. The theory of this method of operation is better understood than the EL panel which still presents large areas of mystery.

If we suppose that a suitable material is available in both forms, i.e. $p$ - and $n$-type, then a pn junction may be formed to make a diode.
In any junction diode, recombination of electrons and holes occurs at the junction when the diode is conducting in the forward direction as this is basic to its operation. If the semiconductor favours radiative recombination then the result will be light emission from the junction, produced directly by the passage of electric current through the diode.
To enable the light to escape the device, special geometries have to be employed in the manufacture of a crystal lamp if the bulk of the material is not to re-absorb it. Basically this means making a large area junction and arranging the emitting surface so that total internal reflection does not occur, which is by no means an easy $\mathrm{j} \circ \mathrm{b}$ with the high refractive indices of most semiconductors. One type of arrangement is shown in Fig. 6.

## MATERIALS FOR ELECTROLUMINESCENCE

How many materials are useful or potentially useful semiconductors? Germanium and silicon certainly and gallium arsenide is by now fairly well
known, but the list doesn't stop there. Hundreds more are possible of which only a few as yet have been investigated. It would seem therefore that suitable EL materials would be commonplace but, unfortunately, this does not appear to be the case.
In the crystal lamp field if not in EL panels there is a desperate need for a good semiconductor. This shortage has led to the employment of one or two sophisticated tricks but the problem is by no means satisfactorily solved.
Consider the common semiconductors to see how they conform to the criterion laid down earlier. Germanium-indirect gap of 0.66 eV , siliconindirect gap of 1.08 eV , both clearly of little use being so far in the infra-red as to make them inconvenient even if they were direct.
Gallium arsenide however is clearly much more


Fig. 6. Hemispherical structure of crystal lamp
promising, having a direct gap of 1.5 eV , tantalisingly close to visible. The available devices, 56CAY, MGA 100 , are made from gallium arsenide.

## PERIODIC TABLE

Why is gallium arsenide a semiconductor? A glance at a section of the periodic table of the elements. Fig. 7, throws some light on this question and indicates where we may look for additional material.
We see that germanium and silicon lie in group IV as do tin and carbon. As diamond can be regarded as a semiconductor and so, under certain

An array of 35 gallium phosphide lamps within a package overall size $\frac{1}{4}$ in $x \frac{1}{4}$ in approximately. In this photograph, the lamps have been illuminated to form the initial letter of the maker's name, Ferranti



Plastic encapsulated gallium arsenide phosphide red light emitting diodes made by Hewlett-Packard. These solid state lamps are designed for panel mounting or for printed circuit board mounting
conditions, can tin, there seems to be something about group IV elements which have some connection with semiconducting properties.

If one element from group III is combined with one from group. V , this also produces a semiconductor. These are known as compound semiconductors or compounds and gallium arsenide falls into this class.

| III | III | II | Y | III |
| :---: | :---: | :---: | :---: | :---: |
|  | B | C | N | 0 |
|  | Al | Si | P | S |
| Zn | Ga | Ce | $\mathrm{As}_{\mathrm{s}}$ | Se |
| Cd | In | Sn | Sb | Te |
| Hg | Tl | Pb | Bi | Po |

Fig. 7. Section of the Periodic table of the elements
In a similar fashion II-VI compounds are possible. Thus many semiconducting materials are possible and the more well known ones are listed in Table I. with their relevant parameters.

Confining ourselves to direct gap materials restricts the field considerably. All of the group IV elements are excluded, as well as three of the III-V compounds, the potentially useful three moreover. This leaves gallium arsenide as the compound with the largest direct gap, but this as we have seen is not good enough for visible EL.

At first sight the II-VI compounds are much more promising, zinc and cadmium sulphides both seem ideal, and indeed they are employed in the EL panel form of device. These materials, however, resist all attempts to grow them in both $p$ and $n$-type forms and therefore crystal lamps cannot be constructed from them.

## DOPING

The position for visible light crystal lamps would appear then to be hopeless, but two tricks are possible which partially solve the problem. Firstly, an indirect gap material with a larger than necessary gap can be used. Doping agents can be added to them which will create energy levels in the forbidden gap, effectively reducing it in size and giving it the characteristics of a direct gap.

Gallium phosphide doping in this way with zinc and oxygen will give a red emitting diode and the highest efficiency that has recently been obtained is in the order of 7 per cent. Inefficient green luminescence may also be produced from gallium phosphide, of approximately 0.1 per cent but this is
stretching the material's capabilities. Gallium phosphide of the high quality needed cannot, as yet, be consistently produced.

## MIXED CRYSTALS

The second trick consists of the formation of a mixed crystal. Most of the III-V compounds are intermixable, that is, they can be grown together as a single, homogenous crystal. If in the growing of gallium arsenide for example, a proportion of phosphorus is added to the molten material, the resultant crystal contains a percentage of gallium phosphide.

This is a mixed crystal and should not be considered to be a mixture of gallium arsenide and gallium phosphide, but as a crystal of gallium arsenide phosphide, written $G a \mathrm{As}_{\mathrm{x}} \mathrm{P}_{1-x}$, the x indicating a whole range of compositions from pure gallium arsenide to pure gallium phosphide.

As gallium arsenide is direct and gallium phosphide is indirect, somewhere in the mixed crystal composition there must be a change over from the one type of de-excitation to the other, and it is possible to increase the energy gap for a considerable way before it becomes indirect.

Red emitting lamps from gallium arsenide phosphide are possible if the crystal is grown with the correct composition. These lamps are becoming quite common in professional areas and it must only be a mater of time before they are generally available.

This is the state of the art at present as regards the crystal lamp field. Several other mixed systems have been tried and are workable propositions but they still only produce red light diodes. Present research is searching, rather desperately, for an efficient green emitter.

| Table I: PROPERTIES OF THE <br> BETTER KNOWN SEMICONDUCTORS |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  |  |  | De-Excitation |

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| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 40362 | 68p | 2N2905A | 47p | 2 N 4292 | $15 p$ | BCl49 | 10p | BFX88 | 26p |
| 2N696 | 17 p | 2N2924 | 20p | AC107 | 46p | BC153 | 19p | BFY50 | $23 p$ |
| 2 N697 | 18 p | 2N2925 | 22p | AC126 | 20p | BC154 | 20p | BFY51 | 20p |
| 2N706 | 12p | 2N2926 | $11 p$ | ACI 27 | 20p | BC157 | 12p | BFY52 | 23p |
| 2N930 | 29p | $2 N 3053$ | 27p | ACl28 | 20p | BCI 58 | $11 p$ | BS $\times 20$ | 16p |
| 2N1131 | 29p | $2 N 3055$ | 60p | AC153K | 22p | BC159 | 12 p | C407 | 17p |
| 2Nil32 | 29p | 2 N 3702 | 13p | ACI76 | $16 p$ | BC167 | $11 p$ | MCI 40 | 25p |
| 2 N 1302 | 19p | 2 N 3703 | $13 p$ | ACY20 | 20p | BC168 | 10p | MPS6531 | 35p |
| $2 \mathrm{Ni303}$ | 19p | $2 N 3704$ | 13p | ACY22 | 16p | BC169 | $11 p$ | MPS6534 | 30p |
| 2 N 1304 | 26p | 2 N 3705 | $13 p$ | ADI40 | 63 p | BC177 | 14p | NKT211 | 25p |
| 2 N 1305 | 26p | 2 N 3706 | $13 p$ | ADI42 | 50 p | BC178 | $13 p$ | NKT212 | 25p |
| 2N1306 | 33p | 2N3707 | 13p | ADI49 | 58p | BC179 | 14p | NKT214 | 23p |
| $2 \mathrm{~N} \mid 307$ | 33p | 2N3708 | 10p | AD161 | 33p | BC182L | $11 p$ | NKT274 | 18p |
| 2N1308 | 36p | 2N3709 | $11 p$ | AD162 | $36 p$ | BC183L | 10 p | NKT403 | 69p |
| 2N1309 | 36p | 2N3710 | 13p | AFII 14 | 24p | BC184L | $11 p$ | NKT405 | 79p |
| 2 N 1613 | 23p | $2 N 3711$ | 13p | AFII 5 | 24p | $\mathrm{BC}_{2} 12 \mathrm{~L}$ | 16 p | 0 O 71 | 38p |
| 2N1711 | 26p | $2 N 3819$ | 23p | AFII 17 | 22p | BC213L | 16p | $\bigcirc \mathrm{OC81}$ | 25p |
| 2 N 1893 | 54p | 2 N 3904 | 35p | AFl 24 | $24 p$ | $\mathrm{BC}_{214}$ | $16 p$ | OC810 | 25p |
| 2N2147 | 95p | $2 N 3906$ | 35p | AFI 27 | 22p | BCY70 | 18p | ZTX300 | 14p |
| 2 N 2218 | 34p | 2 N 4058 | 13p | AFI 39 | 33p | BCY71 | 33 p | ZTX301 | 16p |
| 2N2218A | 44p | 2 N 4059 | 10p | AF239 | 36p | BCY72 | 15 p | ZTX302 | 22p |
| 2N2219 | 38p | $2 N 4060$ | $11 p$ | ASY26 | 27p | BFI 15 | 23p | Z $\times 1 \times 303$ | 22p |
| 2 N 2219 A | 53 p | 2 N 4061 | $11 p$ | ASY28 | 27p | BF167 | 18p | ZTX304 | 27p |
| 2 N 2270 | 62p | $2 N 4062$ | $12 p$ | BC107 | 12 p | BF173 | 19p | ZTX500 | 18 p |
| 2N2369A | 19p | 2 N 4124 | 18 p | BCl 08 | $11 p$ | BF194 | 14p | ZTX501 | $21 p$ |
| 2 N 2483 | 35p | 2 N 4126 | ${ }^{27} \mathrm{p}$ | BC109 | 12 p | BF195 | 15p | ZTX502 | 250 |
| 2N2484 | 42p | 2N4284 | 15p | $\mathrm{BCl}^{8} 25$ | 15p | BFX29 | $31 p$ | ZTX | $25 p$ |
| 2N2646 | $47 p$ | 2N4286 | 15p | BC126 | 22p | BFX84 | 25p | ZTX503 | 22p |
| 2N2904A | 42p | 2N4289 | 15p | BC147 | 10p | BFX85 | 52p | ZTX504 | 52p |

## RESISTORS

| Code | Power | Tolerance | Range | Values ovoilable |  | 10 to 99 below) | 100 up |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| C | 1/20W | 5\% | 82, $220 \mathrm{~K} \Omega$ | E12 | 9 | 8 | 7 |
| c | 1/8W | 5\% | $47 \Omega-470 \mathrm{~K} \Omega$ | E24 | 1 | 0.8 | 0.7 |
| C | 1/4W | 10\% | $4.7 n-10 \mathrm{M} \Omega$ | E12 | 1 | 0.8 | 0.7 |
| C | 1/2W | 5\% | $4.78-10 \mathrm{Ma}$ | E24 | 1.2 | 1 | 0.9 |
| C | IW | 10\% | $4.70-10 \mathrm{Ma}$ | E12 | 2.5 | 2 | 1.9 |
| MO | 1/2W | 2\% | 10a-1Ma | E24 | 4 | 3.5 | 3 |
| WW | IW | $10 \% \pm 1 / 20 n$ | $0 \cdot 22 \Omega-3.9 n$ | El2 | 7 | 7 | 6 |
| WW | 3 W | 5\% | $12 \Omega-10 \mathrm{~K} \Omega$ | E12 | 7 | 7 | 6 |
| WW | 7W | 5\% | $12 \Omega-10 \mathrm{~K} \Omega$ | El2 | 9 | 9 | 8 |

Codes: $C=$ carbon film, high stability, low noise. Prices are in pence each for quantities of the MO = metal oxide. Electrosil TR5, ultra low noise. same ohmic value and power rating. NOT WW = wire wound, Plessey.

Values:
E12 denotes series: $10,12,15,18,22,27,33,39,47$ E24 dis 82 and their decades.
$36,43,51,62,75,91$ and their decades $20,24,30$

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to $2 \cdot 2 \mathrm{M} \Omega, 42 \mathrm{p}$; Dual gang log, $4 \cdot 7 \mathrm{~K} \Omega$ to $2.2 \mathrm{M} \Omega$,
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The crystal systems $\ln _{x} \mathrm{Ga}_{1-x} \mathrm{P}$ and $\operatorname{In}_{\mathrm{x}} \mathrm{A} 11_{1-\mathrm{x}} \mathrm{P}$ are the most promising materials as yet investigated as they can provide direct energy gaps of the order of $2 \cdot 2 \mathrm{eV}$. These materials are exceptionally difficult to grow, however, and while $\operatorname{InP}-G a P$ can be grown to the right composition. InP-AlP has not yet been produced.

Even if these materials were available, they only give green, what do we do to obtain blue? At this stage we enter the realms of conjecture, the nitrogen III-V compounds, largly unknown, appear to hold out some promise although they have a different crystal structure to all of the other semiconductors mentioned here. Aluminium nitride and gallium nitride appear to be the favourites for investigation.

## FOR THE FUTURE

With their limitations therefore, both crystal lamps and EL panels are working propositions. The red crystal lamp is quite well established and will undoubtedly become a part of the vast range of common semiconducting devices, but the EL panel is held up by one snag-its short working lifetime. This is catused by impurities slowly diffusing into the luminescent material and is a major problem, which until it can be overcome, will limit its usefulness seriously.

The "flat" television screen seems to be a very long term project indeed. A panel's electrodes can be patterned so that any particular part of the screen may be addressed in a similar manner to a computer memory matrix, but the definition possible by this method is severely limited and in any case only one colour is possible.

When green and blue crystal lamps are available perhaps this will provide the answer, but the interconnection problems with such a vast number of individual elements is enough to make even the bravest engineer apprehensive.

However, these problems are not insurmountable since the Sony Corporation have shown, by presenting fall colour TV on a large screen consisting of a vast array of red, green and blue light bulbs. It remains therefore to obtain the devices and then await the consequences.

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## PQACTICAL

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!N this the second and final part, constructional details for the electronic ignition system are given together with installation instructions and testing procedures, for both positive and negative earth cars.

## CONSTRUCTION

The prototype system was housed in a $7 \frac{1}{4}$ in $\times$ $4 \frac{1}{2}$ in $\times 2$ in Eddystone die-cast box. All the components are mounted on the printed circuit board (Fig. 11) with the exception of the transformer, TR4, TR5 and the thyristor. These are bolted to the case which acts as a heatsink.

## PRINTED CIRCUIT BOARD

It is strongly recommended that turret tags be used to make connection to the printed circuit board and these should be inserted and soldered before any of the components are mounted. Fig. 12 shows the component locations.

## INVERTER TRANSFORMER

The inverter transformer TI is wound on a Mullard FX2243 pot core with a DT2206 bobbin and should preferably be impregnated when finished to keep out moisture which could cause leakage or damage to the windings of the transformer.
The first job is to wind on 400 turns of $34 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. enamelled wire in eight neat layers of fifty turns. with thin insulation between each layer. Take great care not to cross adjacent turns. This high voltage winding is now insulated from the others with about four layers of insulation tape.

Now wind on 12 turns of 20 s.w.g. bifilar (i.e. two wires wound together) in two layers, and lastly the three turns of 30 s.w.g. bifilar feedback winding. The wires should be colour coded with sleeves as shown in Fig. 13 and another layer of insulation tape wrapped around the bobbin. Try to ensure that the sleeves go as far as possible into the bobbin so. that when it is placed inside the ferrite cups the enamel insulation is not scratched by the core.
If the core cannot be impregnated then it is recommended that the faces are firmly joined with a thin layer of Araldite or the transformer may shriek objectionably. This does not happen of course if the transformer is impregnated.

The reliability of the whole system depends on the care with which the transformer is made.

## UNIT ASSEMBLY

The box should be drilled as shown in Fig. 14 and the holes deburred with larger drills, taking care to remove any roughness where the mica washers are to be placed for TR4, TR5 and the thyristor.

All the components can now be assembled in the box ready for the final wiring. Cadmium plated nuts and bolts should be used, with the exception of the bolt securing the thyristor, since steel ones will soon rust.

Interwiring details for the ignition system are given in Fig. 15.

The thyristor should be mounted with a nylon bolt to remove the possibility of any flash over at this point due to the high voltages. The transistors


By D. S. GIBBS and I. M. SHAW (Forrenti Ldd)


Fig. II. Printed circuit board for ignition system, shown full size


Fig. 12. Component layout and wiring for printed circuit board


Fig. 13. Winding details, for inverter transformer


Fig. 14. Drilling details for die-cast box


Fig. 16. (a) Mounting details for thyristor. (b) mounting details for inverter transistors


Plastic covers are fitted over the inverter transistors to prevent accidental short circuits

TR4 and TR5, and the thyristor, must be mounted on mica insulating washers as indicated in Fig. 16 with solder tags on the lower two transistor fixing screws for the collector connections. Plastic covers should be fixed to the transistors to prevent accidental short circuits when the unit is installed in the car.

The printed circuit board is mounted on spacers of about $\frac{1}{4}$ in in length. Use a spring washer between the transformer mounting bolt and the core so that the transformer can be secured firmly without danger of cracking the ferrite core.

The unit can now be wired with $14 / .0076$ wire in assorted colours for identification. Care must be taken to connect the transformer windings correctly or the inverter will not oscillate.

The wires to the six-way connector block are brought out via two grommets just below it.

## CHECKING THE UNIT

After checking the wiring carefully some simple checks should be performed before installation, if the constructor does not have facilities to fully test the operation of the unit.

If the construction has been carried out as described using only good quality components the only check necessary is that the inverter transformer windings have been correctly connected and that it delivers the correct output voltage.

To do this connect the car battery between (E) and ( $F$ ) on the connector block. The transformer should be heard to "sing" immediately and approximately 500 volts d.c. can be measured between tags 1 and 4 on the printed circuit board (positive on tag 1). If this is not correct then the phases of the transformer windings will have to be examined and corrected before installation.

The current taken from the battery is approximately 500 mA when the inverter is operating correctly.

## INSTALLATION

All connections from the car for both positive and negative earth are made to the connector block, but since there are some differences they will be explained separately.

First, a suitable place in the engine compartment should be found to mount the unit so that the wires can be cut to the correct lengths. In both systems the wire connecting the ignition coil to the contact breaker must be removed but kept, so that the car can be reconstructed to the conventional system if necessary at any time.

## POSITIVE EARTH CARS

The wire or wires connected to the SW or negative terminal of the ignition coil for positive earth systems, must be connected instead to the negative supply input to the unit (F) since this supply is via the ignition switch.

The two connections between the unit and the coil are made next; SW or negative to (A) and CB or positive to ( $B$ ). These must be the only connections to the coil terminals.

The contact breaker is connected to (C) and good earth from the car chassis or positive battery terminal to ( E ). There are a total of five wires to the unit for positive earth cars with contact (D) left disconnected.

## NEGATIVE EARTH CARS

The connections for negative earth cars are somewhat simpler since the wires to the positive terminal of the coil can be left connected and the positive supply terminal of the unit ( E ) connected to it.

The contact breaker is connected to (D) and the negative coil terminal to (B). Contact (F) is earthed making a total of four wires to the unit with contacts (A) and (C) left unconnected.

The astute reader will deduce that the first half cycle of the spark in the negative earth system is positive and not negative as is usually recommended. This can be corrected by reversing the connections to the coil. In some cases this may be inconvenient as the positive terminal is sometimes used as a junction point for leads from other equipment and the coil is supplied with more than one tag on the positive side to accommodate them. It is not really necessary to go to the trouble of reversing the initial spark polarity since the unit generates a dual polarity spark (Fig. 6a, Part One).

The unit can now be fitted to a convenient place in the engine compartment by drilling at least two suitable holes in the bottom of the box, and the car, and screwing the unit in place with self tapping screws.

## COLD START COILS

Some cars are fitted with a "cold start" soil, which is a low voltage coil with a series ballast resistor that is shorted out when starting, to give an increased spark voltage under cold starting conditions. The ballast resistor is usually attached to one of the terminals of the coil but it may take the form of a resistance cable between the ignition switch and the coil.

It does not actually make any significant difference to the spark produced by the unit whether the resistor is left in series with the coil or not, but the resistance cable should not be used to supply power to the unit as there will be a considerable loss of voltage along it. In this case it is best to remove the resistance cable and replace it with a length of wire. In all cases the connection between the coil and the starter solenoid must be removed.

## PRECAUTIONS

Because of the high open circuit voltage produced by this unit never attempt to remove the spark plug leads whilst the engine is running, since not only is there a danger of breaking down the insulation of the coil but the experimenter stands a good chance of getting a nasty shock.

In addition the thyristor can be exposed to transient voltages of up to $1,000 \mathrm{~V}$ under these conditions and may be destroyed.

## PERFORMANCE

In most capacitor discharge ignition systems the usual fault is for the thyristor to "latch on" with the short circuit current available from the inverter, especially when an iron cored transformer is used. This cannot occur in the system described here as explained in Part One.

The spark gap of the plugs may be increased with advantage for smoother idling, but not above 0.04 in or the insulation of the ignition coil will be unduly taxed, especially in cars with high compression engines $:>10: 1)$.

The unit will perform satisfactorily down to at least 8 volts thus giving a good spark at the plugs even under very cold starting conditions when the battery is at its lowest capacity.

The points and timing should be checked when installing the unit since from now on they can be left for many thousands of miles. Cars fitted with this unit can be expected to operate more economically since the car is kept in "peak tune" all the time. This is because the points do not wear and the timing and dwell angle do not change with time, except for the very slight wear on the heel of the contact breaker.

Four units as described in this article have been Huilt and tested in various types of British car under different driving conditions and it can be stated that "P.E. Scorpio" is without doubt an effective transistor ignition system achieving improved performance.

Brake horse power has been measured on a "rolling road" and has shown to be about 5 per cent higher with a subsequent improvement in the "liveliness" of the car. One particular feature is the more consistent performance of the car after tuning has occurred from thousands of miles of driving.


## LOGICAL RADIO CONTROL continued from page 985

## TIMING COMPONENTS

The interval timing resistors R1 to R5 have a maximum value of approximately 490 ohms due to the input threshold of the gates; lower values may be used with a corresponding change (increase) in the capacitors $\mathrm{C} 2, \mathrm{C} 4, \mathrm{C} 6, \mathrm{C} 8, \mathrm{C} 10$ but the CR product must remain the same.

The signal pulse timing potentiometers VRI to VR6 will provide a maximum and minimum value. The minimum value of approximately 200 ohms corresponding to a pulse length of approximately 0.5 ms and the point at which the following gate will just open. The maximum value is given as 1.2 kilohms. With $10, \mathrm{~F}$ capacitors for $\mathrm{C} 3, \mathrm{C} 5, \mathrm{C} 7$, C9, C11, a pulse of 2 ms will be given, while 800 ohms setting for the potentiometers will provide 6 ms pulses.

The recommended arrangement is $240 \Omega$ in series with a 50082 potentiometer to ground. The cycle time is derived from the trigger circuit (Fig. 5) in which TR1 and TR2 form a multivibrator, the period of which is approximately $1 \cdot 4 C R$. If C 12 is $1 \cdot 5 \mu \mathrm{~F}$ and R 6 and R 8 are both 15 kilohms, the pulse time will be approximately 18 ms (Fig. 6).

The positive going edges at the collector of TRI are slowed by the action of the timing capacitors, so the output is taken from TR2. Diode D1 and R10 form an isolation circuit so that C13 will not discharge through the trigger circuit. The output waveform is differentiated by C14 and R1I to give a positive pulse of time constant approxinately 0.3 ms (Fig. 6c). The trigger transistor TR3 switches off producing the sharp-edged negative going pulse (Fig. 6d) to the trigger output every 36 ms .

## LAYOUT

Fig. 7. shows the printed circuit layout full size; it is recommended that extra space along one side is provided for mounting purposes. All holes should be drilled clean and vertical from the copperside with an HSS or tungsten carbide rumber 58 drill, and a small clean tipped soldering iron used.

Fig. 8 shows component layout, the i.c.s being inserted and soldered in first, followed by the fixed resistors and capacitors. Using thin flexible covered wire the four final interconnections to IC7 should be made as shown in Fig. 9, together with the trigger connections, supply leads and six various coloured wire leads which go to the control panel for connection to VR1 to VR6.
Next month: Two more coders and a decoder


Fig. 9. Potentiometer connections to Coder I


THIS circuit is a revised version of the circuit published in "Ingenuity Unlimited," April 1971, now using integrated circuits.

When re-designing the circuit to use integrated circuits it was found that not only is the finished dice smaller and neater, but by using the low price i.c.s. now available, it is also cheaper to build.

## THEORY

The logic diagram of this dice is shown in Fig. 1.
The circuit is basically a divide-by-six counter driven via the control gate Gll by a $4 \cdot 8 \mathrm{kHz}$ clock pulse.

The count is stopped by the control gate when the player presses the play button, and the binary output of the counter is decoded by gates one to nine to light lamps LPI-LP7 set in the usual dice pattern.

The binary output is decoded into four lamp outputs, the first of which decides "even or odd." If the count is odd, the centre lamp, LP3, lights. Next, gate GI decides "NOT 1" which lights two diagonally opposite lamps LP6 and LP7-except during a " 1. "

A third gated output, gates G5, G6 and G7 decide 4. 5 , or 6 and lights the two remaining diagonally opposite lamps, LP4 and LPS on these counts. The last output, gates G8 and G9, decide " 6 " and lights the two remaining lamps on this count. A little thought will show that these combinations will automatically light the correct number of bulbs in the correct patter for each dice position.

Player intervention is prevented in two ways. Firstly, by extinguishing the display lights while the counter is operating, and secondly, by the high clock pulse speed adopted which is far higher than any human reflex.

## CIRCUIT CONSTRUCTION

The circuit is built on a piece of Veroboard ( $0 \cdot 1$ in matrix) as shown in Fig. 2. The board should be cut to size and some strips cut as shown in Fig. 3. before the integrated circuits are fitted.

It is recommended to leave the soldering of the i.c.s. until all the other components and straps have been fitted as it is almost impossible to remove them should they be soldered in the wrong position.

When soldering on $0 \cdot 1$ in matrix Veroboard great care should be taken as the solder easily bridges the closely spaced strips, and if the strips are overheated they could come adrift from the laminate causing intermittent faults.

## COMPONENTS . . .

## Resistors

R1. R2, R3, R4 $4.7 \mathrm{k} \Omega$ (4 off)
R5 $47 \Omega$
R6 $100 \Omega$
All $5 \%, \frac{1}{4} \mathrm{~W}$ carbon
Capacitors
C1, C2 $0.1 \mu \mathrm{~F} 15 \mathrm{~V}$ (2 off)
Semiconductors
TRI, TR2, TR3, TR4 2N3704 (4 off)
DI, D2 OA91 (2 off)
Integrated circuits
ICI, IC3, IC4 SN7400 (BPOO) Quad 2-input gates (3 off)
IC2 SN7492 (BP92) $\div 12$ counter
IC5 SN7410 (BPIO) triple 3-input gate

## Switches

SI Single pole on/off toggle
S2 Single pole push button

## Lamps

LPI-7. $6 \mathrm{~V} \quad 0.3 \mathrm{~N}$ l.e.s. type (7 off)

## Miscellaneous

Lampholders for lamps (7 off)
Vercocard, $2 \frac{1}{2}$ in $\times 2$ it ( $0 \cdot 1$ in matrix)
Battery $6 \mathrm{~V}(4 \times 1.5 \mathrm{~V}$ in series, type HP7)

## CASE

The dice case may be varied to suit individual requirements and materials available. The case in the suggested design is a standard M.E.M. plastics box for mounting a double mains socket, fitted with the appropriate blanking plate. This will make a neat, strong case, which will accommodate the circuit board and lamp display and also four penlight cells in a four-cell battery holder.

If due to anticipated prolonged use a larger battery or 6 V mains power unit is required, a larger case should be used which can be constructed from wood or Perspex.

The lamp display in the author's unit was wired for miniature 6 V l.e.s. lamps and lampholders held in position by using stout copper wire, which acts as both support and electrical common positive bus bar. If required the lampholders could be secured in their correct position by using Araldite.


Fig. I. Circuit diagram of the electronic dice with the logic gates "taken out" of the integrated circuit packs. Circuitry within the grey orea is mounted on the 0 - Iin matrix Veroboard


Fig. 2. Wiring and layout of the logic integrated circuits and lamp drivers

Fig. 3. View of the underside of the Veroboard showing the copper regions that must be removed

## DISPLAY PANEL

The holes for the display are covered by a piece of $\frac{1}{5}$ in translucent Perspex to hide the non-illuminated holes.

This Perspex should be chosen with care, as some colours tend to diffuse the light spots making reading of the display panel difficult.

The Perspex may be selected by holding a sample against a piece of cardboard with a small hole in it and viewing it against background illumination to check that a cleanedged spot can be seen. A blue or violet Perspex was found to be best.

## CHECKING

After assembling the dice, the connections should be rechecked before fitting the batteries since a single wrong strap could easily permanently damage an integrated circuit.

If desired the correct working of the dice can be verified in the following manner.

## CLOCK CHECK

The clock output can be checked by connecting an oscilloscope between holes 8 C and 8 A on the Veroboard where a $4 \cdot 8 \mathrm{kHZ}$ square wave should be observed. In the absence of an


Fig. 4 (a). The lamp driver circuit that amplifies the output from the 4 decoders to light the lamps; (b) Pictorial view of the transistor used in this circuit showing base connections


Fig. 5. Wiring of the SN7400N Quad 2-input nand gate to give a 4.8 kHz multivibrator for use as clock


Fig. 6. Use of half a SN7400N to provide test input signals to the counter

oscilloscope, a pair of high resistance headphones in series with an $0.1 \mu \mathrm{~F}$ capacitor may be used to detect the output from the clock which will be observed as a high pitch whistle.

## TESTING COUNTER AND DECODER

The counter and decoding gates may be checked by injecting test signals into the counter.
This is done by the use of a microswitch buffered by a bistable to remove switching transients. The bistable is constructed from half a SN7400N quad 2 -input gate i.c. connected as shown in Fig. 8. This circuit is worth building as a permanent unit as it is invaluable in all testing work on TTL and DTL circuits.

The output from the bistable should be connected to hole $7 P$ on the Veroboard with the wire to $13 T$ temporarily removed. On operating the microswitch the display should be found to advance one count for every two depressions of the microswitch. The reason why two pulses are required to step the display by one count is that the SN7492N is a divide-by-twelve counter, and in the present application the "A" stage output is not used. The reset line of the counter SN7492N is disabled by applying a permanent "O" on pin 7 of the reset gate, which is the $\mathrm{R}_{\text {。 }}$ gate in this package.

## SWITCHING TRANSIENTS

If any trouble is experienced due to an uneven count, noise on the supply line may be the cause, in which case this is rectified by connecting $0 \cdot 1 \mu \mathrm{~F}$ capacitors between the negative and positive supply lines as near to the i.c.s as possible. This should remove the sharp fronts of any switching transients transmitted on to the supply lines.
This expedient should not be required if the unit is powered by batteries but may be required if the unit incorporates a mains power supply.
As the i.c.s are already working at 0.75 volts over the manufacturers' recommended working voltage, on no account must the supply voltage be raised above six volts.
Providing this value is not exceeded, no damage to the i.c.s should occur.

## 



Lets face it - an immediate success, the HY40 is here to stay HY40 means Hybrid Power, power neatly locked away inside an Intregrated Circuit. Power the modern way, simply mount only five additional components on a printed circuit board (all of which are supplied with the HY40). Power not only for Hi-Fi, power for Groups, for public address, for industry, power for all.
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By merely combining two HY40s with a Stereo Preamplifier (2 $\times \mathrm{HY} 5$ ) and simple Power Supply (PSU45), premium quality stereo may be obtained for a very modest autlay
The free manual supplied with the HY40 gives clear, easy build anstructions for Power Supply, volume, bass, treble and balance controls, together with inputs for Ceramic and Magnetic Pick-ups, Tape, Tuner and Auxiliary functions.
Internally the HY40 is based on conventional and proven circuit techniques developed over recent years.


OUTPUT POWER British Rating 40 WATTS PEAK, 20 watts RMS continuous.
LOAD IMPEDANCE 4-16 ohms INPUT IMPEDANCE 22Kohms at 1 Khz .
INPUT SENSITIVITY 300 mV for maximum output
VOLTAGE GAIN 30 db at 1 KHz . FREQUENCY RESPONSE $5 \mathrm{~Hz}_{\mathbf{z}}$ $60 \mathrm{KHz}+1 \mathrm{db}$
TOTAL DISTORTION less than $1 \%$ (typical $0.1 \%$ ) at all output powers.
SUPPLY VOLTAGE +22.5 volts D.C
SUPPLY CURRENT 0.8 amps maximum.
PRICE: including comprehensive manual, P.C. Board and FIVE EXTRA COMPONENTS
MONO $£ 4-40$ STEREO $£ 880$ all post free.

A WORLDS FIRST TO JOIN THE WORLDS BEST

The HY5 is a unique and revolutionary concept in HighFidelity pre-amplifiers. Thanks to the latest techniques, all feedback and equalization networks are, for the first time, combined into an integrated pre-amplifier circuit

Simply by adding volume, treble, bass potentiometers and only three stabilizing capacitors, which are supplied, your HY5 is complete and ready for use.

The HY5 provides equal ization for almost every con. ceivable input. This years developments in equalization technique enables precise correction for both output voltage and frequency res ponse for any crystal or ceramic cartridge. Yet another feature of the HY5 is its inbuilt stabilization circuit, allowing it to be run off any unregulated power amplifier supply.

The HY5 contains a balance circuit which, when linked by a balance control to a second HY5, forms a complete stereo preamplifier.

Specifically and critically designed to meet exacting $\mathrm{Hi} \cdot \mathrm{Fi}$ standards, the HY5 combines extremely low noise with a high overload capability. When used in conjunction with the HY40 and PSU45 forms a completely integrated system.

INPUTS
Maqnetic Pick-up (within $\pm 1 \mathrm{db}$ RIAA curvel 2 mV .
Tape Replay (external components
to suit head). 4 mV
Microphone (flat) 10 mV .
Ceramic Pick-up (equalized and compensatable) $20-2000 \mathrm{mV}$ variable.
Tuner iflat) 250 mV
Auxiliary 1250 mV .
Auxiliary $2 \quad 2-20 \mathrm{mv}$
OUTPUTS
Main Pre-amp output 500 mV Direct tape output 120 mV
ACTIVE TONE CONTROLS
Treble +12 db .
Bass $\mp 12 \mathrm{db}$
INTERNAL STABILIZATION Enables the HY5 to share an unregulated supply with the Power Amplifier.
SUPPLYVOLTAGミ
15-25 volt.
SUPPLY CURRENT
5mA approx.
OVERLOAD CAPABILITY
better than 28 d o on most sensitive input infinite on turier and auxl.
OUTPUT NOISE VOLTAGE 0.5 mV .

PRICE
Mono $£ 3-60 \quad$ Stereo $£ 7-20$

POWER SUPPLY PSU45


The PSU45 is specifically designed to supply, simultaneously, your HY40 (in mono or stereo format) and one or two HY5s.

Spec.
PSU45 +22.5 volts, 2 amps simultaneously.

PRICE: £4.50 including Postage and Packing

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# INEARE LICGICI.E SURVEY <br> A look at most of the linear and logic integrated 

 circuits now readily available to experimenters, private constructors and designers. More detailed technical information and pin connection diagrams are usually to be found in manufacturers' literature, often available from i.c. suppliers. Devices shown as being in TO5 pąckages may also be in similar packages (e.g. TO99, TO100, etc.) but with a different number of lead-outs. PART 1 LINEAR I.CsTHE first linear integrated circuits to be produced were operational amplifiers designed for instrumentation and similar applications. Introduced by Fairchild in 1963 the "A702 gave a performance equal to or better than the available discrete component amplifiers at a similar cost. The first "high performance operational amplifier was the A709 which Fairchild introduced in 1965. It had more gain, a reduction of input offset current, and allowed a higher output level.

While all operational amplifiers have many characteristics in common, some conditions peculiar to individual amplifiers may be found For example, the A702 has a limited input voltage range of +1.5 V to -6.0 V indicating the possibility of "latch-up" if this is exceeded

The a A 709 output stage uses complementary emitter followers with no bias between the iwo bases. This produces a dead zone which can cause an excessive distortion of the output signal when only a small amount of overall negative feedback is used.

The first of the "second generation" amplifiers was the "A.741. This has built-in frequency compensation, short-circuit protection, no crossover distortion, an offset voltage null capability, low power consumption, no latch-up, and a large common-mode and differential voltage range.

At first sight this built-in frequency compensation appears to solve the problems of operating down to an overall gain of unity, without instability occurring and without using external components. However, it means that the frequency compensation is NOT optimum for overall gains greater than unity.

For a 40 dB gain the 741 has a bandwidth of 10 kHz compared with the 1 MHz bandwidth obtainable with the 709 and recommended compensation.


## FERRANTI

A.C. AMPLIFIERS

TYPE FUNCTION CASE
ZLA10 Wideband linear amplifier for video and i.f. stages

ZLAI5 Wideband linear amplifier, improved version of ZLA1O
D.C. AMPLIFIERS
D.C. AMPLIFIERS
ZLD2S/T/U Differential d.c. amplifiers
ZLDI2C/D Complementary output stages for ZLD2

ZLD7096 Operational amplifier
T05
$\begin{array}{ll}\text { ZLD709C } & \text { Operational amplifier } \\ \text { ZLD709CE } & \text { Operational amplifier }\end{array}$
T05

ZLD741C Operational amplifier
ZLD74ICE Operational amplifier

## OTHER CIRCUITS

ZN400E
ZN402E
ZN403E

Analogue switch
Gated operational amplifier
Servo amplifier

## A.C. AMPLIFIERS

| CASE | TYPE | TYPE | FUNGTION |
| :---: | :---: | :---: | :---: |
|  | Old Number | New Number |  |
| T05 | PA222 | GEL222FI | 800 mW audio amplifier. $22 \Omega$ load |
| T05 | PA230 | GEL230FI | Low level audio preampli fier. 72db gain |
|  | PA234 | GEL234FI | I W audio amplifier. $22 \Omega$ load |
|  | PA237 | GEL237FI | 2 W audio amplifier. $16 \Omega$ load |
| T05 | PA239 | GEL239FI | Stereo version of PA230 |
|  | PA245 | GEL246SI | 5 W audio amplifier. $16 \Omega$ load |
| T05 | PA263 | GEL263SI | 3.5 W audio amplifier |
| T05 | PA266 | GEL266FI | 1.6 W audio amplifier |
| DIL |  | OTHER | CIRCUITS |
|  | TYPE | TYPE | FUNCTION |
|  | PA264 | GEL264SI | Voltage regulator |
|  | PA265 | GEL265S | Voltage regulator |
|  | PA424 | GEL300FI | Zero voltage switch |
| DIL | PA436 | GEL301FI | Phase controller for SCR |
| DIL | PA494 | GEL304AI | Threshold detector |
| DIL | All the abo | devices have | a special package |

## MOTOROLA

## A.C. AMPLIFIERS

TYPE
MCI303L
Stereo pre-amplifier. 68 dB gain. 60dB separation
Stereo demodulator for f.m.
Stereo demodulator for f.m.
Complementary amplifier and preamplifier
Stereo demodulator for f.m. No audio mute
Low level video detector
I.F. amplifier with a.g.c.

Wideband amplifier circuits 16 dB gain
F.M./I.F. differential amplifier 40 dB gain at 10 MHz
Dual differential amplifiers designed for input stage of stereo power amplifiers
IW audio amplifier and preamp
Class B driver
Flexible differential amplifier, 32dB gain
Audio preamp 3dB noise, 80 dB gain
$4 . W$ audio amplifier and preamp
2W audio amplifier and preamp

MCI 304P
MCI305P
MCI306P
MCI307P
MCI330P MCI350P MC1445L

MFC6010
MFC8000
MFC8001
MFC8002
MFC8010
MFC8020
MFC8030
MFC8040
MFC9000
MFC9010

## FUNCTION

S Special package

* 9-lead small power transistor case

CASE

| CASE | TYPE |
| :--- | :--- |
|  | MCI430P |
| DIL | MCI433G |
| DIL | MCl435G |
| DIL | MC1435L |
| DIL | MCI435P |
|  | MC1437L |
| DIL | MCI439G |
| DIL | MCI709CP |
| DIL | MC1710CP |
|  | DIL |
|  | MC1712CP |
|  | MCI741CP |

TYPE

## D.C. AMPLIFIERS

| FUNCTION | CASE |
| :--- | ---: |
| Operational amplifier 69dB gain | DIL |
| Operational amplifier 89dB gain | TO5 |
| Dual operational amplifiers of 7IdB |  |
| gain | TO5 |
| Dual operational amplifiers | DIL |
| Dual operational amplifiers | DIL |
| Dual MCI709 | DIL |
| Internally compensated operational | T05 |
| ampififer of 84dB gain | DIL |
| Operational amplifier | DIL |
| Differential comparator | DIL |
| Operational amplifier | DIL |
| Fully compensated op. amplifier |  |

## OTHER CIRCUITS

TYPE FUNCTION CASE
MCI460G $\begin{gathered}\text { Voltage } \\ 250 \mathrm{~mA}\end{gathered}$ regulator 2.5 to 17 V T05
$\begin{array}{lllll}\text { MCl46IG } & \begin{array}{c}250 \mathrm{~mA} \\ \text { Voltage } \\ 250 \mathrm{~mA}\end{array} \\ \text { regulator }\end{array} 25$ to $32 \mathrm{~V} \quad \begin{array}{lll}\text { T05 }\end{array}$
MCI4.63G Voltage regulator 3.8 to 32 V 250 mA

T05
MCI466L Wide range voltage and current regulator

DIL
Voltage regulator 2.5 to 32 volts *
600 mA
$\begin{array}{ll}\text { Regulator } 4 V 200 \mathrm{~mA} & \mathrm{~S} \\ \text { Regulator } 4 \mathrm{~V} 200 \mathrm{~m} A & \mathrm{~S}\end{array}$
Electronic attenuator

## CASE

DIL $T 05$

T05
DIL
DIL
DIL
T05
DIL
DIL
DIL

MCI4.59R
MFC4060
MFC6030
MFC6040

# NATIONAL SEMICONDUCTOR \& SILICON GENERAL 

D.C
TYPE
LM709CN/SG709CN
LM710CN/SG710CN
LM71ICN/SG7IICN
LM74IC/SG74ICT
LM748C/SG748CT
D.C. AMPLIFIERS

FUNCTION
Operational amplifier
Differential comparator
Core memory sense amplifier
Fully compensated operational amplifier
As LM74IC without compensation

T05

OTHER CIRCUITS

TYPE
LMIO3*
LM305/SG305T
LM309/SG309T
LM309K/SG309K

## FUNCTION

Voltage regulator 1.8 V to 5.6 V available

Positive voltage regulator 4.5 to 30 V 20 mA

Voltage regulator 5 V 200 mA . No external components Voltage regulator 5 V IA. No external components

CASE
T046

[^2]
## MULLARD

|  | A.C. AMPLIFIERS |
| :---: | :---: |
| TYPE | FUNCTION |
| TAA263 | Three stage cascade amplifier |
| TAA293 | Medium frequency, general purpose amplifier |
| TAA300 | Audio amplifier. I watt into 80hms |
| TAA310 | Low noise audio preamplifier for tape recorders |
| TAA320 | Metal oxide silicon I.f. preamplifier |
| TAA350 | Wideband limiting f.m./l.f. amplifier |
| TAA570 | Limiter-amplifier with f.m detector |
| TAA960 | Three stage active filter amplifier. 39dB gain per stage |
| TAA970 | Microphone amplifier |
| TABIOI | Four transistor ring modulator demodulator |
| TADI00 | A.M. receiver circuit |
| TADIIO | A.M./F.M. i.f. amplifiers, three gain blocks |


| CASE | TYPE | FUNCTION | CASE |
| :---: | :---: | :---: | :---: |
| T072 | TAA241 | Operational amplifier (702C) | T05 |
|  | TAA242 | Operational amplifier (702) | T05 |
| T074. | TAA52I | Operational amplifier (7090) | T05 |
| T074 | TAABII | Operational amplifier (74.1C without compensation) | T05 |
| $\begin{aligned} & \text { T074 } \\ & \text { T018 } \end{aligned}$ | TBA22I | Fully compensated operational amplifier (74IC) | T05 |
| T074 | TBA222 | Fully compensated operational amplifier (74।) | T05 |
| T05 |  |  |  |
| T074 |  | OTHER CIRCUITS |  |
| T074 | TYPE | FUNCTION | CASE |
| T074 | TBA281 | Voltage regulator 2 to 37 V 150 mA | TO74 |
| DIL | TBA673 | Modulator/demodulator. Ring of four transistors | T074 |
| DIL | TBA750 | Limiter amplifier | 1601L |

## PLESSEY

## A.C. AMPLIFIERS


R.C.A.
A.C. AND D.C. AMPLIFIERS

TYPE
CA3000
CA3001
CA3002
CA3005
CA3007
CA3010
CA301I
CA3012
CA3013
CA3O14
CA30I5
CA3018

| fferential amplifier gain 28d BW 650 kHz <br> fferential amplifier gain 16 d BW 16 MHz <br> F./I.F. amplifier gain 19d BW 11 MHz <br> F./I.F. differential amplifier ga 16 dB BW 120 MHz <br> fferential audio amplifier ga 20dB BW 20k Hz <br> perational amplifier gain 57 BW 200 kHz <br> ideband amplifier gain 65dB B 20 MHz <br> ideband amplifier gain 65 dB B 20 MHz <br> ideband amplifier gain 65dB B 20 MHz <br> ideband amplifier gain 65 dB B 20 MHz <br> erational amplifier gain 66d BW 200 kHz |
| :---: |
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|  |  |
|  |  |
|  |  | $f_{T} 300 \mathrm{MHz}$

CASE TYPE
TO5 CA3020

T05 CA302I
T05 CA3022

## D.C. AMPLIFIERS

Operational amplifier (702C) T05
Operational amplifier (702) T05
T05
T05
T05
T05

CASE

6

SSB a.g.c. generator used with SL6/0/11/12 crophone/headphone amplifier Double balancer
Receiver mixer, double balanced modulator

## TEXAS

 amplifier circuits (702, etc.)SN76013N 4W audio amplifier similar to Sinclair ICI2

TYPE
CA3040
CA304I
CA3042
CA3043
CA3044
CA3045
CA3046
CA3047
CA3048
CA3049
CA3050
CA305I
CA3052
CA3053
CA3054

Wideband amplifier gain 34dB BW 40 MHz
Wideband amplifier/f.m. detector/ a.f. preamp

Wideband amplifier/f.m. detector/ a.f. preamp
I.F. amplifier/limiter/f.m. detector/ audio preamp
Wideband amplifier/phase detector, for a.f.c. systems
Five transistors $h_{\text {FE }} 110 f_{T} 300 \mathrm{MHz}$
Five transistors $h_{F E} 110 \mathrm{f}_{\mathrm{T}} 300 \mathrm{MHz}$
Operational amplifier gain 84JB BW 20 kHz
Audio amplifier 53dB gain BW 250 kHz
Six npn transistors $f_{T} 1 \cdot 3 \mathrm{GHz}$
Two darlington differential amplifiers $f_{T} 600 \mathrm{MHz}$
Two darlington differential amplifiers $f_{T} 600 \mathrm{MHz}$
Audio amplifier gain 53dB BW 300 kHz
R.F./I.F. differential amplifier gain 35dB BW 5 MHz
Differential amplifier gain 32dB

T05

CA3075
CA3078

TYPE
CA3055
Operational amplifier gain 86 dB (741C)

CASE
FUNCTION

CA3059 Zero voltage switch for thyristor control 105 mA
CA3060 Operational amplifier, characteristics can be varied by user
CA3062 Photo detector and power amplifier
CA3065 I.F. amplifier, limiter, f.m. detector, audio driver
CA3070 Television chroma system, chroma signal processor
Television chroma system, chroma amplifier
CA3072 Television chroma system, chroma demodulator
F.M./I.F. amplifier, limiter, detector, audio preamplifier BW 20 MHz

## S.G.S. A.C. AMPLIFIERS

TYPE
TAAGIIB
TAAGIIC
TAA62I
TAA62IA
TAA66IB
TBA23I
TBA27I
TBA3II
TBA58I
TBA59I
TBA64IA
TBA64IB
TBA65I
TBA63I
$\mu A 710 C$
${ }_{\mu}$ A702C
$\mu \mathrm{A} 709 \mathrm{C}$
$\mu A 74$ IC

FUNCTION
$2 W$ audio amplifier
3W audio amplifier
$3 W$ audio amplifier
4 W audio amplifier
F.M. I.F. amplifier and detector

Dual low noise operational amplifier gain 70dB
Voltage stabiliser for varicap diode
Television signal processing
I.F./F.M. amplifier, detector, preamp
I.F./F.M. amplifier, detector, preamp
2.5W audio amplifier

4 W audio amplifier
A.M. receiver circuit

Television sound circuit
D.C. AMPLIFIERS

High speed differential comparator General purpose operational amplifier High performance op.- amplifier Fully compensated op.amplifier

TAA141 Three stage audio amplifier $70 d B$ gain 20 kHz BW

## CASE

TAAI5I Three stage audio amplifier 70dB gain 600 kHz BW
TAA420 Low noise five stage amplifier 70dB gain 20 kHz BW

TOIOO
TAA435 TOIOO
TBAI20 F.M./I.F. amplifier and demodulator 60dB gain

TOIOO
TBA400
Broadband amplifier r.f./i.f. 75dB power gain at 35 MHz
A.M./F.M. i.f. I.f. amplifier

TYPE FUNCTION CASE
TAA521 Operational amplifier (709C) TO5
TAA522
Operational amplifier (709) T05
TAA861 Operational amplifier 70 mA output current 90dB gain

T078
S Special package

## APPLICATIONS OF LINEAR I.C.s

Both the 709 and 702 require several external components to frequency-compensate the amplifier so that it is stable under closed-loop conditions and cannot oscillate. The 702, 709 and the 710 comparator were the "first generation" amplifiers.

Although fully compensated, operational amplifiers can be used in any feedback circuit without instability occurring, and without external compensation components, bandwidth and slew rate are reduced. The $710,702,709,741$ are produced by many manufacturers. A "C" suffix (for example 702C) has a slightly reduced temperature range and specification over the version without the suffix.

## SERIES 741C OPERATIONAL AMPLIFIER

Although operational amplifiers are perhaps the most useful of the vast range of available linear integrated circuits, with varied applications from multivibrators to active filters, many circuits are designed for specific applications. A glance at this survey will reveal a small part of the range available to commercial and industrial organisations. Most of those listed are also readily available to the home constructor.


## UNITY GAIN VOLTAGE FOLLOWER

$\begin{array}{ll}\text { Input impedance } & 400 \mathrm{Ms} 2 \\ \text { Output impedance } & 1 \$ 2\end{array}$ Output impedance IS


## INVERTING AMPLIFIER

Gain RI R2 BW

| Gain | RI | R2 | BW |
| :---: | :---: | :---: | :---: |
| 1 | 10ks2 | 10 ks 2 | I MHz |
| 10 | 1k!2 | 10 ks 2 | 100kHz |
| 100 | 1ki2 | 100ks2 | l0kHz |
| 1000 | 100s2 | 100 ks | 1 kHz |
| $\mathrm{R}_{11}$ | 10 ks 2 |  |  |



NON-INVERTING AMPLIFIER
Gain RI R2 BW $\begin{array}{llll}10 & 1 \mathrm{k} \Omega & 9 \mathrm{k} \Omega & 100 \mathrm{kHz} \\ 100 & 100 \Omega & 9.9 \mathrm{k} \Omega & 10 \mathrm{kHz}\end{array}$ 1000 100s 99.9 ks I kHz $\mathrm{R}_{\mathrm{in}}>50 \mathrm{Ms} 2$

Logic integrated circuits have opened up a whole new science in systems design that does not necessitate deep technical knowledge of the circuit inside the package. Given a basic set of parameters, logic i.c.s can be built into large systems with little difficulty.

REPRESENTING almost unbelievable value for money in terms of components per penny (the 7400 TTL gate, for example, can be purchased for as little as $15 p$ and contains sixteen transistors, sixteen resistors and four diodes), the logic i.c. families are now available to the amateur from many suppliers who stock an incredibly wide range of types.
To those readers put off by such mystic terms as fan-out", "flip-flop" and "truth-table", take heart, almost anything that can be built with logic i.c.s can also be built (mentally at least) using relays. Logic gates are simply switches with an "on" and
'off" state, and all the more complicated devices, such as bistables, can be built with gates. Loaic really is easy to pick up and after a start has been made, the subtleties can be appreciated one by one.

For those who have always wanted to desian something electronic, but have been put off by simultaneous equations and slide rules, this is the scene for you; after a comprehension of the principles is obtained. all you need to start designing is common sense; all the maths has been done by the manufacturers

## LOGIC FAMILIES

The three main families are RTL, DTL, and TTL while MSI (medium scale integration) uses TTL methods and incorporates several TTL circuits in each package to give a complex function, for example, a counter.

The tables given here are far from exhaustive because of the several manufacturers' different type numbers, but they do represent the majority of devices available through retail outlets.

RTL is gradually losing favour because of the competitive lowering of prices of the DTL and TTL.

In the DTL range, although a common coding can be found among many manufacturers, it is also worth obtaining the leaflets and handbooks on other types. Mullard and Siemens operate Pro Electron codings








n but they are not always interchangeable equivalents in all cases. Ferranti use different codings again for their Micronor series, prefixed ZS , which is divided into a 20ns range, medium speed range (15ns), and a high speed range (9ns).

Readily available types in the DTL range are mostly given a coding which includes digits 930 and 9090 upwards, other letters and figures being added according to manufacturers' choice. The Mullard range uses Pro Electron numbering prefixed by FC.

The widest range is TTL which extends into medium scale integration MSI. Again because of space limitations we have listed the most common in the 74 series, various manufacturers adding their own personal identity prefix. It is worth pointing out here that although many of the Mullard and Siemens TTL types (prefix FJ) are similar to the 74 series, they are not all identical.

| FAIRCHILD | FERRANTI | ITT | MOTOROLA | MULLARD | NATIONAL | SIEMEN8 | TEXAS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 9002 | ZN7400E | 9002 | MC7400P <br> MC7401P <br> MC7402P | FJHI3I <br> FJH23I <br> FJH22I <br> FJH29I | DM8000N | FLHIOI | SN7400N |
|  | ZN7401E |  |  |  | DM8001N | FLH201 | SN7401N |
|  | ZN7402E |  |  |  | DM8002N | FLHI9I | SN7402N |
|  |  |  |  |  | DM8003N |  | SN7403N |
|  | ZN7404E | 9016 | MC7404P | FJH24I | DM8004N |  | SN7404N |
| 9003 | ZN7410E | 9003 | MC7410P | FJHI21 | DM8010N | FLHIII | SN7410N |
| 9004 | ZN7420E | 9004 | MC74.20P |  | DM8020N | FLH:21 | SN7420N |
|  | ZN7430E |  | MC7430P | FJHIOI | DM8030N | FLHI31 <br> FLHISI | SN7430N |
| 9009 | ZN7440E | 9009 | MC7440P | FJHI4, | DM8040N |  | SN7440N |
|  | ZN7441E |  |  | FJLIOI | DM8840N |  | SN744.1AN |
|  |  |  |  | FJH26I | DM8842N |  | SN7442N |
| 9005 | ZN7450E | 9005 | MC7450P | FJHI5I | DM8050N | FLHI5I | SN7450N |
|  | ZN7451E |  | MC745IP | FJHI6I | DM805IN | FLHI6I | SN74.51N |
|  | ZN7453E | 9008 | MC7453P | FJH171 | DM8053N | FLHI71 | SN7453N |
|  | ZN7454.E |  | MC7454P | FJHI8I <br> FJYIOI | DM8054.N | FLHI8I | SN7454N |
| 9006 | ZN7460E | 9006 | MC7460P <br> MC7472P <br> MC7473P <br> MC7474P <br> MC7475P <br> MC7476P |  | DM8060N DM8540N DM8501N DM8510N DM8550N DM8500N DM8530N DM8532N DM8533N | FLYIOI <br> FLJII <br> FLJI2I <br> FLJI4 <br> FLJI5I <br> FLJI3I <br> FLJI6I | SN7460N <br> SN7472N <br> SN7473N |
|  | ZN7472E |  |  | FJYIOI <br> FJJill <br> FJJI2I <br> FJJI3I <br> FJJ181 <br> FJJI91 <br> FJJ141 <br> FJJ25! <br> FJJ2II |  |  |  |
|  | ZN7473E |  |  |  |  |  |  |
|  | ZN7474E |  |  |  |  |  | SN7474N |
|  | ZN7475E |  |  |  |  |  | SN7475N |
|  | ZN7476E |  |  |  |  |  | SN7476N |
|  |  |  |  |  |  |  | SN7490N |
|  |  |  |  |  |  |  | SN7492N |
|  |  |  | MC7493P |  |  |  | SN7493N |

## RESISTOR TRANSISTOR LOGIC

General Characteristics
Gate speed 30 ns , power 20 mW
Supply - Vcc 3.6V $10^{\circ}{ }^{\circ}$
Frequency range 8 MHz
Noise immunity $0 \cdot 3 \mathrm{~V}$
These RTL circuits are suitable for many simple switching operations where relatively medium speed operation is acceptable, for example, as an electronic relay.

## FAIRCHILD

| $\mu \mathrm{L} 910$ | Single inverter/driver | T05 |
| :--- | :--- | :--- |
| $\mu \mathrm{L} 914$ | Dual 2-input gate, positive NOR, |  |
|  | negative NAND | T05 |
| $\mu \mathrm{L} 923$ | Single JK flip-flop with preset and clear | T05 |

MOTOROLA

MC71IG MC714G MC7I5P MC717P MC718P MC7I9P MC722P MC723P MC724P MC726P MC7269 MC778P

Q-input OR/NOR gate Dual 2-input NOR gate Dual 3-input NOR gate
MC8I7P Quad 2-input NOR gate Dual 3-input NOR gate
MC8I9P Dual 4 -input NOR gate JK flip-flop JK flip-flop
MC824P Quad 2-input NOR gate JK flip.flop JK flip-flop Dual D-type flip-flop

MC780P
MC785P
MC787P
MC788P
MC789P
MC790P
MC791P
MC792P
MC793P
MC799P

|  | Decade counter <br>  <br> Ouad 2-infut expander <br> I JK flip flop, I inverter, 2 buffers |
| :--- | :--- |
|  | Dual 3-input buffer, non-inverting |

Quad 2-input expander
I JK flip-flop, I inverter, 2 buffers
Dual 3 -input buffer, non-inverting
Hex inverter
Dual JK flip-flop
Bual JK flip-flop
Triple 3-input NOR gate
Dual buffer
Dual 4 -input NOR gate

Suffix G $=\mathbf{T 0 5}$
P_plastic DIL
MC700 Series +15 C to +55 C
MC800 Series 0 to 75 C

## DIODE TRANSISTOR LOGIC

General Characteristics
The following type characteristics apply to all manufacturers' DTL i.c.s using the coding given in the DTL tables.

Gate speed 25ns, power 5 mW
Supply Vcc $5 \mathrm{~V} \quad 5^{\prime \prime}$,
Frequency range 20 MHz
Input forward current - 10 mA
Input reverse current ImA
Noise immunity 0.7V at 25 C
Temperature range 0 C to 75 C
Case outline DIL
DTL is suitable for most medium speed applications and is compatible with TTL. Positive logic usually applies to DTL circuit descriptions.

MC830P Expandable dual 4 -input NAND gate MC83IP Clocked flip.flop
MC833P Dual 4-input expander
MC836P
MC84.6P
MC848P
MC84.9P
MC85IP
MC856P
MC862P
MC886P

## Hex inverter

Quad 2-input NAND gate
Clocked flip-flop
Quad 2-input NAND gate
Monostable multivibrator
Dual JK flip.flop
Dual 2-input NAND gate and inverter
Dual 4 -input expander

Temperature range $-\mathbf{5 5}^{\circ} \mathrm{C}$ to 125 C
Common Code Digits
930 Dual 4-input NAND gate (expandable) fan-out 8
932 Dual 4-input NAND driver gate (exp) fan-out 25
933 Dual 4 -input expander
935 Hex inverter (expandable)
936 Hex inverter fan-out 8
984 Dual 4 -input NAND with open collector, lamp/ relay driver fan-out 27
945 RS flip-flop with preset and clear (master/slave) fan-out 14
945 Quad 2-input NAND gate fan-out 8
948 RS flip-flop with preset and clear (master/slave) fan-out 13
951 Monostable multivibrator fan-out 10
962 Triple 3 -input NAND gate fan-out 8
9093 Dual JK flip-flops with preset inputs
9094 Dual JK flip-flops with preset inputs
9097 Dual JK with preset inputs and common clear
9099 Dual JK with preset inputs and common clear

CASE OUTLINES (view from above case)
A - T05 - RTL $\mu$ L900
B-T05—RTL $\mu \mathrm{L} 914$
C - T05 - RTL $\mu$ L923
D - Flat-pack - 14 leads
$E$ - Dual-in-line - 14 leads
$F$ - Dual-in-line - 16 leads
G - Dual-in-line - 24 leads


WHITE SPOT


RED SPOT


GREEN SPOT
(E)

s

(D)

## TRANSISTOR TRANSISTOR LOGIC

General Characteristics
The following type characteristics apply to all manufacturers' TTL i.c.s using the 74 series coding given in the TTL table.

Gate speed 10 ns , power 10 mW
Fan-out 10
Supply - I'ce5V 5".
Frequency range 10 MHz
Input forward current - 1.6 mA (logic 0)
Input reverse current ImA
Noise immunity IV
Temperature range ( 74 series) 0 C to $+70^{\circ} \mathrm{C}$
Temperature range ( 54 series) $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
Case outlines: mainly DIL (suffix N), flat pack, TO5 can. TTL is the most comprehensive range of logic i.c.s. It is used in high speed low power applications, where very short connecting lines (12in) can be used to avoid unwanted triggering from noise spikes. Positive logic applies to TTL circuit descriptions. TTL is compatible with DTL, ECL, MSI, using current sinking logic.

Suffix $H$ indicates high speed (6ns) series, 22 mW per gate, and fan-out of 8. Suffix $L$ indicates low power ( 1 mW ) series, low speed (33ns), high fan-out 40.

Suffix $N$ indicates a dual-in-line package although not shown in this list. Some suppliers omit the N in their lists so check before buying whether DIL or flat-pack is available: DIL is usually cheaper and easier to use. DIL package holders are recommended for easy insertion and removal of the i.c.

## TTL GATES, BUFFERS, DRIVERS

7400 Quad 2-input positive NAND
7401 As 7490 but with open collector outputs
7402 Quad 2-input positive NOR
7403 As 7401 but pin connections as in 7400
7404 Hex inverters
7405 Hex inverters, open collector outputs
7406 Hex inverters, open collector buffer/drivers
7407 Hex buffers/drivers, open collector outputs
7408 Quad 2-input positive AND gates
7409 As 7498 but with open collector outputs
74.10 Triple 3-input positive NAND gates

7413 Dual 4 -input Schmitt triggered positive NAND gates
7420 Dual 4 -input positive NAND gates
7430 Single 8 -input positive NAND gate
7440 Dual 4 -input positive NAND buffer
7450 Dual two-wide, 2 -input AND-OR-INVERT (expandable)
7451 As 7450 but not expandable
7453 Single four-wide, 2-input AND-OR-INVERT (expandable)
7454 As 7453 but not expandable
7460 Dual 4 -input expander for 7450 or 7453
7486 Quad 2 -input exclusive-OR gates
74.107 Dual JK master/slave flip-flops as 74.73, different connections
74118 Hex set/reset latch with common reset
74.119 Hex sel/reset latch with common and separate reset
74121 Gated or d.c. triggerable monostable multivibrator

## TTL <br> MEDIUM SCALE INTEGRATION

744।A BCD to decimal decoder/drivers
7442 BCD to decimal decoders
7445 BCD to decimal decoder drivers high power 30V output
7445 BCD to seven segment display decoder/drivers open collector outputs 30 V

7484 As 7481 but with gated write-amplifier inputs
7488 256-bit read-only memory
7490 Decade ripple counter with reset and preset to 9
7491A Eight-bit shift register (serial-in, serial-out)
7492 Divide-by-two and divide-by-six counter ( $\div$ 12) with reset
Four-bit binary counter with reset ( -16 )
Four-bit shift register, dual entry parallel-in, serial-out
Four-bit right-shift left-shift register
Five-bit shift register, parallel-in, parallel-out Eight-bit bistable latches
Improved version of 7441 to minimise switching transients
Four-line to ten-line decoder, open collector outputs
16-bit multiplexer with strobe input
8 -bit multiplexer with strobe input
Dual 4-bit multiplexer with strobe inputs
Four-line to sixteen-line decoder with strobe input Dual two-line to four-line decoder with strobe inputs
74156 As 74155 but with open collector outputs
74160 Synchronous decade counter with parallel inputs
74161 Synchronous binary counter with parallel inputs
74190 Synchronous up/down decade counter
74191 Synchronous up/down binary counter
74192 Synchronous up/down BCD counter
74193 Synchronous up/down 4-bit binary counter

APPLICATIONS

## OF

## LOGIC I.C.s

Fig. 1 shows a simple double puise detector using a couple of DTL devices, especially handy for detecfing index marks in position sensing equipment. If the input trigger pulses are derived from slowly
changing sources a couple of gates will be required ahead of the circuit shown to speed up the edges ready to fire the monostable (these are available anyway in the 946 range of DTL).

Fig. 2 shows a scheme for a solid state stepping switch which could be useful for example for channel selection in a radio or television receiver.

The operation is simple, the single push-button control is pushed an appropriate number of times until the required channel number is displayed on the readout. At this point the selected output of the 74145 is low, and can be used to drive a wide variety of external circuitry.

Many variations of this circuit are possible, and the whole thing can be made little larger than a cigarette packet. This example illustrates an interesting substitute for a wafer switch for some applications.

Fig. 1

## DOUBLE LINE PULSE DETECTOR

Gives an output pulse when a double pulse occurs. Monostable triggers on negative edge of input pulse. One application is to detect a double slot in rotating discs using a photo-electric cell

Fig. 2
SOLID STATE STEPPING SWITCH
This switch uses four TTL/MSI inte. grated circuits and provides seven seg. ment digital readout for ten position channel selection


## I.C. SUPPLIERS

The following advertisers in this magazine specialise in the supply of integrated circuits to readers:
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Having now had some experience in the operation of the simple radio telescope, the "feel" of the subject will no doubt have been acquired. After the experience of sorting out what is interference, what is random noise, and what is the real signal to be acquired, the limitations of the simple unit will be apparent. However, although this article will begin the description of interferometers and details of their construction, it is recommended that the observer should continue with the programme of the first project for two reasons.

The first reason is that it takes some time to become facile at recognition of the many different forms of interference, some of which will be manmade and some random signals due to odd reflections from the upper atmosphere. The second reason is that the data acquired and entered in the $\log$ will be of considerable importance when compared with the measurements made with the interferometer.

Of course, some constructors will not have the space to set up an interferometer and so must in any case continue with the single aerial unit. Bearing this in mind, some suggestions that may help these people will be given in the last article of this series.

## IDENTIFICATION OF SPACE SIGNALS

If the unit has been used during the night with the aerial pointed upwards, it is certain that there will be some signs of the radiation from the sources in Cygnus and Andromeda, and also the Crab Nebula. Also, the general rise in level of the recording at different times during each successive day and night will enable the positive identification of extra terrestrial radiation.

This was, in fact, the way in which the original pioneer Karl Guthe Jansky identified the existence of signals or radiation from space. To quote from his original paper, "In conclusion data have been presented which show the existence of electromagnetic waves in the Earth's atmosphere which apparently come from a direction that is fixed in

# ASTROMOMY tichiniouis 

By F. W. Hyde

paRT 7

An example of a complex group of sunspots. This was an extremely active area on the sun which lasted for several days. The small spot below the main group is about the size of the earth. The magnitude of the group is apparent from this comparison.
space. The data obtained give for the coordinates of this direction a right ascension of 18.00 hours and a declination of $-10^{\circ}$."

Bearing in mind that the width of the aerial beam is quite wide in the vertical direction, tip the aerial so that the dipoles are directed at the Milky Way. Let the system run for a week every night or every three nights if this is more convenient. After the run compare the recordings by laying them side by side with the time marks aligned so that the same time is shown. Examine them for some significant event and check the difference in time that has elapsed. There should be a change of some 15 minutes in three days and some 30 minutes in a week. The exact times can be checked from the charts and the figures given are for the purpose of guidance only.

If it is possible obtain a copy of the Ephemeris issued by the Nautical Almanack Office. This may be available from the library or it can be obtained from the Stationery Office Bookshop in Holborn, London.

Table 7.1 indicates some of the radio sources that should be detectable quite easily on the simple radio telescope. Since there are bound to be satellites recorded, it will be quite clear from the table which these are, because they can be compared with the tracings shown in Part 6 of this series.

## SIDEREAL TIME

The table gives the usual astronomical coordinates of hour angle and declination. This involves certain corrections on the part of the observer for the hour angle or right ascension. This hour angle is in Sidereal Time which differs from ordinary astronomical time by an acceleration of 10 seconds in each hour, or approximately four minutes per day. The time is reckoned from the entry of the first point of ARIES which occurs between the twenty-first and twentythird of March each year. This is the Vernal Equinox, 00.00 hours right ascension. All of this may appear a little complicated but it can be reduced to fairly simple terms for the purpose of the project.

In Table 7.2 a summary is given of the sidereal time for noon G.M.T. for each day of the year. This is approximate, but will not vary by more than about five minutes for each day and this error is insignificant with the system being used. In any case the source will be well within the beam of the aerial.

## SIMPLE INTERFEROMETER

The simple two-aerial interferometer was briefly described in Part 2 of this series, and the modification required to convert the existing simple telescope is really one of addition. Firstly the existing aerial must be duplicated, together with the preamplifier if this is located at the aerial. The two aerials should be set up preferably on an east to west baseline. Within certain limits this is not an absolute requirement, but this will be dealt with later in this article. The distance between the aerial centres should be, if possible, at least five wavelengths. At the frequency that has been adopted this will mean a distance of 36 feet.
It is necessary to connect the aerials together. This may be done at the observing point, or if this distance is greater or about the same length as the distance between the aerials, then the following alternative arrangement is possible.

Having set up the aerials, cut two lengths of cable which will leave sufficient slack for the aerials to be moved further apart and to facilitate stowage. These


Fig. 7.1. Coupling two aerials together as a simple in terferometer
two lengths of cable must be exact to within about one half inch, and of the same type of cable. These two points are very important. As both ends of the cables will terminate in coaxial plugs the equality of length includes the plugs. At the junction of the two aerial cables there will be a connection box as shown in Fig. 4.6 of Part 4. The outlet from this box will go to the converter in the same way as for the simple telescope, that is, with the matching section of 50 ohm cable followed by the 75 ohm cable. The diagram in Fig. 7.1 gives the layout.

The importance of the length of the cables has been stressed because the operation of the two aerial system depends on the combining of the signals which are alternately in phase and out of phase with the other.

TABLE 7.I. LIST OF RADIO SOURCES



Fig. 7.2a. The effects of differing aerial parameters. (1) Single aerial pattern. (2) Two aerial pattern. (3) Interferometer pattern (point source)

When using an interferometer it is usual to have the arials aligned and fixed in azimuth facing south in the northern hemisphere and north in the southern hemisphere. The altitude will depend on the section of sky to be observed, but each will normally be at the same angle of altitude. This will apply to each sweep of the sky. The sweeping will be done by the rotation of the earth so that in the course of 24 hours the whole 360 degrees of sky, as seen from the earth, will be scanned by the width of the aerial beam.

## RESOLUTION

The resolution or ability to distinguish between sources will be dependent on two parameters. One is the distance between the aerials, for this governs the width of the fringes, and the width of at fringe


Fig. 7.2b. (1) Point source pattern. (2) Uniform extended source comparable but smaller than lobe spacing. (3) Uniform extended source equal to or greater than lobe spacing
determines the size of the smallest source that can be recognised. The diagrams in Fig. 7.2a and $b$ illustrate the effects of these varying conditions.

It is essential that the beginner goes through this step of the simple interferometer in order to make practical observations and again have experience of the working conditions.

## FRINGE WIDTH

Before moving on to the more complicated phaseswitched interferometer there are one or two points which need to be covered. Firstly, the simple calculation to be made to determine the fringe width. The relationship between the fringes and the distance between the two aerials is in this form. The beam width between first null points (the fringe spacing) is $I / D \lambda$ radians which equals $57 \cdot 3 \% / D \lambda$.

TABLE 7.2

| DAY | JAN. | FEB. | MAR. | APR. | MAY | MONTH JUNE | JULY | AUG. | SEPT. | OCT. | NOV. | DEC. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 1840 | 2043 | 2233 | 0035 | 0233 | 0436 | 0634 | 0836 | 1038 | 1237 | 1439 | 1637 |
| 2 | 44 | 46 | 37 | 39 | 37 | 40 | 38 | 40 | 42 | 41 | 43 | 41 |
| 3 | 48 | 50 | 41 | 43 | 41 | 43 | 42 | 44 | 46 | 44 | 47 | 45 |
| 4 | 52 | 54 | 45 | 47 | 45 | 47 | 46 | 48 | 50 | 48 | 51 | 49 |
| 5 | 56 | 58 | 49 | 51 | 49 | 51 | 50 | 52 | 54 | 52 | 55 | 53 |
| 6 | 1900 | 2102 | 53 | 55 | 53 | 55 | 54 | 56 | 58 | 56 | 59 | 57 |
| 7 | 04 | 06 | 57 | 59 | 57 | 59 | 58 | 0900 | 1102 | 1300 | 1502 | 1701 |
| 8 | 08 | 10 | 2300 | 0103 | 0301 | 0503 | 0701 | 04 | 06 | 04 | 06 | 05 |
| 9 | 12 | 14 | 04 | 07 | 05 | 07 | 05 | 08 | 10 | 08 | 10 | 09 |
| 10 | 16 | 18 | 08 | 11 | 09 | 11 | 09 | 12 | 14 | 12 | 14 | 13 |
| 11 | 20 | 22 | 12 | 15 | 13 | 15 | 13 | 16 | 18 | 16 | 18 | 17 |
| 12 | 24 | 26 | 16 | 18 | 17 | 19 | 17 | 19 | 22 | 20 | 22 | 20 |
| 13 | 28 | 30 | 20 | 22 | 21 | 23 | 21 | 23 | 26 | 24 | 26 | 24 |
| 14 | 32 | 34 | 24 | 26 | 25 | 27 | 25 | 27 | 30 | 28 | 30 | 28 |
| 15 | 35 | 38 | 28 | 30 | 29 | 31 | 29 | 31 | 34 | 32 | 34 | 32 |
| 16 | 39 | 42 | 32 | 34 | 33 | 35 | 33 | 35 | 37 | 36 | 38 | 36 |
| 17 | 43 | 46 | 36 | 38 | 36 | 39 | 37 | 39 | 41 | 40 | 42 | 40 |
| 18 | 47 | 50 | 40 | 42 | 40 | 43 | 41 | 43 | 45 | 44 | 46 | 44 |
| 19 | 51 | 53 | 44 | 46 | 44 | 47 | 45 | 47 | 49 | 48 | 50 | 48 |
| 20 | 55 | 57 | 48 | 50 | 48 | 51 | 49 | 51 | 53 | 52 | 54 | 52 |
| 21 | 59 | 2201 | 52 | 54 | 52 | 54 | 53 | 55 | 57 | 55 | 58 | 56 |
| 22 | 2003 | 05 | 56 | 58 | 56 | 58 | 57 | 59 | 1201 | 59 | 1502 | 1800 |
| 23 | 07 | 09 | 2400 | 0202 | 0400 | 0602 | 0801 | 1003 | 05 | 1403 | 06 | 04 |
| 24 | 11 | 13 | 04 | 06 | 04 | 06 | 05 | 07 | 09 | 07 | 10 | 08 |
| 25 | 15 | 17 | 08 | 10 | 08 | 10 | 09 | 11 | 13 | 11 | 13 | 12 |
| 26 | 19 | 21 | 11 | 14 | 12 | 14 | 12 | 15 | 17 | 15 | 17 | 16 |
| 27 | 23 | 25 | 15 | 17 | 16 | 18 | 16 | 19 | 21 | 19 | 21 | 20 |
| 28 | 27 | 29 | 19 | 22 | 20 | 22 | 20 | 23 | 25 | 23 | 25 | 24 |
| 29 | 31 |  | 23 | 26 | 24 | 26 | 24 | 27 | 29 | 27 | 29 | 28 |
| 30 | 35 |  | 27 | 29 | 28 | 30 | 28 | 30 | 33 | 31 | 33 | 31 |
| 31 | 39 |  | 31 |  | 32 |  | 32 | 34 |  | 35 |  | 35 |

Using three examples of spacing, the first five wavelengths, the second 10 wavelengths, and a third 20 wavelengths, the approximate figures are 11.5 degrees, 5.73 degrees, and 2.86 degrees respectively. Since this is the point of the nulls the actual fringe width is half each value given at the half power points. The half power points were explained in Part 1 and illustrated in Fig. I.4.

## CHART SPEED

The second point to be noted is the appropriate speed of the chart on the pen recorder consistent with obtaining a useful trace.
Taking the case of the least spacing 11.5 degrees, it will take 46 minutes for passing from the one null to the next, since it takes about four minutes of time for the earth to move 1.0 degree of arc. Therefore in this instance it will be wise to choose a slow speed for the recorder in order that the fringes may be clearly visible. A preferred speed would be 0.5 inches per hour, though 1.0 inch per hour would
be permissible. For the widest spacing quoted a speed of 3.0 inches per hour would give best results. It will be clear from this that the greater spacing offers a better chance of detecting sources than the lesser spacing.

## THE BASE LINE

Finally the setting up of the base line must be considered-for those who have the space to accommodate the interferometer. It is not sufficient to set up the south point using a compass, though this is a first step. The south point required is yeographical south and this differs by several degrees. The exact amount of difference is obtainable for each year from ordnance survey maps. It was 8 degrees 40 minutes West in 1948, and decreasing by about 8 minutes annually.

Next month's article will be concerned with the phaseswitched interferometer.

## NEWS BRIEFS

## TRAFFIC SPEED METER

A completely new road traffic speed measurement system is being developed by GEC-Marconi Electronics, under contract to the Director of Telecommunications. Home Office.
The new system uses an optical method of measurement and the complete system can be contained in a single unit which can be placed at the side of the road. The unit looks at right angles to the traffic flow and as soon as a vehicle passes, and comes within the field of view of the optical system, its speed will be measured almost instantaneously, and shown on a three-digit display. probably using a liquid crystal system.

The image of the vehicle is split into a succession of vertical strips., which are viewed by a photodiode. Any irregularities in the image, whether bright spots or shadow, will move across the slits of the "virtual" grating and produce a fluctuation of the light falling on the diode. The frequency of this fluctuation is measured and, from it, solid-state micrologic circuitry calculates the speed of the vehicle.

## EUROPE'S FIRST FULLY AUTOMATIC NAVIIGATIONAL BUOY

|n almost every field nowadays man is rapidly being replaced by electronic equipment. This is the case at Portland Bill, Dorset, where a fully automatic navieational buoy has recently replaced the manned Shambles lightship.

The 84 ton buoy, made by Hawker Siddeley Dynamics and named Lanby (Large Automatic Navigational Buoy) has a $40 f \mathrm{ft}$ lattice mast topped with a main light beacon giving a luminous range of 16 miles. Also on board is a powerful fog signalling device. with an audible range of more than 3 miles.

The buoy is monitored every 30 minutes by a shore control station using a radio telemetry link. Should any failure occur. standby services operate automatically, and the nature of the fault is relayed to the control station.
The buoy can be moored in depths from 30 to 300 ft , and can withstand winds up to 100 mph , waves up to 40 ft and tidal currents up to seven knots.

## UNDERWATER TELEVISION EQUIPMEMT

THE present system of searching for and locating underwater wreckage, such as crashed aircraft and sunken ships by use of drag lines towed by surface craft seems soon to be superseded by a more efficient electronic method. This is evident following recent trials carried out by EMI Systems \& Weapons Division, Middlesex, with their newly developed "low light" television equipment.

The television camera can quickly scan large underwater areas where visibility is poor and transmit clear pictures of the sea bed to a mother ship on the surface where they can be viewed and recorded. The television equipment will be housed in a midget submarine capable of depths down to $3,000 \mathrm{ft}$.

The "low light" television camera used in the trial is claimed to be the smallest in Britain. measuring 128 $\times 128 \times 152 \mathrm{~mm}$. Its ability to obtain clear pictures in very low lighting conditions is provided by an E.M.I. Ebitron tube. This intensifier-vidicon is 300 times more sensitive than a normal 26 mm vidicon tube.

## COMPUTER NURSE

THE United Birmingham Hospitals have received approval from the Department of Health and Social Security to proceed with the purchase of a UNIVAC $418-111$ computer as part of the Department's experimental programme in hospital computing. Delivery will be made early in 1972 and specially designed accommodation to house it is nearing completion at the Queen Elizabeth Medical Centre. The first real-time application is expected to go live early in 1973.

The initial configuration will provide for complete patient administration and laboratory reporting systems for the Queen Elizabeth Hospital. It is planned to introduce other experimental systems such as laboratory requesting, drug prescribing, nursing records, physical examination of the patients and other medical applications at later stages in the project.

## NORTH MIDLANDS READERS Please note

A short course of six evening leccures entitled "Practical Electronics" will be held at North Staffordshire Polytechnic, College Road, Stoke-onTrent, commencing Wednesday, January 12. The lecturers include well-known contributors to this Magazine.
Further details will be given next month.

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## NEW from Goodmans for constructors

## Din 20 Kit

20 watt, high fidelity loudspeaker kit contains all parts necessary to complete the system, except timber and other material for the cabinet itself, with detailed, illustrated instructions. Specification: 20 Watts DIN, 4 ohms impedance, 8 ins bass unit, dome HF radiator, crossover frequency $4,000 \mathrm{~Hz}$.


## Axent 100



Dome HF Radiator with integral crossover. Capable of high frequency sound reproduction with negligible distortion in systems rated up to 30 Watts DIN, this 'state of the art' drive unit has an integral crossover which cuts frequencies below 3 kHz at a rate of $12 \mathrm{~dB} /$ Octave.

## Audiom 100

12 inch high fidelity bass loudspeaker.
For use as a bass unit in two-way systems, the sensitivity and high frequency roll-off of the Audiom 100 has been tailored to match the Axent 100.


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## SURPRISE SURPRISE!!

The decision by RCA to pull out of the computer mainframe business sent a chill down the spine of the United States' electronics industry-and of Europe too.

Everyone was taken by surprise. It happened almost exactly a year after RCA chairman Robert W. Sarnoff had announced firm plans for a frontal attack on the market held so lucratively by IBM. As recently as July, Sarnoff was still denying that RCA had any intention of pulling out.

The RCA debacle follows a similar withdrawal by U.S. General Electric a year earlier. GE's move, however, was well regulated and involved lengthy negotiations before the sale of its computer interests to Honeywell.

Industry experts on both sides of the Atlantic are asking themselves: if giants like U.S. General Electric and RCA can't stand the pace, who can? Except, of course, the almighty IBM.
Unstoppable IBM has 70 per cent the U.S. domestic market and not far short of that figure in the rest of the world. RCA's share was under 4 per cent with a growth target of 10 per cent of the market by 1975. The crunch came when it was realised that another $£ 200$ million of capital, on top of the $£ 200$ million already spent, would be required before breaking into profit.

Now the RCA market share will be distributed among the survivors with the bulk going to IBM, still further tightening IBM's stranglehold.

## THE AFTERMATH

Britain's ICL is involved only through System 4, a development of RCA's Spectra 70 Series. ICL say they will remain unaffected. But Siemens in Germany will be affected because they rely strongly on close technical links with RCA.

RCA customers have been told that all existing orders and service contracts will be honoured. But the demise of two major mainframe manufacturers in a period of 16 months will influence new buyers toward the most stable source which means even more business for IBM.

Component suppliers to RCA such as Motorola, Signetics and Advanced Memory Systems will all suffer from the closure. Some smaller suppliers stand to lose 10 per cent of their total business.

At the time of writing there was no firm news that RCA had been able to sell its computer interests either in whole or in part. In the present depressed state of the computer market few firms are interested in acquiring extra plant and manufacturing capacity.

One possibility emerging from the debacle is that Britain's ICL and Germany's Siemens could now get together to form a new and potentially extremely powerful computer force in Europe.

## DATA SATELLITE

The computer industry may be suffering at the moment but the demand for data processing and data transmission will continue to increase at a growing rate. In 1970 there were 14,000 data terminals in Britain. By 1980 there might be, and probably will be, 300,000 . This figure will be matched by other major European nations and so far no international agreements on data network standards have been made.

An interesting new development is a study contract awarded to Marconi Communications Systems Ltd. by the European Space Research Organisation (ESRO). The idea is that data links should be through a geo-stationary satellite used in conjunction with large numbers of small and inexpensive earth terminals.

The earth terminals would have dishes no larger than 9 ft in diameter sited within $100 y$ ds of the data terminal. Use of such a system, dedicated entirely to data transmissions, would obviate many of the difficulties of international connections through existing public telephone and data networks where data rates are comparatively low due to line variations.

The preliminary study, involving analysis of present and future traffic, prospective users and costs of the system, should be completed by April 1972

## WHERE THE GIANTS MAY GO

Although the great electronic exhibitions aimed at industry such as the IEA and ILECS at Olympia are by no means on the way out, increasing costs have been forcing companies to look more closely at the less expensive small shows which tend to be more costeffective.

One of the brightest operators in the field is John McNeill, managing director of Electromation Exhibitions Ltd. I have just seen the company's forward programme with electronics shows starting at Birmingham in November and moving round the country to Harrogate, Southend, Stevenage, Portsmouth, Croydon, Glasgow, Dublin, Bristol, Manchester and back to Birmingham in November 1972. This is an impressive programme which includes, apart from exhibitor stands at each venue, a technical convention. Admittance is reserved for professional engi-
neers and you need an invitation before you can get admission.

Another bright operator in small exhibitions is Evan Steadman who has already sold out for his Seminex due to be held next April. This one is for semiconductor manufacturers. Yet another is Gordon Johns who organised the successful Compec '71 computer peripheral exhibition in London last September.

It is too early to predict the effect of this type of exhibition on the IEA at Olympia next year but one thing seems certain. With Mullard, TI, Fairchild, RCA, Emihus, Ferranti, to name but a few who have already booked for Seminex and its accompanying technical symposium, it seems clear that the bulk of the semiconductor companies have already opted out of


## BRIGHTER OUTLOOKBUT SLOW

Sir John Clark, chairman of Plessey, thinks the worst may be over but it will be a long haul back to the halcyon days of prerecession. Ernie Harrison, Racal chairman, believes that it could be another 18 months before business takes off in any big way. Dr F. E. Jones, managing director of Mullard was not exactly cheerful when we last met despite the boom in colour TV sales.

On the whole, however, I have found most industry leaders optimistic. Smaller companies with their greater level of flexibility have in many cases proportionately weathered the storm better than the giants. But 1972 could see further "re-structuring" of the industry through takeover or merger. On one thing they all agree. Even if the bottom has now passed, don't expect a dramatic revival. Recovery will be slow.


Physical inspection of rubyliths (artwork) of a 1,024-bit CDI shift register


Layout of the car safety belt system as installed in a car. Note the narrow ultrasonic beam directed at the receiver mounted in the windscreen pillar


Part of the pre-production line for CDI

## New LSI Combines Logic and Linear Functions on one chip

How does the thought of a high performance computer on a single slice or even a chip strike youimpossible? Well it is not as far fetched as it may seem. While we are just getting accustomed to MOS integrated circuit technology. along comes anotherthe CDI process of producing LSI (large scale integration).
CDI (collector diffusion isolation) is a simple bipolar process originally developed in the Bell Laboratories in the U.S.A. and has now been further developed by Ferranti at Manchester for large scale

## ELECTRD

## Ultrasonic Car Safety Belt System

O
NE of the results of a new car safety belt system, developed by Mullard and the Ford Motor Co., means that even the car thief will have to strap hiniself in if he wishes to make off with his "booty". The belt must not only be fastened but also be correctly positioned across the wearer.

As the block diagram shows, before the ignition will function the driver must occupy his seat, activating a pressure switch fitted beneath it, buckle the belt across his lap and close the belt switch. Provided the belt is worn correctly a 40 kHz signal emitted from an ultrasonic transmitter, mounted in the belt, is received by a detector built in the windscreen pillar, which in turn completes the ignition circuit. If the front passenger seat is occupied then a 50 kHz signal must be received from the passenger`s


Block diagram of the car safety belt system

bipolar integrated circuits at Ferranti
production at an economic cost. This is said to be the first time that LSI has become a practical proposition. combining the high complexity of MOS with the performance advantages of bipolar technology.

The breakthrough is in the combination of both high speed switching logic and linear capability for a common supply of 5 V on the same monolithic chip. The system is completely compatible with existing TTL i.c.s.

Only five masking operations are required compared with nine steps involved in current bipolar processing. It is expected that this new bipolar LSI technology will be used where digital and analogue control. computers and telecommunications already require complex and sophisticated circuitry.

## NORAMA

safety belt before the ignition switch will be functional.

If the correct sequence has not been carried out a logic circuit will trigger an audible and visible alarm mounted on the dashboard.

The system can be arranged so that if the belt is unfastened while the car is moving the ignition is not immediately affected. Instead, the alarm is sounded and if, at the end of a specified time, the belt still remains unfastened, the ignition will then be cut out.

For very short operations, such as parking or garaging the car. the logic arrangement can be adjusted to allow for car movement in first or reverse gear for a specified time without the driver being belted.

As a method of enforcing the wearing of safety belts by driver and passengers, thus cutting down the death and injury toll on the roads, this contribution to road safety should be closely looked at by the Police and the Ministry of Transport.


The ultrasonic transmitter (top) and receiver circuit boards
Practical Electronics
December 1971
The ultrasonic transmitter mounted on a car safety belt



THE stabilised power supply to be described was designed and built to provide a power source for transistorised equipment undergoing development. As such, it had to fulfil certain basic requirements.

A long and varied experience of development and service work had shown the need for a power supply capable of being varied from near zero to a maximum of some 25 V , with a maximum current capacity, within this voltage range, of 1 A .

A further requirement was that the output voltage had to be regulated. The output current also had to be controlled, so that even if the load terminals were made short circuit, the maximum current rating could not be exceeded.

The final requirement was that the maximum current output should be capable of being adjusted


Fig. I. Block diagram of power supply
to values less than the 1 A maximum. In the prototype, two switched ranges of 1 A and 100 mA are provided.

A block diagram of the power supply is shown in Fig. 1 which should be studied in conjunction with the circuit diagram of Fig. 2.

## SERIES REGULATOR

The regulator element of this design is of the series type and is made up of the transistors TR5, TR6 and TR7.

This is a compound configuration where TR5 controls the base to emitter voltage of the output pair TR6 and TR7. The base feed resistor of TR5 also functions as the collector load of TR2. This transistor, together with TR1, constitute a differential amplifier.

## DIFFERENTIAL AMPLIFIER

The differential amplifier provides an output voltage which is proportional to the difference of the input voltages applied to the transistor bases. At the base of TR1 is connected a fixed Zener voltage which is derived from a double diode rectifier and filter circuit.

This stable reference at one base means that any changes in the output voltage-either up or down -will be fed back by way of VR2 to provide a collector current change through R5. If the current flow is low the volt drop at this resistor is low, which corresponds to a movement of the base potential of TR5 towards the collector potential. Increased emitter current therefore flows, and as this current is into the bases of TR6 and TR7, their emitter currents also increase.

If the output load is a resistive one, this will result in an increase of output voltage. A converse feedback action will decrease the output.
The overall gain of TR2, TR5 and the output pair is very high, and very small changes in output voltage can be sensed and automatically corrected. Rapid changes in output voltage, corresponding to a residual 100 Hz ripple, or the signal frequency of a connected high power amplifier, can also be sensed and corrected. In this instance, feedback is via C5, again to the base of TR2 which treats it also as an error signal.

## CURRENT LIMITING

The current limiter circuit comprises TR3 and TR4, in conjunction with VR3, R8 and VR4 and R10.

The transistor TR3 is connected so as to function as a cheap form of Zener diode which fixes the emitter potential of TR4. Under no load or small load conditions TR4 is reverse biased and does not pass any collector current.


Fig. 2. Circuit diagram of power supply

## SPECIFICATION...



## MAIN SUPPLY

Output Voltage Output Current Current Limiting Output Resistance
A.C. Ripple
A.C. Ripple

Output Variation

2 V to 24 V
1 A max
1 A and 100 mA
50 milliohms at IA
With no load is $250 \mu \mathrm{~V}$ r.m.s.

At $20 \mathrm{~V}, \mathrm{IA}$ is 5 mV r.m.s. $0.1 \%$ for $10 \%$ mains variation

## AUXILIARY SUPPLY

Output Current
30 mA max
IIV $\pm 5 \%$ at 30 mA


Fig. 4. Component layout and wiring of rectifler panel

values must be selected. The exact values used may require some adjustment, dependent on the gains of the transistors used in individual versions.

The action of the limiting circuit is not an abrupt one. The limiting process on the 1 A range begins at about 0.8 A and gradually increases. This will affect the output resistance above and below about 1 A .

## AUXILIARY SUPPLY

The transformer $T 2$ has two windings, one of which provides the reference voltage for the differential amplifier, the other provides a stabilised IIV

## COMPONENTS...

```
Resistors
    R1 
    R4 4.7k\Omega R10 12\Omega 2.5W wirewound
    R5 3.9k\Omega RII 299k\Omega 1% metal film (see text)
    R6 390\Omega Rl2* See text
    All 5%, \frac{1}{4}\mathrm{ watt carbon except where shown}
```

Capacitors
$\mathrm{Cl} 2,000 \mu \mathrm{~F}$ elect. $50 \mathrm{~V} \quad \mathrm{C} 4 \quad 1,000 \mu \mathrm{~F}$ elect. 50 V
C2 $100 \mu \mathrm{~F}$ elect. 50 V
C3 $1,000 \mu \mathrm{~F}$ elect. 50 V
$\mathrm{C} 5100 \mu \mathrm{~F}$ elect. 50 V
$2,000 \mu \mathrm{~F}$ elect. 50 V

## Transistors

TRI-TR4 8C109 (4 off)
TR5 2N3053
TR6-TR7 2N3055 (2 off)
Diodes
DI-D4 SX632 (4 off)
D5-8 OA202 (4 off)
D9 IZ3.3T5 IW 3.3V Zener
DIO IZIIT5 IW IIV Zener

## Potentiometers

VRI I $\mathrm{k} \Omega$ miniature horizontal preset
VR2 $2.5 \mathrm{k} \Omega$ multi-turn potentiometer (see text) (R.S. Components)

VR3-VR4 $100 \Omega$ miniature horizontal presets (2 off)

## Switches

SI Double pole mains on/off switch
S2 Double pole, double throw slide switch
S3 Double pole, double throw "centre-off"
toggle switch
Meter
MI $100 \mu$ A Type MR26 (R.S. Components)

## Transformers

TI Mains transformer: Primary 0-240V
Secondary 26 V at 1.6 A
Type X6705 (Belclere Ltd)
T2 Mains transformer:
Primary 0-240V
Secondaries $26-0-26 \mathrm{~V}, 23 \mathrm{~mA}$
and $11-0-11 \mathrm{~V}, 33 \mathrm{~mA}$
Type X676X (Belciere Ltd)
Miscellaneous
LPI Mains neon indicator, FSI 500 mA fuse with holder, $3 \frac{3}{4}$ in $\times 4 \frac{3}{4}$ in pegboard $0 \cdot 1$ in pitch, $2 \frac{1}{2}$ in $\times$ $3 \frac{1}{2}$ in pegboard, 0.15 in piteh, 20 s.w.g. aluminium sheet cut as required
Heat sink $4.875 \mathrm{in} \times 1.05 \mathrm{in}$ extruded aluminium 4 in long with eight pairs of fins, SKI-SK6 insulated sockets ( 6 off)
at 33 mA and can be used for powering any permanently attached piece of equipment, provided it does not draw current in excess of the rated figure.

## METER CIRCUIT

For monitoring purposes a fairly large $100 \mu \mathrm{~A}$ meter is used. The one on the prototype was arbitrarily scaled $0-3$ and $0-10$, which is convenient, since with the multiplier R11 and shunt R12 no scale marking is necessary for the ranges $0-30 \mathrm{~V}$ and $0-1 \mathrm{~A}$.

A two pole, two way, "centre-off" switch (S3) provides the measurement requirement. The centre position provides a standby facility which can be used when changing connections or reversing supply polarities.
The multiplier resistor R11, should ideally have a resistance of 299 kilohms and can be ordered with the meter. It is possible to use a 300 kilohm, 1 per cent type as the loss of accuracy is not severe.
The construction of the meter shunt will be dealt with later.

## OUTPUT TRANSISTORS

The more knowledgeable constructor will have noticed the use of two 2 N 3055 transistors for the series regulator TR6 and TR7, when one would suffice.

For maximum dissipation to occur in these transistors requires the output to be made short circuit and is approximately 45 W . Clearly, a good heatsink and ample ventilation are required, for even under normal load conditions the dissipation can be as much as 30 W .

Since the unit was intended to be used for long periods at fairly high dissipations, it was decided to use two output transistors for the reliability offered.

## CONSTRUCTION

The disposition of the components, whilst permitting the use of a relatively small cabinet, does result in a high packaging density.

Most of the semiconductors, capacitors and resistor are mounted on a $3 \frac{3}{3}$ in $\times 4 \frac{3}{3}$ in plain piece of $0 \cdot 1$ in pitch pegboard as in Fig. 3. Veroboard can be used if desired.

The rectifer panel of Fig. 4 is a $2 \frac{1}{2}$ in $\times 3 \frac{1}{2}$ in plain pegboard of $0 \cdot 15$ in pitch. This should be drilled for mounting to the frame of T1 (see photograph). When the rectifiers are mounted the panel should be attached to the transformer using 4B.A. nuts and bolts and $\frac{1}{4}$ in spacers.

## INTERWIRING

Interwiring details for the complete power supply are given in Fig. 5. The arrowed connections to the boards can be determined by referring to Figs. 3 and 4.

The front panel carries all the controls, the meter, fuse, indicator neon and output terminals (Fig. 5). On the prototype, the output voltage control, VR2 was a ten turn potentiometer, though somewhat expensive it is to be recommended since it enables precise setting to be achieved. Of course, an ordinary wirewound poter tiometer can be substituted.

Transformer $\Gamma 2$ is one of the clamp type of construction and is secured to the rear wall of the case. So too is a five way tag strip, to which the mains input is connected, and the heat sink for TR6 and TR7.


Fig. 5. Interwiring details of power supply


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Fig. 6. Case assembly and drilling details. Self tapping screws are used for all fixings


Transformer T1 and capacitors C1, C4 and C6 are mounted on the cabinet base. The main component board is attached vertically at its two lower corners, one corner by means of a small right angled bracket, the other by the clamp of C4.

## MAKING THE CASE

The cabinet used for the prototype was made from 20 s.w.g. aluminium, bent and cut as in Fig. 6. In assembling, the various panels are all secured together by small self tapping screws.

Ventilation must be adequate and is effected by means of a rectangular hole 3 in $\times 7$ in cut out of the top panel. This is covered by an aluminium plate 4 in $\times 8$ in separated from the top by $\frac{1}{4}$ in spacers.
To maintain air flow through this port $\frac{3}{8}$ in holes should be drilled below the heat sink and around T1.

## TESTING

When all of the components have been assembled and connected, a careful check for wiring errors should be made. If everything appears satisfactory set VR1 and VR2 for maximum resistance, and VR3 and VR4 to mid-range. S3 should be set for a voltage reading. A load is not required.

With the unit connected to the mains and switched on, the voltmeter should indicate. VR2 should be rotated anti-clockwise, when the voltage should fall, then rise again as VR2 is returned to its original fully clockwise position.

The preset VR1 should now be set for a maximum output voltage of 24 V .

## METER SHUNT

The meter shunt, R12, is made up of resistance wire with a measured value of 0.125 ohms. Since the formula for calculating this includes the meter resistance the meter specified, or one with a resistance of 1,250 ohms, must be used.

To actually construct the shunt, a length of resistance wire. somewhat longer than is required, is connected in the position of R12. With S3 set to "Volts" adjust the output of the unit for 10 V . Switch off and connect a 10 ohm, 10 W resistor, in series with a multimeter switched to 1 A d.c., across the output terminals.

If S3 is switched to "Amps" and the unit switched on, R12 can be adjusted so that 1A flows in the multimeter when meter M1 registers full scale deflection.
A high value resistor can be used as a mounting for the resistance wire, it being simply wrapped round the resistor body with the wire ends soldered to the resistor leads.

## CURRENT LIMIT SETTING

Having set the output voltage, attention can now be turned to the current limiting potentiometers VR3 and VR4.
With S2 switched to the IA range a multimeter switched to the IA d.c. range can be connected directly across the output terminals and VR3 adjusted for a reading of IA.

The time taken for this adjustment should be a minimum as the dissipation in the output transistors is high. Adjustment for the 100 mA range is identical with suitable multimeter range switching. Here, of course, VR4 is adjusted.

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A large number of enquiries are received about the communication frequencies used by the Apollo missions and other spacecraft.

The frequencies that are used on spacecraft depend on the matter to be transmitted and also on the propagation conditions. There are normally three bands in use. Thesc are the h.f., v.h.f. and u.h.f. bands. The latter includes the S-band which is of considerable importance since it is less susceptible to propagation anomalies and has perhaps the best noise level characteristic. Noise is at a minimum on this band.
Taking the Apollo spacecraft as an example, there are three units involved. These are the command module, the service module and the lunar module. Communication between ground and the spacecraft from the count down through launch and into earth orbit is maintained by v.h.f. on frequencies of 259 MHz and 296 MHz (exact values for Apollo 15 were 259.7 MHz and 296.8 MHz ). The command and lunar modules also operate on these frequencies. Within the spacecraft the u.h.f. frequencies are used as well as sound frequencies.

After leaving earth orbit and heading toward the target, communications change to the $S$-Band. These frequencies are $2273 \cdot 5 \mathrm{MHz}$ for the television channel and 2106.4 M Hz for speech and exchange of data between the spacecraft and earth. Another channel on 2287.5 MHz gives a link between the command module and the ground for speech, directly given data on the course position and other real time information..

The lunar module has two transceivers one of which is used for speech and data and operates on a frequency of 2101.8 MHz . The other channel uses a frequency of 2282.5 MHz and can be used for data, speech or television. In addition to these systems the craft operates another transceiver which uses a frequency of $10,006 \mathrm{M} \mathrm{Hz}$. The powers that are used are quite low being 2.8 watts and 11.2 watts on the S-band.

## PARACHUTE TESTING

NASA is to carry out free-flight tests on the Viking landers to test the parachute system to be used in the Viking spacecraft when they are launched in 1975. This is a new departure in technique for no parachute landing of spacecraft has been attempted before.

The Viking system is operated by a mortar which is fired automatically and deploys a 50 ft diameter parachute. During tests units will be dropped from $50,000 \mathrm{ft}$ and drogue parachutes will slow a ten foot container to the speed at which the mortar will fire.


## PROJECT EOLE

The new French Eole world wide system of weather watch reported previously ran into difficulties.

One hundred and forty-one balloons had been launched in the southern hemisphere and when the satellite was instructed to interrogate it sent instead a destruct signal to 72 balloons which were lost with all their sensors. This occurred on the 364th orbit.

The origin of the wrong signal was traced to one of the control centres at Bretignay. The French space organisation, CNES, consider that the programme will still be valuable since up to 500 balloons will be released during the 180 day period of observation.

## SEARCH FOR EXTRATERRESTRIAL LIFE

At the final session of a Conference on Communication there was a discussion for a programme to organise a search for intelligent beings in the space of the galaxy. The conference was attended by many countries and the delegates were from many disciplines: astrophysicists, theoretical astronomers, radio astronomers, sociologists. archacologists and anthropologists.

## LUNOKHOD

The Russian roving vehicle Lunokhod was shut down after its eleventh day. The last test period resulted in a movement of only 100 metres. It has been shut down because Russian scientists are unwilling to risk the possible misalignment of the on-board laser reflector.

## LAST LUNAR MISSION

The Apollo 17 is, at the moment, the last lunar mission of the series for manned landing on the Moon. The planning of its landing site is therefore of considerable importance. Last June the target decided upon was the area near the crater Alphonsus. It still is the prime candidate.

There are impertant reasons for the examination of this area. It was from here that the Russian astronomers noted the emmission of gases and changes in the spectrum which suggested vulcanism. Since then, this area has been the subject of close study by professionals and amateurs. Many instances of transient changes in the crater have been noted, some of them detected by spacecraft photographs.

However, after examination of the Apollo 15 data, recovered with much new information, it may be that another site would be of greatet scientific value. This sets a problem because the time for crew training is affected particularly as this is the last mission.

## SPACECRAFT COMPUTER

A low power spacecraft computer has been developed by Honeywell. It is suitable for a wide range of unmanned spacecraft missions and its power requirements are only 26.9 watts. It provides a one microsecond memory cycle time.

NASA's satellites for application technology will use two of these computers in parallel. The package weighs 231 b and is known as HDC 401.

## SATELLITE GANYMEDE

Ganymede is one of the four satellites originally discovered by Galileo to revolve round Jupiter. It has virtually no atmosphere and it was thought, till recently, that the surface would be like that of the Moon. Recent observations, however, have shown that the atmosphere is below one millibar and the surface may be rock powder or ammonia snow.

These observations were carried out at a wavelength of 25 micrometres at the University of Hawaii. Led by Dr. D. Morrison the team at the University and the Los Alamos Scientific Laboratory observed the satellite before, during and after the eclipse by Jupiter, revealing that except for the presence in the spectrum of features which suggest ammonia frost, the surface is indeed moonlike in character.

The presence in the spectrum of features which suggest ammonia frost still leaves some puzzling questions.

# Substitutes for ZENERS 

By J.N.WATT

THE regulation of fairly low voltages using Zener substitutes is fairly straightforward, if a little unconventional, as was illustrated last month. In this final part we shall look at the application of these substitutes in power supply circuits and give some examples of their applications.

## SHUNT REGULATOR

Fig. 9 shows a typical shunt regulator, where the extra transistor handles the additional power.

The Zener substitute and the power transistor's base-emitter voltage hold the output voltage at $V_{0}=V_{\mathrm{Z}}+V_{\mathrm{BE}}$.

The resistor R2 ensures that sufficient current flows through the Zener substitute to give stability.

The current through the shunt transistor is about $h_{\text {FE }}$ times that through the Zener substitute (ignoring the current in R2). It is this that gives the greater power handling capability, which is that of the shunt transistor. This latter could well be one of the unmarked $n p n$ devices mentioned earlier as being suitable for use as a Zener substitute.

Calculation of the value of R1 follows the same procedure as in the simple shunt regulator in Fig. 2.

In common with all shunt regulators there are two disadvantages: firstly, stability of output voltage is not as good as can be obtained by other methods, and secondly, the circuit is very inefficient, since full current is taken from the supply whatever the actual load current. With a shunt regulator, the total of load and shunt element currents is a constant.

There is one compensating advantage though-a shunt regulator is inherently short-circuit proof.

## SERIES REGULATOR

For most purposes, series regulators are used when simple low power Zener shunt regulators are not capable of handling the power level required.

Fig. 10 shows the circuit of a typical simple series regulator. The Zener substitute holds the base of the series transistor at a constant voltage and, by emitter follower action in TR1, the output voltage remains constant also, at $V_{0}=V_{\mathrm{Z}}-V_{\mathrm{BE}}$.
Sufficient base current must be supplied to the series transistor TR1 to permit it to pass the required load current. The maximum base current is $I_{\mathrm{B}}=I_{\mathrm{max}} / h_{\mathrm{FE}}$, which must be made available by the Zener substitute.


Fig. 9. Transistor shunt regulator



Fig. 10 (left). Simple series regulator
Fig. II (above). Series regulator using an "amplified Zener"
Fig. 12 (above right). Division of Zener voltage
Fig. 13 (right). Shunt regulator with Improved performance



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* TRZ is a silicon planar npn transistor, e.g. BSY95A,
BCIO7, BC108, 2 N 2926

Fig. 14. Stabiliser with variable output voltage and short circuit protection

It is as well, therefore, by suitable choice of R1 to arrange for the current through the latter to be, say, $50 \%$ greater than this value of $I_{\mathrm{B}}$.

With no load current, the current taken from the supply is only that through the Zener substitute. However, as it stands the arrangement is unprotected against short circuit of its output, an event which would be most likely to destroy the series transistor, unless protective measures are taken.

## SHUNT TO SERIES CONVERSION

To provide an output voltage greater than $V_{Z}$, the circuit in Fig. 6 can be adapted to series operation, as shown in Fig. 11.

$$
V_{0}=\frac{R_{1}+R_{2}}{R_{1}}\left(V_{\mathrm{Z}}+V_{\mathrm{BE}_{1}}\right)-V_{\mathrm{BE}_{2}}
$$

An output voltage less than the Zener voltage is easy to arrange by simple division of $V_{7}$.

$$
V_{0}=\frac{R_{1}}{R_{1}+R_{2}} V_{\mathrm{Z}}-V_{\mathrm{BE}}
$$

This is shown in Fig. 12.
With these last two circuits sufficient base current must be supplied to permit the series transistor to pass the required full load current.

Thus in Fig. 12, R1 and R2 should be chosen to allow a standing current of, say, five times $I_{B}$ to flow through them. This current can be looked upon as the load of the simple circuit comprising the Zener substitute and its series resistor R3.

With R1 and R2 so chosen, R3 can be selected as before.

## VOLTAGE AMPLIFER

Regulation of the output voltage can be improved by incorporating a further transistor, used as a voltage amplifier, shown in Fig. 13.

If $V_{0}$ tries to rise in level, then so does the voltage at the base of TR2, since the Zener substitute has a constant voltage developed across it. Transistor TR2 amplifies this change, with reversal of sign, so that a larger fall in voltage appears at TR1 base. Emitter follower action in TR1 thus tends to reduce $V_{0}$, so correcting the original rise. TR1 can be a member of the OC28 to OC36 family, while TR2 can be almost any of the popular silicon $n p n$ devices so readily available.

Besides the better regulation given by the gain of TR2, it is noteworthy that the Zener substitute is driven by the regulated output voltage, $V_{0}$. Since this is constant, better stability of Zener voltage is obtained. In those circuits where the Zener is fed from the unregulated supply, some change of Zener, voltage can occur with variation in that supply, due to change of current through the Zener.

An alternative method of obtaining better Zener voltage stability will be mentioned later.

## STABILISED POWER SUPPLY

Having thus briefly described many of the circuits in which Zener substitutes can be employed, we can now turn to the design of a stabilised power supply, using a circuit configuration not previously mentioned, see Fig. 14. It has the following advantages:

1. Being a series regulated supply, it can efficiently handle moderately large currents-up to 1 A or even greater, depending on the transistor and heat sink employed.
2. It gives a variable stabilised output.
3. Despite the series regulator configuration, short circuit protection is provided.
4. Constant current output, in place of constant voltage output, is possible, the level of such constant current being easily adjustable.

## SERIES RESISTANCE FOR ZENER

Initially, consider the "black box" (between $V_{i}$ and TRZ) feeding the Zener substitute to consist of a resistor of an appropriate value, say 330 ohms. Alternative components for the "black box" will be considered later.

Let us assume that an output current of 1 A is required. Then the base current of TR1 will be $I_{\mathrm{B}_{1}}=1 / h_{\mathrm{FE}_{1}}$. Since this current is provided by TR2, its base current will be $I_{\mathrm{B}_{2}}=1 /\left(h_{\mathrm{FE}_{1}} h_{\mathrm{FE}_{2}}\right)$.

Assuming $h_{\mathrm{FE}_{1}}=25$ (OC28 family) and $h_{\mathrm{FE}_{2}}=100$ ( BC 108 ), then the base current of TR2 is $400 \mu \mathrm{~A}$.

We should allow, say, 10 times this current to flow through VR1 as a standing current, i.e. 4 mA . If $V_{Z}$ is made 8 volts (a reasonable value for a Zener substitute), then the value of VR1 is equal $V_{Z} / 4=2 \mathrm{k} \Omega$.

A $2 \cdot 5 \mathrm{k} \Omega$ potentiometer will be suitable; it does of course give us the means of providing a variable stabilised output.

Capacitor C 1 ensures that the Zener voltage is held smoothly, despite any rapid, large changes in supply voltage ( $V_{i}$ ) which could occur if the mains were used


Fig. 15. Constant current drive for Zener substitute
as a primary supply, while $C 2$ reduces any similar changes at the output due to rapid variations in load current. Resistor Rl is necessary to turn on TRI, thereby providing control, by drawing a small load current when no, or only a very small, load current would otherwise be taken. Without R1, $V_{0}$ could rise above the stabilised voltage, due to leakage in TRI.

## SHORT CIRCUIT PROTECTION

Turning now to D1 and VR2, it is these components that provide the short circuit protection and constant current output facility.

In normal running, D1 is forward biased. Should a short circuit be applied to the output of the power supply, it will no longer be so biased, since its anode will then be at negative rail potential (via the short circuit). This results in TR1 passing a current determined by its then existing base current; this will be set largely by the value of VR2, since the lower end of VR2 is now connected directly to the collector of TR1 (via the short circuit).

Thus by choice of an appropriate setting of VR2, limiting of the current to any desired value is obtained.

The constant current mode is brought into play by ensuring that the output voltage setting is such as to attempt to deliver more current than the setting of VR2 allows. Output current is then constant despite supply voltage changes and load changes, provided of course the necessary output voltage can in fact be reached by the supply.

The short circuit protection and constant current output facility are possible in this way because of the method of connection of TR1. It is not, as in previous examples of series stabilised supplies, connected so that emitter follower action takes place. With the output


Fig. 16. Use of npn transistors in place of the silicon diodes shown in Fig. 15
taken from the collector as shown, the output impedance is still low, however, because of the heavy negative feedback present.

## CONSTANT SERIES CURRENT FOR ZENER

There is, of course, a disadvantage in this method of connection.

It has been noted above, in connection with the circuit in Fig. 13, that better output voltage stability is obtained if the Zener substitute is driven from the stabilised supply, $V_{0}$. Because of the circuit arrangement, in Fig. 14, of the series transistor being used with its collector as the output, it is not possible to drive the


Fig. 17a. Graph of output voltage versus output current for the circuit in Fig. 15


Fig. I7b. Output short circuit current versus setting
of VR2

Zener substitute from the stabilised side. Consequently, other means of providing a more constant voltage need to be investigated.

That adopted here concerns the replacement of the "black box" with a constant current circuit similar to that in Fig. 7. The arrangement of this is given in Fig. 15, where a current of 7 mA is given by TR1, 4 mA through VR1, and 3 mA through the Zener substitute.

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| 1N21 | $\begin{aligned} & 19 \\ & 0.17 \end{aligned}$ | Ac127 | $\begin{aligned} & \text { sp } \\ & 0.25 \end{aligned}$ | BFizs ${ }^{\text {in }} 0$ | （iJ）${ }^{\text {a }}$ | $\ln _{0.88}$ | （1）43 | $\begin{aligned} & 2 p \\ & 0.40 \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1N23 | 0.20 | AC12 ${ }^{\text {d }}$ | 0.20 | $\begin{array}{ll}\text { BF181 } & 0.35\end{array}$ | GJう． | 0.85 | OC44 | 0.17 |
| 1N85 | 0.88 | AC187 | 0.25 | BF184 0．20 | 1：J7M | 0.37 | OC44 | 0.17 |
| 1 N 253 | 0.50 | AC188 | 0.25 | BF185 0.80 | H6：100\％ | 0.50 | OC45 | 0.12 |
| 1N256 | 0.50 | ${ }^{\text {ACY } 17}$ | 0.30 | BF194 0.17 | HSt00A | 0.80 | OC45 | 0.18 |
| 1N645 | 0.85 | ACY18 | 0.25 | BF190 0．15 | Mati00 | 0.25 | OC4 4 | 0－27 |
| 1N726A | 0.80 | ACY19 | 0.25 | BF190 0.15 | MAT101 | 0.80 | OC 57 | 0.60 |
| 1 N 914 | 0.07 | ACY20 | 0.20 | BF197 0.15 | Matie 0 | 0.25 | OCうb | 0.60 |
| 1N4007 | 0.20 | ACY21 | 0.20 | BFS6］ 0.28 | Matlel | 0.80 | OC59 | 0.85 |
| 18021 | 0.20 | ACY22 | $0 \cdot 10$ | $13 \mathrm{FS98} \quad 0.28$ | MJEJ20 | 0.87 | OC＇66 | 0.80 |
| 18113 | 0.15 | ACY27 | 0.25 | BFXI： 0.20 | MJE290． | 1.37 | OC70 | 0.18 |
| 18130 | 0.18 | ACY28 | $3 \cdot 17$ | BFX13 0.25 | MJE30． | 0.87 | OC71 | 0.12 |
| 16131 | 0.18 | ACY39 | $0-50$ | BFX29 0．25 | NKT128 | 0.35 | OCT | 0.20 |
| 18202 | 0.23 | ACY40 | $0 \cdot 15$ | $\begin{array}{ll}\text { BFX30 } & 0.25\end{array}$ | NKT129 | 0.30 | OC73 | $0 \cdot 30$ |
| $2 \mathrm{G240}$ | 1.98 | ACY41 | 0.15 | BFX3＊ 0.88 | NKT2．1 | 0.25 | OCT4 | 0.30 |
| 2 G 301 | 0.80 | ACY44 | $0 \cdot 25$ | 13FX63 0.50 |  | 0.25 | OC75 | 0.25 |
| $2 \mathrm{G30} 2$ | 0.82 | AD140 | 0.60 | PFX84 0.25 | NKT ${ }^{\text {d }} 14$ | 0.15 | Or 76 | 0.25 |
| $2 \mathrm{C306}$ | 0.80 | AD149 | 0.50 | HFX85 0．25 | NKT ${ }^{\text {a }} 16$ | 0.37 | OC\％ | 0.40 |
| $2 \mathrm{C371}$ | 3.22 | AD161 | 0.87 | BFX85 0.25 | NKT217 | 0.35 | Oc＇is | 0.20 |
| $2 \mathrm{C381}$ | 0.25 | ADI62 | 0.37 | BFX87 0.20 | SKTolk | 0.40 | 0c＇79 | 0.22 |
| $2 \mathrm{Cl14}$ | 0.80 | AF106 | 0.30 | BFX88 0.85 | NKT 219 | 0.38 | OC＇81 | 0.20 |
| $2 \mathrm{Cl17}$ | 0.22 | AF114 | 0.25 | BFY10 1．00 | NKT | 0.20 | OC811 | 0.20 |
| 2N214 | 0.43 | AF11 | 0.25 | 13FY11 1．25 | NKT | 0.82 | OC813 | 0.20 |
| 2 N 247 | 0.25 | AF＇116 | 0.25 | BFY17 0.25 | NKT251 | 0.24 | OC81DM | $0-18$ |
| 2N200 | 0.50 | AF117 | 0.25 | BYFIS 0.26 | NKT ${ }^{\text {a }}$ I | 0.25 | OC817 | 0－40 |
| 2 N 404 | 0.20 | AFl1\％ | 0.62 | 13FY19 0.26 | NK゙T2\％ | 0.25 | $00^{0} 82$ | 0.26 |
| 2N697 | 0.15 | AFl19 | 0.20 | 13FY24 0.45 | NKT273 | 0.15 | OC8211 | 0.20 |
| 2N698 | 0.40 | AF194 | 0.25 | 3FY44 $\quad 1.00$ | ※KT2す4 | 0.20 | OC83 | 0.25 |
| 2N706 | 0.10 | AF12J | 0.20 | BFYu0 0.82 | NKT2T\％ | 0.25 | OC84 | 0.25 |
| 2N706A | 0.12 |  | 0.17 | $\begin{array}{ll}\text { 1FFYJ } & 0.20\end{array}$ | NKT277 | 0.20 | OC114 | 0.38 |
| 2N708 | 0.15 | $\mathrm{AFH}^{-7}$ | 0.17 |  | NKT\％${ }^{\text {a }}$ | 0.25 | OC122 | 0.60 |
| 2 N 709 | 0.63 | AF139 | 0.30 | $\begin{array}{ll}13 \mathrm{FY} 5 & 0.17\end{array}$ | NKT301 | 0.40 | OCl23 | 0.65 |
| 2N711 | 0.87 | AF178 | 0.55 | BFY64 0．42 | NKT304 | 0.75 | 0 C 139 | 0.25 |
| 2N987 | 0.58 | AF179 | 0.45 | BFY90 0.65 | NKT403 | 0.75 | OC140 | 0.35 |
| 2N1090 | 0.30 | AF180 | 0.62 | 3s8X27 0.50 | NKT40t | 0.55 | OC141 | 0.60 |
| 2N1091 | 0.92 | AF181 | 0.42 | BSX60 0.93 | －K T678 | 0.30 | OC＇169 | 0.20 |
| 2N1131 | 0.25 |  | 0.40 | BSX－6 0.15 | NKTT13 | 0.25 | OC1\％ | 0－25 |
| 2N1132 | 0.25 | AFY19 | 1.13 | $\begin{array}{ll}\text { BSY } & 0.18\end{array}$ | NKTİ3 | 0.25 | $0<171$ | 0.30 |
| 2 N 130 g | 0.18 | AFZ11 | 0.60 | BSY27 0.17 | NKT\％it | 0.38 | OC200 | $0 \cdot 40$ |
| 2N 1303 | 0.28 | AFZ1： | 1.00 | BSY 0 | 0783 | 0.38 | $00^{0} 201$ | 0.70 |
| 2N1304 | 0.25 | ASY26 | 0.25 | 13SY90̈A 0.12 | OAJ | 0.80 | OC30： | 0.80 |
| 2N130\％ | 0.22 | ASYe7 | $0 \cdot 32$ | $\begin{array}{ll}\text { BSY95 } & 0.12\end{array}$ | OAfi | 0.12 | OC203 | 0.40 |
| 2 N 1306 | 0.25 | ASYO8 | 0.25 | BT102／00R | 0.47 | 0.10 | OC：04 | 0.40 |
| 2 N 1307 | 0.25 | ASY：9 | 0.30 | 0.75 | 0A70 | 0.10 | OC20 | 0.75 |
| 2N1308 | 0.25 | ASY36 | 0.25 | 13TY43 0.82 | 0.171 | 0.10 | OC206 | 0.90 |
| 2N1309 | 0.85 | ASY 0 | 0.17 | 13 T | 0 O．7 | 0.10 | OC207 | 0.90 |
| 2N1420 | 0.98 | ASY 1 | 0.40 | 0.75 | 0.474 | 0.10 | OC460 | 0.20 |
| 2N1007 | 0.28 | A8Yü | 0.20 | HT Y79／400R | 0479 | 0.10 | OC470 | 0.80 |
| 2N1526 | 0.38 | ASY ${ }^{\text {a }}$ | 0.20 | 1.25 | 0 A81 | 0.08 | OCP71 | 0.97 |
| 2N1909 | 2.85 | ASY6？ | 0.25 | $13 \times 100 \quad 0.15$ | 0.85 | 0.12 | ORP12 | 0－50 |
| 2 N 214 | 0.75 | ASY86 | 0.38 | BY126 0.15 | 0.486 | 0.15 | ORP60 | 0.40 |
| 2N2148 | 0.60 | Asz 21 | 0.42 | BY⿺𠃊 0.17 | 0 A 90 | 0.08 | ORP61 | 0.42 |
| 2N2160 | 0.80 | AsZ23 | 0.75 | BY18． 0.17 | 0.991 | 0.07 | \＄19T | 0.80 |
| $2 \mathrm{~N} 2 \cdot 218$ | 0.20 | ALY10 | 0.98 | BY18： | $0 \mathrm{A9}{ }^{\text {O }}$ | 0.07 | SAC40 | 0.26 |
| 2 N 2219 | 0.20 | AU101 | 1.50 | BY213 0.25 | OA200 | 0.07 | SFT308 | 0.88 |
| 2N2：87 | 1.08 | ${ }^{\text {BC107 }}$ | 0.10 | B YZ10 0.85 | OA20： | 0.10 | STz2\％ | 0.38 |
| 2N2297 | 0.20 | BC108 | 0.10 | BYZI1 0.32 | OA210 | 0.25 | ST7231 | 0.68 |
| 2N2369．A | 0.15 | BC109 | 0.10 | BYZ1： 0.80 | OA211 | 0.30 | $5 \times 68$ | $0 \cdot 20$ |
| 2 N 2613 | 0.28 | BC113 | 0.16 | $\begin{array}{ll}\text { BYZ12 } & 0.80 \\ \text { BYZ13 } & 0.25\end{array}$ | $0.2 Z 200$ | 0.55 | 8X631 | 0.30 |
| 2N2646 | 0.45 | BC11． | 0.20 | $\begin{array}{ll}\text { BYZ13 } & 0.25\end{array}$ | OAZ201 | 0.50 | $5 \times 635$ | 0.40 |
| 2N2712 | 0.25 | BC116 | 0.25 | BYZ1－ 1.00 | 0A2202 | 0.40 | S ${ }^{\text {c } 640}$ | 0.60 |
| 2 N 2784 | 0.60 | BC116a | 0.30 | $\begin{array}{ll}\mathrm{BYZ} 16 & 0.62\end{array}$ | OAZ203 | 0.42 | 5X641 | 0.55 |
| 2 N 2846 | 0.75 | BC118 | 0.25 | BYZAsčuv3 | OAZ：04 | 0.30 | SX 64. | 0.60 |
| 2N2848 | 0.42 | BC121 | 0.20 | 0.15 | OAZ 205 | 0.42 | SX644 | 0.75 |
| 2 N 2904 | 0.20 | BC122 | 0.20 | Clll 0.85 | OAZ206 | 0.42 | $8 \times 645$ | 0.75 |
| 2 N 2904.4 | 0.38 | BC12 ${ }^{3}$ | 0.68 | CR\＄1／0． 0.25 | OAZ20 | 0.47 | V1－1301 | 0.60 |
| 2N2906 | 0.20 | BC126 | 0.66 | CRSI／40 0.17 | OAZ208 | 0.32 | V30／201P | 0.75 |
| 2 N 2907 | 0.28 | BC140 | 0－55 |  | OAZ909 | 0.83 | 160／201 | 0．75 |
| 2N2924 | 0.23 | BC147 | 0.15 | CS1013 3.13 | OAZ210 | 0.32 | $\stackrel{1}{60 / 201 P}$ | 0.38 |
| 2N292J | 0.15 | BC148 | 0.12 | DD000 0.15 | OAZ211 | 0.82 | X 4101 | 0.10 |
| 2 N 2926 | 0.10 | $\mathrm{BCI}+9$ | 0.20 | DD003 0．15 | OAZ222 | 0.45 | XA102 | 0.18 |
| 2 N 3054 | 0.50 | BClö | 0.20 | UD00fi 0.18 | OAZ223 | 0.45 | XA151 | 0.15 |
| 2N3000 | 0.75 | BClus | 0.15 | ${ }^{\text {D D 0 0 }}$ | 0.12224 | 0.45 | XA152 | 0.15 |
| 2N3702 | 0.10 | BCl 58 | 0.12 | ${ }^{\text {DDP00 }} 00.38$ | OAZ241 | 0.22 | XA161 | 0.25 |
| 2N3705 | 0.10 0.93 | BC16n | 0.63 | $\begin{array}{ll}\text {（iD3 } & 0.33 \\ \\ \text { GD4 } & 0.05\end{array}$ | OAZ：242 | 0.23 0.88 | XA162 | 0.25 |
| 2N3706 | 0.23 | BC169 | 0.12 | CiD4 00.05 | OAZ244 | 0.83 | XA162 $\times \mathrm{X} 101$ | 0.25 0.48 |
| 2N3707 | 0.12 | BCY31 | 0.30 | GD5 00.33 | OAZ：46 | 0.88 | X B101 | 0.48 |
| 2N3709 | 0.13 |  |  | $\begin{array}{ll}4 \mathrm{D} 8 & 0.25\end{array}$ | OAZ990 | 0.38 0.50 | X $\mathrm{CB102}$ | 0.10 |
| 2N3710 | 0.13 0.10 | BCy 32 BCX 33 | 0.55 0.25 | $\begin{array}{ll}\text { GD1 } \\ \text { GET10\％} & 0.05 \\ 0.30\end{array}$ | ${ }_{\text {OC16 }}^{\text {OC16 }}$ | 0.50 0.38 |  | 0.25 0.18 |
| 2N3819 | 0.35 | BCY 34 | 0.30 | $\begin{array}{ll}\text { GET103 } & 0.82 \\ 0.22\end{array}$ | ${ }_{0} 0 \mathrm{Cl19}$ | 0.58 0.37 | X B 113 | 0.12 |
| 2N3820 | 0.80 | BCY38 | 0.40 | GET113 0．20 | OU20 | 0.88 | XB12I | 0.48 |
| 2N3823 | 0.75 | BCY39 | 1.00 | GFT114 0．15 | 0 C 22 | 0.50 | ZR24 | 0.68 |
| 2N5027 | 0.63 | BCY40 | 0.50 | GETIIJ 0．45 | OC23 | 0.80 | ZS170 | 0.10 |
| 2N5088 | 0.33 | BCY42 | 0.25 | GET116 0.50 | 0 OL 4 | 0.80 | z8271 | 018 |
| 28005 | 1.00 | BCY70 | 0.15 | GET120 0.25 | OC25 | 0.37 | ZT ${ }^{\text {］}}$ | 0.25 |
| 28301 | 0.50 | BCY71 | $0 \cdot 20$ | GET872 0.30 |  | 0.25 | ZT 43 | 0.26 |
| 2玉304 | 0.75 | BCZ10 | 0.35 | （EET875 0.26 | OC＇26 | 0.25 | ZT 43 | 0.25 |
| 28501 | 0.32 | BCZ11 | 0.50 | $\begin{array}{lll}\text { GET880 } & 0.37\end{array}$ | OC28 | 0.60 | 7TX107 | 0.15 |
| 29703 | 0.62 | BD121 | $0-65$ | GET881 0.25 | OC29 | 0.80 | ZTX108 | 0．12 |
| AA129 | 0.20 | BD123 | 0.80 | GET882 0.25 | OC30 | 0.40 | ZTX 300 | 0.12 |
| AAZ12 | 0.30 | ${ }_{\text {BD }}{ }_{\text {BD }}$ B11 | 0.75 1.82 | $\begin{array}{ll}\text { GET883 } & 0.25 \\ \text { GEX44 } & 0.08\end{array}$ | Oc3s | 0.50 | 2TX304 | 0.25 |
| AAZ13 | 0.12 | ${ }_{\text {BF110 }}^{\text {BDY }}$ | 1.62 0.25 | $\begin{array}{ll}\text { GEX44 } \\ \text { GEX4／1 } & 0.08 \\ & 0.07\end{array}$ | 0c36 | 0.80 | ZTX 000 | 0.16 |
| AC107 | 0.37 | BF117 | 0.50 | $\begin{array}{lll}\text { GEX } 941 & 0.15\end{array}$ | OCll | 0.25 | ZTX 503 | 0.17 |
| AC126 | 0.20 | BF167 | 0.25 | GJ3M 0.25 | OC 42 | 0.80 | ZTX0̄31 | 0.25 |

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A modification of the original constant current circuit is employed, due to the need for it to function correctly with a varying supply voltage, $V_{i}$. The two silicon diodes (these can be of any low power type, or alternatively two npn silicon transistor base-emitter junctions, forward biased as shown in Fig. 16) provide a constant voltage at the base of TRA-constant, that is, with respect to the positive supply line. With the base and hence emitter so held constant, the 100 ohm resistor passes a constant 7 mA current through TRA and hence to the TRZ and VR1 in parallel.

It was from this configuration that the graphs in Fig. 17 were obtained, to show how output voltage varies with output current, and how the level of short circuit current varies with the setting of VR2.

In passing, note that the driving of the TRZ from the input voltage allows the output voltage to be made variable down to zero volts. If TRZ is driven from the output side, output voltages of less than Zener voltage would cease to give regulation.


Fig. 18. Suggested mains supply unit for the stabiliser shown in Fig. 14

## VOLTAGE FEEDER

Practical construction of such a stabilised power supply can follow usual techniques. After maximum output current and maximum output voltage have been decided upon, a decision is made on the method of driving the regulator. A simple mains unit is shown in Fig. 18, and the voltage rating of the transformer secondary is chosen to give, at full load current, a value of $V_{i}$, about 4 volts more than the maximum regulated output voltage. Alternatively, a battery or accumulator could provide power.

## HEAT SINK FOR TRI

As for the size of the heat sink required for TR1, calculate first the maximum power dissipated in that transistor. This will be:

$$
\begin{aligned}
&\left.V_{i}-(\text { minimum output voltage used })\right] \\
& \times \text { maximum load current }
\end{aligned}
$$

It is at minimum output voltage and maximum load current that maximum power will be dissipated in TR1, and a suitable heat-sink should be chosen.

Generally speaking, for an OC28 or similar transistor, bolted to a heat-sink with an insulating mica washer, a rise in heat-sink temperature of about $40^{\circ} \mathrm{C}$ above ambient room temperature can be permitted.

Heat-sinks of various ${ }^{\circ} \mathrm{C} /$ watt ratings are readily available from components stockists.

As an example, for a 1A 9 volts output, driven from a 13 volts supply, with operation down to 3 volts expected, maximum power dissipated in TR1 is $(13-3) \times 1=10$ watts, so a heat-sink of $4^{\circ} \mathrm{C} / \mathrm{watt}$ is needed for an OC28. About 20 sq in of 16 s.w.g. aluminium sheet will suffice.

Having thus decided on transformer and heat-sink requirements, the size of the final unit can be settled. VR1, and possibly VR2, are front panel controls, but otherwise layout is unimportant, and can be adapted to suit individual requirements.

## TRICKLE CHARGER FOR SMALL BATTERIES

Mention was made earlier of deriving the supply from an accumulator and this is a useful way of obtaining a good general purpose stabilised voltage for powering many circuits. Recharging of the accumulator can be carried out overnight or a trickle charger can be used while the power supply is in use.

It is worth noting that many 12 volt car batteries can still provide a handy 1 A even after their useful life in a car is over.
Constant current output can prove useful in the charging of some types of small batteries.

Batteries such as nickel-cadmium have a low internal resistance; hence, when in a discharged state, they would draw very large currents if the usual constant (or almost constant) voltage charging system is employed. This large current could give rise to overheating, tending to reduce the internal resistance still further and hence allowing thermal runaway to take place, and so leading to destruction of the battery.

However, for battery charging applications, adjust VR2 to give the required current, ensuring that VR 1 is set to give a high enough voltage to charge the battery in question when it reaches its full final voltage. The battery manufacturer's instructions should be followed if in doubt.

Otherwise, the constant current circuitry is used as an overload limiter. With VR2 at maximum resistance, short circuit the output terminals; then bring down VR2 until the output current is a little greater than the maximum expected to flow in the unit being powered, under normal conditions.

Thereafter, should excessive current attempt to flow due to a fault, such current will be safely limited to that previously set by VR2.

Although the uses of Zener substitutes so far described have been in power supplies, this does not exhaust their usefulness.

## OTHER USES

Clipping and limiting of waveforms to specific amplitudes can easily be accomplished. In the design of d.c. amplifiers, shifts of voltage by the constant voltage of the Zener substitute enable large gains to be achieved without limiting due to saturation of later stages.

With the ready availability of npn silicon transistors for use as substitute Zeners, it is hoped that experimenters will be encouraged to make greater use of them in stabilised power supplies, on the lines suggested, with corresponding improvement in equipment performance.

# market PLaIE 

Items mentioned in shis feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then be made direct to the firm concerned.

## PHOTOCONDUCTIVE CELL DESIGNERS' KIT

Designed primarily for educational establishments as well as industrial organisations interested in developing light activated controls, Photain Controls have produced a Photoconductive Cell Designers' Kit which readers may like to experiment with.

The kit consists of five different photoconductive cells together with 13 circuit diagrams each complete with a brief explanation of the circuit. Each diagram enables the student to build devices embracing light intensity measurement; feedback gain controllers; photo switches; signal modulating and musical instrument volume controller.

The kit is available from Photain Controls Ltd.. Randalls Road, Leatherhead, Surrey, price $£ 2$.

## SOLDERING ACCESSORIES

Leaving one hand free to hold the work piece, the Anextra Solder Feed fits the majority of soldering irons and carries up to 4 oz reels of flux cored solder.

Solder from 22 to 18 s.w.g. can be used and is fed to the joint by operating the solder feed trigger.

Supplied with an initial $10 z$ reel of $60 / 40$ cored 22 s.w.g. solder, the recommended retail price is $£ 4 \cdot 25$. Further details of the Solder Feed is obtainable from Anextra Ltd.,

Chiltern Works, 77-78 Chiltern View Road, Uxbridge, Middlesex.

Standard package integrated circuits, despite their numerous advantages, suffer from one major drawback as far as the amateur is concerned. The removal of these units from printed circuit boards, since it is necessary either to remove all traces of solder from all the connecting pin joints or melt all the joints simultaneously.

A range of desoldering heads for the Solderstat HMS miniature irons is an accessory which, using the method of simultaneous desoldering, removes the standard dual-in-line packages within a few seconds is claimed by Solderstat Ltd.

The desolder head is placed on the iron in place of the copper bit and aligned with the i.c. connecting pins on the printed circuit board. Both 14 -way and 16 -way dual-inline heads are available.

Readers can, of course, use standard i.c. holders available from advertisers when they construct prototype circuits.

## LOW VOLTAGE TOOLS

A range of battery-operated miniature tools capable of operating drills, cleaning brushes, abrasive stones, cutting burrs, polishing mops and other tools for precision work has been introduced by Expo (Drills) Ltd.

Two basic models are available. The "Reliant" designed for lighter work such as model making, and has a full load current of 1.5 A , and the "Titan Super". Rated torque of the "Reliant" is 1.38 oz in ( 100 gm cm .). For jobs requiring a more powerful tool and for professional applications the "Titan Super", rated at $3 \cdot 5 \mathrm{~A}$ on full load, should be used. It has a rated torque of $350 \mathrm{c} . \mathrm{m} . \mathrm{p}$. operating at $4,000-9,000$ r.p.m.

Accessories include a diamond bonded drill, for gem stones up to 7MOHS, various types of abrasives, cutters and saws. Different
collets and accessories are supplied according to model.

Prices range from $£ 3$ to $£ 5 \cdot 50$ and full details are obtainable from Expo (Drills) Ltd., 62 Neal Street. London, W.C.2.

## INSTANT LETTERING

To help finish-off equipment Instant Plastic Ltd. have introduced a range of p.v.c. self-adhesive plastic lettering kits.

Designated type $K$, the kits consist of 40 different sets in five colours, sizes from 10 mm ( $\frac{3}{1}$ in) high upwards, in capitals and lower case letters and numerals.
The smallest set contains 578 letters and numerals and the number in each set reduces as the letter size increases.

The K sets cost approximately fl per set, and details of their other ranges can be obtained from Instant Plastic Ltd., 101 Bramley Road. London, W. 10.

## PRINTED CIRCUIT BOARDS

A new price list of printed circuit boards for previously published P.E. projects has just been issued by P.H. Electronics.

Copies of the price list can be obtained from P.H. Electronics, Industrial Estate, Sandwich, Kent.

## SWITCH KIT

The mention of the versatile switch kit in the September issue should have included the following firms who are all part of the buying group set up by Home Radio (Components Lid.).

Crescent Radio, 40 Mayes Road, London, N. 22 .
Garland Bros., Chesham House, Deptford Broadway, London, S.E.8.
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Are you apt to experiment . . . this circuit-building deck has been specially designed for the 'Teach-In' series. It will also be useful for general experimental work and for prototype testing.
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We have huge numbers of components in quantities too small to advertise ndividually. in order to "clear the decks" we have made up parcels containing densers, controls, transistors, diodes etc, for a tiny fraction of normal price it is emphasised that these are mixed parcels only-contencs cannot be stipulated! Sold only by weight.

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## Unrepeatable Offer ! ! ! ! <br> Surplus VEROBOARDS, $3^{\frac{3}{4}} \times 2 \frac{1}{2} \times 15^{\prime \prime}$ <br> Only $10 p$ each or $£ 1 \cdot 00$ per dozen

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## NEW! NEW! NEW! NEW!

An aerosol spray providing a convenient means of producing any number of copies of a printed circuit both simply and quickly.
Method: Spray copper laminate board with light-sensitive spray. Cover with transparent film upon which circuit has been drawn. Expose to light. (No need to use ultra-violet.) Spray with developer, rinse and etch in normal manner. Light sensitive aerosol spray
Developer spray

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Pye Transistorised Car Radio Panels includes $2 \times$ Double Tuned IFs, 5 NKT Transistors, etc., etc. Your bargain for 50 p, post 10 p U.K. Assorted Coil Formers includes Aladdin types, erc., etc. Amazingly cheap.
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## Highfidelity Monolithic Integrated Circuit Amplifier

Two years ago Sinclaır Radınics announced the World's first monolithic integrated circuit $\mathrm{H}_{1}-\mathrm{FI}_{1}$ amplifier, the IC.10. Now we are delighted to be able to introduce its successor, the Super IC. 12 This 22 transistor unit has all the virtues of the original IC. 10 plus the following advantages

1. Higher power.
2. Fewer external components
3. Lower quiescent consumption.
4. Compatible with Project 60 modules
5. Specially designed built-in heat sink. No other heat sink needed.
6. Full outputinto $3,4,5$ or 8 ohms.
7. Works on any voltage from 6 to 28 volts without adjustment.
8. NEW 22 transistor circuit.

Output power 6 watts RMS continuous (12 watts peak).
Frequency Response 5 Hz to $100 \mathrm{KH}> \pm$ 1 dB

Total Harmonic Distortion Less than $1 \%$. (Typical 0.1\%) at all output powers and all frequencies in the audio band.
Load Impedance 3 to 15 ohms.
Power Gain 90dB (1,000,000,000 times) after feedback.
Supply Voltage 6 to 28 volts (Sinclair PZ-5 or PZ-6 power supplies ideal)
Size $22 \times 45 \times 28 \mathrm{~mm}$ including pins and heat sink.

Input Impedance 250 Kohms nominal
Quiescent current 8 mA at 28 volts

With the addition of only a very few external resistors and capacitors the Super IC. 12 makes a complete high fidelity audio amplifier suitaple for use with pick-up. F.M. tuner etc. Alternatively, for more elaborate systems, modules in the Project-60 range such as the Stereo 60 and A.F.U. may be added. The comprehensive manual supplied with each unit gives full circuit and wiring diagrams for a large number of applications in addition to high fidelity. These include car radios, oscillators etc. The very low quescent consumption makes the Super IC. 12 ideal for battery operation.


Price, inc. FREE printed circuit board for mounting.
$£ 2.98_{\text {free }}^{\text {Post }}$

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Should you not be completely satisfield with you purchase when you receive it from us. return the goods without delay and your money will be refunded in full, including cost of return postage. at once and without question. Full service facilities are available to all Sinclar customers,

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## Sinclair Project 60

## The World's leading range of high fidelity modules



## New!



The easy way to buy and build
Project 60


Project 605 is one pack containing: one PZ5. two $Z 30$ s, one Stereo 60 and one Masterimk. This new module contains all the input sockets and output components needed together with all necessary leads cut to length and fitted with neat little clips to plug straight on to the modules Thus all soldering and hunting for the odd part is eliminated. You will be able to add further Project 60 modules as they become avallable adapted to the Project 605 method of connecting. Complete Project 605 pack with $£ 29.95$ All you need for a superb 30 watt high fidelity siereo amplifier

Sinclair Radionics Limited, London Road, St. Ives. Huntıngdonshire PE174HJ. Tel: St. Ives (04806) 4311



Project 60 offers more advantage to the constructor and user of high fidelity equipment than any other system in the world.
Performance characteristics are so good they hold their own with any other available system irrespective of price or size.
Project 60 modules are more versatile - using them you can have anything from a simple record player or car radio amplifier to a sophisticated and powerful stereo tuner-amplifier. Either power amplifier can be used in a wide variety of applications as well as high fidelity. The Stereo 60 pre-amplifier control unit may also be used with any other power amplifier system, as can the AFU filter unit. The stereo FM tuner operates on the unique phase lock loop principle to provide the best ever standards of sensitivity and audio quality. Project 60 modules are very easily connected together by following the 48 page manual supplied free with all Project 60 equipment. The modules are great space savers too and are sold individually boxed in distinctive white and black cartons. With all these wonderful advantages, there remains the most attractive of all - price. When you choose Project 60 you know you are going to get the best high fidelity in the world, yet thanks to Sinclair's vast manufacturing resources (the largest in Europe) prices are fantastically low and everything you buy is covered by the famous Sinclair guarantee of reliability and satisfaction.
Typical Project 60 applications

| System | The Units to use | together with | Cost of Units |
| :---: | :---: | :---: | :---: |
| Simale battery record player | 2.30 | Crystal P.U., 12 V battery volume control | ¢4.48 |
| Mains powered record player | 2.30, PZ.5 | Crystal or ceramic P.U. volume control etc. | ¢9.45 |
| $20+20$ W. stereo amplifier for most needs | $\begin{aligned} & 2 \times 2.30 \text { s, Stereo } 60, \\ & \text { PZ.5 } \end{aligned}$ | Crystal. ceramic or mag. P.U.. F.M. Tuner. etc. | £23.90 |
| $20+20 \mathrm{~W}$. stereo amplifier with high performance spkrs. | $\begin{aligned} & 2 \times 2.30 \text { s, Stereo } 60 \text {, } \\ & \text { PZ. } \end{aligned}$ | High quality ceramic or magnetıc P.U., F.M. Tuner, Tape Deck. etc. | £26.90 |
| $40+40$ W. R.M.S. de-luxe stereo amplifier | $2 \times 2.50$ s, Stereo 60 PZ.8, mains trsfrmr | As above | £34.88 |
| Indoor P.A. | 2.50, P2.8, mains transformer | Mic.. guitar, speakers, etc., controls | £19.43 |

# from a simple amplifier to a complete stereo tuner amplifier with Project 60 modules 

## Z. 30 \& Z. 50 power amplifiers



The $Z .30$ and $Z .50$ are of advanced design using silicon epitaxial planar transistors to achieve unsurpassed standards of performance. Total harmonic distortion is an incredibly low $0.02 \%$ at full output and all lower outputs. Whether you use $Z .30$ or $Z .50$ amplifiers in your Project 60 system will depend on personal preference, but they are the same size and may be used with other units in the Project 60 range equally well. SPECIFICATIONS (Z.50 units are interchangeable with Z. 30s in all applications). Power Outputs
Z. 3015 watts R.M.S. into 8 ohms using 35 volts: 20 watts R.M.S. into 30 nms using 30 volts. 2.5040 watts R.M.S. into 3 ohms using 40 volts 30 watts R.M.S. into 8 ohms using 50 volts. Frequency response: 30 to $300,000 \mathrm{~Hz} \pm 1 \mathrm{~dB}$. Distortion: $0.02 \%$ into 8 ohms.
Signal to noise ratio: better than 70 dB unweighted Input sensitivity: 250 mV in to 100 Kohms .
For speakers from 3 to 15 ohms impedance.
Size: $14 \times 80 \times 57 \mathrm{~mm}$.
2.30

Built, tested and guaranteed with crrcuits and instructions manual.
2.50
with circurts and instruc-
tons manual.
$£ 5.48$

## Project 60 Stereo F.M. Tuner



## First in the

world to use the phase lock loop principle

The phase lock loop principle was used for receiving signals from space craft because of its vastly improved signal to noise ratio. Now. Sinclair have applied the principle to an F.M. tuner with fantastically good results. Other original features include varicap diode tuning, printed circuit coils, an I.C. in the specially designed stereo decoder and squelch circuit for silent tuning between stations. Good reception is possible in difficult areas, and often a few inches of wire are enough for an aerial. In terms of a high fidelity this tuner has a lower level of distortion than any other tuner we know. Stereo broadcasts are recelved automatically as the tuning control is rotated, a panel indicator lighting up as the stereo signai is tunedin. This tuner can also be used to advantage with any other high ficelity system.
SPECIFICATIONS-Number of transistors: 16 plus 20 in I.C. Tuning range: 87.5 to 108 MHz . Capture ratio: 1.5 dB . Sensitivity: $2 \mu \mathrm{~V}$ for 30 dB queting: $7 \mu \mathrm{~V}$ for lock-In over full deviation. Squelch level: $20 \mu \mathrm{~V}$. A.F.C. range: $\pm 200 \mathrm{KHz}$. Signal to noise ratio: $>65 \mathrm{~dB}$. Audio frequency response: $10 \mathrm{~Hz}-15 \mathrm{KHz}( \pm 1 \mathrm{~dB})$. Total harmonic distortion: $0.15 \%$ for $30 \%$ modulation. Stereo decoder operating level: $2 \mu \mathrm{~V}$. Cross talk: 40 dB . Qutput voltage: $2 \times 150 \mathrm{mV}$ tion. Stereo decoder operating evel: $2 \mu$. Cross talk: $40 d B$, Cutput
R.M.S. Operating voltage $: 25-30 \mathrm{VDC}$. Indicators : Power on/tuning/stereo. R.M.S. Operating volta
Size: $93 \times 40 \times 207 \mathrm{~mm}$.

Built and tested. Post free.

## Stereo 60 Pre-amp/control unit

$\rightarrow$ -

Designed for Project 60 range but suitable for use with any high quality power amplifier. Again silicon epitaxial planar transistors are used throughout, achieving a really high signal-to-noise ratio and excellent tracking between channels. Input selection is by means of push buttons and accurate equalisation is provided for all the usual inputs.
S.PECIFICATIONS-Input sensitivities: Radio - up to 3 mV . Mag. p.u. 3 mv : correct to R.I.A.A curve $\pm 1 \mathrm{~dB}: 20$ to $25,000 \mathrm{~Hz}$. Ceramic p. $u$, up to 3 mV : Aux - up to 3 mV . Output: 250 mV . Signal to noise ratio: better than 70 dB . Channel marching: within 1 dB . Tone controls: TREBLE +15 to -15 dB at $10 \mathrm{KHz}: \mathrm{BASS}+15$ to -15 dB at 100 Hz . Front panel: brushed aluminium with black knobs and controls. Size: $66 \times 40 \times 207 \mathrm{~mm}$. Built testedand guaranteed.
£9.98

## Power Supply Units

Designed special for use with the Project 60 system of your chorce. Use PZ. 5 for normal $Z .30$ assemblies and PZ. 6 where a stabilised supply is essential.
PZ. 530 volts unstabilised $£ 4.98$
PZ. 635 volts stabilised $\mathbf{£ 7 . 9 8}$ PZ. 845 volts stabilised
(less mains transformer) £7.98


## The Sinclair Guarantee

If within 3 months of purchasing Project 60 modules directly from us, you are dissatisfied with them, we will refund your money at once. Each module is guaranteed to work perfectly and should any defect arise in normal use we will service it at once and without any cost to you whatsoever provided that it is returned to us within 2 years of the purchase date. There will be a small. charge for service thereafter. No charge for postage by surface man. Air-mall charged at cost.

## A.F.U. High \& Low Pass Filter Unit



For use between Stereo 60 unit and two $Z .30$ s or $Z .50$ s. and is easily mounted. It is unique in that the cut-off frequencies are continuously variable. and as attenuation in the rejected band is rapid ( 12 dB /octave), there is less loss of the wanted signal than has previously been possible. Amplitude and phase distortion are negligible. The A.F.U. is suitable for use with any other amplifier system. Two filter stages - rumble (high pass) and scratch (low pass). Supply voltage -15 to 35 V . Current -3 mA . H.F. cut-off ( -3 dB ) varrable from 28 KHz to 5 KHz , L.F. cut-off ( -3 dB ) variable from 25 Hz to 100 Hz . Distortion at 1 KHz ( 35 V . supply $(0.02 \%$ at rated output. Size: $66 \times 40 \times 90 \mathrm{~mm}$.

Built tested and guaranteed.
£5.98

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## Sinclair Q16/Micromatic

## 016 High fidelity loudspeaker

The 016 employs the well proven acoustic principles specially developed by Sinclair in which a special driver assembly is meticulously matched to the characteristics of the uniquely designed cabinet. In reviewing this exclusive Sinclair design. technical journals have justly compared the 016 with much more expensive loudspeakers. Its shape enables the 016 to be positioned and matched to its environment 10 much better effect than is the case with conventionally styled enclosures. A solid teak surround with a special all-over cellular foam front is used as much for appearance as its ability to pass all audio frequencies without loss.

This elegantly designed shelf mounting speaker brings genuine high fidelity within reach of every music lover.

## Specifications:

Construction: Special sealed seamless sound or pressure chamber with internal baffle.
Loading: up to 14 watts RMS.
Input Impedance: 8 ohms.
Frequency response: From 60 to 16.000 Hz . confirmed by independently plotted $B$ and $K$ curve.
Driver unit: Special high compliance unit having massive ceramic magnet of 11,000 gauss, aluminium speech coil and special cone suspension for excellent transient response.
Size and styling: $9 \frac{3}{4} \mathrm{in}$. square on face $x$ $4 \frac{3}{4} \mathrm{in}$. deep with neat pedestal base. Black all over cellular foam front with natural solid teak surround.
Price £8.98.

## Britain's smallest radio

Considerably smaller than an ordinary box of matches, this is a multi-stage AM receiver brilliantly designed to provide remarkable standards of selectivity, power and quality for its size. Powerful AGC counteracts fading from distant stations: bandspread at higher, frequencies makes reception of Radio 1 easy. The plug-in magnetic earpiece provided, matches the Micromatic's output to give wonderful standards of reproduction. Everything including the special ferrite rod aerial and batteries is contaned within the minute attractively designed case. Whether you build a Micromatic kit or buy this amazing receiver ready built and tested, you will find it as easy to take with you as your wrist watch, and dependable under the severest listening conditions.

Specifications:
Size: $36 \times 33 \times 13 \mathrm{~mm}(1.8 \times 1.3 \times 0.5 \mathrm{in}$.)
Weight: including batteries, 28.4 gm (1 oz.)
Case: Black plastic with anodised alumınium front panel and spun aluminium dial.
Tuning: medium wave band with bandspread at higher frequencies (550 to 1.600 KHz ).

Earpiece: Magnetic type.
On/off switching: By inserting and withdrawing earpiece plug.
Kit in pack with earpiece, case, instructions and solder $£ 2.48$.
Ready built, tested and guaranteed, with earpiece $£ 2.98$.
Two Mallory Mercury batteries type RM675 required from radio shops, chemists, etc.

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