PRACTICAL

#  <br> JANUARY 1971 

## ADCOLA Soldering Instruments add to your efficiency

## ADCOLA 64

for Factory Bench Line Assembly
A precision instrument-supplied with standard $3 / 16^{\prime \prime}(4.75 \mathrm{~mm})$ diameter, detachable copper chisel-face bit*.
Standard temp. $360^{\circ} \mathrm{c}$ at 23 watts.
Special temps. from $250^{\circ} \mathrm{C}$ $410^{\circ} \mathrm{c}$.
*Additional Stack Bits
(illustrated) available
COPPER

|  |  |
| :---: | :---: |
| - |  |
| B $14{ }^{\frac{7}{12}}{ }^{\prime \prime}-2.4 \mathrm{~mm}$ | chisel face |
|  |  |
|  |  |
| $\square$ |  |
| B $12 \frac{1}{16}{ }^{\circ}-4.75 \mathrm{~mm}$ EYELET Bit |  |
|  |  |
| B $588^{\frac{1}{4}}$ " -6.34 mm chisel face |  |
| LONG LIFE |  |
| $\square$ - 0 |  |
| $\bigcirc 42 \mathrm{LL} \frac{1}{16}$ - 4.75 mm |  |
|  |  |
| B 38 LL $\frac{1}{*}^{\circ}$ - 32 mm chisel face |  |
| $\longrightarrow \longrightarrow$ |  |
| B 14 LL $\frac{7}{12}^{\circ}$ - 24 mm Chisel face |  |
| $\square$ |  |
| B 44 LL $\frac{1}{14}$ - 475 mm | SCREWDRIVER face |

Don't take chances. We don't. All our ADCOLA Soldering Instruments are of impeccable quality. You can depend on ADCOLA day after day. That's why they're so popular. You get consistent good service. . . reliability . . from our famous thermally controlled ADCOLA Element and the tough steel construction of this ideal production tool.

*
Write for price list and catalogue
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(Dept. L), ADCOLA HOUSE, GAUDEN RD., LONDON, S.W.4. Telephone: 01-622 0291/3 - Telegrams: Soljoint London Telex • Telex: Adcola London 21851


## PHOTOELECTRIC KIT

Contents: 2 P.C. Chassis Boards, Chemicals, Etching Manual, Intra-Red Phototransistor, Latchigg Relay, 2 Transistors, Condensers, Resistors, Gain Control, 8 teady-Light Photo-Switch/Counter/Burglar Alam, etc. (Project No. 1) which can be modified for modulated-light operation.


## INVISIBLE BEAM OPTICAL KIT

Everytbing needed (except plywood) for building: 1 Invisible-Beam Projector and 1 Photocell Receiver (as illustrated). Suitable for all Photoelectric Burglar Alarms, Counters, Door Openers, etc.
CONTENTB: 2 lenses, 2 mirrors, 245 -degree wooden blocks, Infra-red filter, projector lamp holder, build ing plans, performance data, etc. Price 19/6. Pontage and Pack. 1/6 (U.K.). Commonwealth: Surface Mail 2/-; Air Mail 8/.
LONG RANGE INVISIBLE BEAM OPTICAL KIT
CONTENTS: As above. Twice the range of standard kit. Larger Lenses. Filter. etc. Price 29/B. Postage and Pack. 1/B (U.K.). Commonwealth: \&uriace Rail etc. Price 29/6.
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## JUNIOR PHOTOELECTRIC KIT

Versatile Invisible-bearn, Relay-less, Steady-light Photo-Switch, Burglar Alarm. Door Opener, Counter, etc., for the Experimenter.
CONTENTB: Infra-Red Sensitive Phototransiator, 3 Transistors, Chasois, Platic Case, Resistori, Screws, etc. Full Size Plans, Instructions, Data Sheet ' 10 Advanced Pice 1010 Pota
and Pack. 1/6 (U,K.). Commonwealth 2/; Air Kail 4/-

## JUNIOR OPTICAL KIT

CONTENTS: 2 Lenses, Infra-red Filter, Lampholder, Bracket, Plans, etc. Everything (except plywood) to huild 1 miniature invisible beam projector and photocell
recefver for use with Junior Photoelectric Kit.
Price 10/6. Post and Pack. 1/6 (U.K.). Commonwealth: Surface Mail 2/: : Air Mail 4/.

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Due to huge purchase these superb U.S.A. construction kits can be offered at $25 \%-50_{\%}^{\circ}$ off recommended list prices. Absolutely complete with most detailed construction and operating books. Available from all branches or by mail order for 7/6 CARRIAGE AND PACKING on each kit. Two kits or more carr. free.

KG-865 50 Watt Stereo Amplifier Kit
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All Bilicon Transiatora for stability, Cleaner Sounde Extremely Wide Power Bandwidth and Frequency Reaponse 50W IHF Power Output Specially selected Low-Noine Preamy Tranaistorse Two Printed Circuit Boarda for Fabt Easy Assembly Convenient Front-Panel stereo Headphone Jack Teakfiniahed Extraded Aluminiam Front Panel-Two a.c. Convenience OutLete.



 pluk sterca headphoner - IM Distortion: zuder $1{ }^{\circ}{ }^{\circ}$,
of fullz and ;,monllz mised +1 at rated nomer

 ment: :3n- Aur, juhz a.c.
 Lasembled 84 GRS. Teak Cise 25 EAtra

KG-625 Deluxe fin Vacuum Tube Voltmeter Kit
 $I-5-5-15-50-150-500-1,500)^{\circ}$ fuld senle.
Accuracy: $1.3 \%$ of froll sente reading - Accuracy: I- $3 \%$ of foll scale readinf

 $1,400-1,2 \mu 0 \mathrm{~J}$ full scale Accuracy: $\pm 5 \%$ Prequency Reaponse: 1 IdB+30 IFz







KG-375A Deluxe Solid-State Auto Analyzer Kit
Twie-up ned tronble-shool miny ellr. perforum In' nevernd restera in omp... do nll thin Sat Engine Idie and Automatic Transmisaion Shilt Pointe Detect Condition of Point 8urfacen Detect Distributor Wrear - Check Voltage and Current Regulatort Yoltsece Oqunerators for both Current and Circulta Detect Variation in Dwell Angle. Circuiti betect Variation in Dwell Angle. 18 Gms . Asmembled Price 21 GNs.


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critical Lixtecier

R.F, and I.F. Panely Asembled and Aligned Front Panel Stereo Headphone Jack snd Speaker Muting \$witche Full Controln imelading Tape Monitor 50 FW I, H. F. Output Urable I.R.F. Senitivity: $3 \mu 1$ Frequency Response: $1-$ Id $B$, 30 to
 3HilB Chsnnel Separation: 3ivd B Complementary Traniformerlease Power




STAR ROAMER 5-Band Shortwave Receiver Kit
Bandspread: electrical. calibruted
"-10.1 Sennitivity: $10 \mu 1$ for Van sighnt-to-noise raito I,F. Bandwidth: skHzz at bil $B$ down ".-.j.iliz Speaker: li, perniauent maguel lype Power Supply: fused, hiansformer operated Headphone Oatpat: lore impedance Valve Complenients:


 Requirements: $\because: 3 H-: j 0 I^{\prime}$, 50 Hz H.c.; fill R Coveri 200 to 400 KHz and 550 kHz to 30 MHz in 5 Band-awitched Ranges Superheterodyne Circnit includes controlled regenerative I.F, stage ©A.V.C. reducea tading and blating; A. N.L. ents nolse to a minimum Aerial Trimmer
 Eany-to-Rend 7 in Silde Rule Din).

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| 1N4009 1N4148 | 1/8 | AC153 $4 /-$ <br> AC154 $3 /-$ <br> 185  | $\begin{array}{ll}\text { B8Y50 } & 5 /- \\ \text { BSY53 } & 5\end{array}$ | $\begin{array}{llll} \text { PA246 } & 52 / 6 & 48 \end{array}$ | $45 /-40 /-35 /-$ |
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| 2G302 | 4/8 | AC188 6/- | BSI95 3/- | cilicon Power | $\begin{gathered} 25+6 / 3 \\ 100+5 / 3 \end{gathered}$ |
| $2 \mathrm{G303}$ | $5 /$ |  | $\begin{array}{ll}\text { BY100 } & 3 / 6 \\ \text { BY103 } & 4 / 6\end{array}$ | $100+11 /-$ | $\begin{aligned} & 100+5 / 3 \\ & 500+4 / 9 \end{aligned}$ |
| 2G306 2 C 308 | $8 / 8$ | $\begin{array}{ll}\text { ACY18 } & 4 /- \\ \text { ACV19 } & 5\end{array}$ | BY103 ${ }^{\text {BY126 }}$ A/6 | $100+(1)-$ | $500+4 / 9$ |
| 2G309 | $81 /$ | ACY20 4/- | BY127 4) | 2/6 | 646 10/6 |
| 20371 | 4/6 | ACY21 $4 / 6$ | BYZ10 8/- | NPN Pianar | Motor |
| 2G374 | $5 / 6$ |  | BYZ11 | All Colours | nijunc |
| 2G381 | $5 /-$ | $\begin{array}{ll}\text { ACl } 28 ~ & 3 / 6\end{array}$ | BYZ12 B/- | All Colours | $25+8 / 9$ |
| 2G382 | B/ | ACY34 4/ | BYZ13 4/- | $100+1 / 6$ | $100+7 / 6$ |
| 2 G 383 | 51 | $\begin{array}{ll}\text { ACY36 } & 5 / 0\end{array}$ | BYZ15 $20 /-$ |  | $500+6 / 9$ |
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| 2N697 | 3/6 | AD140 10/- | GET103 4/6 |  | ullard |
| 2 N 698 | $8 / 6$ | AD149 101- | MPF102 8 8/6 | $25+5 / 3$ |  |
| 2N706 | $2 /-$ | AD161 7/6 | MPF103 | $100+4 / 6$ | $00$ |
| 2N706A | 218 | AD162 7/6 | MPF104 $7 / 6$ | $500+3 / 9$ |  |
| 2 N 707 | $8 / 6$ | $\begin{array}{rrr}\text { AF102 } & 12 / 8 \\ \text { AF114 } \\ 8 / 6\end{array}$ |  |  |  |
| 2N914 | 4/8 | AFl15 6/- | OA7 41 |  |  |
| 2N916 | 4/8 | AF116 $6 / 6$ | OA9 | 70 | Illa |
| 2N918 | $7 / 6$ | AF117 5/- | OA10 | 25+4/3 | $1 \mathrm{amp} P$ |
| -N919 | 4/- | AF118 12/6 | OAt\% | $100+36$ | am |
| 2N920 | $5 /-$ | AFl24 6/- | OA70 |  | $00+2 / 3$ |
| 2N922 | 8/6 | AF125 5/- | 0.471 |  |  |
| 2N930 | $5 /-$ | AF126 4/- | OA73 2/- |  | YZ13 |
| 2N1131 | 6/- | AFl27 4/- | OAIt 2/- | $25+$ | Mullard 6a 200 |
| 2N1132 | B/- | AFl39 8/- | $\begin{array}{ll}\text { OA79 } & 2 /- \\ 0.481 & 2 /-\end{array}$ | 100+4/6 | $25+3 / 6$ |
| 2N1303 | 4/6 | AF178 9/6 | OA81 2/- |  | $100+3 / 2$ |
| 2N1304 | $5 /-$ | AF181 8/6 | OA85 2/6 |  | $500+2 / 10$ |
| 2N1305 | $5 /-$ | $\begin{array}{ll}\text { AF186 } & 8 /- \\ \text { AF } 239 & 8 /-\end{array}$ | OA86 |  |  |
| 2 N 1306 | $5 /-$ | $\begin{array}{ll}\text { AF239 } & 8 /- \\ \text { AFY19 } & 22 / 6\end{array}$ | $\begin{array}{ll}\text { OA90 } & 2 /- \\ \text { OA91 } & 1 / 6\end{array}$ | Mullard 1000 y | 107/8/92/6 ea. |
| 2 N 1307 | 5/- | AFY19 22/6 | OA91 1/8 |  | T.T. Planars |
| 2N1308 | $8 /-$ | AFZ11 12/6 | OA95 1/6 | 1 amp Plastic | $25+2 / 3$ |
| 2N1304 | $5 /-$ | AFZ12 15/- | OA200 1/6 | $25+3 / 3$ | $100+2 /-$ |
| 2N1613 | 4/6 | ASY26 5/- | OA202 2/- | $100+3 /$ | $500+1 / 9$ |
| 2N2147 | 15/- | AsY27 $7 / 6$ | $0 \mathrm{~A} 210 \mathrm{~J} /$ |  |  |
| 2 N 2160 | 13/6 | AsY28 5/- | OA211 7/6 | BTI02/500R 15/- | P71 19/6 |
| 2N2287 | 25/- | Asi29 6-- | OAZ225 7/6 | Mullard Thyristor | ullard Pho |
| 2N26 | 10/6 | ASY67 9/6 | OAZ228 7/6 | 500 p.i.v. 8.5a | $25+17 / 3$ |
| 2N2904 | ${ }^{6 /-}$ | A8Z21 8/6 | OAZ229 $9 / 6$ |  | $100+14 / 9$ |
| 2N2905 | $7 / 6$ | AUY10 19, | OAZ231 9/8 | $100+11 /-$ | $500+13 / 6$ |
| 2N2925 | 4/- | B3M 19/6 | OAZ234 7/6 |  |  |
| 2N2926 | $2 / 6$ | BA110 5/- | OAZ238 |  |  |
| ${ }_{2} 2 \mathrm{~N} 3011$ | $5 /-$ | BA×31 ${ }_{\text {BCl07 }}$ 2/- | ${ }_{0}^{0 \mathrm{C} 10} 8$ | 9 | C28 12/6 |
| 2N3053 | $\begin{array}{r} 5 /- \\ 10 /- \end{array}$ | $\begin{array}{ll}\mathrm{BC107} & 2 / 8 \\ \mathrm{BC} 108 & 2 / 6\end{array}$ | $\begin{array}{rrr}\text { OC19 } & 7 / 6 \\ \text { OC20 } & 19 / 6\end{array}$ |  | Mullard Powe |
| $\begin{aligned} & \text { 2N305 } \\ & \text { 2N } 305 \end{aligned}$ | 15/- | $\begin{array}{ll}\text { BC108 } & 2 / 6 \\ \text { BCi09 } & 2 / 6\end{array}$ | OC22 $\begin{array}{ll}\text { 18/6 }\end{array}$ | $25+1 / 6$ | $25+11 /-$ |
| 2N3702 | 2/6 | BC113 6- | OC23 10/- | $100+1 / 3$ |  |
| 2N3703 | 2/6 | $\mathrm{BC116}$ 8/- | OC24 10/- | $500+1 / 1$ |  |
| 2 N 3704 | 3/6 | BC118 7,6 | OC25 7/8 |  |  |
| 2N3705 | 3/6 | BC134 7/6 | OC26 5/- | OC42 | 71 3/- |
| 2N3707 | $3 /-$ | $\underline{\mathrm{BCl} 35}$ 6/- | OC28 12/6 | Mullard | Mu |
| 2N3709 | $2 / 6$ | 73 Cl 36 7/- | OC29 12/8 | $25+5 / 3$ | $25+2 / 3$ |
| 2N3710 | $2 / 6$ | BC137 8/- | O<35 10/- | $100+4 / 9$ | $100+2 /-$ |
| 2N3711 | 2/6 |  | OC36 $12 / 6$ | $500+4 / 3$ | $500+1 / 9$ |
|  | 10/- | BCY $30 \quad 5 / 8$ |  |  |  |
| $\begin{aligned} & 2 N 3731 \\ & \text { 2N3794 } \end{aligned}$ | $12 / 6$ $2 / 8$ | $\begin{array}{ll}\text { BCY31 } & \text { 6/- } \\ \text { BCY } 32 & 10 /-\end{array}$ | $\begin{array}{ll}\text { OC42 } & 6 /- \\ \text { OC43 } & 8 /-\end{array}$ | OC45 3/6 | Y34 5/- |
| 2N3819 | 7)- | BCY33 4/- | OC44 4/- | Mullard | Mullard |
| 2N3820 | 17/8 | BCY34 5/- | OC45 3/6 | $25+3 /-$ | 9 |
| 2N3823 | 17/6 | BCX38 6/- | OC44 5/6 | $100+2 / 6$ $500+2 /-$ | $100+3 / 9$ $500+3 / 6$ |
| 2N 4058 | ${ }^{3 / 8}$ | BCY39 9/6 | $0 \mathrm{ClO} \quad 2 / 8$ | $500+21-$ | $500+3 / 6$ |
| 2N4061 | 3/- | BCY40 8/6 | 0 C 71 |  |  |
| ${ }_{2}^{2 N 428}$ | 3 3/- | BCY442 5/- | OC72 | OC75 | IN4001/2/3 1/6 |
| $\begin{aligned} & 2 \mathrm{~N} 42 \\ & 2 \mathrm{~N} 42 \end{aligned}$ | ${ }_{3 / 8}^{3 /}$ | $\begin{array}{ll}\text { BCY4 } & \text { B/- } \\ \text { BCY } & \text { 4/- }\end{array}$ | OC74 | $25+4 / 3$ | amp $100-300$ |
| 2N4290 | 3/- | BCZ11 7/8 | OCTo 5/- | $100+3 / 6$ | $100+1 / 2$ |
| 2N4291 | 3/- | BC147 3/9 | 0 Cat 51- | $500+3 /-$ | $500+1 /-$ |
| 2N4292 | 3/- | BC148 2/9 | OC77 8-- |  |  |
| 40361 | 11/- | BC149 4/- | $0 \mathrm{C78}$ 5\%- | OC20 19/6 |  |
| 40362 | 12/- | BF152 | OC81 5/- | Mullard 100v |  |
| ${ }_{28002}^{2 \mathrm{~S} 001}$ | 10/- | $\begin{array}{ll}\text { BF194 } & 3 / 8 \\ \text { BF195 } & 3 /-\end{array}$ | ${ }_{\text {OC8 }} \mathrm{CC8}$ | $25+15 / 9$ |  |
| 2 | $10 / 6$ | $\begin{array}{ll}\text { BF195 } \\ \text { BD124 } & \text { 12/6 }\end{array}$ | ${ }_{\text {OC8 }}$ | $100+14 / 6$ | ER |
| ${ }_{2 S 004}$ | 9/6 | BEN3000 5/- | OC84 5/- | $500+13 / 3$ | - |
| ${ }^{2 S} \mathbf{S 0 0 5}$ | 14/- | BF115 5/- | OC122 10/- |  | 400 MW 5\% |
| 28012 | 25)- | BF154 8/- | OC123 10/- | IN 4004/5 2/6 | ZYB8 Range |
| 2 S 013 | 201- | BFi58 8/- | OC139 5/- | 400-600y 1 amp | All Voltages |
| $2 \mathrm{S017}$ | 15/- | BF159 12/- | $0 \mathrm{Cl40}$ 7/8 | $25+2 / 9$ |  |
| ${ }_{2}^{2 S 034}$ | 12/6 |  | $\begin{array}{ll}\text { OC141 } & 15 /- \\ 0 C 169 & 4\end{array}$ |  | $25+219$ |
| 28036 | 251- | $\begin{array}{ll}\text { BF167 } & \text { 5/- }\end{array}$ | $\begin{array}{ll}\text { OC169 } & 4 /- \\ 0 \mathrm{Cl} 170 & 5\end{array}$ | $500+1 / 6$ | $100+2 / m$ |
| 28320 28321 | 9/- | $\begin{array}{ll}\text { BF173 } & 8 /- \\ \text { BF180 } & 7 / 8 \\ \text { BF18 }\end{array}$ | $\begin{array}{ll}0 C 170 & 5 /- \\ 0 \mathrm{Cl} 11 & 8 /-\end{array}$ |  | $500+1 / 9$ |
| 28321 28322 | 7/8 | $\begin{array}{ll}\text { BF180 } \\ \text { BF181 } & 7 / 8 \\ 7 / 6\end{array}$ | $\begin{array}{ll}\text { OC171 } & \text { 6/- } \\ \text { OC200 } & 7 / 8\end{array}$ | IN 4006/7 <br> $800-1000 \mathrm{y}$ <br> 1 mp | $1000+1 / 7$ |
| ${ }_{28323}^{2832}$ | 10/- | BFX30 6/- | OC201 9/8 | 00-1000v I amp $25+2 / 10$ | any one type |
| ${ }_{2} 8324$ | 12/6 | BFX 88 5/- | OC202 12/6 |  |  |
| $2 \mathrm{S512}$ | 9/6 | BFY20 12/8 | OC203 7/6 | $500+2 / 3$ |  |
| 28701 | 8/6 | BFY50 4/8 | OC204 8/ |  |  |
| $2 \mathrm{S702}$ | 11- | BFY51 4/- | OC205 12/6 |  |  |
| ${ }_{2}^{28731}$ | $8 / 8$ | BFY52 ${ }^{\text {BFY }}$ | $\begin{array}{ll}\text { OC206 } & 15 /- \\ \text { Oc20 } & 15 /-\end{array}$ |  | OCl40 ${ }_{\text {Mullard }} 7 / 6$ |
| $2 \mathrm{S732}$ | $8 / 8$ | BFY53 3/8 | OC207 15- | Mullard | Mullar |
| 25733 | 9/6 | BFY64 8/6 | OCP71 19/6 | $25+4 /-$ | $25+6 /-$ |
| AA178 | 8/6 | BLY10 20/- | ORP12 11/ | $100+3 / 3$ | $100+51-$ |
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Clearance of manufacturers' seconds, selected in types and guaranteed no open or short circuit units. Ideal cheap transistors for radio enthusiasts, manufacturers, schools and colleges

## TYPE STNI8

Silicon Planar Transistors npn TO 18 Metal Can. Types similar to 2N706, 2N2220, BSY27-95A, BSX44-76-77. Price: $500 £ 9,1,000 £ 15$
TYPE STPI8
Silicon Planar Transistors pnp TO- 18 Metal Can. Types similar to BCY70-72, 2N2906-7, 2N2411 and BC186 7. Also used as complementary to the above npn type device type STNI8. $\quad$ Price: $500 £ 9,1,000 £ 15$

## TYPE STNL

Silicon Plastic Epitaxial Planar Transistors TO-92 case. I.C. 200 mA , 300 mW medium to high gain, available in npn or pnp and types similar NPN 2N2926-2N271I-2N3391-2N3707-2N371I BC167-89.

TYPE STPL
As above but in pnp and similar to types 2 N 5354 56, 2N4058 2N4061 and 2 N 3702 -3. Also used as complementary to the above npn devices type STNL.

Price:- $500 £ 7.10 .0 ; 1,000 £ 13$
TYPE STNK
Silicon Planar Plastic Transistor non with TO-I8 pin circular lead configuration, I.C. $200 \mathrm{~mA}, 300 \mathrm{~mW}$ and similar to $\mathrm{BCl} 07-89, \mathrm{BCI} 70$, BCI73, BC182-184, BC237-8-9 and BC337 8.

Price: $500 £ 9.10 .0 ; 1,000 £ 16$
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How to assemble and use Project 60 modules to best advantage in the above and other applications will be found in the fully descriptive Project 60 manual included with Project 60 systems. This 48 page manual is available separately, price $2 / 6$ d including postage.

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| B | Simple battery powered record player | 2.30 | Crystal pick-up. 12V or more battery supply and volume control | 89/6 |  |
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| J | High pass and low pass filters | A.F.U. | D. E or F as above | £5.19.6 |  |

## Z.30 \& Z.50 power amplifiers

The 2.30 together with the 2.50 are both of advanced design using silicon epitaxial planar transistors to achieve unsurpassed standards of performance. Total harmonic distortion is an incredibly low $0.02 \%$ at full output and all lower outputs. Whether you use the $Z .30$ or 2.50 power amplifiers in your Project 60 system will depend on personal preference, but they are the same physical size and may be used with other units in the Project 60 range equally well. For operating from mains, for the 2.30 use PZ. 5 for most domestic requirements. or PZ. 6 if you have very low efficiency loudspeakers. For Z.50. use the PZ 8 described below.

SPECIFICATIONS ( $Z .50$ units are inter-
changeable with $Z .30$ s in all applications). Power Outputs
Z.30 15 watts R.M.S. into 8 ohms. using 35 V : 20 watts R.M.S. into 3 ohms using 30 volts.
Z.50 40 watts R.M.S. into 3 ohms from 40 volts: 30 watts R.M.S. into 8 ohms, using 50 volts.
Frequency response 30 to $300.000 \mathrm{~Hz} \pm 1 \mathrm{~dB}$ Frequency response 30 to 300
Distortion $0.02 \%$ into 8 ohms Distortion $0.02 \%$ into 8 ohms
Signal to noise ratio better tha
Signal to noise ratio better than 70 dB unweighted Input sensitivity 250 mV into 100 Xohms . For speakers from 3 to 150 hms impedance. Size $3 \frac{1}{2} \times 2 \frac{1}{4} \times \frac{1}{2}$ ins.

2.30

Built, tested and guaranteed with circuits and instructions manual

89/6
2.50

Built. tested and guaranteed with circuits and instructions manual

## Stereo 60 pre amp/control unit <br> Designed for the Project 60 range but suitable for use

 with any high quality power amplifier. Again silicon epitaxial planar transistors are used throughout, achieving a really high signal-to-noise ratio and excellent tracking between channels. Input selection is by means of push buttons and accurate equalisation is provided for all the usual inputs.
## SPECIFICATIONS

- Input sensitivities - Radıo - up to 3 mV . Mag. p.u. 3 mV : correct to R.I.A.A. curve $\pm 1 \mathrm{~dB}: 20$ to $25,000 \mathrm{~Hz}$. Ceramic p.u. - up to $3 m$ V: Aux. - up to 3 mV .
- Output - 250 mV .
- Signal-to-noise ratio - better than 70dB
- Channel matching - within 1 dB .
- Tone controls - TREBLE +15 to -15 dB at $10 \mathrm{kHz}:$ BASS +15 to -15 dB at 100 Hz .

- Front panel - brushed aluminium with black knobs and controls
- Size $8 \frac{1}{4} \times 1 \frac{1}{2} \times 4$ ins.

Buitt. testedand guaranteed
£9.19.6

## Active Filter Unit

For use between Stereo 60 unit and two 2.30 s or 2.50 s, the Active Filter Unit matches the Stereo 60 in styling and is as easily mounted. It is unique in that the cut-off frequencies are continuously variable, and as attenuation in the rejected band is rapid (12dB/octave). there is less loss of the wanted signal than has previously been possible. Amplitude and phase distortion are negligible. The Sinclair A FU is suitable also for use with any other ampl fier system. Two stages of filtering are incorporated - rumble (high pass) and scratch (low pass). Supply voltage (high pass) and scratch (low pass). Supply voltage -
15 to 35 V . Current -3 mA . H.F cut-off $(-3 \mathrm{~dB})$

## Power Supply Units

The units below are designed specially for use with the Project 60 system of your choice.
Illustration shows PZ. 5 power supply unit to left and PZ. 8 (for use with Z.50s) to the right. Use PZ. 5 for normal $Z .30$ assemblies and PZ. 6 where a stabilised supply is essential.

PZ-5 30 volts unstabilised $£ 4.19 .6$
PZ-6 35 volts stabilised $\mathbf{f} 7.19 .6$
PZ-8 45 volts stabilised
(less mains transformers) $£ 5.19 .6$
PZ-8 mains transformer $\mathbf{£ 6 . 1 9 . 6}$


## Stereo FM tuner


first in the world to use the phase lock loop principle
Before production of this tuner. the phase lock loop principle was used for receiving signals from space craft because of its vastly improved signal to noise ratio over other systems. Now. for the first time the principle has been applied to an FM tuner with fantastically good results. By the inclusion of other original features such as varicap diode tuning. printed circuit coils and an I,C. in the specially designed stereo decoder, the tuner has an unsurpassed specification, which also incorporates a squelch circuit for silent tuning between stations. A.F.C. and A.G.C. Sensitivity is such that good reception becomes possible in difficult areas, foreign stations can be tuned in suitable conditions and often a few inches of wire are enough for an aerial. In terms of high fidelity. this tuner has a lower level of distortion than any other tuner we know. Stereo broadcasts are received automatically as the tuning control is rotated, a panel indicator lighting up as the stereo signal is tuned in. Although the tuner is intended primarily for use with a Project 60 system, it can be used to advantage with any other high fidelity system. It is easily mounted into any cabinet as shown in the manual supplied with it.

## Specifications

Number of transistors 16 plus 20 in I.C
Tuning range 87.5 to 108 MHz
Capture ratio 1.5 dB
Sensitivity $2 \mu V$ for 30 dB quieting $7 \mu \mathrm{~V}$ for full limiting
Squelch level $20 \mu \mathrm{~V}$
A.F.C. range $\pm 200 \mathrm{KHz}$

Signal to noise ratio $>65 \mathrm{~dB}$
Audio frequency response $10 \mathrm{~Hz}-15 \mathrm{kHz}( \pm 1 \mathrm{~dB})$
Total harmonic distortion $0.15 \%$ for $30 \%$ modulation
Stereo decoder operating level $2 \mu \mathrm{~V}$
Pilot tone suppression 30 dB
Cross talk 40 dB
t.F. frequency 10.7 MHz

Output voltage $2 \times 150 \mathrm{mV}$ R.M.S
Aerial tmpedance 75 Ohms
Indicators Mains on: Stereo on : tunıng indicator
Operating voltage 25-30VDC
Size $3.6 \times 1.6 \times 8.15$ inches : $91.5 \times 40 \times 207 \mathrm{~mm}$


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GUARANTEE If within 3 months of purchasing Project 60 modules directly from us, you are dissatisfied with them, we will refund your monev at once. Each module is guaranteed to work perfectly and should any defect arise in normal use we will service it at once and without any cost to you whatsoever provided that it is returned to us within 2 years of the purchase date. There will be a small charge for service thereafter. No charge for postage by surface mail, Air-mail Charged at cost.
—n $\quad$ for which / enclose cash/cheque money
To: Sinclair Radionics Ltd., 22 Newmarket Road, Cambridge


## Sinclair IC-10



## the world's most advanced high fidelity amplifier

Specifications
Output: 10 Watts peak, 5 Watts R.M.S. continuous
Frequency response: -5 Hz to $100 \mathrm{KHz} \pm 1 \mathrm{~dB}$ Total harmonic distortion: Less than $1 \%$ at full output.
Load impedance: 3 to 15 ohms.
Power gain: 110 dB (100.000,000,000 times)
total.
Supply voltage: $\quad 8$ to 18 volts.
Size: $\quad 1 \times 0.4 \times 0.2$ inches.
Sensitivity:
5 mV .
Input impedance: Adjustable externally up to
2.5 M ohms.

## Circuit Description

The first three transistors are used in the pre-amp and the remaining 10 in the power amplifier. Class $A B$ output is used with closely controlled quiescent current which is independent of temperature. Generous negative feedback is used round both sections and the amplifier is completely free from crossover distortion at all supply voltages, making battery operation eminently satisfactory.

## Applications

Each IC-10 is sold with a very comprehensive manual giving circuit and wiring diagrams for a large number of applications in addition to high fidelity. These include stabilised power supplies, oscillators, etc. The pre-amp section can be used as an R.F. or I.F. amplifier without any additional transistors.

The Sinclair IC-10 is the world's first monolithic integrated circuit high fidelity power amplifier and pre-amplifier. The circuit itself, a chip of silicon only a twentieth of an inch square by one hundredth of an inch thick, has 5 watts R.M.S. output ( 10 W . peak). It contains 13 transistors (including two power types), 2 diodes, 1 zener diode and 18 resistors, formed simultaneously in the silicon by a series of diffusions. The chip is encapsulated in a solid plastic package which holds the metal heat sink and connecting pins. This exciting device is not only more rugged and reliable than any previous amplifier, it also has considerable performance advantages. The most important are complete freedom from thermal runaway due to the close thermal coupling between the output transistors and the bias diodes and very low level of distortion.
The IC-10 is primarily intended as a full performance high fidelity power and pre-amplifier, for which application it only requires the addition of such components as tone and volume controls and a battery or mains power supply. However, it is so designed that it may be used simply in many other applications including car radios, electronic organs, servo amplifiers (it is d.c. coupled throughout). etc. Once proven, the circuits can be produced with complete uniformity which enables us to give a full guarantee on every $1 \mathrm{C}-10$. knowing that every unit will work as perfectly as the original and do so for a lifetime.

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## Q. 16 High fidelity loudspeaker

Developed out of the revolutionary and much praised design of the original Sinclair Q. 14 comes this more advanced version to meet the requirements of even greater numbers of high fidelity enthusiasts. The 0.16 employs the same well proven acoustic principles in which a special driver assembly is meticulously matched to the physical characteristics of the uniquely designed housing. In reviewing this exclusive Sinclair design, technical journals have been loud in their praise for it and it comfortably stands comparison with very much more expensive loudspeakers. The shape of the 0.16 enables it to be positioned and matched to its environment to much better effect than is the case with conventionally styled enclosures, and with its improved styling. the 0.16 presents an entirely new and attractive appearance. A solid teak surround is used with a special all-over cellular black foam front chosen as much for its appearance as for its ability to pass all audio frequencies unimpaired.
The 0.16 is compact and slim and is the ideal shelf-mounted speaker, and brings genuine high fidelity within reach of every music lover.

## Specifications

Construction:

Loading
Input impedance:
Frequency response
Driver unit:

Size and styling:

Price:

## Micromatic Britain's smallest radio

Considerably smaller than an ordinary box of matches, this is a multi-stage A.M. receiver meticulously designed to provide remarkable standards of selectivity. fower and quality. Powerful A.G.C. is incorporated to counteract fading from distant stations: bandspread at higher frequencies makes reception of Radio 1 easy at all times. Vernier type tuning plus the directional properties of the self-contained special ferrite rod aerial makes station separation very much easier than with many larger sets. The plug-in high fidelity type magnetic earpiece which matches exactly with the output of the Micromatic provides wonderful standards of reproduction both for speech and for music. Everything including the batteries is contained within the attractively designed case. Whether you build your Micromatic or buy it ready built and tested, you will find it as easy to take with you as your wristwatch, and dependable under the severest listening conditions.

## Specifications

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Earpiece:
Battery requirements:
Case:
Controls:
Price:
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## SEENG WTTH SOUND

Almost two whole years have passed since fog was last discussed on this page. More specifically, we then aired our hopes for an electronic system that would, to some degree, restore visibility to drivers of motor vehicles in bad weather conditions.

Two years is a long time in electronics, and one could quite reasonably expect important steps to have been taken by now toward the solution of this serious problem. But nothing concrete has.emerged so far from industry or from government research establishments.

We do know that much effort has been put into the development of a radar system for vehicles operating inside airports. There is a vital need for emergency service vehicles to be able to move at speed to the scene of an aircraft mishap, no matter what state of visibility obtains. This particular radar system has not yet been put into production, presumably because the airport authorities have not yet been decided to buy.

By normal standards, a radar system is necessarily expensive and involves much sophisticated engineering. It is true that solid state devices such as Gunn-effect diodes are available for microwave operation, and these help to simplify the receiver and transmitter design and so reduce the overall bulk of the installation. But there still remains a major problem in the form of the display device. Until the c.r.t. is superceded by a solid state electro-luminescent panel, there is little likelihood that radar, even in an elementary form, can be seriously considered as a feasible proposition for installation on all types of road vehicles.

While waiting for further advances in technology to solve these engineering (and cost) problems, there is no reason why private individuals should not on their own account investigate other methods to combat the common enemy, fog.

Leaving aside electro-magnetic waves, why not explore the possibility of sound waves? The equipment needed for an acoustical system is not highly complicated, and the basic principles involved are familiar enough to most electronics enthusiasts. The Sonic Obstacle Locator described in this issue has proved itself to a marked degree. Admittedly, as a driving aid it is not infallible, and lacks, as yet, the ability to discriminate between tree, lamp post, or car. Serious limitations, in fact, for a car-borne device. But it is not offered as an instant solution to a formidable problem. It is offered as an idea worthy of further development by the experimentally inclined. And other uses may well become apparent.

We hope many will rise to the challenge. Amateurs have blazed the trail before. Most significantly, in proving the usefulness of the then neglected high frequency radio waves. Maybe private experimenters can achieve some noteworthy success in electro-acoustics.
F.E.B.

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Our February issue will be published on
Friday, January 15

[^1]ATMOSPHERIC pollution and weather conditions often combine to produce thick fogs which reduce visibility to as little as $10-20$ feet. Much the same applies where there is smoke or driving snow. In such conditions man's senses are virtually useless and there is a real need for technical aids to penetrate the murk.
Short range radar would seem to provide an answer to the problem, but the equipment is expensive and involves radio licensing complications. Fog is transparent to certain bands of infra-red, but infra-red emitters and sensors for use at such wavelengths are not, as yet, sufficiently developed. Another approach would be ultrasonics, but high frequency sound waves tend to be scattered by fog droplets, and have a short range.

## AUDIBLE SOUND

The Oil-bird of South America points the way to a possible solution to the problem; it finds its way around dark caves by emitting audible clicking noises, like the sound of castanets. Audible sound is, of course, only slightly attenuated by fog and other atmospheric particles, hence the use of the fog horn, and the explosive cap on railways.

Sonar has long been employed for navigation and ranging purposes underwater, and there seems to be no reason why audible sound pulses in air should not be similarly used if the equipment can be made to operate against a background of day-to-day ambient noise.

The Sonic Obstacle Locator is the result of experiments to test the practicability of using audible sound to detect the presence of unseen objects in fog. With a range of 100 feet, the equipment could be used on board small boats for docking in conditions of poor visibility, or as an outdoor, all weather intruder alarm. There are many other possible applications, such as counting passing cars on a roadway, and educational demonstrations involving the speed, refraction, and reflection of sound waves.

## SENSITIVITY

Although not recommended for use on cars in fog, the Sonic Obstacle Locator will work at low speeds, and was tested on a vehicle as this provided the most adverse conditions of high ambient noise.

To give an idea of sensitivity, a response is obtained from an adult pedestrian at up to 22 feet, a small parked car at 50 feet, or a stone wall perpendicular to


By D. Bollen

the sound source axis at 100 feet. When an obstacle in the road ahead is detected, the unit inside the car emits a bleep warning note, of duration directly proportional to distance.

The note varies from a short click at 95 feet range, to an almost continuous tone at the minimum range of 5 feet. A meter linearly calibrated 0-100 feet is incorporated to show the actual distance of the obstacle.

Because of sonic interference from the exhaust noise of other vehicles, particularly diesel lorries, the equipment will only operate reliably at traffic speeds below 30 m.p.h., under typical fog driving conditions.

## MEASURING DISTANCE THROUGH AIR WITH SOUND

High frequency sound is readily reflected from solid surfaces, and sound waves conform to the same general laws of reflection as light.

The time taken for a sound to travel to a reflecting surface and back again can be used as a measure of the intervening distance, with an accuracy dependent on slight changes in the speed of sound resulting mainly from variations of temperature and humidity.

Sound velocity is not significantly influenced by droplets or particles in the atmosphere, and is largely independent of air pressure and altitude when corrections are made for temperature.

The velocity of sound in dry air at 0 degrees centigrade is 1,087 feet per second; about $4 \frac{1}{2}$ times slower than sound in water. If the air temperature is raised to 20 degrees centigrade a velocity increase of about 3.7 per cent will be noted.

When the air is fully saturated with water vapour, this causes a velocity increase of about 0.5 per cent, because water vapour decreases the density of air.

From the above it can be estimated that, over a temperature range $\pm 10$ degrees centigrade, and in varying humidity, a sonic yardstick should offer a potential accuracy of about $\pm 2.5$ per cent.

## OPERATING FREQUENCY AND TRANSDUCERS

The frequency of the transmitted sound should be high, to combat interference from man-made noise, the bulk of which lies in the lower part of the audio spectrum. However, sonic transducers operating above 15 kHz are usually not sensitive enough for low power sound-through-air applications, although they may be suitable for service underwater where sound conduction


Fig. I. Area of response of the Sonic Obstacle Locator when used for pedestrian detection
is good. In the interests of economy, it was decided that special transducers should be avoided.

Tests were made with several types of loudspeaker and microphone, to establish a suitable compromise between cost and noise rejection when choosing the operating frequency. Firstly, the upper limit was set by microphones offering a useful output above 15 kHz , as they were too expensive to qualify.
It was discovered that good quality tweeters operating at $10-15 \mathrm{kHz}$ gave markedly inferior results when compared with small, cheap loudspeakers handling 10 kHz . This somewhat surprising result was eventually attributed to sound reinforcement arising from resonances $\mathrm{in}^{\text {the }}$ theap loudspeakers, and further improvements at 10 kHz were effected by cutting down the size of the loudspeaker cone, and stiffening it with cellulose dope. Obviously, if the sonic transmitter can be encouraged to provide a large acoustic output, this will tend to minimise noise, and compensate for the lower frequency.

When a loudspeaker is handling frequencies above a few kilohertz there will be only a small excursion of the voice coil and cone, and the limit to the amount of power the loudspeaker can handle is determined by voice coil overheating. If the high frequency input is pulsed on and off, with a relatively long pause between pulses, the loudspeaker can sustain very high levels of input without damage, and at the same time yield an output of high intensity.


Fig. 2. Block diagram of the Sonic Obstacle Locator. 10 kHz bursts of radiated acoustic energy are made to bounce off the obstacle. These are then ampliffed and the distance of the obstacle computed by the decoder for display by a meter. An audio bleep warning is also triggered at this final stage

## DIRECTIONAL CHARACTERISTIC

In using a locator, echoes should only be obtained from obstacles in the road ahead, and not from objects which do not lie in the path of the vehicle.

A loudspeaker offers a polar response, at high frequencies, which is directional, but is not sufficiently narrow in shape for the present application; the requirement taking the form of a broad sphere slightly flattened along the sound source axis. However, a microphone can be made to see only a narrow angle by placing it at the focus of a parabolic reflector which looks along the source axis.

When loudspeaker and microphone polar responses are combined, the result is a thin, almost ideal cigar shape extending in front of the vehicle, as depicted in Fig. 1.

The plot upon which Fig. 1 is based was obtained from a 5 gallon oil drum which simulates a pedestrian wearing normal, soft clothing.

It can be seen that the area of response is roughly the width of the car and extends forwards to a distance of 22 feet. For larger obstacles, the area of detection will be wider and longer, though still of the same cigar shape.

## SONIC TRANSMITTER

The basic features of the Sonic Obstacle Locator are outlined by the block diagram of Fig. 2, with associated waveforms in Fig. 3.

Dealing first with the transmitter, this consists of a pulse generator, a 10 kHz square wave oscillator, and a tuned power amplifier. The pulse generator provides a master pulse of 2 ms duration (Fig. 3A) at intervals of 178 ms , the time taken for sound to travel to the obstacle and back at the maximum range of 100 feet.

On receipt of the pulse, the 10 kHz oscillator provides an output of 2 ms duration, consisting of 20 cycles of a 10 kHz square wave, which is then passed to the power amplifier. Being tuned, the power amplifier converts the square wave into a sine wave, and the input to the loudspeaker is an envelope containing 20 cycles of a 10 kHz sine wave as seen in Fig. 3B.

To detect echoes from obstacles very close to the transmitter, the radiated sound must be of short duration, so that it dies away completely before the
echo arrives. A time of 2 ms for the acoustic burst from the transmitter is well inside the period fixed by the minimum range of 5 feet, to allow for various circuit delays and loudspeaker lag.

In passing, it is of interest to note that the short burst of 10 kHz sound from the transmitter is registered by the human ear merely as a loud click, with virtually no tone content.

## RECEIVER

If an untuned amplifier was used with a microphone to pick up the minute reflected signals from the transmitter, the echo pulses would be completely swamped by


Fig. 3. Waveforms associated with Fig. 2 and identiflable by the letter. Fig. 3D is the tuned amplifier gain characteristic


Fig. 4. Circuit diagram of the transmitter consisting of pulse generator, square wave oscillator and tuned power amplifier
broadband ambient noise, except in a very quiet environment. Therefore, the sonic receiver is tuned to the transmitter frequency. In the block diagram the receiver is seen to consist of a microphone, a tuned high gain amplifier, and an echo pulse decoder circuit.

A typical microphone response to the burst of sound from the transmitter is shown by waveform Fig. 3C. Note the presence of back-scatter, noise, and the echo pulses.

Because echoes from near objects are of greater amplitude than echoes from distant objects, rough surfaces and very small objects close to the loudspeaker and microphone tend to cause unwanted back-scattering of sound, and there is also direct pick-up of the unreflected burst from the loudspeaker.

Ideally, the receiver response should be proportional to distance; low for near objects and high for distant objects, so that an echo of the same amplitude is obtained from a given object at any distance with the range of the equipment. In practice, however, it is sufficient to ensure that the receiver is pulsed off while the acoustic burst is travelling over the first few feet of its journey, and then gain can be allowed to rise quickly to maximum for the reception of echoes.

## ELIMINATING BACK-SCATTER

Controlled by the gain delay pulse derived from the pulse generator, the tuned amplifier gain characteristic takes the form shown, on an exaggerated time-distance scale, in Fig. 3D. Gain is almost zero at the time of the acoustic burst, and remains low over the region of back-scatter, then rises quickly to a stable level for the remainder of the period.

The effect of gain delay is demonstrated by comparing the resulting tuned amplifier output Fig. 3E with the amplifier input Fig. 3C. Back-scatter has been virtually eliminated from the waveform, without influencing the echo pulses. Although the tuned amplifier is switched off during the transmitter burst, it is almost impossible to remove evidence of the burst from the amplifier output (Fig. 3E) because of electrical interaction between transmitter and receiver circuits, despite careful screening. However, the decoder circuit takes this into account.
The function of the decoder, which will be examined in greater detail later, is to measure the time interval between the start of the control pulse and reception of the first significant echo, while ignoring as far as possible noise, and secondary echoes.

Distance is computed by the decoder for display by a meter, and an audio bleep warning oscillator is also triggered to give an output from a crystal insert loudspeaker.

## TRANSMITTER CIRCUIT

The transmitter circuit of Fig. 4 was designed to feed a peak pulsed power of more than 20 watts, at 10 kHz , into a 3 ohms nominal impedance loudspeaker ( 12 ohms at 10 kHz ), using as few components as possible.

Looking first at the pulse generator of Fig. 4, Cl is positively charged through VR1, the unijunction transistor TR1 triggers into conduction, whereupon Cl discharges rapidly through R2 to produce a steep-sided positive pulse. The interval between pulses is set by adjustment of VR1, and the duration of the pulse is determined by the fixed values of C 1 and R 2 .

## COMPONENTS . . .

## SONIC TRANSMITTER

```
Resistors
\begin{tabular}{llll} 
R1 & \(560 \Omega\) & R5 & \(4.7 \mathrm{k} \Omega\) \\
R2 & \(180 \Omega\) & R6 & \(4.7 \mathrm{k} \Omega\) \\
R3 & \(330 \Omega\) & R7 & \(330 \Omega\) \\
R4 & \(220 \Omega\) & & \\
All \(10 \%\) & \(\frac{1}{2}\) watt carbon & &
\end{tabular}
```


## Capacitors

CI $4 \mu \mathrm{~F}$ electrolytic 50 V
C2 $0.01 \mu \mathrm{~F}$ miniature polyester 250 V
C3 $0.01 \mu \mathrm{~F}$ miniature polyester 250 V
C4 $4 \mu \mathrm{~F}$ electrolytic 50 V
C5* $4 \mu \mathrm{~F}$ metallised 150 V (see text)
C6 $1,000 \mu \mathrm{~F}$ electrolytic 50 V
Transistors
TRI 2N2646
TR2, TR3, TR4 2S512 or BSY27 (3 off)
TR5 ACl57 or ACl 27
TR6 BCY39 or 2N2904
TR7 OC28
Potentiometers
VRI $100 \mathrm{k} \Omega$ horizontal preset
VR2 $4.7 \mathrm{k} \Omega$ horizontal preset
Inductor
LI $\quad 2.5 \mathrm{mH}$ RF choke
Transformer
TI LAS pot core (see text)
Loudspeaker
LSI 3 ohm Sin dia. (see text)
Socket
SKI Coaxiai socket
Plug
PLI Coaxial plug
Miscellaneous
0. in matrix Veroboard 4.1 in $\times 1.7 \mathrm{in}$ $3 \mathrm{in} \times 1.8 \mathrm{in} 16$ s.w.g. sheet aluminium TO3 transistor mica washer and spacers 20 s.w.g. enamelled copper wire

Timing components R5, R6, C2 and C3, in Fig. 4, will cause the multivibrator circuit TR2 and TR3 to oscillate close to 10 kHz , but a fine frequency adjustment is provided by VR2 (tune transmitter).
The multivibrator will only oscillate when the, emitters of TR2 and TR3 are connected to the negative, rail by transistor switch TR4. In turn, TR4 is only switched on for a period of 2 ms when it receives the pulse from TR1, via R4.

Driven by the multivibrator output, the complementary pair TR 5 and TR6 alternately clamp C4 to positive and negative supply rails, and owing to the presence of tuned circuits L1, C4, and L3, C5, the switching waveform is converted into a sine wave by output transistor TR7.

Approximately 15 V r.m.s. is developed across the 12 ohm speaker load during the 2 ms pulse period.
Transformer T1 is hand wound on a. Vinkor assembly, and one end of the secondary winding L3 is taken to a separate earth connection to accommodate positive or negative earth battery supplies.

## REGENERATIVE RECEIVER

Receiver tuned amplifier and decoder circuits will be considered separately. Fig. 5 shows the tuned amplifier circuit.

Looking first at the very important stage TR9 in Fig. 5, it can be seen that this closely resembles a regenerative tuned stage in a t.r.f. radio receiver. Regeneration does away with the need for multiple tuned stages and simplifies tuning adjustments, while still giving reasonable selectivity.

Winding L6 is phased with L7 to give positive feedback between collector and base of TR9, and thus multiplies the Q value of tuned circuit $\mathrm{L} 7, \mathrm{C} 12$, and TCl . Regeneration is smoothly controlled by the VR5 setting.

TR9 is operated at a low collector current, near the point where gain falls off as collector current is decreased. VR4 sets the working point by establishing the amount of positive bias on the base of TR9.

A positive going pulse from the pulse generator is converted by C9, D1, and D2 into a negative pulse which counteracts the bias on the base of TR9, thus reducing gain. Owing to the slow discharge of C9 through VR4, R11, and R12 at the termination of the pulse, TR9 remains off for a period much longer than the time of the control pulse. The gain delay period is adjustable by VR4, and back-scatter control VR3.

Transformer T3 is hand-wound on an LA2107 pot core assembly.

## EMITTER FOLLOWER

The signal from the microphone could be fed straight to the base of TR9, but this would demand a microphone transformer of non-standard impedance. Also, a long run of microphone cable could cause detuning. Therefore, emitter follower TR8 is included to serve as an impedance converter and buffer stage. Transformer T2 is of the type found in hand-held, moving coil stick microphones, and has an output impedance of 50 kilohms.

As the collector load of TR9 is a high Q tuned circuit; it will present a high impedance, with large voltage amplification, to a signal of 10 kHz , but side frequencies will see a low impedance and are hardly amplified at all. Overall rejection of the Fig. 5 circuit is better than 30 dB for signals 1 kHz above and below the centre frequency.

## TUNED AMPLIFIER

Triple stage amplifier TR10, TR11, and TR12, is designed to offer some selectivity, but not enough to cause problems with instability. Emitter and coupling capacitors cause some attenuation of frequencies below 10 kHz , and the response is sharpened still further by tuned collector load C16, and L8.

When viewed on an oscilloscope, the tuned amplifier output will appear similar to Fig. 3E when echo signals are present. If the oscilloscope is d.c. coupled, and can offer linear timebase sweeps at around $0 \cdot 2 \mathrm{~s}$, then distance can be read off the X axis to give the precise location of all echoes.

## DECODER

Having obtained satisfactory echo signals from the receiver tuned-amplifier, these must be converted into regular shaped pulses for timing purposes.

In the decoder circuit of Fig. 6, the monostable (TR13 and TR14), responds only to signals above a certain amplitude, determined by the VR7 setting, and thus rejects noise and small, spurious echoes.


Fig. 5. Circuit diagram of the receiver tuned amplifier


Fig. 6. Circuit diagram of decoder which converts the outpur from the tuned amplifier into regular shicped pulses for timing purposes

## RECEIVER DECODER

## Resistors

|  |  |  |  |
| :---: | :---: | :---: | :---: |
| F. 25 | 120k $\Omega$ | R32 | $10 \mathrm{k} \Omega$ |
| F. 26 | $10 \mathrm{k} \Omega$ | R33 | 3.9k! |
| F. 27 | $3.9 \mathrm{k} \Omega$ | R34 | 10kS |
| F. 28 | $8 \cdot 2 \mathrm{k} \Omega$ | R35 | 1.2 k ! |
| F. 29 | $5.6 \mathrm{k} \Omega$ | R36 | $12 \mathrm{k} \Omega$ |
| F. 30 | $10 \mathrm{k} \Omega$ | R37 | 2-2kg |
| F31 | $3.9 \mathrm{k} \Omega$ |  |  |

## Capacitors

C21 $0.1 \mu \mathrm{~F}$ moulded polyester 250 V
C22 $0.047 \mu \mathrm{~F}$ moulded polyester 250 V
C23 $0.022_{\mu} \mathrm{F}$ miriature polyester 250 V
$\mathrm{C} 24 \quad 1,000 \mu \mathrm{~F}$ electrolytic 12 V
C25 $0.022 \mu \mathrm{~F}$ miniature folyester 250 V
C26 $\quad 0.033 \mu \mathrm{~F}$ miniature folyester 250 V

Transformer
T4 LT44 trawistor driver transformer (Henry's Radio)

## Meter

MI 0-100 $A$ A MR.65P

## Loudspeaker

X2 lin- $\frac{1}{2}$ in diameter crystal microphone insert
Transistors
TRI3, TR.15, TR16 2S703 or 2 N929
TRI4, TF. 17 OC7I

## Diodes

D3, D4, D5, D6 OA6
Miscellaneous
0.1 in matrix Veroboard 3.9 in $\times 1.7$ in

## COMPONENTS...

| RECEIVER TUNED AMPLIFIER |  |  |  |
| :---: | :---: | :---: | :---: |
| Resisto |  |  | Trimmer Capacitor |
| RB | 330 R 2 R17 | $5.6 \mathrm{k} \Omega$ | TCI 3,000pF Buigin C.P. 7 |
| R9 | 270ks R18 | $39 \mathrm{k} \Omega$ |  |
| R10 | 47ks R19 | $6.8 \mathrm{k} \Omega$ | Inductor |
| RII | $120 \mathrm{k} \Omega \quad \mathrm{R} 20$ | $1 \mathrm{k} \Omega$ | L8 10 mH RF choke |
| R12 | $120 \mathrm{k} \Omega$ R21 | $12 \mathrm{k} \Omega$ |  |
| R13 | 330k $\Omega$ R22 | 2.7kS |  |
| R14 | $100 \mathrm{k} \Omega \quad \mathrm{R} 23$ | 2-2k $\Omega$ | Transformers |
| R15 | 47k $\Omega$ R24 | $560 \Omega$ | T2 50k $\Omega$ moving coil microphone. matching |
| R16 | 22k $\Omega$ |  | transformer |
| All 10 | \%, $\frac{1}{2}$ watt carbon |  | T3 LA5 pot core (see text) |
| Potentiometers Transistors |  |  |  |
| VR3 | $600 \Omega$ panel mounting |  | TR8, TRIO, TRII, TRI2 25703 or 2 N929 |
| VR4 <br> VR5 | $200 \mathrm{k} \Omega$ sub-min horizo $25 \mathrm{k} \Omega$ panel mounting | tal mounting | TR9 2S512 or BSY27 |
| V25 | $25 \mathrm{k} \Omega$ panel mounting | - |  |
| V26 loks panel mounting - Diodes |  |  |  |
| All presets |  |  | Diodes |
|  |  |  | D1, D2 DA4: |
| Capacitors |  |  |  |
| 57 | $25 \mu \mathrm{~F}$ electrolytic 50 V |  |  |
| C3 | 680pF palystyrene 125 |  | PLI Coaxial plug |
| C) | $0.5 \mu \mathrm{~F}$ metallised 250 V |  | PLI Coaxial plug |
| clo | $0.0047 \mu \mathrm{~F}$ miniature po | ester 500 V |  |
| Cll | $0.25 \mu \mathrm{~F}$ metallised 250 V |  | Sceket |
| C12 | 2,000pF polystyrene 12 |  | SK2 Coaxial socket |
| C13 | 680pF palystyrene 125 |  |  |
| C14 | $0.022 \mu \mathrm{~F}$ miniature poly | ester 250 V |  |
| C15 | 0.14 F moulded polyest | er 250 V | XI DMI 07 or similar |
| C16 | $0.022 \mu \mathrm{~F}$ miniature poly | ester 250 V |  |
| C17 | $0.047 \mu \mathrm{~F}$ miniature poly | ester 250 V |  |
| C 18 | $0.1 \mu \mathrm{~F}$ mculded polyest | er 250 V | Miscellaneous |
| C19 | ${ }_{1} / \mathrm{F}$ miniature electroly | tic 15 V | 0.1 in matrix Verobcard 6.9 in $\times 1.6 \mathrm{in}$ |
| C30 | $1 \mu \mathrm{~F}$ miniature electrol | tic 15 V | $40 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. eniamalled copper wire |



Fig. 7. Waveform sequence appearing at decoder. (A) Typical input signal. (B) Response of monostable. (C) Output of bistable. (D) Audio oscillator output

Demodulated by D3, the leading edge of a signal envelope triggers TR13 on, which in turn switches TR14 on. With the collector of TR14 now close to the positive rail, TR13 remains biased on by the slow charge of C22 through R29, giving a rectangular pulse output from the collector of TR14, of length dependent on C22 and R 29 values.
Owing to the positive voltage present on the base of TR13, D3 is reversed biased, and blocks further signals until the monostable switches back into its stable state. The duration of the monostable output pulse determines the minimum distance which can be measured. If C22 is made smaller in value, a point will be reached where the monostable begins to deliver two pulses from each input signal.

## BISTABLE

Looking next at the bistable in Fig. 6; a pulse via R35 and D6 from the pulse generator, at the commencement of each acoustic burst, will switch TR 15 off and TR16 on. With the collector of TR15 almost at positive rail voltage, C24 will begin to charge via R31 and VR8. If no echo signals are present, C24 is allowed to become fully charged and the meter M1 will read full scale.

In the event of an echo being repeatedly received some time after each transmitter burst, however, the leading edge of a pulse from the monostable will switch TR 15 on and TR16 off, thus interrupting the slow charge of C24, and giving a meter reading which is directly proportional to the distance of the nearest echo producing obstacle.

## DECODER WAVEFORMS

The decoder waveforms of Fig. 7 show clearly the sequence of events. Fig. 7A is a typical input signal to the decoder, consisting of an acoustic burst waveform followed by four sizeable echo waveforms spaced in time. Fig. 7B gives the response of the monostable to the Fig. 7A signals, with a pulse for each signal envelope.

As the acoustic burst from the monostable, and the reset pulse from the pulse generator occur simultaneously, the bistable ignores the former, but will be


Fig. 8. Power supply circuit suitable for car battery or dry battery working. Battery impedance is minimised by large electrolytic

## COMPONENTS . . .

## POWER SUPPLY AND 8OX <br> Capacitor <br> C27 $5,000 \mu \mathrm{~F}$ I5V

Battery
BYI 12 V accumulator or two 996 dry batteries
Plug
PL3 Non-reversible 2 way
Switch
SI Toggle d.p.s.t.

## Socket

SK3 Non-reversible 2 way

## Miscellaneous

Universal chassis 6 in $\times 8$ in $\times 3$ in with extra 6in $\times 8$ in top plate and $8 \mathrm{in} \times 3$ in side (Home Radio)
set by the leading edge of the first echo pulse to be received, see Fig. 7C. The bistable can only respond again after it has been reset, and gives an output of duration exactly corresponding to the time taken for the sound to complete its journey.
The inverse occurs with the audio oscillator waveform Fig. 7C; in the event of no echo there is no output from TR17 but an echo from a close obstacle gives an audio oscillator output of long duration.

## POWER SUPPLY

There are two possible snags when the Obstacle Locator is powered from a car battery; interference and voltage fluctuations.

Charging circuit contacts, dynamo brushes, and , gnition spark gaps generate quite a lot of interference, which could increase the sonic receiver noise input to the point where maximum range suffers.

Although the situation could be improved by incorporating filters and some form of voltage stabilisation, the easiest and cheapest solution is to run the equipment from suitable dry batteries.

Average current consumption of the Obstacle Locator is in the region of 50 mA , so two 996 batteries coupled in series will give 100 hours operation.

A power supply circuit suitable for car battery or dry battery working is shown in Fig. 8. The large electrolytic C27 helps to minimise battery impedance.

Part two next month will cover the construction, testing, and calibration of the Sonic Obstacle Locator.

## SIREN <br>  <br> A SPECIAL PROJECT FOR BEGINNERS



Fig. I. Circuit diagram of the Siren; link wires are shown as thick lines. Note that an electrolytic capacitor should not be substituted for C3

## COMPONENTS . . .

| Resistors |
| :---: |
| RI $47 \mathrm{k} \Omega$ |
| R2 $47 \mathrm{k} \Omega$ |
| $\frac{1}{2}$ watt, $10{ }_{0}$ ocarbon |
| Capacitors |
| Cl $100 \mu \mathrm{Felect} 15 \mathrm{~V}$ |
| C2 $0.01 \mu \mathrm{~F}$ |
| C3 $2 \cdot 2 \mu \mathrm{~F}$ polyester |
| Transistors |
| TRI ZTX 300 |
| TR2 ZTX 500 |
| Switch |
| SI On/off press switch |
| Loudspeaker <br> LSI $3 \Omega 5$ inch |
| Miscellaneous <br> T-Dec, connecting wire |

With S1 closed, Cl charges on a relatively long time constant to a maximum of 4 V . A rising voltage across R2 and C2 means, in effect, a diminishing time constant, so the overall frequency starts to rise.

Just as the charge transition is smooth on C2, so is the frequency change which starts at zero and glides up to about 2 kHz . The output at LS1 remains constant at 4 V during this period, the waveshape produced being shown in Fig. 2.



The release of S 1 causes the frequency to fall. Since C 1 is charged to about half the rail volts, the discharge period is reduced by this factor.

## INCREASING THE OUTPUT

Without the inclusion of C3 the power available at the output would be negligible. This capacitor has the effect of increasing the pulse width and so raising the mean d.c. level.
It is most important that a capacitor with a low
power factor be used here; an electrolytic type just will not do.
To alter the frequency of the siren C2 should be changed. An increase in capacitance will mean a reduction in peak frequency and a decrease will raise it.

## ALTERNATIVE CONSTRUCTION

A permanent assembly of this unit can be made on Veroboard. Component mounting can be identical to that given for the T-Dec as the hole matrix for the board is identical.

## NEWS BRIEFS



## Soults Communications

The centre of the 3rd Bracknell Scouts was turned into a complete communications centre for a weekend in October by equipment loans from the nearby Racal Electronics Group. The Racal Amateur Radio Club GB3RAC, also loaned the Scouts equipment and the members assisted operations for day and night watches.

The centre had four complete transmitting/receiving stations, five separate monitoring stations manned by Scouts, a news and weather bureau with all necessary sound and TV monitoring receivers, teleprinter, and Mufax picture receiver.

Pictures of weather charts included one from Khabarovsk in Eastern Siberia. Other weather information was intercepted from weather ships in the Atlantic.

Jamboree contacts were made with numerous Scout Groups throughout the world, including Australia, $S$. Africa, U.S.A., Middle East and Europe.

Over 200 Scout Leaders and Scouts shared watch duties throughout the weekend and there werc over 200 spectators. The photograph left shows part of the communications centre operating.


## A DECADE AHEAD

As 1971 opens it is perhaps worthwhile to look at the next decade as planned at this moment. Certain programmes have already been put in hand and these have been covered by previous Spacewatch articles. However, in order to take an overall look at the next decade some of the programmes will of necessity be noted again.

One of the most important items in the programme is to find ways of reducing the cost of space operations. For this reason the development of the space shuttle will be a priority venture. The prospect of a re-usable shuttle vehicle which can travel between the Earth and space stations in Earth orbit offers a system that is within the range of present technology. Already the design stage has reached a definitive study. The vehicle in its present form will be launched by a booster from a rocket pad and return to Earth to land like an aircraft.

## SPACE TUGS

To this must be added the rather newer proposal for a space tug which would serve for goods and personnel $/$ transport from space station to space station. Other operations to be undertaken would be the raising of satellites from lower orbits to higher and synchronous orbits and the firing of automated probes from Earth orbit into orbits that will enable a further investigation of the planets. The tug would ride in the shuttle cargo compartment. Already these programmes envisage international participation in a greatly extended manner.

In the past decade there has been considerable co-operation so far as contractors were concerned, but the new thinking is more in the terms of a cost sharing as well as contracting between the U.S.A., the United Kingdom, Federal Republic of Germany, France, Belgium and Italy. This could mean that the European Nations will provide half of the cost of the space tug (about 1,000 million dollars). The cost of the space programme for the shuttle is estimated at about 4,500 million dollars, and for the 12 man space station
upwards of 4,000 million dollars. There is to be a standardisation of technique, and both vehicles should be operative by 1978.

## SKYLAB

This will be a large orbiting workshop which will examine the capability of men to live and work in space over long periods. It will use systems already developed for the Apollo missions. Later, there will be even more sophisticated vehicles with the exchange of personnel on varying tours of duty.

## EARTH RESOURCES SATELLITES

One of the major projects already well under way is that of surveying the earth to assess its potential for the future. This programme will have an unprecedented facility for the study of crops, the location of new sources of water and the prospecting for mineral deposits. There will be the more extended use of space vehicles in the fields of weather forecasting, communications, air traffic control, geodosy map making and navigation. There will also be concentration on the use of television for. education particularly directed toward the under-developed countries.

The Apollo programme, although curtailed somewhat from the original one, will nevertheless offer an exciting new extension of moon exploration. Apollo 14 site will be the same as that which was originally scheduled for Apollo 13, that is the Fra Mauro rugged region. The site for Apollo 15 has now been decided; it will land in the lunar plain much further North than any previous mission. There is an area known as the Hadley-Apennine region which has the $8,000 \mathrm{ft}$. Apennine range on one side and the 60 mille long meandering canyon, 600 ft . deep, known as the Hadley Rille, on the other. It is an important region and will yield a great deal of valuable scientific data.

The astronauts will be able to gather material from the base of the mountains, some of which may date from the old lunar crust, which existed more than 4,000 million years ago. It is thought this may be older
than the Mare Imbrium basin which was probably formed by meteoritic impact. There may also be materials, thrown out by this cataclysmic impact, which originated deep within the moon.

On this mission the Lunar Rover vehicle, electrically powered, will be in operation for the first time. The crew will be able to go some miles from the base of operations in order to carry out experiments, and set up scientific instruments. The crew will be David Scott, Alfred Warden and James Irwin. Launch date is likely to be July 1971. The following two Apollo missions (16 and 17) are scheduled for January 1972 and June 1972; no sites or crews have been decided at the moment.

## MARS PROBES

These have already been noted in some detail in Spacewatch for June 1970 and this programme gets under way with the first launches in 1971.

## GRAND TOURS

These will take advantage of the position of the planets in the solar system during the next few years. In the case of the first mission the next possible time for this to be done will not come again for 200 years and the second planned mission could not be undertaken for another 100 years. Therefore, these programmes are of great importance, for to achieve the objectives of these missions with our present technology, the gravitational effects of the planets will be needed for an effective gain in momentum for the vehicles as they move from one area to another. These will of course be unmanned. The manned tours will need nuclear or some other source of power for this to be successful. The testing of nuclear powered rockets will be made in 1978 with unmanned craft.

The Mariner Venus-Mercury tour will begin the series and for the first time man may be able to see a closeup of the surface of Mercury. The next will begin in 1977 with one or perhaps two unmanned craft launched toward Jupiter.

When the spacecraft arrives in the vicinity of Jupiter it will swing round the planet under the influence of the local gravitational field; four years later it will reach the planet Uranus where again gravity will influence the path to project it toward the end of its journey five years later at the planet Neptune in 1988 after a 3,000 million mile trip.

A further launch will be in 1979. The route will on this mission be more direct to Jupiter and by 1980 will swing round Saturn and will head for Pluto which it should reach five years later. Pluto itself is the most distant planet from the Earth and will at that time be some 3,700 million miles distant.



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PART SEVEN-By R. W. COLES TTL FLIP-FLOPS

THe range of bistables, or flip-flops as they are usually termed in i.c. parlance, is more varied in the TTL family than in either DTL or RTL. There is, in fact, a flip-flop to suit every occasion, or for those who dislike using a different component for each specialised application, there is one flip-flop which may be used for any of the normal storage circuits.

Flip-flops are one of the most important elements in a logic system, all types having one feature in common. They provide storage of one binary digit for any length of time which may be necessary, as long as the power is applied.
The differences between the various types occur only in the way that the data to be stored is "written" into them, their outputs always consist of a true and an untrue indication of the digit they contain. By connecting them together in the appropriate fashion, it is possible to build many types of counters or dividers, shift registers, and memories, which may contain a complete binary word.

## DATA ENTRY

Data may be entered into a flip-flop either asynchronously, i.e. at any time, regardless of the original contents or the state of the clock pulse, or synchronously, i.e. the data is entered only when a clock pulse "enables" the data inputs.
The flip-flops in the TTL family usually have both synchronous and asynchronous inputs, which makes them most versatile in use. In fact, so many useful features have been packed into such a small package, that the discrete, specialised designs of ten years ago seen primitive in comparison.

Apart from the usual types of data entry mentioned above, TTL synchronous inputs can be further subdivided into either those which employ the positive edge of the clock pulse for gating (edge-triggered), or those which employ both edges of the clock pulse (master/slave).

## EDGE TRIGGERED TYPE

With the edge-triggered type, assuming that the data to be entered is waiting at the inputs, as the clock pulse starts to rise this data is entered into the storage latch, and is thus immediately present at the outputs. As soon as the clock pulse has passed the threshold of the input gates, the input data is locked out, and can have no effect on the stored information until the clock rises again.

## MASTER/SLAVE

The master/slave type is a little more complex in operation, as the individual flip-flops of this type really consist of two interconnected bistable sections, termed the master section and the slave section. Information enters the master section as the clock pulse rises, but this information does not appear at the outputs immediately because of the interposition of the slave section.
The slave bistable is also clocked, but through an inverter. The slave sees a positive clock pulse when the main clock pulse is actually falling, and enters the data present at the output of the master latch, and transfers it immediately to its outputs, which are also the outputs of the flip-flop as a whole.

Ignoring what goes on inside such a flip-flop and looking at it just as a "black box", information enters on the positive excursion of the clock, and appears at the outputs on the negative excursion. A simplified logic diagram of this type is shown in Fig. 7.1, together with a diagram of clock action.

Master/slave JK flip-flops form the backbone of TTL counting circuits, giving maximum versatility at the expense of a slightly higher power dissipation and slightly lower maximum toggle frequency than the edgetriggered type.


Fig. 7.1. Master/slave flip-flop. This is a simplifled explanatory arrangement, using the master/slave principle. As it stands, it is a set/reset type, but $J K$ operation is simply achieved by connecting the $Q$ output to the reset input gate, and the $\bar{Q}$ output to the set input gate. No preset or clear inputs are shown

A few examples of the many flip-flops available of both edge-triggered and master/slave type are given with their package outlines in Fig. 7.2. Note that some JK types have several J and K inputs, so that gated inputs for synchronous counters will not require separate gate packages.
Some of the flip-flops have no preset input-only clear. These are useful for counter circuits, where the asynchronous inputs are not needed other than to set up the all zeros condition before a count sequence.

## TYPE-D FLIP-FLOP

There is one "odd-ball" which is a little different from the rest-the 7474 type " $D$ " flip-flop. This one is intended specifically for shift-register applications, and belongs to the edge-triggered group.
It embodies full asynchronous input capability, but has only one data input D . The other data input is obtained internally by inverting the information on the D input. This means that only one connection is necessary to transfer data from one stage of a shift register to the next.
Perhaps this is not so obvious an advantage until you consider that many shift registers require inputs from several sources, in which case the external gating is cut by half when using the type D.
Although it is intended only for register use, it may be used in ripple counters by connecting the $\bar{Q}$ output back to the D input, but this is by no means the best choice of flip-flop for such use.

## TTL COUNTERS

Having discussed what types of flip-flop are available, it is possible to have a look at some applications. To start, let us examine counting circuits. Simple ripple counters were described in the sections on RTL and DTL, and should help here to understand the principles involved in TTL.

## RIPPLE COUNTER

Ripple counters are the simplest type of design using TTL i.c.s; all that is necessary is to connect the Q output of the first flip-flop to the clock input of the second, and so on, the input being connected to the clock of the first flip-flop in the chain.
The maximum number of separate states is given by $2^{n}$, where $n$ is the number of flip-flops so connected. Thus a four-stage counter has sixteen states, and a five-stage counter has thirty-two states.
If a count which is not a binary multiple is required, it is necessary to force the counter to skip some of its usual states. There are several ways to achieve this, the simplest method being to detect the desired final count +1 with a NAND gate, and use its output to reset all the flip-flops in the counter to zero.
This method may, however, result in a "race" condition if one of the counter stages resets more readily than the others, causing the NAND gate output to disappear before all stages are reset. For this reason it is not commonly used, although it is possible to include some sort of delay, such as a monostable, in the reset path to ensure that the reset pulse will last long enough to do its job properly.

## IMPROVED DIVIDER

A much better method of dividing by a number which is not a binary multiple is to feed the flip-flop outputs which are in the 1 state at the desired final count, along with the counter input, or clock, to a NAND gate, and use the resultant output to set all the counter stages to 1 .


Fig. 7.2 Logic circuits and pin connections of TTL fip-fops


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Fig. 7.3. Improved divider witt truth table


Fig. 7.4 Effect of compounded propagation delay

When the final clock pulse falls (assuming JK master/slave flip-flops), the counter will count to the all zeros state, ready to begin another cycle. The appearance of the all 1 state during the final clock pulse is unlikely to cause a problem, although this depends on the application.
To see this system operating, take as an example a divide-by-five counter using type 7472 JK flip-flops. The nearest power of 2 which is higher than 5 is $2^{3}$, so a three stage counter will be necessary as a basis of the design.

The final counter state will be binary four, 100 , because the first state of the counter will be zero, i.e. 000 . The only flip-flop which has an output of 1 at this stage is the third one, so we must feed this output together with the clock into a 2 -input nand gate, and connect the output of this gate to the preset inputs of the first two flip-flops.

The final circuit is shown in Fig. 7.3, together with a truth table and the clock pulse action. Note that the $J$ and $K$ inputs are not used in this application, and are therefore left disconnected, simulating a 1 input. They can, in practice, be connected to $+V_{\mathrm{cc}}$ to ensure this state.

## PROPAGATION DELAY

The counters we have considered so far have all been ripple types, and, as such, they all suffer from a drawback which can be serious in some applications.

If we look closely at such a counter which is in the all 1 state, it will be required to count to the all zeros state on the arrival of the next clock pulse. The final flip-flop in the chain will not be able to do this until all the stages preceding it have already done so; after all, it gets its own clock edge from the flip-flop immediately before it, which in turn gets its clock edge from the stage before it, and so on.

This all sounds quite straightforward, until we consider the propagation delay incurred from each stage, which in the case of the 7472 , is 20 ns . This seeningly insignificant delay is multiplied by the number of flip-flops there are in the counter, and may reach alarming proportions in a counter working at high speed and with a long cycle length.

Even so, it need not be a problem unless several of the counter outputs are gated together. Spikes appearing at the gate output at the wrong time may cause other circuits connected to the gate output to mistrigger. To illustrate the effect of this ripple delay, Fig. 7.4 shows the output waveforms obtained from a three stage counter, with the error due to the delay magnified.

To overcome this ripple delay error, a different kind of counter called a synchronous counter is used. With this type, the clock input is fed to every stage, not just the first. Input gating is used to determine the conditions when a flip-flop is required to "toggle", or change state.

Next month's article will continue with synchronous counters, shift registers, and ring counters using TTL flip-flops.

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## WASH-WHPE

## An electronic device for intermittent wiping and automatic wiper operation with washers, on cars with 12 volt field coil wiper motors.

AFaCILITY not provided as standard on older cars but which proves to be very useful in the British climate is that of intermittent operation of windscreen wipers. Another useful facility is windscreen wiping initiated by operation of the windscreen washer control and continuing for a limited period after its release. Both facilities are provided by the circuit to be described which can be added to the existing circuits in the car without impairing the normal operation of the standard controls.

The circuit takes advantage of the windscreen selfparking mechanism fitted to most cars; reference to back numbers of practical motoring magazines will show that this mechanism can be fitted to almost any vehicle very easily at little cost. The circuit is also basically designed for negative earth connection but a modified version to suit positive earth connection is given. The unit cannot be operated with the latest permanent magnet wiper motors, or in conjunction with 6 volt systems.

## CIRCUIT DESCRIPTION

The circuit diagram for negative earth connection is given in Fig. 1 and the mode of operation is as follows.

The thyristor SCRI is connected in parallel with the manual switch controlling the windscreen wipers (S4) and when triggered will initiate wiper action. A small amount of wiper action closes the selt parking switch S3, short-circuiting SCR1 and turning it off; the wipers continue under the control of the self parking switch to the end of a single sweep. If SCR1 is not in a triggered state at this time, no further wiping action will take place until SCR1 is again triggered or the wiper switch (S4) is closed.

The function of diode D2 is to bypass the inductive surge current generated when the motor is switched off. The necessary trigger pulses for SCRI are provided by the unijunction oscillator TR2 at intervals determined by the time constant (VR1 + VR2 + R5)C2. The function of R5 will be described later. VR2 is a preset potentiometer which determines the fastest intermittent rate and VRI is the rate control which can be omitted if fixed rate operation only is required, in which case VR2 determines the rate. Switch S2 is the on-off control and is incorporated in VRI.

With the component values given, the sweep rate can be adjusted to cover a range from one sweep every 1.5 seconds to one sweep every 15 seconds. Resistor


Fig. 1. Circuit diagram of the negative earth wash-and-wipe control

## CONTROL or susemen

R8 and capacitor C3 form a filter which prevents voltage disturbances on the battery line from producing false trigger pulses. If R5 is omitted together with that part of the circuit to the left of the chain dotted line (the components on tagboard A), the circuit as described will provide the intermittent sweep facility alone.

## LIMITED CONTINUOUS OPERATION

The limited continuous wiper operation is obtained in the following way. In the quiescent state, with SI open, point A (Fig. 1) is approximately at the same potential as the junction of R1 and R2 and, since this is less than $\eta V_{\mathrm{b}}$, the unijunction oscillator will not function. When S 1 is closed C 1 is charged rapidly through D1 and R3 and by emitter follower action in TR1 point A is raised above $\eta V_{\mathrm{b}}$, allowing the unijunction oscillator to function at a rate determined by the time constant $R_{5} C_{2}$. (Where $\eta V_{\mathrm{b}}$ is the intrinsic stand-off ratio.)

If the resistor R 5 has the minimum value consistent with reliable operation, a high repetition rate is obtained so that there is minimal delay at the end of each wiping stroke before SCR1 is re-triggered. When S1 is opened C1 discharges slowly through R3 in series with the parallel combination of R1 and R2 towards a potential $R_{2} V_{10} /\left(R_{1}+R_{2}\right)$ and the potential of point A follows. When this falls below $V_{\mathrm{b}}$ the unijunction oscillator again ceases to function.

The effective time constant of the component values suggested allows three or four sweeps after the opening of S , depending upon the speed of the windscreen wiper motor. The elaborate charge-discharge circuit
is incorporated to prevent an unwanted sweep occuring when power is first applied. It will be noted that the limited continuous wiping facility is obtained irrespective of the state of S2. If the latter is closed, intermittent operation automatically follows the end of the continuous wiping period.

## COMPONENTS

The modified circuit for use in vehicles with positive earth connection is shown in Fig. 2. Two additional components are required to provide the necessary improvement in false trigger pulse suppression.
The choice of $n p n$ transistor TR1 and unijunction transistor TR2 are in no way critical, the particular types listed being those which were at hand. The thyristor SCR1 should have current rating at least equal to the running current of the windscreen wiper motor, typically 3 to 4 amps , although in the interests of reliability the thyristor should be of sufficiently high rating to handle the hot stall current, typically 7 to 8 amps.

Diode D2 should have a forward current rating equal to at least half of the normal running current. Other component values are selected to suit the particular devices used and to provide the desired timing intervals.

## CONSTRUCTIONAL DETAILS

The simplicity of the circuit means that any form of construction which is convenient may be used without difficulty. However, details of the form of construction used in the prototype are given here as a guide.


Fig. 2. Circuit diagram of the positive earth wash-and-wipe control


Fig. 3. Semiconductor connections for the components listed


Fig. 4. Generallayout of the tag boards


Fig. 5. Tag board B, for negative earth version


Fig. 6. Tag board A, for both versions

Resistors
RI $1.2 \mathrm{M} \Omega$
R2 $82 \mathrm{k} \Omega$
R3 $4.7 \mathrm{k} \Omega$
R4 ik $\Omega$
R5 $4.7 \mathrm{k} \Omega$
$\begin{array}{ll}\text { R6 } & 150 \Omega \\ \text { R7 } & 27 \Omega\end{array}$
R7 $27 \Omega$
R8 $100 \Omega$
All $\frac{1}{4} \mathrm{~W}, \pm 10 \%$ carbon
Potentiometers
VRI $500 \mathrm{k} \Omega$ with single pole on-off switch ( 52 )
VR2 $50 \mathrm{k} \Omega$ skeleton preset

## Capacitors

$\mathrm{CI} 150 \mu \mathrm{~F}$ elect. 15 V
C2 $25 \mu \mathrm{~F}$ elect. 15 V
C3 $25 \mu \mathrm{~F}$ elect. 15 V
C4 $2 \mu \mathrm{~F}$ paper (for positive earth version only)
Semiconductors
DI OA202
D2 ZR20
SCRI C20F thyristor
TRI 2N3704
TR2 2N1671 B unijunction

## Miscellaneous

LI IA T.V. suppressor choke
Control knob
Tag boards and aluminium for heat sink
6B.A. fixings


Fig. 7. Tag board C, replaces tag board 1 for pesitive earth version


Fig. 8. Sub-assembly for positive earth vertian

For the benefit of readers who are not familiar with the unijunction transistor and the thyristor, details of the electrical connections are shown in Fig. 3. Although these are typical, should other types be used the relevant manufacturer's information must be followed.

## NEGATIVE EARTH VERSION

The general layout for a negative earth system is shown in Fig. 4, SCR1 and D2 being fixed directly to the base plate which acts as a heat sink. Two tag boards are also attached to the base plate. Since the base plate is live to the circuit, it is enclosed in a small plastics box using nylon screws as mounting studs; this box can be fixed at a convenient point behind the car facia panel (the base plate must not be earthed to the car).

Detailed component layouts on the two tag boards are shown in Figs. 5 and 6. Although VR2 is shown on the circuit diagrams as a preset component, once its value has been found it can be replaced by a fixed resistor.

Two groups of leads are taken from the box, one group going to S1, S2 and VR1 mounted on the facia panel and the second passing through the bulkhead to the washer pump, the wiper motor and to the positive and negative battery connections.

## POSITIVE EARTH VERSION

Because the stud connections to SCR1 and D2 are not common when a positive earth system is required, a slightly different form of construction has to be adopted. The general layout of the base plate differs from that shown in Fig. 4 in that D2 is omitted and one of the tag boards carries an additional component (L1) - Fig. 7 replacing Fig. 5.

Diode D2, together with the additional capacitor C4, is mounted as a separate assembly in a small insulated tube (see Fig. 8) fixed near the wiper motor. In all other respects the details of construction are similar.

## CONNECTION TO WIPER MOTOR

Examination of the electrical connections to a wiper motor already fitted with a self-parking switch will show two live leads and an earth return. Reference to the workshop manual, or a few minutes work with a voltmeter, will establish which of the live leads is connected to the battery and which is the switch lead. The lead from the base plate (i.e. the common connection of the thyristor and diode anodes) is connected to the switch lead terminal. It is stressed that no modification to the wiper motor is required unless the motor does not have the self-parking facility.

In the case of wiper motors not fitted with a selfparking switch, the first step is to make the necessary modifications. When the modifications have been completed the connection can be made as previously described.

## WASHER PUMP SWITCH

An electric washer pump switch is sometimes fitted in cars but is normally connected between the pump and earth. If this is so it is a simple matter to reconnect is as shown in the circuit diagram (Fig. 1).
Although the control for manual and vacuum washer pumps is not electrical, it should be possible to fit a microswitch, actuated by the normal control, in place of the washer switch. Other methods of switching may well be possible and no doubt some readers will add this facility to non-electric washers.


This electronic control system can be added to any tape recorder to control an automatic slide projector in sequence with a recorded sound track. It has been designed so that no modifications are necessary to the tape recorder or the

## SUBMARIME CHASER

A follow-up "war" game to Operation Seasearch (December 70). This game uses the same chassis and much of the wiring of Operation Seasearch but introduces depth. In Submarine Chaser, "Asdic" is used to narrow the search and the destroyer must adjust his depth charges correctly to destroy the submarine.


FEBRUARY ISSUE -ON SALE JANUARY 15-

# $\sqrt{1045}$ Iupt дicioname 

By C.W. Smedley c.Eng., MI.E.E.

Southall College of Technology

TAPE recording has been covered by many text books, and the general principles are well known. The explanations given of bias, however, tend to be vague, and those who would like to pursue the matter are often deterred by the $B / H$ loop, with its associated mathematics, implied if not stated. This article is intended to be a reasonably valid description avoiding the use of complicated analysis.
The object is to record signals, which can be audio, video, digital, etc. on a tape which is either coated or impregnated with magnetic material, and the system described here will be longitudinal.

## THE MAGNETIC CIRCUIT

The recording/playback head consists of a winding on a ferromagnetic core, and current fed to the head will produce a magnetic flux. Fig. I shows the relationship between flux, $B$, and the magnetising force $H$, which is proportional to ampere-turns. If the current is increased slowly from zero, the value of flux can be plotted against the value of $H$, and the line OA will be traced.
At some point, A on our diagram, the value of flux will remain constant for any further increase in $H$, and the core is said to be saturated. Prepresents this maximum value of flux and $\mathbf{S}$ shows the corresponding value of $H$. If.the current, i.e. $H$, is now reduced to
zero, the flux decays along the curve $A R$, and $R$ is the remanent value of the flux.

The ratio of OR to OP is called the retentivity of the core material. Some ferrites produce a curve in which OR and OP are practically equal; these are called square loop materials and are used in computer stores.

## HYSTERESIS LOOP

If the current is now increased in the reverse direction, at some point the flux will have fallen to zero, shown as point C. Further increase will bring the flux to point D , which is reverse saturation, identical in value with point A, but opposite in polarity. Reduction of current, towards zero, will reduce the flux, and the curve DQ will be traced out.
Note that this curve does not pass through point $\mathbf{O}$, but rises, as current is increased in the original direction, through E to A. It can be seen that, for any value of $H$, there are two possible values of $B$, and the perpendicular drawn at the point where $H$ equals X cuts the $B / H$ loop at points Y and Z . When $H$ is rising, the value of flux corresponding to Y is less than on the original trace, OA, whereas, when $H$ is falling, the value of flux corresponding to Z is higher. This lagging effect is called hysteresis, and the curve ACDEA is called the hysteresis loop.

## THE AIRGAP

If the magnetic core formed a complete ring there would be no flux outside the ring, and a tape drawn across it would be unaffected. To produce flux outside the ring it is necessary to break the magnetic circuit, i.e. to have an airgap, and it will be at this point that the moving tape will be brought into contact with it.
The airgap will increase the reluctance of the magnetic circuit, and the flux for any given value of head current will be less than the value without an airgap. Another effect, however, offsets this apparent loss, and Fig. 2 has been drawn to illustrate it.
If we assume that the $B / H$ loop is very narrow, then let OA represent the loop with no airgap. OA' shows the loop when a small gap is introduced, $\mathrm{OA}^{\prime \prime}$ the loop for a bigger gap. The first trace, OA, shows extreme curvature near the ends, and would not, in fact, be linear at any point. $\mathrm{OA}^{\prime}$ is much more linear, although the slope is less, and OA" is better still, although the value of $H$ necessary is increasing with the size of the airgap. This is not too important, since the amplifier gain can be increased to compensate. For the sake of linearity, therefore, the gap should be as big as possible.


Fig. 1. B H curve or hysteresis loop
 magnetic core. The larger the gap, the better the linearity

But it can be shown that, for any given tape speed, the bigger the gap the poorer is the high frequency response of the system, and the two fundamental requirements appear to be in conflict. The practical solution is to make the actual recording/playback gap as small as possible, then to insert a second gap diametrically opposite to it.

## DIAMAGNETIC GAP

Although the term airgap remains in use, the actual gap must be filled, for two reasons. One, if left open it would collect magnetic material from the tape, to re-form a closed magnetic circuit. Two, the external flux would be low in value. The gap is closed by a material which is both hard, to resist wear, and diamagnetic. Brass is a good example, and is commonly used. The flux now "fringes" out across the gap to make recording more effective

The actual distribution, however, is not at all like the pattern across the open end of a horseshoe magnet. The brass insert acts as an obstruction to the flux and the tape itself closes the magnetic circuit. The flux can be said to enter and leave the tape at right angles to the surface of it, but all of the flux does not leave the core at the edge of the gap. It is spread each side of the gap to form a fringe which tails off within a short distance, and the electrical gap is therefore greater than the physical gap in the head. It is important to remember this when considering erasure.

## THE NEED FOR BIAS

The effect of recording a signal has been shown by superimposing it on the vertical axis of Fig. 2. The recorded pattern on the tape will show the curvature near to point O , and harmonic distortion has been introduced. To prevent this effect the working area should not include the extreme curvatures near zero and saturation.

Direct current bias can be used, for example sufficient current can be passed through the record head so that every tape element is, in the absence of a signal, magnetised in the same direction, to the same degree. Such a current would correspond to a value of $H$ somewhere between $S$ and $S^{\prime}$, so that the effective working point is M on the curve OA". The signal to be recorded is now shifted bodily to the right, so that it becomes symmetrical about the vertical drawn through M .

This bias system closely resembles that used for valves and transistors, where the transfer, or mutual, characteristics exhibit similar curvature at cutoff and bottoming.


Although this d.c. system is used on cheap tape recorders, there are two major limitations. One, the peak to peak signal amplitude must be only half the value of that when no bias is used. This can be partially overcome by shifting the d.c. bias point, but the benefits are marginal and still include some distortion. Second, every particle on the tape is magnetised and, on playback, will make a contribution to the output voltage in the form of noise, mainly high frequency (hiss). For these reasons supersonic (a.c.) bias is now almost universal.

## HIGH FREQUENCY BIAS

The frequency chosen must meet two requirements: it must be high enough to be filtered out on playback without attenuating the wanted signal and, in general, should not be less than three times, preferably five times, the highest frequency to be recorded. At the same time, it must not be too high because the record head is highly inductive. The practical value lies between 50 and 75 kHz and a sinewave form is essential to prevent head magnetisation by second harmonic. LC oscillators of the Hartley or Colpitt type are popular, and the stage often "doubles" as audio output on playback

To understand the full effect of a.c. bias, two points should be kept in mind. One, the gap includes the fringe each side of it, and the actual flux entering the tape from the head is almost perpendicular to the surface. Two, the flux is alternating at bias frequency, and the actual value affecting each tape element will be the instantaneous value as that element leaves the fringe. Each tape element, or particle, is therefore exposed to a flux which can have any value from zero to maximum, of either polarity, but the resultant magnetic pattern on the tape is not sinusoidal.

Fig. 3a will show this more clearly, and the applied flux, $H$, is shown to have sine wave form. Point A on the sine wave corresponds to saturation, i.e. point $A$ on the $B / H$ curve, and any tape element exposed to this value of flux will have a remanent magnetisation equal to point R when it leaves the fringe. Following elements will be exposed to a flux decaying towards point R but all will have the same remanent value as point A.

## REMANENT FLUX

Fig. 3b shows the remanent flux plotted against the sine wave producing it, and it can be seen that the waveform written on to the tape has a flat top from point $A$ to point $R$. From $R$ to $B$ the applied flux is increasing in the reverse direction, but the remanent flux does not fall to zero until the applied flux has reached a point between $T$ and $B$. At point $B$ reverse saturation is reached and from $B$ to $Q$ the remanent flux again shows a flat top. In the absence of any other signal the bias continues to write this pattern.

If the tape is now played back at the same speed the pattern will induce small voltage pulses in the head at bias frequency, but since the pulses are symmetrical about zero, simple integration (to be discussed later) will virtually eliminate them. One thing the bias has established is the maximum wavelength of each pattern on the tape. Larger groupings, which would give rise to low frequency noise if exposed to direct current bias, have been restricted to this wavelength.

It should be noted at this stage that, although the terms amplitude and frequency are used for signal and bias currents, they should not be used when referring to the recorded tape. The pattern on the tape has intensity and wavelength.

## SATURATION BIAS

If a low frequency signal, for example audio, is now fed to the head at the same time as bias the pattern on the tape will be modified. Fig. 4 shows the effect, in terms of $H$ and $B$. The l.f. signal shifts the bias waveform progressively, in this case, to the right. The bias current remains the same in amplitude but now "rides" on the signal. The value of " $H$ ", in the right hand direction, increases to well beyond saturation, but the remanence pattern cannot increase with it, as it already corresponds to the saturation value.

However, due to the shift, much more than a half period, in terms of bias, is spent at and beyond saturation, and the pattern on the tape covers a greater distance. On the left hand side, not only is " $H$ " reduced in value, but less than half a period is spent in this region. The pattern on the tape therefore shows a reduction in intensity and in length. The overall bias wavelength has not changed, but the pattern distribution has, and any pulses produced by playback will not be equal and opposite. Integration will show a progressive change in one direction, the value depending upon signal amplitude and the direction upon signal phase.
show the scope for improvement. Assuming a bias frequency of 70 kHz and a tape speed of $7 \mathrm{in} / \mathrm{sec}$ the recorded wavelength will be one tenth of a thou ( 0.000 lin ) the pattern representing each flat topped pulse will therefore be one tenth of that. Since the process produces a serics of "bar magnets" along the length of the tape, the smallest unit will be the dust particle of the coating, and this will be in the region of one micron, which is about one twenty-fifth of a thou.

The flat top cannot be considered as any more than one particle, and it remains flat with or without a signal. If the dust particles were made smaller, and/or the tape speed increased the recorded length of the flat top could cover many particles and any change of signal level during that passage would modify the top. The slope of the signal would then appear superimposed on the flat top.

## PLAYBACK

The surface induction (remanence pattern) on the tape has been shown to be a distorted version of the bias waveform. At very short wavelengths there is a

\title{



Fig. 5. Correct application of bias magnetises the tape by a series of pulses which are proportional to the opplied audio signal

Fig. 4. The effect of applying an audio signal ot the same time as the bias signol, producing a soturated magnetisation of tape particles

(a)

(b)

Fig. 6. Fiter networks used on playback to eliminate the high frequency bias pulses. A simple CR network is shown in (o) and on LR network using the tope head inductonce in (b)

## DISADVANTAGES

This system uses saturation bias and has disadvantages. One, the high value of bias tends to demagnetise the pattern recorded, leading to a loss of high frequency response; two, the limiting action described still gives distortion. The optimum value of bias is about half the saturation value, and the remanence pattern on the tape will be lower in intensity. The flat tops will still be present exactly as before, but, when a signal appears, both polarities will be affected, i.e. one half will be increased, the other decreased.

Integration will produce an output whenever the remanence pattern is unbalanced by a signal, but, for any given value of signal, the output will be greater than when using saturation bias because both polarities change in length and intensity.

## SCOPE FOR IMPROVEMENT

The system so far described is the typical domestic sound recording, and a few practical details will help to
strong demagnetising effect, and the actual value measurable would be less than expected. The presence of the bias has, however, made the signal effective over the linear part of the characteristic and this is its true function. Fig. 5 shows the effect of a signal on the pattern, the applied signal represented by the dotted line. Each half cycle of signal now appears as a group of pulses.

When the tape is passed over the gap this pattern will produce voltage pulses in the head circuit and the next process can be considered as filtering. Fig. 6a shows a CR network which will act as a low pass filter, suppressing the high frequency pulses, but producing an output proportional to their amplitude/duration. This action is integration, the pulses producing the charging current for the capacitor, the output signal being the voltage developed across it.

Fig. 6b, although less well known, is also an integrating circuit, having the same effect, but the inductance is that of the playback head, usually 0.5 to 1.0 henries.

## ERASURE

The advantage of tape is that it can be used many times, and, to achieve this, some means must be used to remove the recorded signal.

The most common method is to mount an additional head-the erase head - fairly close to, and just ahead of, the record head. A much larger gap is used and the same bias waveform of greater amplitude, is fed to the winding. The current should be sufficient to take each tape element beyond saturation.

This head is usually tuned to bias frequency since it does not handle signal. Although the gap is large, and the erase current high, it is found that the original pattern on the tape, especially that of strong signals, often recovers after erasure. To avoid this effect a double gap is used, the gaps, in this case, being adjacent to each other.

Because the gap is larger, and bias current higher, the flux extends well beyond the physical gap, i.e. the fringe is longer. Each tape element spends much more time in the fringe than it would in that of the recording head, and many loops are traversed in that time. The loops fall off in amplitude as each element moves out of


Fig. 7. The series of BH loops through which each tape particle passes, due to large air gap and increase h.f. current for erasure by soturation
the fringe so that the remanent flux is negligible. Fig. 7 shows the series of loops to which each element is exposed as it leaves the erase gap, the actual number of loops being much greater than a clear sketch could show.

## CONSTANT CURRENT RECORDING

When a signal is recorded on tape the pattern must represent the amplitude as well as the frequency. The flux and therefore the head current should be independent of frequency. The recording head is highly inductive and its reactance would reduce the current as the frequency became higher, assuming a constant voltage drive.

A resistor is usually connected between the output of the signal amplifier and the head to reduce this effect, which could lead to a fall-off of 6 dB per octave. If the resistor value is high, it will swamp the reactance of the head, so that the head current will be the same for all frequencies of the same original amplitude. This is called a constant current system.

## NEWS BRIEFS

## B.A.E.C.

The British Amateur Electronics Club recently held its annual exhibition at Penarth, Glamorgan, and as a result of this, some of the exhibits were used in a BBC television programme on BBC Wales; the programme-"Heddiw"-was televised in Welsh but the B.A.E.C. were provided with an English translation of the text that they published in their October Newsletter. The games illustrated on television were all constructed by members of the B.A.E.C

## Vidicon Storage Tube

DEvelopment of the first television camera tube to feature built-in stop-action capabilities was reported recently by RCA.

The unit is a small, vidicon-type device that can takeand store for later play-back-single electronic "snapshots" or "stills" of continuous programmes being transmitted by the tube.

The still picture is electronically stored inside the tube itself thus eliminating the need for an external storage medium such as film or videotape. It can be relayed to a TV monitor immediately, or kept intact within the tube for several days.

The new tube is technically referred to as a silicon storage vidicon and could be used, for example, in a space, satellite to eliminate the bulk and weight of the present video storage devices which relay individual TV pictures to earth by relatively slow communications systems.

The picture below shows the new tube in use. Theimage on the left of the screen was recorded by the tube a few minutes before, and the one on the right is being transmitted "live". giving the impression that the subject is talking to himself.


# 踢 <br> oualPulipost PARET 1 AWPCIITEI 

## By D. S. GIBBS and I. M. SHAW (ferranti ltd)

AHIGH quality pre-amplifier must perform several important functions, it must have an input impedance sufficiently high not to load the signal sources, it must provide frequency equalisation where necessary, it must have filters to remove spurious noise or harsh treble from poor recordings and it must have tone controls to accommodate the preferences of the listener, the deficiencies of the loudspeakers and the acoustics of the listening room. Furthermore it should do all this without any significant increase in either distortion or background noise level over a wide range of input signal levels.
The design evolved meets all these requirements, as the specification published previously shows, and it has several other valuable facilities. A microphone channel is included which can be mixed independently with any other input. This facility is useful to tape recording enthusiasts or can be used as a "home discotheque" for parties etc. Another feature valuable to the tape recorder enthusiast is the recorder output which is taken after the equalisation and filter stages but is unaffected by volume or tone controls. The circuit diagram of the right hand channel of the pre-amplifier is shown in Fig. 24, the left hand channel is identical.

## DISC INPUT

The disc input stage comprising T14, T15, T114 and T115, provides frequency equalisation to the RIAA L.P. characteristic with an accuracy of approximately $\pm 0.5 \mathrm{~dB}$ between 30 Hz and 15 kHz as shown in Fig. 25. The circuit has been designed so that the frequency response curve falls below 20 Hz . This attenuates low frequency components in the output of the pickup and makes a separate rumble filter unnecessary. The sensitivity is 3 mV in the "Lo" position and about 60 mV in the "Hi" position so that either magnetic or ceramic pickup cartridges may be used.

The circuit used is a complementary two stage feedback amplifier, a more detailed circuit of which is shown in Fig. 26. This was chosen in preference to the more popular circuit shown in Fig. 27, because of the greatly improved bias stability and its freedom from the subsonic ringing which the circuit of Fig. 27 tends to produce. This arises through the feed-back provided by R1 becoming positive at certain frequencies due to the phase shifts produced by C1 and C2 (Fig. 27).

The shape of the frequency response curve is defined by the negative feed-back provided by R39, R40 and C19 to 21; the overall gain is defined by R38. The circuit gives an output of 100 mV for an input of


Fig. 25. Frequency response curve of disc input stage


 ponent numbers being right-hand channel plus 100, except for the switches and sockets which serve both channels by being double pole types


3 mV at 1 kHz . Distortion generated by the input stage is below 0.005 per cent with 3 mV input at 1 kHz and below 0.05 per cent at 20 dB overload. Between 100 Hz and 10 kHz the distortion does not exceed $0 \cdot 1$ per cent at any level up to 30 dB overload.

## MICROPHONE INPUT

The circuit of the microphone input stage (Fig. 28) is virtually the same as the disc input except that the feed-back network is replaced by a single resistor to give a flat frequency response and the bias resistors at the input are bootstrapped to increase the input impedance. Two input sensitivities have been provided. In the "Lo" position of S2 the input sensitivity is 1 mV with an input impedance of 1 megohm which is suitable for most low and medium impedance dynamic and ribbon microphones. With the switch in the " Hi " position the sensitivity is reduced 1010 mV and the input impedance rises to 2 megohms. This sensitivity is suitable for high impedance dynamic or crystal microphones.
Constructors not requiring the microphone input can simply omit all the components associated with this stage, or if only mono microphone operation is required only one microphone input stage need be built and the output can be fed to both mixers via separate 100 kilohm resistors (R56, R156). Potentiometer VR 3 in this case need only be single gang.

The table below summarises the performance of the microphone input stage:

## INPUT MIXER

The input mixer works on the virtual earth principle which is explained in Fig. 29. Because the input impedance at the base of the TR18 is low-only about 100 ohms-it can for all practical purposes be considered as a short circuit to earth, hence the name virtual earth amplifier. The total input current when

## Table 1

"Lo"
"Hi"

| Input Sensitivity | 1 mV | 10 mV |
| :---: | :---: | :---: |
| Input impedance | $1 \mathrm{M} \Omega$ | $2 \mathrm{M} \Omega$ |
| Distortion | less than $0.005 \%$ at 1 mV input and less than $0.05 \%$ at 20 dB overload | less than $0.005 \%$ at 10 mV and less than $0.02 \%$ at 20 dB overload |
| Frequency response | $\begin{aligned} & 65 \mathrm{~Hz} \text { to } 70 \mathrm{kHz} \\ & \text { at }-3 \mathrm{~dB} \end{aligned}$ | 10 Hz to 200 kHz at -3 dB . <br> With 500 pF <br> source (crystal <br> microphone) <br> frequency response is -3 dB at 60 Hz |
| Signal to noise ratio at rated sensitivity | -60 dB with $100 \Omega$ source, -56 dB with $1 K \Omega$ source, unweighted, 20 Hz to 20 kHz | -70 dB with Ik $\Omega$ source, -56 dB with $50 \mathrm{~K} \Omega$ source, unweighted, 20 Hz to 20 kHz |



Fig. 29a. Virtual earth amplifier-functignal diagram


Because point $X$ is "virtually" at earth, potential the tocal input current when used as a mixure is

$$
i_{1}+i_{2}=\frac{V_{1}}{R_{1}}+\frac{V_{2}}{\bar{R}_{2}} i_{1}
$$

The output voltage $V_{0}=i_{\mathrm{f}} R_{\mathrm{r}}=\frac{V_{1} R_{\mathrm{r}}}{R_{1}}+\frac{V_{2} R_{\mathrm{f}}}{R_{2}}$
i.e. the gain from each input is given by the ratio of $R_{r}$ to the appropriate input resistor and the output is proportional to the sum of the individual input currents

Fig. 29b. Virtual earth amplifier as a mixer
used as a mixer is then just simply the sum of the input currents from each signal source, which enables the microphone input to be mixed with any of the other inputs without interaction.

The input sensitivity at the tape, tuner and aux, inputs is 100 mV with an input impedance of 50 kilohms, and the output at the emitter of TR19 is 400 mV . Capacitor C34 is a bootstrap capacitor which increases both the gain and the available output voltage swing to give minimum distortion. At 1 kHz the distortion produced is below 0.005 per cent at rated sensitivity and below 0.02 per cent at 20 dB overload, at 10 kHz these figures become 0.01 per cent and 0.1 per cent respectively. The circuit will give an output of 10 volts r.m.s. ( 28 dB overload) before clipping occurs. Signal to noise ratio at rated sensitivity is -80 dB (unweighted 20 Hz to 20 kHz ).

To be continued


WHEN stereophonic sound reproduction made its debut, only a few years ago, the various manufacturers who exhibited at the annual Audio Fair were quick to take advantage of the "moving sound" illusions that could be created with stereo. These consisted mainly of the sounds of express trains and racing cars, etc. which hurtled across the demonstration rooms at breakneck speed, and of phantom ping-pong games in which you followed the movement of the ball without seeing it. After being subjected to this and other subtle forms of audio brain-washing, you went home either cross-eyed or cross-eared but nevertheless convinced that stereo had something even if it was only two of everything.

Now it seems that stereo is out-iwo channel stereo that is. The "in thing" is four channel stereo alias quadrasonic, alias CD-4 alias surround sound, call it what you like. At least that's one impression gained at the 1970, 16th International Audio and Music Fair held in October at Olympia. This time, no trains and no ping-pong game. This time they sat you down in the middle with the sound all around and intensified the audio brain-washing with real solid state power.

Practical Electronics went one better. With their demonstration of the "P.E. Gemini" stereo amplifier, visitors were also treated visually with the "P.E. Aurora", an audio controlled light display, which can be adapted by the constructor for domestic settings. (Details of "Aurora" will be published very soon as a constructional project-Ed.)

## FOUR CHANNEL-ON DISC

JVC-Nivico (Victor Company of Japan) recently announced their new four channel disc stereo system and its reproducing equipment known as CD-4. This was demonstrated at Olympia and is claimed to be high fidelity and also compatible with existing two channel reproducing equipment, i.e. the CD-4 four channel discs can be played on conventional two channel equipment and two channel discs can be played on CD-4 equipment.


The four channels are cut in one groove and according to the very limited technical information at present available, the system features both frequency and phase modulation which are combined to carry the signals for the extra channels. Reproduction requires four separate amplifier channels and four loudspeakers. Two loudspeakers are positioned in front of the listener and two at the rear. The photographs show the difference between recording on a two channel stereo disc and the new CD-4 disc. The U.K. distributors are Denham and Morley Limited, Denmore House, 453 Caledonian Road, London, N. 7.

## FOUR CHANNEL-ON TAPE

The Audiosonic system, which is four channel stereo on tape, was also demonstrated at Olympia by JVC-Nivico. The system employs four separate tape heads and offers various ambionic arrangements for the listener, i.e. two speakers in front and two at the rear or three in front and one at the rear or all four at the front. The makers claim that the system presents truly "solid music" with complete freedom of movement for the listener. As the two recommended twin channel Nivico amplifiers for this system are each rated for 140W music power (total 280W) the system could present some pretty solid sound as well.

## Nivico four channel disc player

## Model R. 130

 King diant fl 130 which makes is the (iiant f.130 whith makes up neluding Radio Heceivers, Transinitters, Tiwhometer, Ratin Alarm, Tesicrs, Electronic Swltehes, Ampli: fiers and ceves an Electronic Target Giame. As well as carphone, spcaker. meter, relay, transomer, solar cel, nuphiel complete with it litted hardwool cilse PRICE £7.19.0 20 PROJECT SOLAR ELECTRONIC KIT Mod. R. 128 This ultranoiern Proiect $K$ it is shaped for the space age. Cinriath inaide
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[^2]
## SURROUND STEREO

A cheaper and perhaps equally effective "ambionic" system known as "surround stereo" can be devised using any conventional two channel stereo equipment plus one extra loudspeaker. This system is introduced in a leaflet included with the new EXP-70 LP disc called "This is Stereo".
This record, one of the EXP Technical Series, will by now be available from all record dealers price 39 s . It is a combined stereo demonstration and test record containing various recordings that illustrate not only the dynamic and spatial possibilities that stereo offers, but also includes test tracks for adjustment of experimental "surround stereo" systems described in the leaflet and for balance and phasing, etc. of two channel stereo.

## SOME NEW AUDIO PRODUCTS

Although over 80 manufacturers were exhibiting at the 1970 Audio and Music Fair, few completely new products were to be seen. Most manufacturers were showing their current ranges of equipment, a good deal of which has already been publicised in the technical press.
The new Wharfedale Aston wall mounting loudspeaker was one new product and this breaks away from the old hi-fi tradition of dark veneer and rather uninspiring front covering. These speakers are available in glossy white enamelled cabinets and the front is finished with what Wharfedale call their "silver fox" fabric which looks like shiny metal. The Aston speaker system comprises an 8 inch bass unit and 3 inch treble unit and the cabinet measures $19 \times 11 \frac{1}{2} \times 4 \frac{1}{2}$ inches. Power handling capacity is 18 watts r.m.s. and the input impedance 4 to 8 ohms . These and the new Wharfedale Triton speakers, also released at the same time, are available in matched stereo pairs complete with 15 feet of connecting cables and DIN plugs.

## LOUDSPEAKERS FOR THE FUTURE

The loudspeaker has always been the most inefficient link in an audio reproducing system requiring as it does a comparatively large amount of audio power to produce a useful sound output. J. B. Lansing Sound Inc. of America have not only found a way of increasing efficiency but also of producing better sound distribution. Most of their loudspeakers are intended for professional studio use but one known as Aquarius 4 has been designed for domestic use and was introduced at the Audio and Music Fair.

Mounted inside the enclosure is an 8 inch wide range transducer directed upward into a radial horn. This spreads the sound through a 360 degrees horizontal radial defraction slot. The higher frequencies emerge in a 360 degree vertical plane from a 2 inch driver separately loaded by a radial horn mounted at the rear of the enclosure. The result is a much wider distribution of sound than would be obtained with a conventional system.

The Aquarius 4 will handle power up to 30 watts r.m.s. but nothing like this power is necessary for life size sound because of the high efficiency. The same company also manufacture a very compact studio monitor speaker for power capacity up to 75 watts. The U.K. distributors are Feldon Recording Limited, 126 Great Portland Street, London, W.I.


Wharfdale Aston, wall mounting loudspeaker

## LESS NOISE PLEASE

The Dolby noise reduction system has, until recently, been available only for studio use. Kellar Electronics Limited have now introduced two Dolby type B noise reduction units for use with any domestic tape recorder having radio or line inputs and line or external amplifier outputs. The KDBl unit is switchable from record to replay and is for use with recorders having a combined record/replay head. This retails at $£ 45$. The second unit, known as the KDB2, has two separate record and replay channels for use with machines having separate record and replay heads and retails at $£ 75$. Both units are supplied complete with stabilised power supplies and VU meters and are for two channel (stereo) operation.

Kellar Electronics have also released a combined cassette recorder/amplifier known as type DTA50, with a. built-in Dolby noise reduction system the retail price of which will be $£ 1484 \mathrm{~s} 8 \mathrm{~d}$. It seems likely that a noise reducing system will become an integrated part of all domestic as well as professional tape recorders. Cassette tape recorders are a very obvious choice to begin with as few have a noise performance of better than -40 dB .

Tape head magnetisation is one of the most common causes of high noise level on tape recordings. Tape heads should therefore be frequently demagnetised with a defluxing tool. Together with many other new audio accessories that Ferrograph have just released is a new tape head demagnetiser, their Defluxer type D/2 which retails at $£ 4$. The outlay is a very worthwhile


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| B8R C1 (NT3) |  | $8 / 6$ | 9/6 | CM50 . | $2 / 6$ | 7/6 |
| B8R TC8H |  | 2/6 | $7 / 6$ | CM60 . | 2/6 | 7/6 |
| HSR TC8M |  | 2/6 | 7/6 | MX1.. | $2 / 8$ | $7 / 6$ |
| BSR ST8 |  | 8/6 | 9/6 | MX2 <br> sterco Cs80 | $2 / 6$ | 7/6 |
| BSR ST9 |  | 6/6 | 9/6 |  | 2/6 | $7 / 6$ |
| BsR ST10 |  |  | $8 /-$ | PERPETUOM ERAER |  |  |
| BSR X MM |  | $8 / 6$ | $8 / 6$ | PE188 | B/6 | 9/6 |
| 84R X 1 H |  | ${ }^{8 / 6}$ | 9/6 | PHILIPS |  |  |
| 13sR $\times 3 \mathrm{M}$ |  | $8 / 6$ | $8 / 6$ | .193016 | 2/6 | 7/6 |
| HSR X 3 H |  | ${ }_{8 / 6}^{8 / 6}$ | $8 / 6$ | .163063 | $2 / 6$ | 7/6 |
| 134R N5H |  | 6/8 | $8 / 6$ | $\begin{aligned} & \text { LG330 } \\ & \text { IG3310/330t } \end{aligned}$ | 6/6 | $9 / 6$ |
| BNIC X 4 H |  | 8/B | $8 / 6$ |  | $6 / 6$ | $9 / 6$ |
| collaro |  |  |  | 1G3400 | 2/6 | 7/6 |
|  |  |  |  | RONETTE BIMOFLUID |  |  |
|  |  |  |  | BF40 | 8/8 | 7/6 |
| Coblaro-honet |  |  |  | $1 \mathrm{LCO}^{4}$ | 2/6 | 7/6 |
|  |  | $2 / 8$ | \%/6 | sonotone |  |  |
| Cullel 8 K 1 |  | 2/6 | 7/6 | 2 T . | 6/8 | $9 / 6$ |
| Duat CDs $/$ Clas 3(DN2) |  |  |  | 37. | $8 / 8$ | $9 / 6$ |
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| Dual CDer3:- |  |  |  | 9TA | $8 / 6$ | $9 / 6$ |
| (DN3) ${ }^{\text {(D) }}$ |  | 6/6 | $9 \cdot 6$ | 9TS/HC | 6/6 | $9 / 6$ |
| ELAC KST ${ }^{\text {a }}$ |  |  |  | 19T . | 2/8 | $7 / 6$ |
| (PE10) |  | 6/6 | $9 / 6$ | 20 T | 2/6 | 7/6 |

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2N404
2N641 NN64
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2N688
safeguard against the high noise level that can be imparted even to pre-recorded tapes if a tape head has become magnetised.

Another new and useful device suitable for all current Ferrograph tape recorders is a signal operated switching unit which starts the recorder on arrival of wanted signals and stops it again automatically when the signals cease. The device operates from a lowest signal level of 400 mV and has an adjustable over-run time of 5 to 25 seconds; retail price $£ 30$.

## MUSIC AT THE FAIR

A few large musical instrument distributors were showing currently available instruments, such as electronic organs, guitars and amplifiers, but nothing that could be described as new could be found.

One of the highlights in the music field, however, was the Moog Synthesiser which is an electronic music composition and performance system. The system is partly keyboard operated but consists mainly of a large number of voltage controlled oscillators and filters


The Moog Synthesiser - this version would cost between £2,250 and £3,000
which can be interconnected to produce the pitch, waveform and envelope for an infinite range of musical sounds and even of "pseudo" human voices. One of the larger versions of the Moog Synthesiser is shown on this page but such devices are hardly for domestic use since the price range is $£ 2,250$ to over $£ 3,000$. However, a small synthesiser suitable for schools and small studio use will shortly become available at less than $£ 600$. The present U.K. distributors for Moog Synthesisers are Feldon Recording Limited, 126 Great Portland Street, London, W.I.

The designer of the Synthesiser, Dr R. A. Moog, who gave a leature on and a demonstration of the instrument at Olympia, spoke of the possibility of combining the music synthesiser with a conventional electronic organ resulting in a musical instrument with variable voicing and effects that could be chosen at will by the player. There is already some indication of this idea in modern electronic organs.

## AUDIO PRESENTATION PROGRAMME

Visitors to the Fair were also able to hear a. series of lectures (many with demonstrations) by well-known
experts in sound reproduction and music. Among these were Dr R. A. Moog mentioned above, P. J. Baxandall, inventor of the almost universally used Baxandall tone çontrol network, Desmond Briscoe from the BBC Radiophonics Workshop and Sir Arthur Bliss (Master of the Queen's Music). Many well-known audio journalists and designers also gave talks on high fidelity sound reproduction. Other special features included a recording studio where visitors could make a record, a search for a singer contest, a music tuition bookshop and a comprehensive display of musical instruments of ail kinds.

A number of films of interest to audio enthusiasts such as "The Timeless Track" and "The Magic Tape" by B.A.S.F. and "Sound on Tape" by Philips Limited, were also shown during the week of the fair. All this, together with the displays and demonstrations of products by over 100 exhibitors made the 1970 Audio and Music Fair a successful and interesting venture for both the exhibitors and the visitors. However, the exhibiting manufacturers of audio equipment still had the problem of accommodating a worthwhile number of people during demonstrations; the demonstration cubicles were just not large enough.

## NEW RELEASES

Rola Celestion Limited-a new loudspeaker-the Ditton 120. The 120 was displayed at the Audio Fair; it consists of three units, an HF1300 treble speaker, a long throw mid range and bass speaker and an audio bass resonator (A.B.R.) all housed in a case measuring $17 \times 7 \frac{3}{4} \times 8 \frac{3}{4}$ inches. Frequency response is 30 Hz to $15,000 \mathrm{~Hz}$, power handling is 20 W DIN, impedance is 4 to $8 \Omega$; the speakers are available in matched pairs and cost $£ 48$ per pair.
K.E.F. Electronics Limited-a new range of loudspeakers known as the Cresta, Chorale, Cadenza and Concerto and each has a three speaker system. The Cadenza was released at the Audio and Music Fair and is a completely new system with a passive bass radiator. Frequency range 30 to $30,000 \mathrm{~Hz}$. Power handling capacity 25 watts at 8 ohms.

Uher-Bosch Limited-a new tape recorder, the Uher 724 , which is a quarter-track stereo machine for $7 \frac{1}{2}$ and $3 \frac{3}{4}$ i.p.s. Specification is to DIN standards with an output power of 2 watts per channel. The recorder operates in the vertical or horizontal position. Retail price is $£ 103$ 10s 0 d plus $£ 2511 \mathrm{~s} 5 \mathrm{~d}$ p.t. Uher have also introduced a new portable cassette recorder for C60, C 90 or C 120 tapes and which features a reversible drive system to obviate having to turn the tape cassette over. Price not available at time of writing.

## B.A.T.R. CONTEST

Prizes were presented at the Audio and Music Fair to winners of the 1970 British Amateur Tape Recording contest. The "Tape of the Year" winner was Mr K. McKenzie for his documentary tape called "Sunderland Hospital Broadcasts". For this he was awarded the Emitape Challenge Cup and also the Philips Shield for the best recording in the documentary class.

The 1971 and 14th British Amateur Tape Recording Contest has already been announced with a closing da.te of 30 th June, 1971. Entry forms are available from the Secretary, B.A.T.R. Contest, 37 Fairlawnes, Maldon Road, Wallington, Surrey. As before there are six categories to choose from including one for schools.

# DI|EDOCLOER By R.W. Coles 

THE alarm board "B" carries the logic and oscillator circuitry required by the alarm system, and the 5 volt regulator, which provides the highly stable supply required by all the i.c.s used in the clock. The board is physically smaller than the main clock board "A", being a "Dualine" DL109/22, which has positions for nine $14 / 16$ pin DIL packages, and is equipped with a single-sided 22 -way edge connector.
Six i.c.s are used in all, but two of these are CA3046 transistor arrays which are 14-pin packages containing five individual transistors. These are not members of the TTL family used to perform all the clock and alarm logic.

## ALARM OPERATION

The time at which the alarm is required to sound is entered by means of a bank of thumbwheel switches S2a and S2b mounted adjacent to the display on the front of the clock.

These switches are so constructed that they give a four-bit binary coded decimal output pattern for each number selected. This b.c.d. word is continuously compared with the b.c.d. output from the clock counters in a digital comparator (see Fig. 7). When the switch setting and the display are the same, the output of the comparator falls to a low level and is used to set a simple "latch" bistable in IC18.

The output of the latch enables a gate G5c which allows the alarm tone through to a 40 ohm moving-coil speaker, and thus the alarm is sounded. The alarm will
continue indefinitely unless reset by means of the miniature toggle switch S3, also mounted on the front of the clock.

The alarm setting accuracy could be extended to any degree by simply adding more switches and increasing the comparator size. A setting to within 0.1 second is quite possible if required, but with the prototype the system was intended for domestic use, and time setting in ten minute increments was considered adequate.

It was also considered unnecessary to include the hours 10.0 to 12.50 in the comparison, thus saving an expensive switch bank which would have been necessary in the "tens-of-hours" position. If this expense is unimportant, or if the aesthetic consequences of using a toggle switch in this role (for this is all that is required in this position) can be tolerated, these times are casily added because the comparator as described has allowance for this extra switch.

There is therefore a considerable amount of flexibility in the alarm circuitry which allows individual constructors to tailor the system to their own requirements.

## ALARM CIRCUIT

The circuit of the alarm system is given in Fig. 7, and it can be seen that it is not very complicated at all. Of course, the alarm circuit can be omitted altogether without affecting the remainder of the clock circuit.
The digital comparator is formed from two SN7483 MSI packages, each containing four complete fulladders, with internally connected ripple through carry.


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| 26-3\% | 7 A Mas it $30{ }^{*}$ | 240 | * | ${ }^{*}$ | \$30 |
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| Adjustable |  |  |  |  |  |
| 125 | 1A | 240 | - | Furuel | 215 |
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| 124 | 20.4 | 110 | .. | , | 295-0-0 |
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| 6 V | 14 A | , | ., | , | 123-0-0 |
| 20 V | +iA | , | " | " | 218-0-0 |
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| 6 GV | 10A |  | Arljustable | computer |  |
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These circuits are mainly intended for high-speed multiple bit parallel addition in computer arithmetic units, but binary adders are versatile devices, capable of performing several logic operations. The availability of four such devices in one package opens all manner of possibilities to the logic designer.

To understand exactly how these circuits can be used as comparators, consider Fig. 8. Fig. 8a shows the truth table of an "exclusive-or" gate, which is some-
should be equal to the switch outputs before the completed comparator registers an equality. To achieve this, the eight sum outputs from the adders (each of which registers equality of one digit) are fed to an SN7430 8-input NAND gate G4 in ICl7, the output of which will only fall to a logic 0 when all the individual comparisons are valid.

The output of this gate will remain low for ten minutes, until a further increment in the "tens-ofminutes" count invalidates the comparison. A period of this length would probably be sufficient for alerting the sleeper, but to make absolutely sure that the alarm


Fig. 7. Block diagram of the alarm circuit using five integrated circuits and showing their pin connections. Thie full circuit of the alarm oscillator appears in Fig. 10
times called a "non-equivalence" gate because its output is a logic 1 only when its inputs are exactly opposite.

To use such a gate as an equivalence circuit which gives a logic 1 output when its inputs are identical, it is only necessary to invert one of those inputs by means of an inverter gate, ol if complementary inputs are available anyway, to compare the true form of one input with the negated form of the other. It follows then that this type of gate could be used to build the complete alarm comparator we require.

As Fig. 8b shows, the truth table of a full-adder is identical to that of the "exclusive-or" gate, if we ignore the carry output and keep the carry input at 0 . In fact, using the SN7483 as four "exclusive-OR"" gates suits our comparator design admirably. The inverted form of one of the words to be compared is already available, because the thumbwheel switches give outputs in this form. The fact that we do have a carry input available can be put to good use in correcting the lagging output of the hours counter.

## EIGHT DIGIT EQUALITY

So far this article has only considered the comparison of single digits, but all eight digits from the counters
cannot be ignored, the output of 1 CI 7 sets a' latch formed from two cross-coupled 2 -input NaND gates G5a and G5b in IC18, which ensures that the alarm tone will continue until reset, even if this process takes longer than ten minutes.


Fig. 8a. Truth table for "exclusive-OR" function
Fig. 8b. Truth table for full adder when $\mathrm{C}_{\mathrm{in}}=0$

## ALARM TIME SWITCHES

The switches used to set the alarm time were chosen from the Birch-Stolec "standard" range, and both banks are coded S.B.10.N1248. These switches are well suited to this application because they give an output on four lines which conforms to the binary code, a separate pattern being produced for each of the ten positions of the numbered wheel.

Any number of switch banks can be mounted together if the constructor wishes to expand the alarm system, the final assembly being finished off by means of a pair of end cheeks which give a neat appearance and provide the mounting holes.

The principle on which these switches are based is that of a four-track printed, gold plated stator, traversed by four phosphor bronze rotor contacts. The life of this assembly is at least 100,000 operational cycles when switching 24 watts, so in this application the life will be much extended due to the very small current being carried.
To fabricate a similar system using wafer switches, a four pole ten-way switch would be required in each position, with a considerable amount of wiring necessary to programme the required code.
The wafer switch system does provide a useful way of explaining the action however, and a diagram with output logic table conforming to the thumbwheel switch coding is given in Fig. 9. Note that the output is in the complement form (assuming positive logic). A logic 0 is represented by a ground connection, and a logic 1 by an open circuit.

## DIGITAL COMPARISON

As discussed earlier, this inverted code is just what is required for connection to the comparator. Of course, numbers one to nine only are required as inputs to the hours comparator, and numbers zero to five for the tens-of-minutes circuit. Stops could be fitted to the switches if desired, although this is not really necessary; blanking the undesired numbers with paint would be a simple alternative.
Only six positions are required for the tens-ofminutes switch. It follows, therefore, that the full output of four digits is not needed. In fact, the " 8 " output from this switch can be ignored in the comparison, leaving a spare comparator section in IC15, which is put to good use as an "inhibit" while the "tens-of-hours latch" E output is high. Remember that the alarm system is not operative during this three-hour period as it stands.
This inhibiting logic is simply achieved by comparing the $E$ line with a permanent " 1 ", thus only permitting a 1 output from this section when the $E$ line is at 0 .

As an alternative, it would be possible to ignore this spare comparator section, and feed the $\overline{\mathrm{E}}$ output from 1C10 (Fig. 2) to the 8 -input gate in IC17 which, when using this method, would have an input àvailable. This system was not used in the prototype simply to retain the design simplification of feeding only true outputs from the main clock board.

## HOURS COUNT CORRECTION

So far so good, but up to now we have ignored the point made last month that the hours counter outputs do not conform directly to the binary code, 0000 representing decimal 1 instead of 0 , and so on up to 1000 representing 9 instead of 8 .

The thumbwheel switches, being "off-the-shelf" items, do not allow for this idiosyncrasy. The comparison as previously described would reveal that, when


Fig. 9. Wiring of a 4 -pole 10 -way switch to give a four-bit binary coded decimal output in complement form. The truth table shows positive logic
the switch was set to, say, four o'clock, the alarm would sound at five o'clock, and so on, a most unhappy state of affairs.
It is evident, however, that the code output from the hours counter could be corrected by simply adding binary 0001 to it, increasing its binary value by one. This simple conversion could be carried out using a four-bit parallel adder such as the SN7483N. (A fourbit adder is necessary because carries must be allowed for over all four bits.)
A separate SN7483N could be employed to carry out this correction, but as these circuits are used as comparators anyway, this is not necessary. A 1 can be added in by feeding a carry in on the "carry" input to the least significant adder. Admittedly this is all a little difficult to grasp, but it does work, and the best way of proving it is to set an exàmple down as a sum, thus:

Hours display: 7, Switch setting: 7 .
Binary 7: 0111
Therefore switch output (inverted) $\left.: \begin{array}{c}\text { Binary } \\ \text { Hours counter output } \\ : 1000 \\ 0110\end{array}\right)$ add
Hours counter output: 0110) add

$$
=1110
$$

1 (plus 1 carry)
$=1111$ equal (alarm sounds)

## RISK OF TRANSIENT OPERATION

During the "paper" design of this part of the clock it occurred to the author that, due to the relatively lengthy propagation delay of the SN7483N, and the non-synchronous inputs from the MSI ripple counters, it might be possible for all the adder outputs to be at 1 when the comparison should not show equal, thus giving a transient output from 1C17.

As any such transient "low" output longer than a few nanoseconds is likely to set the latch and sound the alarm (at the wrong time), this state of affairs must obviously be avoided. A good deal of thought was

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Fig. 10. Alarm oscillator circuit using an integrated package of five transistors. TRE is unused
put in to discover whether this could in fact occur. It was decided that the only way to find out was to "try it and see"!

In the event, it was found impossible to generate this condition in the completed clock, and so all was well, but the point has been made because, with the wrong combination of adders and counters, it was thought to be a possible occurrence.

If any reader should be unlucky enough to experience this phenomenon, the solution is simply to gate the comparison with a clock waveform from one of the divider stages not used in the comparison. In this way the output from IC17 could be strobed only after the hours and tens-of-minutes counters have changed and the SN7483N "carries" have propagated. Obviously some circuit modifications would be necessary to achieve this.

## ALARM OSCILLATOR

The alarm oscillator circuit is shown in Fig. 10, which may attract the interest of those who are not otherwise concerned with the clock, because. of its obvious applications in many other designs.

The circuit was described by A. B. Blackwell-Jones in a letter to Wireless World (August 1967). He called it a "Trivibrator" or "Donkey Simulator", a very apt pair of names indeed. The circuit is based on the usual two transistor astable multivibrator arrangement, to which an extra transistor has been added, so that three separate cross-coupled timing networks result instead of the usual one, as shown in Fig. 11.

Each of the timing networks has a different natural frequency, two being audio tones of about one and two kilohertz respectively, and the other being a low frequency timing circuit giving a 5 Hz gating effect. The output is taken from the centre transistor of the two a.f. arms, and consists of alternate bursts of the two tones, each lasting for about 0.2 second, controlled by the l.f. arm. When fed to the speaker this tone gives a very pleasing "space age" warble effect.

If the reader is familiar with the basic multivibrator circuit action, there should be no problem at all in
understanding this extension of the idea. It will help to refer to Fig. 11.

## CIRCUIT OPERATION

Either transistor TRA or TRC will be saturated at any time. This saturation will last for about $0 \cdot 1$ second, as controlled by the 5 Hz timing network coupling them. The saturated transistor is unable to react via its a.f. timing network with TRB during this time, so this tone is not produced. Meanwhile, however, the other transistor (of the pair TRA or TRC) is not saturated by the l.f. arm and is free to interact via its a.f. timing network with TRB, giving an a.f. output of the appropriate frequency. When the 0.1 second period has expired the 1.f. arm changes over, saturating the other (TRA or TRC) transistor, and allowing the other a.f. tone to be produced, and so on.

The only unfamiliar parts of the circuitry are the two resistors, R4 and R9, which are necessary in the trivibrator to prevent the very low impedance of the I.f. coupling capacitors shunting the a.f. arms. In effect, the capacitors C4 and C6 coupling this section are taken to taps on the CR timing resistance chain.


Fig. II. Principle of operation of the alarm oscillator

The output of the oscillator is taken from TRB via a resistor R11 to gate G5c (Fig. 7), where it is held until allowed through by a logic 1 input from the alarm latch G5a. Note that the alarm oscillator runs continuously, whether the latch is set or not.

When G5c is not enabled, its output will be at a permanent 1 , or positive, level. If this output is fed to the simple saturating switch used to drive the speaker directly, although the alarm would not sound, this transistor would be hard on, giving a high power drain and possibly damaging the speaker. To avoid this, G5d is interposed between G5c and the switch TRD to act as an inverter, ensuring that when the alarm circuit is inactive, the output transistor will be off, and no current will flow through the speaker.

The three transistors forming the oscillator, and the transistor used to drive the speaker, are all contained in an integrated package IC19. The other transistor in this array is not used, but it is vital that its emitter (pin 13) should be grounded, as it is connected to the substrate of the device. An open circuit on this pin would jeopardise the isolation between the separate transistors.

The positive supply for the a.f. output stage is taken from the unregulated 10 V line, not to gain any power advantage, but to isolate the 5 V line powering the logic circuitry from any ripple produced.

The emitter of the output stage is grounded through the combined "alarm-off/reset" switch S3. When this switch is in the "off" position the alarm cannot sound under any conditions, and a 0 is fed to G5b to reset the latch ready for the next alarm setting.

Note that during the ten-minute period that the alarm output from IC17 is active, it is not possible to reset the latch by simply flicking the switch up and down.
This concludes the description of the alarm circuitry; the 5 V regulator, which is wired up on the same board, follows next. It is recommended that the 5 V regulator be wired up before the alarm circuit as will be realised later in this article.

## FIVE VOLT REGULATOR

The 5 V regulator uses a total of seven transistors, five being contained in another CA3046 (IC20), one being a discrete 2 N 706 , and the last being a plastic encapsulated power transistor, type MJE521, which is mounted off the board using the chassis as a heat sink.

The regulator features fold-back current limiting, excellent temperature stability, and an output impedance of less than 0.1 hm from d.c. to 100 kHz together with line regulation of better than 10 mV per volt.

Inputs to the circuit are 10 V d.c. (nominal) and 200 V d.c. (nominal), provided by conventional power supplies which are described next month. The regulator circuit is shown in Fig. 12.

The 200 V line is reduced to provide a 12 V biasing supply set by a discrete Zener diode D3, which is bypassed by Cl to suppress any high voltage transient surge when switching on. The 12 V is used to power the temperature compensated 7.5 V reference supply and the differential amplifier. Using a separate bias supply rather than the unregulated 10 V to power these sections gives a great improvement in line and load regulation, and increases ripple rejection.

In the alarm oscillator circuit the advantages conferred by employing a transistor array were those of compactness and convenience, but in this circuit the benefits of the monolithic construction provide a specification which could not be equalled simply by replacing the array with discrete devices. Because
each transistor is mounted in close thermal contact with its neighbours, tight temperature tracking is assured, enabling the construction of an accurately compensated reference and a differential amplifier with a negligible input offset voltage temperature coefficient.

As each transistor was made at the same time and experienced the same diffusion process, parameter matching is also assured. The transistors' $V_{\text {be }}$ are guaranteed to be within 5 mV of each other; all these factors can be put to good use in this type of circuit.

## REFERENCE SUPPLY

The reference supply is provided by TRD and TRE, which are not used as transistors in this application, but as a Zener diode and forward diode respectively. The use of a base emitter junction as a Zener is not new, and the transistors in the CA3046 provide a breakdown voltage of about seven volts when used in this way, with a temperature coefficient of plus $2 \mathrm{mV} /$ degree $C$.

A temperature induced variation of this order is not serious in itself, but if it occurs at the same time as other variations, it could take the 5 V supply outside the necessary design limits of plus or minus 250 mV .

To eliminate this drift, a forward biased diode, formed from another base emitter junction (TRE), is connected in series with TRD. A junction biased in this way exhibits a forward voltage of around 600 mV , but it has a negative temperature coefficient of 2 mV / degree $C$, which cancels the plus $2 \mathrm{mV} /$ degree C of the TRD "Zener" junction.
A variable potential divider is used to tap off an accurate 5 V from the reference line, and is used as an input to the non-inverting input of the differential amplifier (TRA and TRB). A capacitor Cll is used to bypass any noise which may be present on this supply.
Because a 5 V reference is used, the differential amplifier can be used in the voltage-follower mode, with the final 5 V output being fed back directly to the inverting input without the usual potential divider chain being interposed. This connection gives a maximum value of loop gain for good regulation, and also increases the frequency response of the amplifier, giving good transient response.

## DIFFERENTIAL AMPLIFIER

A differential amplifier was used because of its high voltage gain and absence of temperature problems which affect single-ended designs.

The output of the "error" amplifier is taken from the collector of TRA and used to drive the base of TR1 which, with the series pass power transistor TR2, forms a compound emitter follower with a very high current gain, and consequently low output impedance. TR2 has to pass all the current required by the load, which could be as high as 1 A , and is therefore a power device bolted to a heat-sink.

## FOLDBACK CURRENT LIMITING

The foldback current limiting circuit is provided by TRC with R21, R22, and R23. The following paragraphs describe its operation using Fig. 12.

As many constructors are aware, a short-circuit on the output of a semiconductor regulator can wreak havoc with the regulator devices in a matter of microseconds, and with a project of this complexity a shortcircuit is very likely to occur, especially in the early stages of testing. Protection against this sort of disaster


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| 18131 | $2 / 6$ | ACY39 | 11/- | BFX29 | 6/- | NKT128 | $61-$ | OC72 | 5/- |
| 18202 | 4/6 | ACY 40 | 3/- | BFX 30 | 6/6 | NKT129 | 6/- | 0 C 73 | $6 /-$ |
| 2 C 240 | 39/6 | ACY41 | 5/- | BFX35 | $19 / 6$ | NKT211 | 5/- | OC74 | 6\%- |
| 2 G 301 | 3/6 | ACY 44 | 7/6 | BFX63 | 10/- | NKT213 | $51-$ | $0 \mathrm{C75}$ | 5/- |
| 2 G 302 | 4/6 | AD140 | 10/- | BFX84 | 6/- | NKT214 | 3/- | 0076 | 5/- |
| 2 G 306 | 6/- | AD149 | 101- | BFX85 | $8 /-$ | NKT216 | 7/6 | $0 \mathrm{C77}$ | $8 /-$ |
| 20371 | 4/6 | AD161 | 7/6 | BFX86 | 6/6 | NKT217 | 8)- | 0078 | 4/- |
| 2 G 381 | $5 /-$ | AD162 | 7/6 | BFX87 | 8/6 | NKT218 | 22/6 | 0C78D | 2/6 |
| 2 G 414 | 6/- | AF108 | 6\% | BFX 88 | $5 /-$ | NKT219 | 8/6 | OC79 | 4/6 |
| 2 Cl 17 | 4/6 | AFI14 | 6/6 | BFY10 | 20/- | NKT228 | 4/- | OC81 | 5/- |
| 2N214 | $8 / 6$ | AFI15 | 6/- | BFY11 | 25/- | NKT224 | 4/6 | 0C81D | 4/- |
| 2N247 | 510 | AF116 | $6 / 6$ | BFY17 | 5/- | NKT251 | 4/9 | $0 \mathrm{C81M}$ | 4/- |
| 2 N 250 | 10/- | AF117 | $51-$ | BFY 18 | 5/- | NKT271 | 5/- | OC81DM | 3/6 |
| 2N 404 | 4/6 | AF118 | 12/6 | BFY19 | 5/- | NKT272 | 51- | OC812 | 11/- |
| 2N697 | 3/6 | AF119 | 4/- | BFY24 | $9 /-$ | NKT273 | 4/- | 0082 | 5/- |
| 2N698 | 8/6 | AF124 | 5!- | BFY44 | 201- | NKT274 | 4/- | OC82D | 3/- |
| 2N706 | $2 /-$ | AF125 | 4/- | BFY¢0 | 4/6 | NKT275 | 5/- | OC83 | 5)- |
| 2N706A | $2 / 6$ | AF126 | $3 / 6$ | BFY51 | 4/- | NKT277 | 4/- | $0 \mathrm{C84}$ | $51-$ |
| 2N708 | 3/- | AF127 | $3 / 6$ | BFY 52 | 4/6 | NKT278 | 5/- | OC114 | 7/6 |
| 2N709 | $12 / 6$ | AF139 | 8/- | BFY53 | $3 / 6$ | NKT301 | 61- | OC122 | 101- |
| 2N711 | 7/6 | AF178 | $9 / 6$ | BFY64 | 8/6 | NKT304 | 71- | 0 Cl 23 | 10/- |
| 2N987 | 10/6 | AF179 | $9 / 6$ | BFY90 | 13/6 | NKT403 | 15/- | OC139 | 5j- |
| 2N1090 | 6/- | AF180 | 10/6 | BEX27 | 10/- | NKT404 | 12/6 | OC140 | $7 / 6$ |
| 2N1091 | 6/6 |  | 8/6 | B8X60 | 18/6 | NKT678 | 61- | 0 Cl 41 | 12/6 |
| 2N1131 | 6/- | AF186 | $8 /-$ | B8X 78 | 3/- | NKT713 | $51-$ | OC169 | 4/- |
| 2N1132 | 6/- | AFY19 | 22/6 | B8Y26 | $3 / 6$ | NKT773 | $51-$ | 0.6170 | 5/- |
| 2N 1302 | 4/- | AFZ11 | 12/6 | B8Y27 | 4/- | NKT777 | $7 / 6$ | 0 O171 | 6/- |
| 2N1303 | 4/6 | AFZ12 | 15/- | B8Y51 | 10/- | O78B | $7 / 6$ | OC200 | 7/6 |
| 2N 1304 | $5 /-$ | A8Y26 | 51- | B8Y95A | 3/- | OAS | $3 /-$ | OC201 | 9/6 |
| 2N 1305 | 8/- | ASY27 | $6 / 6$ | B8Y95 | 3/- | OA6 | $2 / 6$ | OC202 | 12/6 |
| 2N1306 | 5/- | A8Y28 | 5/- | BT102/ |  | 0 OA 47 | $21-$ | OC203 | 7/6 |
| 2N1307 | $5 /-$ | A8Y29 | 6/- | 300R | 15/- | OA70 | $2 /-$ | OC204 | 9/- |
| 2N1308 | 6/- | A8Y38 | 5/- | BTY42 | 18/6 | OA71 | 21- | OC205 | 12/6 |
| $\text { 2N } 1309$ | 51- | A8Y50 | 3/6 | BTY791 |  | OA73 | $2 /-$ | OC208 | 15/- |
| $2 N 1420$ | 18/6 | A8Y51 | $8 /-$ | $100 \mathrm{R}$ | 151- | OA74 | $21-$ | OC207 | 15/- |
| 2N1507 | 5/6 | ABY53 | 4/- | BTY79/ |  | 0 OA79 | $2 /-$ | OC460 | 4/- |
| 2N1526 | 7/6 | ASY 55 | 4/- | 400 R | $351-$ | OA81 | $2 /-$ | OC470 | 91- |
| 2N1909 | 45/- | A8Y62 | B/- | BY100 | 3/6 | OA85 | $2 / 6$ | OCP71 | 19/6 |
| 2N2147 | 15/- | A8Y88 | $6 / 6$ | BY126 | 3/- | OA86 | $3 /-$ | ORP12 | 101- |
| 2N2148 | 12/- | A8Z21 | $8 / 6$ | BY127 | 4/- | OA90 | $2 /-$ | ORP60 | 8/- |
| 2 N 2160 | 12/6 | A8Z23 | 15/- | BY182 | 15/- | OA91 | 1/6 | ORP61 | 8/6 |
| 2N2218 | 6/- | AUY10 | 19/6 | BY213 | $51-$ | OA95 | 1/6 | 819 T | -8/- |
| 2N2219 | 6/6 | AU 101 | 30/- | BYZ10 | 81- | OA200 | 1/6 | 8AC40 | 51- |
| 2N2287 | 20/6 | BC107 | 2/6 | BYZ11 | 71- | OA202 | $21-$ | 8FT308 | $7 / 6$ |
| 2 N 2297 | 61- | BC108 | 2/6 | BYZ12 | $81-$ | OA210 | 5/- | 87722 | 7/6 |
| 2 N 2369 A | 4/- | BC109 | $2 / 6$ | BYZ13 | 81- | OA211 | 7/6 | 8 T 7231 | 12/6 |
| 2N2613 | $5 / 6$ | BCl 13 | $5 /-$ | BYZ15 | $201-$ | OAZ200 | 11/- | $8 \times 68$ | 4/- |
| 2N2646 | 10/6 | BC115 | 6/6 | BYZ16 | 12/6 | OAZ201 | 101- | 8X631 | 4/- |
| 2N2712 | 3/- | BC116 | $81 /$ | BYZ88C3 | V3 | OAZ202 | $8 / 6$ | 8X 635 | 61- |
| 2N2784 | 107- | BC118A | $8 /-$ |  | 3/6 | OAZ203 | $8 / 6$ | 8X640 | 51- |
| 2N2846 | 451- | BC118 | $7 / 6$ | C111 | 13/- | OAZ204 | $8 / 6$ | 8X641 | $5 /-$ |
| 2N2848 | $8 / 6$ | BC121 | 4/- | CR81/05 | $51-$ | OAZ205 | $8 / 6$ | $8 \times 642$ | 716 |
| 2N2904 | ${ }^{6 /-}$ | ${ }^{\text {BC122 }}$ | $4 /-$ | CRS1/40 | $9 / 6$ | OAZ206 | $8 / 6$ | BX644 | 9/6 |
| 2N2904A | 8/6 | BC125 | 13/6 | C84B | ${ }^{501}$ | OAZ207 | 816 | BX645 | 10/- |
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| 2N3054 | 10/- | BC157 | 4/- | DD008 | $7 / 6$ | OAZ223 | 81 | XA102 | 3/6 |
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| 2N3702 | 2/6 | BC160 | 12/6 | GD4 | 1/6 | OAZ241 | 4/6 | XA152 | $3!-$ |
| 2N3705 | $3 /-$ | BCl69 | $2 / 6$ | GD5 | $6 / 6$ $5 /-$ | OAZ242 | 4/6 | XA161 | $5!-$ |
| 2N3706 | 3/6 | BCY 31 BCY 22 | 81-1- | GD8 ${ }_{\text {GD12 }}$ | 5/- | OAZ244 | 4/6 | XA162 | 51- |
| 2N3709 | $2 / 6$ | BCY32 | 10/- | GET102 | 6/- | OAZ246 | $4 / 6$ $7 / 6$ | XB10] | 876 |
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| 2N3819 | 7/- | BCY39 | $9 / 6$ | GET114 | $3 /-$ | ${ }^{\text {OC19 }}$ | 7/6 | $\times{ }^{\times 1} \times 121$ | $2 \%-$ $8 / 6$ |
| 2N3820 | $17 / 6$ $15 /-$ | BCY40 | $8 / 6$ | GET115 | 9/- | 0 C 20 | 19/6 | ZR24 | $12{ }^{16}$ |
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| 2N5088 | 6/6 | BYC70 | 4/- | GET120 | 5/* | 0 C 23 | 101- | Z8271 | 316 |
| 28005 | $15 /-$ | BCY71 | $6 /-$ | GET872 | 6/- | $0 \mathrm{OC24}$ | 10\% | ZT21 | $5{ }_{5}$ |
| 28301 | $8 / 6$ | BCZ10 | 6/- | GET875 | ${ }^{5 /-}$ | 0 C 25 | 7/6 | ZT43 | 5 |
| 28304 | 12/6 | BCZ11 | 7/6 | GET880 | 7/8 | 0 C 26 | 51. | ZTX107 | 3 O |
| 28501 | 7/6 | BD121 | 13/- | GET881 | $51-$ | 0 C 28 | 12/6 | ZTX108 | 3 F |
| 28703 | 12/6 | BD123 | 16/6 | GET882 | $5 /-$ | OC29 | 12/6 | ZTX300 | $2 / 6$ |
| AA129 | 4/- | BD124 | $12 / 6$ | GET885 | 4/- | 0 O 30 | 81- | ZTX304 | 3/6 |
| AAZ12 | 6/- | BDY11 | 32/6 | GEX44 | 1/6 | 0C35 | 101- | 2TX500 | 3/- |
| AAZ13 | $2 / 6$ | BF115 | 5/- | GEX45/1 | 1/6 | 0 O 36 | 12/6 | ZTX 503 | 4/m |
| AC107 | 7/6 | BF117 | 10/- | GEX941 | 3/- | 0c41 | 5/- | ZTX331 | 6/- |
| AC126 | $\overline{5} /$ | BF167 | 51- | GJ3M | 5/- | OC42 | 6/- |  |  |

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Fig. 12a. Circuit diagram of the $5 V$ regulator circuit including foldback current limiting


Fig. 12b. Skeleton circuit of a 5V regulator with constant current and foldback current limiting

is easily provided by adding a current limiting circuit, of which there are numerous designs available, the simplest being the one transistor "constant current" arrangement.

A skeleton circuit of the regulator with this kind of limiting is shown in Fig. 12b, along with its limiting characteristic. The operation is quite straightforward; a current sense resistor in series with the 5 V supply is used to develop a voltage proportional to the current drawn.

When this voltage approaches the $V_{b e}$ of the current limit transistor, it turns it on, diverting some of the base drive to the series pass transistor, and causing the output voltage to fall to maintain the preset current. If the load on the supply is reduced to zero ohms (i.e. a short circuit) the output voltage falls almost to zero allowing only a current preset by the sense resistor value to flow.

The short-circuit current must be set to greater than the permissible load current. For this particular regulator this would be up to 1 A , giving a very high dissipation in the power transistor under short circuit conditions, because most of the unregulated voltage appears across this device.

## handling the heavy load

This problem could be overcome by using a heat sink large enough to handle the extra dissipation under fault conditions, but there is a much simpler method that is used in this regulator and requires only the addition of two resistors.
In Fig. 12b these extra resistors have been added, and are used as a potential divider to "buck-out" the voltage developed across the sense resistor, preventing it from turning on the transistor at the previous current level.
With the resistor values used in this regulator, the limit transistor will not be able to turn on until the

## COMPONENTS . . .

## ALARM \& 5V REGULATOR BOARD

(a) Alarm Oscillator
(b) 5V Regulator


## Diode

> D3 152120,
> 12 V 400 mW Zener

Transistors

> TRI 2 N706
> TRS MJE521 (with mica washer)

## Integrated Circuits

| IC15 | SN7483N | (BP83) |  |  |
| :--- | :--- | :--- | :--- | :--- |
| IC16 | SN7483N | (BP83) |  |  |
| IC17 | SN7430N | (BP30) |  |  |
| IC18 | SN7400N | (BP00) |  |  |
| IC19 | CA3046 | (R.C.A.) | IC20 CA3046 | (R.C.A.) |

Further notes on purchasing i.c.s given in the article "Making the Most of Logic ICs" last month

## Miscellaneous

S2 2-bank printed thumbwheel switch 10-way (see text) (Birch-Stolec or Radiospares)
S3 Miniature toggle switch, single-pole, changeover
LSI $40 \Omega$ or higher miniature
"Dualine" type DL109/22 printed circuit card (Shirehall Electronics Ltd., Station Yard, Borough Green, Sevenoaks, Kent)

current drawn approaches 1 A , instead of the 250 mA level set with constant current limiting and the same sense resistor.

When the transistor does turn on, however, the output voltage will drop, removing the effect of the "bucking" voltage and reducing the output current. When the output load becomes a short on the output, the final voltage will have dropped almost to zero, and the output current fallen to less than half the value at which limiting began.

This effect is termed "fold-back" limiting, for obvious reasons, and cuts the dissipation in the power transistor, under short circuit conditions, to about a third of that obtained with "constant current" limiting, operating at the same maximum current-a very worthwhile return for a couple of resistors.

In the prototype the limiter began to operate at about 1 A , and with a shorted output the current dropped to 400 mA , although resistor tolerances will affect these figures with later versions. The current limiting transistor TRC, is contained within the CA3046, which gives the added advantage that the operating and shortcircuit current will decrease as the chip warms up, due to the negative temperature coefficient of the device's $V_{b e}$, giving a degree of thermal feedback.

## OUTPUT VOLTAGE CONTROL

The output voltage of the regulator is not affected by the limiting circuitry under normal conditions, because the feedback to the error sensing amplifier is taken from after the current sense resistor R22, the voltage drop across this resistor being automatically allowed for by the amplifier.

The capacitor C 12 , which should either be a $22 \mu \mathrm{~F}$ tantalum type or an aluminium electrolytic in parallel with a $0 \cdot 1 \mu \mathrm{~F}$ paper capacitor, reduces transient spikes on the output and also ensures that the regulator remains stable at all times.

Using a miniature helical preset potentiometer for VR1 allows accurate setting of the output voltage. This type of component also features excellent temperature and long term stability. There is, however, no overriding reason why a single turn rotary control should not be substituted as an economy if a sacrifice of these qualities can be tolerated.

The construction of the regulator should precede that of the alarm circuitry; details of these will be given next month before describing the main power supply.

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 $15 \cdot 14$ in., 15/; $9 \cdots 7$.. 2in., $8 / 6 ; 14 \cdot: 11:: 2 \mathrm{in}, 14 / 6$
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## BATH GOSSIP

"Daddy", chorused my two girls the other day, "Mummy says you're trying to talk through your bathwater. Is that true?" Well how can you best answer this sort of question when you have the bath half-full of tepid water and a whole maze of electronics and tangled wires connected to electrodes dipping over the side!
"You look as if you're going to 'lectracoot yourself", they continued, quite unconcerned about the unanswered question, "Can we help?" Since I had no immediate desire to meet an early demise, I thought it prudent to explain just what was going on.

Actually, I had for a long while been toying with the possibility of using one of Marconi's earlier ideas for transmitting through water. His technique, which he later abandoned, was to utilise a pair of conductors on one side of a river connected to a carbon microphone and battery, and another couple of electrodes on the opposite bank hooked up to a pair of headphones. This arrangement essentially amounts to a bridge circuit, which, provided the impedances between the various electrodes are maintained as in Fig. 1, some fair results can be obtained, but only fair. Put a little two stage amplifier in at the transmitter and another in at the receiver
and you have something that will "go-out" quite a way. Try it and see.

Two hours and four ounces of liquorice comfits later, and my bathtub project, still not off-the-ground, saw me leaving this silent epitaph of despair for the more successful reaches of the Basingstoke Canal.

## WALKING MACHINES

Who, you might well ask, wants a machine that walks! Particularly since there are already such perfectly satisfactory devices as the wheel, and even caterpillar tracks for more difficult terrain. Well, it seems that at least two groups of people are extremely interested in having a viable mechanised walker at the earliest opportunity. One, the Army Tank Automotive Command in Michigan, have already got an experimental "clomper" to play with, and I understand that such a machine is likely to be brought into use where wheels or tracks leave off. This pretty sizeable piece of "ironmongery" is apparently capable of climbing over objects in its path and walking through swamps.

The Army machine weighs 1,400 odd kilogrammes and each of the powerful legs is operated by an hydraulic "muscle" driven by highpressure oil. Incorporated within the legs are tactile sensors which permit a degree of force-feedback control, so enabling any forces encountered by the machine to be sensed and dealt with accordingly!

Likely to have a more valid interest in walking devices are the gentlemen of the medical profession, who for years have sought the perfect walking prosthesis with precious little success. However, there seems no reason why hope should be completely abandoned because Prof. A. A. Frank of the University of Wisconsin (who has already completed a very successful four-legged creation) is currently engaged in work on a machine having two legs. This set-up, he forecasts, is anticipated to take the place of the conventional wheelchair and, in time, would give

amputees more freedom than they currently enjoy.
Coordination of the legs in Frank's device is envisaged to be under the direction of a miniaturised digital computer. This, basically, will consist of a random access core memory which can be addressed by associated read/write registers. Initially, the handicapped person would need to make the rather tedious walking movements via the artificial legs himself, but once a satisfactory gait had been established the data could be written into the memory for subsequent use. From then on, whenever walking was required, the memory would be read in a continuous fashion, rather like a tape-recorder using a loop of tape, so producing cyclic leg action; left, right, left, right, and so on until they were required to stop or maybe perform some other function.

A working prosthesis is expected to be in existence round about 1972; it would be nice to think that such worthwhile devices will be capable of being mass-produced fairly soon. Without a doubt the demand will be high.


## CHATTY COMPUTER

For 'umpteen years now, workers in the field of speech research have hankered for a computer that could produce good synthetic speech without the prior need for human trànslation of input data into "machine-language" before it could "understand". They need hanker no more, for scientists at the Bell Laboratories in New Jersey have recently taught a computer to convert printed English directly into synthetic speech.

During the experiments the computer was programmed with a basic vocabulary of words, together with their categories and definitions plus such related data as the pitch, stress, and timing as used in average normal conversation. In order to achieve the most natural sound, the computer was additionally given mathematical approximations to the changes in shape which occur in the human vocal tract when making speech sounds.

In use, information for conversion to synthetic speech is fed to the computer via a conventional teletype machine. Since the programme incorporates stress data, one must trust that its digestion is not upset 100 greatly by such word-group similarities as "a noisy noise", and "a noise annoys", or, dare I say it, "a noisy noise annoys". "Burp!"

# Rerdout <br> A SELECTION FROM OUR POSTBAG 

Correspondents wishing to have a reply must enclose a stamped addressed envelope. We regret we are unable to guarantee a reply on matters not relating to articles published in the magazine. Technical queries cannot be dealt with on the telephone.

## "P.E. Gemini"

Sir-I unfortunately do not know whether Mr D. S. Gibbs or Mr I. M. Shaw was responsible for writing the introductory article for "P.E. Gemini" Stereo Amplifier, but this preamble was one of the most interesting articles which I have read for quite some time. It could easily serve as a general guide to basic amplifier design and pointing out some of the pitfalls of design and, of course, teaching one how to gain more useful information from the various technical data leaflets published by the manufacturers of stereo equipment.

I particularly liked the point about distortion at low levels. I had, up till now, thought only that distortion at the high power levels to be of importance, and how it could be "swept-under-the-carpet" by plotting the graph with a linear scale instead of a logarithmic one.

Of course, my impression of this preamble is purely subjective, not being connected in any way professionally with electronics, only in an amateur capacity, but still nevertheless, possessing a fairly good knowledge of basic valve and transistor theory through spare time reading of your own journal and various other publications. I only hope that next month's article can achieve the same level of interest; and I suspect that with the same author it will probably surpass it. As always, looking forward to next month's issue.
D. J. Whitaker,

Bromley,
Kent.


#### Abstract

"GEMINI" When the name P.E. Gemini was adopted for the Practical Electronics Stereo Amplifier, we overlooked the fact that Gemini was already associated with products of the Tripletone Manufacturing Co. This company has in fact used the name Gemini for a range of low cost amplifiers since 1959. The current version Tripletone Gemini Mk II is an all solid state design, previous models being valve designs. The output is 5 W per channel and the retail price is $£ 23$ 10s.

We hope there has been no confusion. In fact the P.E. Gemini and the Tripletone Gemini Mk II are quite dissimilar, both in specification and physical appearance.-Ed.


## Beginning

Sir-I was interested to read Mr Bennett's letter (November 1970) and I appreciate the difficulty he and other beginners experience trying to find suitable books to guide them. Perhaps my own experience will be of interest to anyone finding it difficult to get started.

My interest in electronics began about 11 years ago at the age of 13. At that time transistors were relatively expensive, but' I found that old televisions and radios could be acquired for next to nothing, so naturally I began by building valve circuits. 1 visited my local public library, renounced the children's section forever, and looked instead at the mysterious books on electronics. Nearly all of these books explained how the various electronic components functioned but hardly any of them gave practical advice on making a circuit work.

It was around this time that I discovered that magazines were available describing practical circuits and they proved to be very useful. There was nearly always some simple project that I could build and the more complex designs could be left until later. I quickly found that the practical experience helped to make the theory more meaningful.

After a year or two I was building circuits to my own designs and some of the circuits behaved in the most extraordinary way. There were audio amplifiers which oscillated and audio oscillators which didn't. This was very frustrating at the time, but clearing the faults was a challenge and further knowledge was gained in this way. Progressing from one mistake to another may not sound exciting, but its amazing how much you learn. Touching the tags of a capacitor charged to 300 volts d.c. is a mistake that you learn not to repeat too often!

Transistors and small components bring electronics within the reach of anyone having a table and a few hours to spare. A 9 volt battery makes an excellent (and inexpensive) power supply and a surprising number of projects can be built around just two transistors.

My advice to anyone thinking of taking up electronics is quite simple-
have a go! Read as many books and magazines as possible and spend as much time as you can trying out the circuits that are published.
A. M. Rudkin B.Sc.(Eng.),

St. Albans,
Herts.

## Second string boosi

Sir-Your reader Mr A. D. Jones (Readout, November 1970) refers to my article on guitar pick-up construction (September 1970) and points out that an important point was omitted. He is, of course, quite correct in his comment that the magnets should be mounted with like poles at the same end and I regret that this point was not included in the article.

The phenomenon known as "second string boost" amongst guitar players, has for a long time been believed to be due to the particular character of the material, tension, etc. of the second string and it is supposed that this string vibrates with a slightly wider arc than the rest of the set, so producing a stronger signal than its neighbours.
Mounting magnets with opposite poles adjacent will cause interaction between strings and certainly aggravate any tendency the second string may have to vibrate over a wider arc. Fortunately, modern technology has done much to eliminate this irritating feature and good makes of guitar strings do not usually produce this bugbear.
I have made quite a number of pick-ups in the manner described in the article, assuring that all magnets are the same way up, and all have produced an even response on all strings.
L. F. Dickson,

Salisbury,
Wilts.

## MEETINGS

## INSTITUTION OF ELECTRICAL ENGINEERS

LONDON
January 11, 5.30 p.m.
Amorphous Semiconductors, R W. Brander, at Savoy Place. London, WC2R 0BL.

## INSTITUTION OF <br> ELECTRONIC AND RADIO ENGINEERS

LONDON
January 13, 6.0 p.m.
Integrated Circuits for Colour Television, J. C. MacKellar, at 9 Bedford Square, London, WCIB 3RG.

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