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| 1 mA . . . . . $25 /-$ | 50 V d.c. $. . . .26 /-$ | $1 \mathrm{~mA} \cdot \ldots . . . .48 / 9$ | 300 V a.c. |
| $1.0 .1 \mathrm{~mA} \quad . .25 /-$ | 100 V d.c. . .28/- |  | \& Meter lma |
| $2 \mathrm{~mA} \quad . . . .25 /-$ | 150 V d.c. ..25/- | $5 \mathrm{~mA} \quad . . . .4$ 49/6 | VU meter .... 69 |
| $5 \mathrm{~mA} \quad . . . . .28 /-$ | 300 V d.c. . $25 /-$ | $10 \mathrm{~mA} \quad . . . .49 / 6$ | 1 ump a.c.* |
| $10 \mathrm{~mA} \quad . . . .9 .25 /-$ | 500 V d.c. . .28/- | 50 ma . ${ }^{\text {c....49/6 }}$ | 5 amp , a.c.* |
| 20 mA . ${ }^{\text {c. }}$. $25 /-$ | 750 V d.c. . $28 /-$ | $100 \mathrm{~mA} \quad . . .49 / 6$ | 10 amp a.c.* 49/8 |
| $50 \mathrm{~mA} \quad . . . . .28 /-$ | 15 V a.c. $. . .25 /-$ | $500 \mathrm{~mA} \quad . . .49 / 6$ | 30 amp a.c.* . $49 / 6$ |
| $100 \mathrm{~mA} \quad . . .25 /-$ | 50 V a.c. $\quad . . .85 /-$ | 1 mmp . $\quad . . .49 / 6$ | \$0 mmp. a.c." . $49 / 6$ |
| $150 \mathrm{~mA} \quad . . .25 /-$ | 150 V a.c. $\cdot . . .95 /-$ | 5 amp . |  |
| $200 \mathrm{~mA} \quad . . .25 /-$ | 300 V a.c. . . 26 j- |  |  |
| 900mA $\quad . . .85 /-$ | 500 V a.c. . . 25 | Type 18.88 | Inin tronts |
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| $5 \mathrm{~mA} \quad$ - . . . $27 / 8$ | 8 meter $\operatorname{lma} 36 /-$ | 500 mA - . . $89 / 6$ | 50 mA a.c.* . .89/6 |
| 10mA . . . . . $27 / 6$ | VU meter . . 42/6 | 1 mmp . $. . .889 / 8$ | 100 mA a.c.* . $89 / 6$ |
| $50 \mathrm{~mA} \quad . . . . .27 / 6$ | 1 amp a.c.* . $27 / 8$ | 5 amp. . . . . . $89 / 8$ | $200 \mathrm{~mA} \mathrm{a.c.*} \mathrm{}. \mathrm{} 80 /$. |
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$0 / 2 \mathrm{~K} / 200 \mathrm{~K} / 2 \mathrm{M}$; $0 / 2 \mathrm{~K} / 200 \mathrm{~K} / 2 \mathrm{M} /{ }^{2} \mathrm{M}$ )



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## THE TRANSISTOR THREATENED?

「N the two decades since the Americans Bardeen, Brattain, and Shockley invented the transistor, countless new and dramatic developments have occurred in the field of semiconductors. The material originally used, the rare element germanium, has given way to the more common material silicon, with drastic reduction in costs. Manufacturing techniques have advanced, so bringing about further economies, and also making possible a variety of active circuit devices closely related to the transistor, but endowed with certain features which allow each of them to make a marked individual contribution to progress in electronics technology.

Dissimilar in certain respects, such as construction, mode of operation and circuit function, all present day semiconductor devices do share a common physics background: all exploit the property of conduction through solid crystalline material by means of positive and negative current carriers.

But now a further discovery has been made that may revolutionise solid state technology. Another American, S. Ovskinsky, has discovered that non-crystalline substances possess the property of changing from high to low resistivity under certain conditions, and he has invented a microminiature two-way switching device based on this effect. The material used is an inexpensive amorphous glass substance and it is claimed that this device will have considerable advantages over the transistor, especially for computer and communications applications. Apart from cheapness of production, this new device is claimed to be more reliable than existing devices.

Professor Mott, director of the Cavendish Laboratory at Cambridge has described this as the newest, and most exciting discovery in solid state physics at the moment. Prof. Mott was speaking as a scientist of course, and it rests with the technologists to develop and prove this as a practical circuit element. There may be certain yet unseen snags in transferring this invention from the research laboratory to the production floor. There may also be a lack of enthusiasm from the semiconductor manufacturers if this newcomer seems likely to become a serious rival to the established transistor and diode. But the expansion of the computer industry and the widespread adoption of pulse techniques for control and communication purposes suggest there will be an ever growing demand for electronic switching devices in the future. Such commercial doubts are likely therefore to be quickly dispelled.

For the layman, it is difficult to contemplate a component that will outshine the transistor in efficiency, cost, and size. We can only wait and see.
F. E. Bennett-Editor

## CONSTRUCTIONAL PROIECTS

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Fig. I. Block diagram of the metal locator

## OSCILLATOR CIRCUIT

The complete metal locator circuit appears in Fig. 2. Looking first at the search coil, this consists of two windings, L1 and L2, which are inductively coupled. Ll , tuned by fixed capacitor Cl and trimmer VCI, is intentionally left "floating" to reduce capacitive effects when the coil is brought close to non-metallic bodies. Also, 4-turn coil L1 has a low intrinsic value of inductance, so that any unavoidable stray capacitance changes will be swamped by the large values of Cl and VCl needed to bring L1 to resonance at the working frequency of 300 kHz .

Coupling coil L2 forms the collector load of common base oscillator TR1, while collector-emitter capacitor C4 introduces positive feedback, and trimmer VC2 acts as a fine frequency control for adjustment of the audio note when the metal locator is in use. A small proportion of the available output from TR1 emitter is tapped off by VRI, and this particular arrangement largely eliminates the tendency of one oscillator to "pull" and lock with the other when their frequencies are almost the same.
The combined mixer and reference oscillator TR2 is similar to that used in a superhet receiver, and in fact employs a standard transistor medium-wave
oscillator coil (L3, L4, L5), tuned by C8 to a fixed frequency of about 300 kHz . Inductive coupling between L3 and L4 provides self-oscillation, and the reference frequency, while the signal derived from the search oscillator is fed to the base of TR2 and is amplified. Both frequencies are combined at the collector of TR2, but instead of the usual i.f. transformer, a simpler arrangement of a fixed resistor and a capacitor (R7 and C7) suffices to filter out the two high frequencies and leave the resulting difference or beat frequency.

The two remaining stages, consisting of components associated with TR3 and TR4, merely serve to amplify the audio beat note to a level adequate for headphone listening in a noisy environment.

## CONSTRUCTION

A 6 ft length of nylon curtain rail is bent round to form a 23 in diameter circular search coil former. Although not particularly rigid when unbraced, the coil former is tough and light, and can be prevented from vibrating by a string stay. The stay also serves as a shoulder strap when transporting the folded metal locator to and from a site.

Fig. 2. Circuit diagram of the metal locator and connection details for the oscillator coil and transistors



Fig. 6. Component layout and wiring details and drilling template for the circuit panel contained in the handle of the metal locator
$R I N$

## Potentiometer

VRI $100 \Omega$ miniature horizontal skeleton preset (Body size $0.5 \mathrm{in} \times 0.37 \mathrm{in} \times 0.12 \mathrm{in}$ )

| Capacitors |  |  |  |
| :---: | :---: | :---: | :---: |
| *CI | 1,000pF polystyrene | 125 V | $0.6 \mathrm{in} \times 0.25 \mathrm{in} \mathrm{dia}$ |
|  | $0.047 \mu \mathrm{~F}$ polyester | 250 V | $0.45 \mathrm{in} \times 0.17 \mathrm{in} \mathrm{dia}$ |
|  | 3.000 pF disc ceramic |  | $0.1 \mathrm{in} \times 0.5 \mathrm{in} \mathrm{dia}$ |
|  | 470pF polystyrene | 125 V | $0.4 \mathrm{in} \times 0.2 \mathrm{in} \mathrm{dia}$ |
|  | $0.047 \mu \mathrm{~F}$ polyester | 250 V | $0.45 \mathrm{in} \times 0.17 \mathrm{in} \mathrm{dia}$ |
|  | $0.047 \mu \mathrm{~F}$ polyester | 250 V | $0.45 \mathrm{in} \times 0.17 \mathrm{in} \mathrm{dia}$ |
|  | $0.047 \mu \mathrm{~F}$ polyester | 250 V | $0.45 \mathrm{in} \times 0.17 \mathrm{in} \mathrm{dia}$ |
|  | 1,000 pF polystyrene | 125 V | $0.6 \mathrm{in} \times 0.25 \mathrm{in} \mathrm{dia}$ |
|  | $100 \mu \mathrm{Felect}$. | 15 V | $0.75 \mathrm{in} \times 0.3 \mathrm{in} \mathrm{dia}$ |
|  | $0.1 \mu \mathrm{~F}$ polyester | 250 V | $\begin{aligned} & 0.53 \mathrm{in} \times 0.41 \mathrm{in} \times \\ & 0.26 \mathrm{in} \mathrm{dia} \end{aligned}$ |
| CII $1 \mu \mathrm{~F}$ elect. C12 $16 \mu \mathrm{~F}$ elect. |  | 15 V | $0.5 \mathrm{in} \times 0.12 \mathrm{in} \mathrm{dia}$ |
|  |  | 15 V | $0.6 \mathrm{in} \times 0.22 \mathrm{in} \mathrm{dia}$ |
| $\mathrm{Cl} 31 \mu \mathrm{Felect}$. |  | 15 V | $0.5 \mathrm{in} \times 0.12 \mathrm{in} \mathrm{dia}$ |
| $\mathrm{Cl} 416 \mu \mathrm{Felect}$. <br> * CI, C4, C8 are $\pm$ |  | 15 V | $0.6 \mathrm{in} \times 0.22 \mathrm{in} \mathrm{dia}$ |
|  |  | \% t | yp |

Trimmer Capacitors
VCI 500 pF
$0.94 i n \times 0.62 i n \times 0.18 i n$
VC2 140pF 0.81 in $\times 0.38$ in $\times 0.25 i n$ Both VCI and VC2 are "postage stamp" type

Inductors
LI Wound with $14 / 0076$ p.v.c. covered wire (see text)
L2 Wound with $7 / .0048$ p.v.c. covered wire (see text)
L3, L4, L5 M.W. oscillator coil type P50/IAC (Weyrad)

Transistors
TRI. TR2 2N2926 (orange grade) (2 off)
TR3, TR4 OC7I (2 off)

## Switch

SI Single-pole changeover miniature toggle switch

Plug and Socket
PLI and SKI Jack plug and socket open circuit jack type J 2 and plug $\frac{1}{4}$ in dia, 2 contacts (Bulgin)

Miscellaneous
Headphones 2 to $4 \mathrm{k} \Omega$ d.c. resistance
Nylon curtain rail 7 ft (Swish) (see text)
Plywood $\frac{1}{4}$ in thick, I sq ft
Plywood $\frac{3}{6}$ in thick, 8 in $\times 4$ in
Dowel rod $\frac{3}{4}$ in dia. 3 lin long
S.R.B.P. sheet 6 in $\times \frac{3}{4}$ in $\times \frac{1}{16}$ in

Battery connectors, plastics wheel, nuts and bolts (see text)
will help to hold the dowel in place. Take care to keep the slot in the dowel free from glue, and facing towards the interior of the handle.

The $\frac{3}{8}$ in diameter plastics screw used to clamp the dowel and discs on the prototype was taken from a broken toilet seat; it is important to avoid the use of metal attachments (bolts) at the search coil end of the metal locator. Drill the discs and flat end of dowel to suit the size of clamping screw employed.

Drill holes in the handle to take S1 and SK1, in the positions shown in Fig. 4, and make up a battery box from three pieces of s.r.b.p. sheet or plywood. All wooden parts should be finally sandpapered and given one or two coats of protective varnish.

## WINDING THE SEARCH COIL

Small holes can be drilled through the nylon coil former to take start and finish leads of L1 and L2. L1 consists of four close wound turns of $14 / \cdot 0076$ p.v.c. covered wire, while L2 is a single turn of $7 / 0048$ insulated wire, the latter with ends left long enough to go up the slot in the dowel and into the handle (shown colour coded yellow in Fig. 2). Note that the single turn L2 is wound in the upper channel of the curtain rail, away from L1. It is advisable to fit protective sleeving to all coil lead-outs; L1 turns should be covered with a layer of plastics insulating tape to prevent movement.

Obtain a small plastics box, or make one up from wood, s.r.b.p. or polystyrene sheet glued at the edges, to take Cl and VCl . Two holes are drilled at one end of the box to admit L1 leads. There are holes also to take VCl mounting screw, and a trimming tool. To mount the completed box assembly on the search coil


## Layout of components mounted inside the handle

Drill a $5_{\frac{7}{8}} \mathrm{in} \times{ }^{18}$ in piece of s.r.b.p., from the full size plan given in Fig. 5. Prior to mountirg components, fit a lin long 8B.A. screw to trimmer VC2, in place of the existing adjuster screw, and bend up one of the trimmer solder tags. Also, at this stage, use the circuit panel as a template to mark out and drill holes in the handle for VC2 screw, and the two 6B.A. countersunk mounting screws.

former, carefully make a hole to clear VCl mounting nut, in the top of the two short rail sections and glue the box to the rails in the position shown in Fig. 2.

If it is intended to use the search coil submerged in several inches of water, when exploring river bed or pond, for example, then the box, VCl mounting screw and nut, and L1 lead entry holes must be thoroughly waterproofed with a generous layer of epoxy resin glue, before fixing to the search coil former. The trimming tool hole can be rendered waterproof with a small rubber bung.

## CIRCUIT PANEL

In order to accommodate the complete circuit panel in the space provided by the hollow handle, components used must not be much larger than those employed in the prototype. It is often the case, especially with capacitors, that components of near identical electrical specification will differ in size, and for this reason certain dimensions are included in the component list, to assist the reader.

After riveting the two turret tags to the panel, progressively mount all components and complete underside wiring. VC2 only needs to be initially secured to the circuit panel by blobs of solder on its push-through tags. Ensure that one of the oscillator coil can tags is wired to the positive circuit rail for screening purposes, and that the polarity of electrolytic capacitors is correct.

## FINAL ASSEMBLY

Mount S1 and SK1 in the handle, and solder a 4B.A. nut to the lug on SK1, to take the lid fixing screw (see Fig. 3). Wire the battery connector to S 1 and leave short red and blue leads for later connection to the circuit panel turret tags.

Slide the plywood discs on to the search coil former and clamp the dowel between the discs. Take the L2 leads and thread them into the handle, then retain the leads in the dowel slot with a thin smear of glue.


Referring to Fig. 3, two plywood discs are slotted on to the search coil former, and a wooden dowel stick is clamped between the discs by a large plastics screw and nut. Leads from L2 are taken up the stick, inside a groove, to the handle, which houses the circuit panel, battery, on/off switch, fine frequency control, and a headphone socket (Fig. 4). The handle is made from a hollowed out sandwich of plywood,

Close-up view of the search coil fixing




Fig. 7. Sensitivity response across the search coll


Fig. 8. Method of pinpointing location of small metal object

Next, install the circuit panel on stand-off spacers inside the handle, and connect up yellow panel wires to L2 leads, green and blue wires to JK1, and the red battery wire to its turret tag. A small plastic disc or gear wheel should be fixed between two 8B.A. nuts on the VC2 screw.

## TESTING OPERATION

To test for correct circuit action, connect a milliameter between the blue battery wire and its turret tag, fit a battery, and switch on S1. A meter reading of about 3.75 mA will show that there are no serious faults. Set VR1 to half track and insert the headphone plug in SK1. A hiss should be heard with perhaps a low level whistle. Rotate VC2 knob fully clockwise (maximum capacitance), then turn back half a turn. With a trimming tool, screw down VC1 slowly, and listen for a succession of whistles produced by oscillator harmonics. Near the maximum capacitance position of VCl a much louder whistle should occur, indicating that L 1 is tuned close to the reference oscillator frequency.
Now with the search coil laid flat on the ground, away from metal objects, it will be found that a null point-where the audible note drops in pitch to zero frequency and then begins to rise again-can be obtained by adjusting VC1 and trimming VC2. Without moving the search coil, bring a metal object into the field of the coil and listen for a note of rising pitch.
It only remains to attach the handle lid by its screws and fit the search coil string stay. Drill a small hole in the top of the search coil former at a point opposite the plywood discs, also drill a hole in the dowel just below the handle. Set the dowel at an angle to the search coil which suits the height and arm length of the user, then tie plastics sleeved string between the search coil and the dowel.

## HINTS ON METAL LOCATION

Best sensitivity results from having a steady low frequency note in the headphones, which is caused to increase in pitch as the search coil is brought near metal. As a given beat frequency occurs twice, on
each side of the zero frequency null point, it is important to select the right one. First obtain zero frequency with VC1, then unscrew VC2 towards minimum capacitance, so that the frequency of the search oscillator is slightly above that of the reference oscillator.

A sudden decrease of beat frequency will be observed when the search coil is lowered through the last $\frac{1}{2}$ in before touching the ground. It can be easily verified that this phenomenon is not wholly due to capacitive effects, but that the ground is acting as a "lossy" insulator. Suspend a large sheet of p.v.c. from the ceiling, positioned well away from all metal objects, and then bring the search coil towards the plastic, whereupon a similar decrease of frequency will be experienced. The "lossy" insulator effect is not too troublesome, and will only reduce sensitivity when searching over rough ground or in long grass.

It is possible to locate metal objects when they are submerged in fresh water, but sea water-being a good conductor-will affect the inductance value of the search coil. Nevertheless, it is still feasible to search for metal on a dry beach, or above the high water mark.
Despite the large diameter of the search coil, small buried objects can be located precisely by making use of the peak of maximum response which is situated near the inside edge of the search coil, see Fig. 5. The method given in Fig. 6, of first sweeping the edge of the coil across the object, then making a second sweep at 90 degrees to the first, will reveal the exact point of intersection.

As a rough guide to sensitivity, a 5 in diameter 9 ounce brass ashtray should be detected under some 15 in of soil, and something the size and weight of a motor car body will give a clear response at a distance of more than 9 ft when submerged in fresh water.


## BATTERY CHARGER

The demand for small battery chargers for the more common alkaline cells now being used more frequently in transistor radios, cameras, tape recorders, etc. has long been needed to offset the relatively high cost as well as the superior performance obtainable with these types of batteries.
The Pencel battery charger from DCB Instrument and Lighting Co., Austin House, Croft Road, Crowborough, Sussex, is one of many now appearing on the market designed specially for the more standard type of alkaline battery. Designed to cover the type of battery used in domestic equipment, it is claimed that 1.5 V cells can be recharged between ten and thirty times, depending on operational conditions.

The Pencel charger is very neatly finished and as our photograph shows the cells are simply pressed into a holder mounted on the top of the unit. The charger costs 79 s 6 d , including four alkaline batteries which should soon pay for itself in increased battery life/hours.

## TEST METERS

A neat and versatile pocket test meter has just been introduced by West Hyde Developments Ltd., 30, High Street, Northwood, Middlesex.

Called the Tech Test Meter it costs only 45 s including test leads, has 15 ranges for measuring 0 to 1 kV a.c./d.c. and 0 to 100 kilohms. The meter movement has jewelled bearings and has a sensitivity of 1,000 ohms per volt.

The low cost of this meter means that it is possible to leave a number of multi-meters in circuit on the bench instead of using a more expensive, single range meter.

A more expensive, sensitive and professional multimeter is the Kelvo 6E Electronic Multimeter. This meter incorporates a self contained battery-operated d.c./a:c. amplifier enabling measurements to be made with an accuracy of $\pm 1$ per cent.

The instrument has 26 voltage ranges, permitting measurements from 1 mV f.s.d. to 1 kV d.c. and a.c. The sensitivity of the meter is 1 megohm per volt at 1 mV to 10 V and 10 megohm per volt at 30 V to 1 kV . There are 28 current ranges from $1 \mu \mathrm{~A}$ to 3 A d.c. and a.c. Also, there are six resistance ranges for measurements between 0.2 ohm and 50 megohms, and six capacity ranges from 50 pF to $5,000 \mu \mathrm{~F}$.

The meter is fitted with a sensitive and effective automatic electrical cutout which operates independently of the condition of the batteries, protecting the instrument from damage if subjected to overload.

It is claimed that under continuous 24 hour usage the batteries will enable the instrument to operate satisfactorily for approximately two months.

The Kelvo 6E multimeter is marketed by Smiths Industries Ltd., Kelvin House, Wembley, Middlesex, and cost $£ 72$.

## RESEARCH INSTRUMENT

Designed for Industrial Laboratories and University Research Departments is the Universal Digital Instrument type EU-805 from Daystrom Ltd., Bristol Road, Gloucester.

The Heath 805 Universal Digital lnstrument is a multi-purpose instrument capable of many high accuracy standard measurements and has the adaptability and versatility required of modern research instrumentation. Some of the functions include events counter; frequency meter; integrating digital voltmeter; ratio meter; time interval meter and voltage integrator.

The unit incorporates a modular design based on 16 plug-in cards using TTL Integrated Circuit. This design enables card additions for new functions to be added, protecting it against obsolescence.

In both academic and industrial use it is intended as a companion to the Heath Analogue Digital Designer EU-801-A, a device for teaching digital logic and instrumentation.

## FILMS FOR CLUBS AND SCHOOLS

Of interest to schools and clubs are two films from the Central Office of Information and Mullard Ltd.

The Mullard film is entitled Mullardability, is in colour, runs for 9 minutes and is of 16 mm gauge.

The film begins with illustrations of Mullard's capability to massproduce vast ranges of electronic components, from television picture tubes to micro-electronic circuits. Finally the film deals with the Mullard Research Laboratories and its study and development of new materials, systems and techniques of the future.

Copies of the film are available on free loan from Mullard Film Library, Kingston Road, Merton Park London, S.E. 19.
The COl film is entitled Movi= from Computers, runs for 20 minutes and is of 16 mm gauge. The film contains extracts from existing com-puter-generated industrial and research films. The use of computers in mathematics, physics and engineering, and also the use in flight training techniques are shown.

The film is available from the Central Film Library, Government Building, Bromyard Avenue, Acton, London, W.3. The hire charge is $£ 1$ for one day only (reduced charges for additional days). In addition to the hire charge there is a surcharge of 1s 6 d per 16 mm reel to cover despatch and certain other handling costs for the first day of hire only.

The current edition of the Films in Industry catalogue is also available from the Central Film Library, price 4s 6d post free.


Items mentioned in this feature are usually available from electronic equipment and component retailers advertising in this magazine. However, where a full address is given, enquiries and orders should then $b=$ made direct to the firm concerned.


Pencel battery charger from DCB Instrument and Lighting


Heath EU-805 Universal Digital Instrument from Daystrom


Kelvo 6E meter marketed by



# IIST OSCIILIATOR 



This is the concluding project in our five-part series featuring the integrated circuit linear amplifier Type SL701C

The expected waveforms are drawn in Fig. 2. If we assume that immediately we switch on, the output $c$ goes to +2 V , then the input $b$ will go to +1 V , and Cl will charge up (through R2) from zero towards +2 V . When the input a just exceeds the +1 V of input $b$, the circuit will rapidly switch to the other state of $c$ at -2 V and $b$ at -1 V . Cl then discharges from $+1 V$ towards -2 V and the circuit reverts to its previous state when a now reaches -1 V ; and so on. The $a$ waveform is therefore of an exponential form. The mark to space ratio of the waveform should be one to one, and the frequency is given by:

$$
f=\frac{1}{2 \cdot 2 C_{1} R_{2}} \mathrm{~Hz}
$$



Fig. 2. Waveforms associated with Fig. I arrangement


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## SQUARE WAVE OSCILLATOR CIRCUIT



Fig. 3. Complete circuit diagram for the square wave test oscillator

## CONSTRUCTIONAL DETAILS



## COMPONENTS . . .

Resistors

| R1 | $12 \mathrm{k} \Omega$ |
| :--- | :--- |
| R2 | $5.6 \mathrm{k} \Omega$ |
| R3 | $12 \mathrm{k} \Omega$ |
| R4 | $12 \mathrm{k} \Omega$ |
| R5 | $3.3 \mathrm{k} \Omega$ |
| R6 | $680 \Omega$ |
| R7 | $1.2 \mathrm{k} \Omega$ |
| All | $10 \% \cdot \frac{1}{4} W$ carbon |

Capacitors
CI $\quad 0.047 \mu \mathrm{~F}$ ceramic or polyester
39pF ceramic
1,000pF ceramic
1,000pF ceramic


Fig. 4. Layout and wiring for the oscillator

## Miscellaneous

ICl Linear integrated circuit (d.c. coupled amplifier - SL70IC) (available direct from the makers: The Plessey Co. Ltd., Components Group. Cheney Manor, Swindon, Wiltshire. Price: 18s)
VRI $10 \mathrm{k} \Omega$ carbon potentiometer
D1, D2 IN914, IN916 (2 off)
SKI $\quad 4 \mathrm{~mm}$ ' socket and matching plug or coaxial socket and plug (Radiospares)
Die-cast box $4 \frac{3}{4}$ in $\times 3 \frac{3}{4} \mathrm{in} \times$ lin (Electroniques 46R.043A)
Perforated s.r.b.p. 3 in $\times 2$ ifin
Three insulated feed-through terminals
Control knob
6BA screws and nuts


Fig. 5. Some other passible applications of the operetional amplifier: (a) integration; (b) summation; (c) Icw pass filter; (d) limiting; (e) backlash circuit

## THE PRACTICAL CIRCUIT

The practical circuit is shown in Fig. 3, and there are several additional points worthy of explanation.

The output is limited by the diodes D1, D2 to $\pm 0.6 \mathrm{~V}$, which keeps $b$ within the permitted value $\overline{\text { of }}+0.5 \mathrm{~V}$ for correct common mode working of the differential input stage. The peak output current from the amplifier through the diodes is limited in a positive direction by the 680 ohm resistor R 6 .

The Plessey SL701 integrated circuit is intended primarily for linear applications, rather than as a switching comparator, and therefore exhibits some hysteresis in its switching characteristics (like a Schmitt trigger) so that the output waveform from a circuit like Fig. I would be a rectangular waveform of about three to one mark to space ratio (rather than a square waveform as we would expect). This could be adjusted by returning R 1 to a voltage rail slightly offset from earth, but this would mean a potential chain of two resistors. For our application we have chosen to include a resistor in series with the timing capacitor as an alternative approach. This ensures that the mark to space ratio is almost one to one, and for our particular circuit values, this measure increases the frequency from the 800 Hz calculated from the formula to about 1 kHz .

## PERFORMANCE

The maximum output is about IV peak to peak, spaced equally about earth potential, and has a rise time of $0 \cdot 2 \mu$ seconds which is much faster than that required for audio applications. An amplifier with a bandwidth of 1.5 MHz would degrade this edge to $0 \cdot 3 \mu$ seconds.

Since the output impedance of the circuit depends on the setting of the output potentiometer VRI and is in any case 1 kilohm at best, any capacitive load will degrade the edge. For video testing the output lead should be kept as short as practicable, while for audio use the rise time will be adequate in all positions of the potentiometer, provided the capacitive loading does not exceed 50 pF .

## CONSTRUCTION

Construction is detailed in the diagram of Fig. 4 and should present no difficulty provided a similar layout is followed.

All components except R7, SKI and VR1 can be mounted on a piece of perforated s.r.b.p. $3 \mathrm{in} \times 2 \frac{3}{8} \mathrm{in}$. Holes are drilled in the base to accommodate VR1, SK 1, the three feed through supply terminals and the mounting screws for the board. Spacers are used under the board to prevent shorting between it and the base.

It is essential to use reasonably fast diodes in order to maintain the fast rise time inherent in the circuit, but the type specified is available from a number of suppliers.

## OTHER APPLICATIONS: USING THE <br> I.C. IN TRUE OPERATIONAL MODE

In this series of five articles based upon the SL701C integrated circuit we have considered some of the many possible ways of using this high gain amplifier, but ironically we have not in fact considered any of the analogue computing techniques which would use the amplifier in a true operational mode. An operational amplifier is one which takes the input signal and operates on it in a known mathematical way. Some of the analogue computing circuits which can be realised with a high gain amplifier are given in Fig. 5.


Fig. 6. Further applications using more than one operational amplifier: (a) square and triangular generator; (b) selective amplifier; (c) low frequency oscillator

## Integration

We can, for example, integrate a square wave to obtain a triangular waveform.

## Summation

We can add or mix a number of inputs to provide a combined output.

## Low pass filter

This circuit could be used as a scratch filter for an audio amplifier.

## Limiting

This circuit uses diodes to provide heavy negative feeđback to limit an input signal to $\div 0.6 \mathrm{~V}$.

## Backlash circuit

This can be used in a similar fashion to a Schmitt trigger.

If we are more ambitious we can use several integrated circuits, and projects such as Fig. 6 become possible.

## Square and triangular waveform generator

By combining an integrator and a backlash circuit we can produce a square and triangular waveform
generator relatively simply. We could even shape the triangular waveform with yet a third circuit, to produce a sinewave.

## Selective amplifier

Two integrators and an inverter can be used to form a frequency selective amplifier.

## Low frequency oscillator

By adding positive feedback to the selective amplifier, this can be made into an oscillator which will work over a frequency range from seconds per cycle (heavy negative feedback sets the d.c. conditions and autputs can be directly coupled) up to about 20 kHz for the Plessey amplifiers.

By using a fourth integrated circuit in a peak amplitude detector (not shown) the distortion of the arrangement can be kept down to $0 \cdot 1$ per cent. Using variable resistors and switched capacitors this circuit can give us a two phase oscillator from 2 Hz to 20 kHz , and, with appropriate switching, a selective amplifier covering the same range with a variable, known Q . This shows the range and versatility of the analogue approach.

But an instrument such as this must wait until we can afford to use four integrated circuits in one constructional project, and for the moment we must be content to incorporate the odd single integrated circuit. We hope that these five articles have been of some help in this respect.


## BY G.c.BROWN

M.S.H.A.A., A.M.R.S.H.

LAST month we examined the basic conditional reflex response as exhibited by some animals, and considered the possibilities for its synthesis. We shall discuss now the form which the hardware will take for the demonstration of this simple learning, also how it may be interconnected with the existing circuitry mentioned in the last article. There are in fact numerous circuit arrangements which will "fit the bill", but we shall deal with just one relatively simple example, this however will work quite successfully in our model.

## LEARNING CIRCUIT

It will be remembered from our earlier "black box" hypothesis of a learning circuit that only a very• few building bricks were utilised; i.e. a short term memory to extend $S_{n}$ for a period of time to allow for coincidences with $S_{\mathrm{s}}$, a differentiator for $S_{\mathrm{s}}$, a means for summing the coincidences, and a few gates to route pulses at the correct times. A look at Fig. 3.1 will disclose just how simple the circuit really is. There are a few additional items of course such as the bistable flip-flop and the Schmitt; these however provide certain improvements and will be explained later on. Fundamentally, the results from a circuit without these extras would be very similar, in that it would exhibit the characteristics we are looking for.

The present circuit configurations of the model demand that the learning section be rather more digital than analogue, and so the basic schematic diagram has been tailored a little to meet this need. The reader will notice at once that our use of the term "differentiator" has been stretched considerably, so far in fact as to include in this instance a monostable! However, there is a reason for this.

## LONG OVERLAP PERIOD

Look at Fig. 3.I again. Imagine that the neutral stimulus has just occurred, and suppose now that the specific stimulus appears. Now if we are going to achieve any useful summing action during the overlap period of the specific and neutral stimuli, a differentiator using a capacitor of massive proportions would be required. It needs a while for us to appreciate this situation-in electronics we are used to things happening at enormous speeds: in this particular case though, everything has slowed right down and we are dealing with quantities which occur at rates we could comfortably count on our fingers.

The requirement then for our differentiator seems to be not thal it chops off a very thin "slice" from the beginning of each $S_{5}$ event, but that it takes a good sized chunk. If we make it behave in this way we are now the possessors of a very sloppy differentiator indeed, however this is of ro concern

The output waveiform from this version naturally has none of the slender proportions of that seen from the normal type, and so it was decided that it might be as well to dispense with it altogether and use a monostable instead. This we have done. And where the "chastity" of the nice neat spikes from a normal differentiating circuit would have meant summing several hundred repetitions of the specific stimulus during the extension of $S_{\mathrm{n}}$, we now need only a few.

## CIRCUIT INTERCONNECTIONS

So that the reader can appreciate how the learning circuit is interconnected with the existing hardware, the relevant sections of the reflex circuitry are reproduced again in Figs. 3.2 anc 3.3.


Fig. 3.I. The simple learning circuit. This is interconnected with the sensor and reflex sections of Fig. 2.4


Fig. 3.2 Part of Fig. 2.4 with additions to permit extraction of the neutral stimulus

Fig. 3.2 shows the method for extracting the neutral stimulus $S_{\mathrm{n}}$.

The specific stimulus $S_{\mathrm{s}}$ is picked-off direct at the tactile probe in the reversing monostable indicated in Fig. 3.3; a diode D9 has been included here, the purpose of which will be seen shortly. A further connection is also made to the other side of the reversing monostable for use when the model has "learnt" a particular combination of stimuli-this will be discussed later.


Fig. 3.3 Part of Fig. 2.4 with modifications to permit extraction of the specific stimulus and application of the "learnt" signal

## OPERATION

Assuming now that all the necessary connections have been made between the reflex and "learning" sections of the model, we are now in a position to see how the circuits operate and interact with each other.

If a neutral stimulus (light) is applied by way of the photo-transistors in the reflex section, then a positive pulse will pass via either diode D1 or D2 to turn off transistors TR1a and TR1b of the extension mono-

Stable. This monostable will now turn over and hence TR2 collector will become positive, resulting in one side of the summer gate being opened. The extension monostable will remain in the quasi-stable state for approximately 60 seconds. If the tactile sense is stimula ted by way of the probe in the reflex section, then a positive pulse will be fed via diode D7 resulting in transistor TR7 of the differentiator monostable being switched off. Hence this monostable turns over and produces our so-called "differentiated" pulse which in fact lasts about 1 second.

The collector of TR8 being connected via R8 to the other side of the summer gate hence causes the base of TR4 to go positive, opening this side of the gate. Now if the stimulus $S_{\mathrm{n}}$ occurs any time up to 60 seconds before $S_{\mathrm{s}}$, then the result of their coincidence, or overlap, will cause the common collector point of the summer gate to go negative (i.e. both sides of the gate opened).

Diode D5 will now be forward biased and capacitor C3 will commence charging towards the negative rail potential. This capacitor thus serves as a sort of shortterm memory for coincidences of $S_{\mathrm{s}}$ and $S_{\mathrm{n}}$, and at this stage could be connected almost directly to the learnt gate were it not for the fact that in practice we require a more permanent memory.


Fig. 3.4 Case where a conditioned reflex is evoked during one application of the neutral stimulus

## THRESHOLD SCHMITT

In order to improve the learning circuit a little, the threshold Schmitt and the semi-permanent memory bistables have been added. Once potentiometer VR1 has been set to some value, the Schmitt will act as a threshold device and fire when the voltage due to the charging of C 3 reaches a certain level (see Fig. 3.4). This will be equivalent to a given number of repetitions of $S_{\mathrm{s}}$ during extension of $S_{\mathrm{n}}$, and in practice should be between 10 and 20 separate stimulations.

When the threshold Schmitt fires a negative going pulse will be passed via the diode D6 and capacitor C7 to the semi-permanent memory bistable.

## MEMORY BISTABLE

It will be noticed that the base bias for transistor TR11 of the bistable is derived via a resistor (R25) whose value is relatively small compared with that in the opposite side (R24). This has been. done deliberately to ensure that upon switching the circuit on an irrelevant memory is not produced.

Once the bistable has switched, one side of the learnt gate will be opened and hence any further $S_{\mathrm{n}}$ stimuli will open the other side also. The common collector point of the gate will thus go negative for all future applications of $S_{\mathrm{n}}$ and switch the reversing monostable via capacitor C 6 (this could be replaced by a diode for analogue functions). Hence instead of $S_{\mathrm{n}}$ causing the usual homing response, this is now inhibited; the model now "cowers" and backs away with "its tail between its legs".

This will always be the result from now on, unless the permanent memory is disturbed by, say, the power supplies being switched off, or unless we make special arrangements for inhibiting the response. Suitable circuitry can be designed for this, modelled upon the black box systems shown earlier: however it is not at this stage intended to go into the actual design for the electronics.

## CASE OF INSUFFICIENT STIMULI

Hitherto for the convenience of describing the operation of the learning circuit, we have assumed that a sufficient number of repetitions of $S_{\mathrm{s}}$ always occur during the extension period. But suppose $S_{\mathrm{s}}$ appears only a few times, insufficient to fire the threshold Schmitt: what will be the effect? The capacitor C 3 will have received some charge, but if no further $S_{\mathrm{s}}$ arrives its voltage level will gradually diminish. However, this is the way we want it to be. This after all is the situation where an insufficient number of coincidences have occurred for the model to "see" any significant connection between $S_{\mathrm{s}}$ and $S_{\mathrm{n}}$.
If on the other hand further combinations of the two stimuli occur during the slowly decaying voltage across C3, their result will be added to whatever level the capacitor has discharged to. This may, or may not, cause the threshold Schmitt to fire for it will depend solely upon the threshold voltage being reached.

In practice, if the coincidences are sufficiently infrequent, conditioning could take hours, and in fact may never take place at all. During conditioning experiments with real live animals exactly the same effect takes place if the neutral and specific stimuli are not allowed to overlap of ten enough.

## FUNCTION OF D9

Earlier we mentioned the inclusion of diode D9 in the reversing monostable, its purpose will become clear upon examination: without the diode, after conditioning has taken place the neutral stimulus could thereafter trigger the differentiator monostable via the "avoidance system". This would result in capacitor C3 being charged due to $S_{\mathrm{n}}$ alone-not that this would matter, you might say, because the model has been conditioned.

However, if we decided to build in an inhibit circuit (which the constructor might choose to do) an irrelevant "short term" memory would already be stored in C3, and although this might not affect the inhibition it could none-the-less cause some problems. D9 thus overcomes this difficulty.

## MORE FACTS ABOUT ANIMAL BEHAVIOUR

Having satisfied ourselves that we can indeed simulate the simple conditional reflex (albeit by quite different and less sophisticated means than those used by a biological example), we must now "press on" and discover some more facts about animal behaviour.

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Also whether or not we can arrange these facts to fit within our framework of black box analogies.

When we began discussing the conditional reflex, the reader will remember how we stressed the importance of the neutral stimulus being presented just before the specific stimulus if conditioning were to occur. As in all things there is the inevitable exception to the rule; this particular exception is, however, an important one as we shall see.

Let us assume we have conditioned an animal (it might be one of Pavlov's dogs) to, say, salivate when a bell is rung-the bell (neutral stimulus $S_{\mathrm{n}}$ ) having been previously "paired" with food (specific stimulus $S_{s}$ ). We might now require the dog to salivate upon our touching its left ear. This presents no problem, we can simply combine this stimulus with $S_{\mathrm{s}}$ when the dog is fed, as we did with the bell. Suppose though that in this particular experiment we are forbidden to make this conditioning direct, is there any other method that can be adopted? This is where we meet the exception to the rule.

## SECOND ORDER CONDITIONED REFLEX

Bearing in mind that the dog is conditioned to "bell-means-food", if we now wish to introduce a further stimulus (in this case tactile), to mean food, all that is


Fig. 3.5 Basic arrangement for synthesising higher or second order conditioning
necessary is to couple the new stimulus with $S_{\mathrm{n}}$. After several repetitions of the new stimulus (call it $S_{\mathrm{n}_{2}}$ ) just prior to the original neutral stimulus (let us now call this $S_{\mathrm{n}_{1}}$ ) we will find that the condition "touch on left ear-means-bell-means-food" has been established. There will hence be an indirect connection between $S_{\mathrm{n}_{2}}$ and $S_{\mathrm{s}}$ via $S_{n_{1}}$, with the result that the dog will salivate upon application of $S_{\mathrm{n}_{2}}$ alone.

This type of conditioning is called a second order conditioned reflex, and has been taken experimentally to third, fourth, and even higher orders. There does in fact seem to be almost no limit to how far this effect can be extended.

This carries us on to another interesting phenomenon, that of stimulus generalisation. Now that touching the dog's left ear elicits the salivation response, what if we touch the right ear, or the nose, or the right paw, or the tip of his tail-will there still be a response? Certainly there will: however if we measure the amount of saliva produced there will be a noticeable decrease as we move away from the site of initial conditioning.

The important fact that remains is that the animal was never conditioned to anything other than the tactile stimulation to the left ear. The dog seemingly generalises about all the other stimuli.

## TRACE REFLEX

Yet another effect emerges in many ways similar to the phenomena just described, which is worthy of some explanation. Generally speaking if a conditional reflex is to be produced, the neutral stimulus must come just before $S_{\mathrm{s}}$, certainly within a minute or so. There is though a method whereby even this rule may be broken.

If during the time we are establishing the conditioned reflex we gradually make $S_{\mathrm{s}}$ come later and later, it is possible to increase the $S_{\mathrm{n}} / S_{\mathrm{s}}$ interval to quite considerable lengths and yet still produce the conditioning effect. This is known as a trace reflex and extends the period of time between stimuli whilst still allowing conditioning to take place.

Following our brief examination of higher order conditioned reflexes, stimulus generalisation, and the trace reflex we should now be able to see if the black box technique can help us establish what mechanisms might produce these effects.

## THE BLACK BOX APPROACH

The black box in Fig. 3.5 shows one way in which second order conditioning might occur. The real picture must of course be very much more complex than in the example-in our case we are only manipulating just a few elements, whereas in a live animal millions of nerve cells could be involved.


Fig. 3.6 Stimulus generalisation effect

As the reader will see, this hypothetical arrangement amounts to a compounded ensemble of our original learning units. $S_{\mathrm{n}_{1}}$ as before has the ability to pair itself with $S_{\mathrm{s}}$ (there is no reason why it should net have equal facilities for its possible combination with $S_{\mathrm{n}_{2}}$ either, except that the schematics become more complicated!). However, $S_{\mathrm{n}_{2}}$ can be conditioned to either $S_{\mathrm{s}}$ direct, or indirectly via $S_{\mathrm{n}_{1}}$ if $S_{\mathrm{n}_{1}}$ has been previously conditioned to $S_{s}$.

Looking even closer into the black box we notice that $S_{\mathrm{n}_{2}}$ could even become conditioned to $S_{\mathrm{s}}$ in another way. Suppose for a moment that the animal is in the unconditioned state, then we condition $S_{n_{2}}$ to $S_{n_{1}}$ so that "touch on left ear"' means "bell". If later we then condition $S_{\mathrm{n}_{1}}$ to $S_{\mathrm{s}}$ (the food) we have also involved $S_{\mathrm{n}_{2}}$. There are thus several possible ways here in which conditioning could take place.

## STIMULUS GENERALISATION EFFECT

The stimulus generalisation effect probably does not need black box treatment here. One could liken the
effect to the response of some filters, say a low $Q$ tuned circuit. Fig. 3.6 gives the general idea.

If the peak in the graph indicates the maximum amplitude of the animal's response to a given stimulus, then for stimuli even some distance away from it there would still be some response. The generalisation about the stimuli would thus seem to depend on the "passband" of the filter. As a general term this type of device might be called a "property filter", for it could be involved with the separation of pitches, amplitudes, pulse widths, spatial positions, etc.


Fig. 3.7 The arrangement of Fig. 3.5 now modified to incorporate trace reflex conditioning

Trace reflex conditioning could be shown quite conveniently with a slight modification to our basic "learning" circuit Fig. 3.7. The "short-term" memory which-provides for the extension of $S_{\mathrm{n}}$ has a fixed period, therefore any attempt to combine $S_{\mathrm{s}}$ with $S_{\mathrm{n}}$ after this time would be futile. However, if we arranged to feed part of the summation output back to control the short-term memory-so that increases in the summer level caused increases in the extension periodthen the model would now by itself be capable of eliciting a trace reflex.

## ABILITY TO ADAPT

Hitherto, our inquiries into learning led us to believe that the conditioned reflex depended upon it being bound quite rigidly to the exclusive characteristics of the stimuli that had been paired. Assuming this were the case, no further learning could occur-the animal would be in no position to vary or modify its responses and update them for current purposes. It therefore seems, that amongst other things, we have discovered in "generalisation", "higher order responses", and "trace reflexes", a quality which would allow the animal to adapt.

Animals do adapt, they are doing it constantly. Take, as an example, a television service engineer. At some early time in his career he undoubtedly received many electric shocks. However, the shocks weren't all the same, or from the same places. As time went by he adapted himself to avoid the characteristics that resulted in getting a shock: such things as seeing line output transformers, e.h.t. capacitors, large ceramic insulators, and so on. If it were not for "generalisation" in particular, he would probably now be the proverbial dead duck!

## DEGREE OF REINFORCEMENT

It seems that we can therefore come to a very positive conclusion about this "generalisation", in that without it a difference in stimulus would have no effect in eliciting a response. Conversely, of course, it is equally
important that the animal can produce differing responses despite similarities in the stimuli that may be presented.

What a paradox all these requirements seem to cause-nonetheless they are necessary. The inevitable balance that occurs between the generalisation of stimuli on the one hand, and differentiation on the other, is seemingly dependent upon the degree of reinforcement, and indeed which stimuli are reinforced.

Throughout the article we have made considerable use of such indefinites as "seemingly" and "perhaps"there will be no apologies though, for the subject of animal behaviour is fraught with so many variants that to be too specific about any particular aspect could be dangerous.

## TRIAL AND ERROR

Because an animal exhibits what appear to be specific responses for certain stimuli there is no reason to believe that without these stimuli these characteristics will not still occur. There is a general theory that during the process of learning, animals may very well hypothesise (or make an assumption) about a situation. This would pre-suppose that the animal "sits tight", makes a number of guesses, and then proceeds to reach a goal (if one exists) by trial and error, eliminating all un-rewarded responses.

We can only conjecture about all these multifarious possibilities at present, though in later articles we may be considering how they could be implemented in a synthetic device. Next month we shall be adding to our breadboard device a rather crude "self and mutual recognition" circuit. We will also see whether it might be feasible to conceive of a machine which could display a type of anxiety neurosis. Imagine an "electronic animal" with ulcers! This is perhaps being a little too anthropomorphic!

To be continued


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Besides offering an improved audio bandwidth of 14 k Hz and excellent noise immunity this system simplifies the operational requirements as the separate circuits and often different routes used in transmitting the two components of the conventional television signal are reduced to one circuit and one route in this composite system

## SYNC INTERYAL

How this is done without interaction between the composite video and sound components is by making use of the line synchronising interval. Reference to the sound-in-vision oscillogram (Fig. 1) shows that the sound signal occupies a period of $3.8 \mu \mathrm{~s}$ within each $4.7 \mu \mathrm{~s}$ line synchronising interval, its placement being equally disposed compared to the leading and trailing edges of the synchronising pulse. Apart from this interval the vision signal occupies the transmission circuit for the rest of the time. As can be seen the system constitutes in essence a form of time division multiplex.

## SAMPLING

By sampling the sound signal at twice the 625 line frequency an audio bandwidth of 14 kHz is possible for transmission. The two samples produced during each line period are converted to pulse code modulation signals then delayed and compressed in time then inserted into the television waveform during the next line synchronising interval.

In the 625 video oscillogram shown the p.c.m. signal precedes the colour burst. It consists of a marker pulse which identifies the start of the sound pulse group containing two 10 digit binary code groups so that 21 pulses in all are accommodated within each line synchronising period.

Fig. I. 625 line video waveform during line blanking interval showing p.c.m. signal consisting of a marker digit plus two 10 digit groups inserted in line synchronising pulse

After transmission via the distribution link the sound pulses are extracted and reconverted to normal signals at the receiving terminal. The video waveform is also restored to standard form.
The use of p.c.m. and high amplitude pulses ensure that the sound signal is immune from all but the most severe interference and distortion.

## TERMINAL EQUIPMENT

The sending and receiving terminal equipment are shown in block form, Fig. 2. The compressor and expander form a syllabic companding system which ensures that the mean signal level into the analogue to digital converter is as high as possible. This converter samples the audio frequency signal presented to it at twice line rate and provides an output in p.c.m. form to the combiner unit. This unit accepts the vision signal clamps it during the back porch and inserts the sound pulses.

At the receiving terminal this process is reversed with the combined sound-in-vision signal being fed to the separator unit from which a clamped and restored vision signal is produced. Separated sound pulses are decoded in the digital to analogue convertor which delivers an audio frequency signal.

Extensive use is made of integrated circuitry for the digital operations together with discrete transistor circuits for the analogue operations.


Fig. 2. Block diagram showing how vision and sound signals are combined at the sending terminal equipment, prior to transmission via the distribution link, and separated at the receiving terminal equipment

ANY seaman who has worked with sonar equipment knows that fish are attracted by certain man-made noises. Perhaps the fish believe that the sound must be coming from some source of food such as a trapped insect-or perhaps their motivation is simply curiosity.

The device described here produces a repeating high pitshed bleep under water which will help lure some species of fish to the fly. In a practical test on a quiet stretch of the North Tyne, the author consistently landed a better average than nearby anglers.

## CIRCUIT DESCRIPTION

The circuit consists of a single transistor Hartley oscillator with feedback via the tapped inductance (Fig. 1). With the component values shown the output frequency is about 1 kHz with a bleep-rate of 60 to 150 times a minute depending on the setting of VR1. With VR1 at maximum resistance the unit produces a continuous tone.

The bleeps are emitted from a high impedance earphone or microphone insert (detailed later), connected in parallel with the inductor.

## CONSTRUCTION

The original model was built on a small 12-way tagboard, as shown in Fig. 3; layout is in no way critical. To augment the output in one direction a cone cut from the top of a plastics detergent bottle is glued to the face of the transducer, which is then glued to the underside of the tagboard. The battery is held by a Terry clip screwed to the tagboard on the same side as the transducer.

The unit must be fairly robust to withstand "casting" and retrieving when in use; it should also be kept as small as possible. All sharp projections must be rounded off of covered to protect the bag in which the unit is placed.

## OPERATION OF "BAIT"

The completed bleeper can then be sprayed with a waterproofing compound as used on car electrical systems and enclosed in a polythene bag-carefully tested beforehand for leaks! Enough air should be left inside so that it will just float and the top sealed with an


Fig. I. Circuit diagram of fish bait
elastic band. A line and sinker are attached, as shown in Fig. 2, but the float is not required. To use the device adjust the distance between the unit and the sinker, so that the unit is held at hook depth, and cast the "bait" to the required position.

An alternative method is to squeeze the excess air out of the polythene bag and support the unit by using a float (Fig. 2). The float can be made from a small plastic detergent bottle or similar item and the depth of the unit is then set by adjusting the length of line between the float and the unit. If this method is adopted a weight may be required in order to keep the unit vertically below the float.

Note: if practicable the polythene bag can be heat sealed to ensure an airtight unit. This method of sealing can be carried out by some bait shops selling live bait in polythene bags.

Repetition rate is adjusted by trial and error to give the best results-VR1 can be manipulated through the polythene bag.

## DIRECTION VANE

Where there are currents it will be possible to direct the sound by adding a vane (Fig. 2), as is done on hydrophones. The vane should be made as shown and the

## COMPONENTS . . .

## Resistors

RI $56 \Omega$
R2 $22 \mathrm{k} \Omega$
All $\pm 10 \%, \frac{1}{4} \mathrm{~W}$ carbon

## Potentiometer

VRI $5 \mathrm{k} \Omega \mathrm{min}$. with switch (Radiospares)
Capacitor
$\mathrm{Cl} 25 \mu \mathrm{~F}$ elect. 25 V
Transistor
TRI OC7I
Miscellaneous
LI Repanco TT4 using secondary with centre tap
XI Crystal earpiece type MCI (Olrus Electronics,
748 High Road, London, E.II)
BYI 1.5V penlight cell
Tagboard: 12 way
Terry clip for battery
Float, small plastic detergent bottle (see text)
Fishing line and weight
Polythene bag


Fig. 2. Bait set up for use with float, vane and weight

length of the wooden shaft set to balance the weight of the vane when in the water. The vane can be attached to the unit by a shaped wire drawn around the polythene bag. Ideally, sound should be transmitted across currents.

## COMPONENTS NOTE

Any crystal earpiece or microphone insert can be used--but the one specified in the components list gives more output than most. Use of the specified transformer for LI is strongly recommended-although other types having a centre tapped winding with a total d.c. resistance of between 100 and 300 ohms may be tried.


Fig. 3. Layout and wiring diagram showing position of components and tags removed from board



World-wide sest reservations


Statistics


Cre~ schedules

## GLECTRONDEAMA



Accounting


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Weight and balance


Flight planning


Message switching

$B^{\mathrm{R}}$RItish Overseas Airways Digital Information Computer for Electronic Automation is one of the world's largest computer complexes linked to one of the most comprehensive communications networks surrounding the globe.

The complete computer, power supplies, air conditioning units and all other ancillary equipments are housed in Boadicea House at London's Heathrow Airport. The complex is protected by one of the most sophisticated security systems in the country and is completely screened by aluminium foil from radar interference

BOADICEA is designed to meet the needs of all BOAC's requirements well into the future and will cover all operations shown. The main "brain" consists of three 1BM 360 model 65 computers, interlinked to form a comprehensive back up system. These computers work in conjunction with three other computers also at Boadicea House. All information passing through the computer is taped and microfilmed to provide three information storage systems, enabling BOAC to continue operations in the event of the complex being destroyed.
The main advantage of BOADICEA to the travelling public is the reservations system which uses cathode ray tube readout and solid state keyboards for operator's use. Reservation staff in the UK, Europe and North America have, at their fingertips, instant reservation information on all flights of BOAC and its associated companies. Seat availability on any flight can be established within seconds and four flights are displayed on request to aid passengers selection. Seat reservations, hotel bookings, car hire and any special requirements can be made through the computer system, up to 11 months in advance.

BOADICEA network is comprised of over 50 computers and handles 350,000 messages a day.


Magnetic data storage machines and information input and eccess points, in use inside the computer room, are shown chow, whilst below an engineer is shown setting up one of ihe three IBM computers which form the main brain of 3OADICEA


Maintenance schedules

Personnel records


#   Puwtif sipy 

By J.T.Tiernan

## SPECIFICATION

Input: 240 V 50 Hz .
Output: 0 to 22 V d.c. continuously variable.
Maximum load current: 1A continuous.
Current overload protection range: 25 mA to $\mathbf{2 . 5 A}$.
Output ripple at 1A load: $500 \mu \mathrm{~V}$.

THE power supply design described in this article is a general purpose unit covering the range 0 to 22 V at load currents up to 1 A r.m.s. Silicon transistors are used throughout and the unit includes an extremely reliable overload protection circuit. Standard components have been used where possible, and little difficulty should be experienced as regards component availability.
The design is presented with the reasonably experienced constructor in mind, and though practical details are accordingly brief, a comprehensive testing and setting-up procedure is included.

Fig. 1 shows a general block diagram of the unit, and should be studied in conjunction with Fig. 2.

## D.C. SUPPLY AND OVERLOAD CIRCUIT

The circuit diagram of the unit is shown in Fig. 2. The "main" a.c. secondary voltage is full wave rectified by D1, D2, D3 and D4, and smoothed by C1, before being routed to the overload protection circuit and series stabiliser elements (TR3, TR4 and TR5).

In series with the common line is a resistor, selected by S2, across which is developed a voltage proportional to the load current being supplied. S3a is normally closed and, if the load current increases such that 2.5 V is developed across the selected series resistor, the thyristor will be triggered into its conducting state. The anode of the thyristor will fall from around 30 V , to practically 0 V , the thyristor current being limited to

Fig. I. Block diagram of power supply

between 30 and 40 mA by R7 and LP2. Thus, 0 V is reflected in the stabiliser section, via D8 and D9, causing the output voltage to fall to zero.

Momentarily opening S3a to the reset position will stop current flow in the thyristor, and on returning S3a to the normal position the output voltage will return to its original value, provided the excess load has been removed.

When the thyristor is not conducting, D8 and D9 are reverse biased and have no effect on the operation of the stabiliser section.

Reliable triggering of the thyristor is ensured by D7 and R8, and VR1 acts as a trip sensitivity control. D14 ensures tripping if LP2 is open circuit and C10 slows up the action of the trip circuit to prevent tripping when the output is increased (due to charging of (8).

## REFERENCE SUPPLY

The auxiliary a.c. supply is rectified via the voltage doubler D5, D6, C2, C3 and fed to the reference supply diodes D10 and D11. The current through the Zener diodes is limited by R9 to approximately 25 mA and C4 ensures minimum ripple on the reference supply lines.

The junction of D10 and D11 is connected to the common line, and the voltages at the cathode of D10 and the anode of D11 become the +27 V and -10 V lines respectively.

## DIFFERENTIAL AMPLIFIER

The differential amplifier consists of TR1 and TR2, with a variable reference voltage applied to TR1 base, via VR3. The collector load for TR2 is R10, and feedback to TR 2 base, via TR3, TR4, TR 5 and D12, sets the voltage at TR2 base equal to that applied to TR1 base. Thus, by varying the reference voltage via VR3, the output voltage at the emitter of TR5 may also be varied.

A reasonable and steady collector-base voltage for TR2 is ensured by D12, a 3.3V Zener diode, which also allows any variations of the output line voltage to be passed in full to TR2 base. This results in maximum compensating action by TR2, keeping the output voltage constant over a wide range of load currents.

It is essential that the current through D12 is held as constant as possible, and to achieve this end D12 is fed from the constant current circuit formed by TR6. The values of R12, R13 and R14 are such that the current through D 12 is approximately 2 mA , and this changes by less than 2 per cent for an output voltage variation from 0 to 22 V .

Since the voltage at the bases of TR1 and TR2 is 3.3 V less than the output voltage, due to D12, the variation of voltage required from VR3 is -3.3 V to +18.7 V for an output range of 0 to 22 V ; VR2 and VR4 are adjusted to provide this variation across VR3.

The "trip 1" and "trip 2" lines are connected from D8 and D9 in the overload section to the bases of TR1 and TR3.

S3b ensures the load is removed when the overload circuit is being reset, and may be used to isolate the load from the unit. Whether a normal on-off switch or a spring loaded switch is used for S3 is a matter of individual preference.

It is important to note that C8 must be connected at the supply side of the switch, otherwise it will often not be possible to reset the overload circuit. The diode D13 is included to ensure tripping of all units if power supplies are used in series.

## COMPONENTS AND CONSTRUCTION

A suggested layout and case size have been given (Fig. 8), but both depend on the size and shape of the transformer(s) and smoothing capacitors used. Most of the smaller components can be mounted


Fig. 2. Complete circuit diagram of the high performance stabilised power supply


Fig. 3. Layout and wiring of overload and rectifier board "A"


Fig. 4. Layout and wiring of differential amplifier and constant current board "B"
on pre-wired boards; layout and wiring diagrams for these are given in Figs. 3, 4 and 5. Plain Veroboard can be used with pins for component mounting. Solid wiring lines are on the non-component side of board and dotted lines indicate wiring on component side.

Layout of components within the unit is not critical and connections may, in general, be made with lightweight wire. It is recommended, though, that wiring carrying the main load current be 20 s.w.g. copper, or reasonably substantial multistrand wire.

## TRANSFORMER DETAILS

The mains transformer is probably the only component which may prove at all difficult to obtain. Among firms which are able to supply transformers with a standard primary, and secondaries of 24 V at 1 A and 50 mA are-T.R.S., Thornton Heath, Surrey, and Osmabet Ltd., Edgware, Middlesex. The cost being about 35 s each.

However, in case of difficulty, the secondary voltages required may be conveniently obtained by using two separate transformers. The main secondary winding is required to deliver 1 A , but the auxiliary supply may be obtained from quite a small transformer, since 24 V at only 50 to 100 mA rating is required. A suitable item is the Belclere type MS3169, however the one-off price for this item is a little high and cheaper alternatives can no doubt be found.


Fig. 5. Layout and wiring of overload resistor board "C"


Fig. 6. Transistor connection outlines
Secondary voltage requirements are not critical; for the main supply this may be within the range 18 to 24 volts. The maximum output available from the unit, maintaining the 1 A load capability, is about 2 V below the a.c. voltage available. The auxiliary a.c. supply should be within the range 20 to 25 volts.

## SEMICONDUCTORS

The thyristor specified is rated 1A at 50 p.i.v. and any similar component will be suitable provided trigger sensitivity is good. Poor trigger sensitivity will perhaps mean increasing the values of R2 to R6 if the overload current ranges are to be set as specified.

For example, a T140A4, 3A 400 p.i.v. thyristor, requires a 3 ohm series resistor for R3 before the unit will trip at IA load current. The other series resistors would have to be increased by a factor of 1.2.

Transistor types used are also non-critical, apart from TR4 and TR5, and three suitable types have been specified in each case.

## HEAT SINK REQUIREMENTS

The heat sink requirements for TR5 will vary according to the anticipated conditions under which the unit will work. The following may be used as a guide:

1. For continuous operation at full load, with output voltage above 12 V , mount TR5 on an aluminium panel with a mica washer and insulating bushes. Minimum panel area required is 36 sq in.
2. For continuous operation at full load, with output voltage above 6 V , insulate the panel from the unit and mount TR5 in direct contact. Panel area 36sq in.
3. For continuous full load operation below 6 V output, it is advisable to use two transistors in parallel for TR5, with a 0.5 ohm 3 W wirewound resistor in each emitter lead. Mounting as in para. 2 above, but with a panel area of 48 sq in.
A separate heat sink assembly may be used to meet all requirements, and a suitable item, size approx. $5 \mathrm{in} \times 4 \mathrm{in} \times 2 \mathrm{in}$, is given in the component list. A finned clip-on heat sink should be fitted to TR4.

## COMPONENTS

## Resistors

RI $270 \mathrm{k} \Omega$ (may be incorporated in LPI)
R2 $1 \Omega 7 \mathrm{~W}$ wirewound
R3 $2.5 \Omega 7 \mathrm{~W}$ wirewound (may be made up from 2.7 27 W and $33 \Omega$ IW in parallel)

R4 $10 \Omega 3 \mathrm{~W}$ wirewound
R5 $25 \Omega$ IW (may be made up from $27 \Omega$ IW and $330 \Omega$ in parallel)
R6 $100 \Omega \mathrm{IW}$
R7 $820 \Omega$ IW
R8 $82 \mathrm{k} \Omega$
R9 $820 \Omega$ IW (for aux. supply voltages less than 22 V , R9 is $680 \Omega$ (W)
RIO $27 \mathrm{k} \Omega$
RII $3.9 \mathrm{k} \Omega$
R12 $10 \mathrm{k} \Omega$
RI3 10k $\Omega$
R14 $2.2 \mathrm{k} \Omega$
$18 \times 1 \mathrm{k} \Omega 1 \%$ resistors (only needed if $S 4$ is fitted)
(All $\frac{1}{2} \mathrm{~W}, 10 \%$ carbon except where stated)
Potentiometers
VRI $500 \Omega$ skeleton preset
VR2 10k $\Omega$ wirewound preset
VR3 $10 \mathrm{k} \Omega$ wirewound (not needed if S 4 is fitted)
VR4 $5 \mathrm{k} \Omega$ wirewound preset
VR5 $25 \mathrm{k} \Omega$ wirewound preset (only needed if S4
VRG $2.5 \mathrm{k} \Omega$ wirewound $\quad\}$ is fitted)

## Capacitors

$\mathrm{Cl} 3,000 \mu \mathrm{~F}$ elect. 50 V
C2 $250 \mu \mathrm{~F}$ elect. 50 V
C3 $250 \mu \mathrm{~F}$ elect. 50 V
C4 $32 \mu \mathrm{~F}$ elect. 64 V
$\left.\begin{array}{ll}\text { C5 } & 80 \mu \mathrm{~F} \text { elect. } 25 \mathrm{~V} \\ \text { C6 } & 100 \mu \mathrm{~F} \text { elect. } 6.4 \mathrm{~V}\end{array}\right\}$ (Mullard)
C7 $\quad 0.01 \mu \mathrm{~F}$ polyester or metal foil 160 V
C8 $100 \mu \mathrm{~F}$ elect. 25 V
C9 $0.1 \mu \mathrm{~F}$ polyester or paper 160 V
CIO $0.47 \mu \mathrm{~F}$ polyester or paper 160 V
Transistors

| TRI | 2N697, 2S004, ZT82 | TR6 | 2N697, 2S004, ZT82 |
| :--- | :--- | :--- | :--- |
| TR2 | 2N697, 2S004, ZT82 | TR4 | BFY50, 2N3053, 2N1507 |
| TR3 | 2N697, 2S004, ZT82 | TR5 | 2N3055, 2S024, ZTI702, |
|  |  |  |  |

TRI 2N697, 2S004, ZT82
TR2 2N697, 2S004, ZT82
TR3 2N697, 2S004, ZT82
TR6 2N697, 25004 ZT82
TR5 2N3055, 2S024, ZTI702
2 SO 33

| iod | and Rectifiers |
| :---: | :---: |
| DI | 1.5A 100 p.i.v. (IS020) |
| D2 | 1.5A 100 p.i.v. (ISO2O) |
| D3 | 1.5A 100 p.i.v. (15020) |
| D4 | 1.5 A 100 p.i.v. (IS020) |
| D5 | $0 \cdot 2 \mathrm{~A} 100$ p.i.v. (\|SI3|) |
| D6 | 0.2 A 100 p.i.v. (ISI3\|) |
| D7 | 0.2 A 100 p.i.v. (ISI3\|) |
| D8 | 0.2 A 100 p.i.v. (IS13\|) |
| D9 | 0.2 A 100 p.i.v. (\|S|3|) |
| D10 | 27V 1.5W (Zener) (IS4027) |
| DII | 10V 1.5W (Zener) (IS4010) |
| D12 | 3.3V 0.4W (Zener) (157033) |
| D13 | 1.5A 100 p.i.v. (15020) |
| D14 | 9V I.5W (Zener) (VR9B) |

## Thyristor

SCRI IS610, 2N|595

## Sockets

SKI Mains (Bulgin type P360)
SK2, 3 and 4 Output terminals (Bulgin type Tl03)

## Miscellaneous

TI Mains transformer sec. $1,24 \mathrm{~V}$ at $\mid \mathrm{A}$ sec. 2, 24 V at 50 mA (see text)
LPI Neon panel indicator (may include RI)
LP2 6V, 0.04A lamp. (Electroniques SGF9/G/RD/6) FSI, FS2 2A fuse holder and fuse
Plain Veroboard and Veropins (size depends on components used) (3 off)
Heat sink-see text (Electroniques HSD5)
18 s.w.g. aluminium for case

## Switches

S1 Double pole single throw (see text)
S2 Maka-switch shaft assembly and 2 Maka-switch 2-pole, 5 way, make-before-break wafers (Radiospares)
S3 Double pole single throw toggle
S4 Maka-switch shaft assembly and 2 Maka-switch single-pole, II-way, make-before-break wafers (Radiospares) (if used)

## こURRENT AND VOLTAGE CONTROLS

Switch S2 should be a make-before-break type and, unless a rotary stud switch is available, is made up from two 2 -pole 5 -way wafers. Each switch section should be connected in parallel as shown in Fig. 7.

The only other component worthy of special mention at this point is VR3, the output voltage control. This may be a normal 10 kilohm linear potentiometer as indicated in Fig. 2. S1 must then be a separate switch. The output will be varied linearly with rotation of VR3. However, with a power supply of this type it is somewhat pointless to use such a coarse control, and the following alternatives are suggested:

1. Use a 10-turn helical potentiometer for VR3, which will enable the output voltage to be set extremely accurately. These components occasionally appear on the "surplus" market at prices ranging from 10 s to 30 s, although they may need looking for. Again SI will have to be a separate double-pole, single-throw switch.
2. Replace VR3 with the network as shown in Fig. 9
(S4). This arrangement gives switched increments of


Fig. 7. Wiring of S2: 4 poles are wired in parallel to switch large currents


2V. and the 2.5 kilohm potentiometer (VR6) will vary the output $\pm 2 \mathrm{~V}$ on the selected value. It is probably also the most convenient arrangement to use; Sl can then be incorporated in S 4 , as shown in Fig. 8. This is done by using a Radiospares "Mains-switch" which is made to mount on "Maka-switch" assemblies.

## TESTING AND SETTING UP PROCEDURE

Complete assembly and wiring of the unit, but omit the following connections:
(a) Positive d.c. line between component boards;
(b) Connection between $\mathrm{C} 2 \dagger$ and the stabiliser board (B).
(c) All connections between TR4, TR5, and stabiliser board.

When this stage has been reached, first check that all diodes and rectifiers have been wired in the correct polarity, then switch on a.c. mains, whereupon LP1 should light.
(1) Check d.c. voltage across $C 1$, which should be about 1.4 times the a.c. voltage available from the "main" secondary winding of the transformer.
(2) Check the d.c. voltage between C2 + and $\mathrm{C} 3-$, which should be aoproximately three times the a.c. voltage available from auxiliary secondary.
(3) Set VRI for minimum resistance and select 100 mA overload current with S 2 . Touch a 100 ohm resistor between the d.c. positive and the common output pins on the rectifier-overload board. Check that the voltage across the thyristor is less than IV and that LP2 lights.
(4) Reset overload with S3 and return S3 to normal. LP2 should extinguish and the voltage across the thyristor should rise to equal the voltage across $C l$. Repeat Test 3.
(5) Switch off a.c. mains and wire in the connection between $\mathrm{C} 2+$ and the stabiliser board.
(6) Wire teniporary links on stabiliser board as follows
(a) Between pins 11 and 12 on board.
(b) Between d.c. positive pin and $\mathrm{C} 4+(-+27 \mathrm{~V}$ line).
Set VR2 and VR4 to maximum resistance.
Note: d.c. line between boards and TR4, TR 5 still out of circuit.
(7) Recheck connections, switch on a.c. mains and check that the voltage between common (-ve output) line, and $C 3-$ equals $-10 \mathrm{~V}+10$ per cent.
(8) Check that the voltage between $\mathrm{C} 4+$ and common line equals $+27 \mathrm{~V} \pm 10$ per cent.
(9) Check that the output voltage is available from the unit, and may be varied by VR3 (or S4 if fitted).
(10) Set output voltage control to maximum and adjust VR2 to give 20 V output.
(11) Set output voltage control to minimum and adjust VR4 to give 0.5 V output.
(12) Set output to about 10 V , and check that the overload setting is 100 mA .
(13) Repeat Test 3, and check that the output from the unit falls to less than $2 \cdot 5 \mathrm{~V}$, with LP2 lit.
(14) Switch off a.c. mains and remove temporary links wired in Test 6.
(15) Connect TR4 and TR5 to the stabiliser board, and link the d.c. positive line by connecting 100 ohm resistor between the d.c. positive pins on the two boards (positions $1 A$ and $8 B$ ).
(16) Recheck connections, select 25 mA overload, turn VRI to maximum and switch on a.c. mains. (Overload circuit will probably operate.)


## (C5/त̄ $6 / \mathrm{VRA} 4)$

Fig. 9. Wiring of $\$ 4$ output volts control. The $18 \times 1$ kilohm resistors are ${ }_{i}$ Wh.s. and are wired around the switch wafers


Leave power unit switched on for about 10 minutes to settle down, and then reset with S3 if necessary.
(17) If a variable resistor is used for VR3, repeat Tests 10 and 11 , setting these voltages to 22 V and 0 V respectively, then go to Test 25 when completed.

Note: VR2 and VR4 interact, and Tests 10 and 11 should be carried out as many times as necessary to achieve an optimum result.

If S4 is fitted as in Fig. 8, proceed as follows:
(18) Replace VR6 and VR5 variable resistors with two 1 kilohm 1 per cent resistors connected in series; junction of the two resistors to be connected to TR 1 base.
(19) Select 20 V output position and adjust VR2 to give 20 V output.
(20) Select 2V output position and adjust VR4 for 2 V output. Repeat Tests 19 and 20 as necessary.
(21) Remove the two 1 kilohm resistors and connect VRS and VR6 back in circuit.
(22) Select the $2 V$ output position, turn VR6 for minimum output, and adjust VR 5 to give 0 V output.
(23) Check that VR6 can now vary the output from 0 to 4 V .
(24) Select 20 V output position and check that VR6 can vary the output between 18 V and 22 V .
(25) Select 100 mA overload and set output to 10 V .
(26) Load output with 100 ohm, 1W resistor and adjust VR1 slowly until overload circuit operates.
(27) Remove 100 ohm load resistor and reset.
(28) Check overload trips when 100 ohm load is reconnected.
(29) Remove 100 ohm resistor, in series with +d.c. line, between boards, and replace with permanent connection. (Switch off to do this.)
(30) Recheck for correct output voltage variation range, and recheck operation of overload circuit as in Tests 25 and 26 (VR1 should not need to be altered).
(31) Select 20V output and 1 A overload, connect an 18 ohm resistor across output terminals and check that unit trips.
(32) Check short circuit output current is less than 10 mA . It will probably be much less than this, but is dependent on leakage current through TR3, TR4 and TR5.

## PERFORMANCE DETAILS

The unit built by the author used a transformer with secondaries of $22 \cdot 3 \mathrm{~V}$, 0.9 A (main) and $21 \mathrm{~V}, 60 \mathrm{~mA}$ (auxiliary). The voltage range was set for an output of 0 to 20 V and the output control was as per Fig. 8.

Measured performance was as follows:
Voltage range-
0 to 20 V
Max. r.m.s. load current- 1A
Output resistance at $20 \mathrm{~V}, 1 \mathrm{~A}-$
30 milliohm ( 0.03 ohm )
Output resistance at $0.2 \mathrm{~V}, 1 \mathrm{~A}-$
40 milliohm ( 0.04 ohm )
Output ripple at 1A load-

$$
500 \mu \mathrm{~V} \text { r.m.s. }
$$

Voltage setting accuracy-
Better than 1.5 per cent
Output variation for $\pm 10$ per
cent mains variation-
Better than $0 \cdot 1$ per cent Overload protection range-

$$
25 \mathrm{~mA} \text { to } 2 \cdot 5 \mathrm{~A}
$$

Short circuit output current- $\quad 1 \mathrm{~mA}$
The only parameter not mentioned in the table is that of temperature stability.

When first switched on the output voltages will be, at worst, 6 per cent low, and this is due almost entirely to the temperature coefficients of D10 and D11. The output voltages settle very quickly and are within 1 per cent of their final value in about one minute of switching on.

If it is required to decrease the effects of temperature variation, then the following is suggested: make D10 from five Zener diodes, each rated at $5 \cdot 6 \mathrm{~V}$, connected in series, and D11 from two Zener diodes each of $5 \cdot 1 \mathrm{~V}$, connected in series.

However, unless the unit is to be used for applications where temperature stability better than 1 or 2 per cent is mandatory, then the extra cost of replacing D10 and Dll is not justifiable.

## FINAL COMMENTS

There are two further points worth mentioning.
Although the unit is only intended to handle a maximum load current of 1 A , a $2 \cdot 5 \mathrm{~A}$ overload position has been included. This allows the unit to be used where average currents are less than 1A but where high current peaks may be encountered. For example, class-B and class-C power amplifiers.

The -10 V line may be made available on the front panel of the unit. Although currents of only 2 or 3 mA may be drawn from this line without affecting the main supply, this can nevertheless be useful for small signal biasing applications.

## RCA HOBBY CIRCUITS MANUAL

Published by RCA Great Britain Limited, Lincoln Way, Windmill Road, Sunbury-on-Thames, Middlesex

## 224 pages, $8 \frac{3}{8}$ in $\times 5_{8}^{8} \mathrm{in}$. Price 17 s 6d.

1N this one reasonably priced volume are presented thirty five circuits which should have a broad appeal to electronics hobbyists

Whilst including some games and novelty circuits, the majority fall into specific categories such as photography, motoring, ham radio, audio, etc., providing solid state aids or accessories in these pursuits.

The Manual opens with brief, but adequate, descriptions of the theory and operation of the semiconductor devices used in the various circuits and a general introduction to some commonly encountered circuit "bricks". A section on construction techniques provides all the information necessary both in the handling and assembly of the components used.

Sections on test circuits for component troubleshooting, and suggested circuit uses, preface the thirty five projects all of which are dealt with in detail both for circuit operation and construction.

Semiconductor types used can be obtained from RCA Great Britain at the address given above.
G.M.H.

## COLOUR TELEVISION :

A Background to Colour Tube Adjustment for the Service Engineer
Published by Distributor Sales Division,
Mullard Limited
48 pages $8 \frac{1}{2} \times 6$ in. Price 17 s 6 d .

WITн the advent of colour television has come more rigorous demands on the service technician both in the understanding of new circuit techniques and the application of new and necessarily more complex setting up procedures.

In two parts this book provides a liberally illustrated "teach-in" on colour tube control assemblies and their adjustment. Part 1 provides a brief description of the shadowmask picture tube with explanations of convergence and colour purity and associated assemblies necessary to achieve this. Part 2 gives a typical setting up sequence of adjustments, the emphasis being on a simple easy-to-follow procedure which should appeal to the service man.
Colour pictures and diagrams are used to show the operation of the various controls and to give examples of the displayed picture before and after adjustments are made.
G.M.H.

## AUDIO DIARY 69

Published by Link House Publications Ltd. 60 pages of information plus diary section, $4 \frac{1}{8} \mathrm{in} \times 2 \frac{7}{8} \mathrm{in}$. Price 8 s 6 d .

THE information section of this diary will be a most useful and convenient work of reference for all who appreciate good music and the means for its reproduction in the home. The subject matter has been selected to embrace the musical art as well as acoustic science and audio engineering. Topics succinctly but objectively dealt with include: frequency and pitch, gramophone records and their reproduction, loudspeakers, and tape recording. There are also other data such as music terms, index of composers, audio terms, and circuit symbols. Illustrated with charts and diagrams.

## TRANSISTOR SERVICING GUIDE

Prepared and published by RCA Institutes Inc. 194 pages, 8 in $\times$ Sin. Price 35 s.

Service engineers, who prefer to learn from straightforward factual reporting of, technical information, might find this book helpful. For those hitherto unfamiliar with transistor techniques, the early chapters lay out clearly and concisely the necessary basic theory of circuit operation of amplifying and r.f. stages.
Typical circuits used in transistor radio receivers are given in Chapter 3, while the next chapter looks at transistor television circuitry. This is dealt with in some considerable length. One would have thought that by this stage in the book one might have read about typical fault diagnosis and practical work-according to the title-but this aspect is given only in the last 40 pages.

Although of American origin, this book has been Anglicized to a considerable degree making it more digestible to the British service man.
M.A.C.

## NEWS BRIEFS

## Safer Level Crossings

Television cameras are being mounted on Italian unmanned level crossings to provide an unobstructed view of the whole crossing area. The cameras, which use English Electric Vidicon picture tubes, feed monitors mounted in the signal box, these monitors provide the signalman with a continuous view of the crossing and, in the event of the track being obstructed when the barriers are lowered, allow him time to warn approaching trains.

## Britain Contributed to Apollo 7!

$B^{\text {ritain's contribution to the Apollo } 7 \text { space mission }}$ Bconsisted of five Cable and Wireless stations ir Antigua, Ascension, Bermuda, Suva (Fiji) and Tortola (B.V.I.). The "NASCOM" communications network, used to control and track the entire flight, consisted of teleprinter, voice and data circuits and covered a large part of the globe. Circuits had to be maintained in the highest state of efficiency throughout the mission; reliability requirements being as high as $99 \cdot 8$ per cent

## Computer-Aided Design Centre

The Ministry of Technology have been negotiating with ICL to operate the new computer-aided design centre at Cambridge under Mintech direction. The design centre, which is expected to be operational in mid 1969, will provide multi-access facilities working largely through the medium of teleprinters but using displays and other devices in some instances. Expected costs and running expenses for the first five years are about $£ 2 \frac{1}{2}$ million.

## Integrated Circuits Joined by Electrons

A ${ }^{\text {n electron beam cutting and welding unit, which is }}$ to be used for connecting together micro-integrated circuits, is being developed by the research division of ICL. The heart of the control system for the unit is to be an English Electric M2140 multi-processor computer

## Computing Past and Present

A small exhibition of mechanical, electro-mechanical and electronic computing machines, past and present, is now being shown in the Science Museum, South Kensington, London. It is open every day (except Christmas Day) until January 12, 1969 , admission free.


## Zener Diodes

## By J.S. LAMB

BEFORE using any unfamiliar electronic device it is wise to examine the characteristics carefully and if possible try to understand its physical properties. With Zener diodes both these points are relatively simple to master. The Zener diode is very similar to any silicon diode and if used in the forward biased condition will give similar results. The difference, due to impurities injected during manufacture, is found when the device is reverse biased as it breaks down at a given voltage and will conduct large currents (see Figs. 1a and lb).
Before proceeding it is important to note that, like all semiconductors, Zener diodes are likely to be damaged if excessive currents are allowed to pass through them. To avoid this ensure that a series resistor is used to limit that current whenever the breakdown voltage is to be exceeded.


Fig. I. Characteristic curves and circuit symbols of (a) silicon diode and (b) Zener diode


Fig. 2. Characteristic of Zener diode IN753

## ZENER BREAKDOWN

Looking at a typical characteristic curve (Fig. 2) of a Zener diode type $1 \mathrm{~N} 753, \mathrm{AB}$ is a region in which very little current flows and the device presents a high impedance usually above 50 kilohms. Region BC is the breakdown voltage $V_{z}$ and is usually a very sharp knee but it can be a steep curve especially at very low breakdown voltages, i.e. 3 to 4 volts. Finally in the region $C D$ the breakdown voltage is exceeded and very large currents flow for small increases in voltage.

The manufacturers data for Zener diodes is presented with a definite value of Zener current $I_{z(\min )}$ being quoted for the breakdown voltage. This is usually greater than required and lies well into the conducting portion of the curve, to ensure that the working point of the device never lies on the curved portion BC. It is important to note, however, that for large variations in Zener current the breakdown voltage increases slightly, i.e. this device still has a finite resistance $\boldsymbol{R}_{\mathbf{z}}$ after breakdown. Two other parameters are also quoted $I_{\mathrm{z}(\text { max })}$ and maximum power dissipation $P_{\mathrm{c}(\text { max })}$. The maximum power dissipation depends on temperature, but for the purpose of this article it is assumed that an ambient temperature of 25 degrees $C$ prevails and the devices are never subjected to maximum power dissipation. All Zener diodes are subject to a 5 or 10 per cent tolerance and it is not unreasonable for a nominal 6.2 V Zener to have an actual breakdown voltage between 5.6 and 6.8 V , although any one diode will always have a constant breakdown voltage within this range. Zener diodes also vary with temperature; those below 5 V have a negative coefficient, those above 6 V a positive coefficient, and those at 5 to 6 V have a zero temperature coefficient. Thus by combining selected diodes (i.e. above 5 V and below 5 V ) the total temperature coefficient of several devices can still be less than the temperature coefficient of one diode of corresponding voltage.

## STABLE VOLTAGE SOURCE

The Zener diode is most commonly found in voltage reference and stabilising circuits. Fig. 3 shows the most common Zener diode circuit used to produce a constant voltage with a varying load current and supply voltage. As the latter varies, the voltage across R1 varies, allowing $V_{\mathrm{z}}$ to remain constant provided that $I_{\mathrm{z}(\mathrm{min})}$ is allowed to flow continuously through the Zener diode. Under the conditions where no load current is taken, the maximum load current required in the external circuit plus $I_{Z(\min )}$ flows through the diode, while on full load the Zener diode conducts only $I_{z(\min )}$.

## DESIGN PROCEDURE

The design procedure for a stabiliser of $6 \cdot 2 \mathrm{~V}$ (as in Fig. 3) is as follows:

1. Stipulate maximum and minimum current $I_{\mathrm{L}}$ to be drawn in load (e.g. 10 mA and 0 mA ).
2. Stipulate the maximum supply voltage $V_{i}$ likely to occur (e.g. 12 volts) and ensure that the minimum supply voltage is at least 1 V above the breakdown voltage of the Zener diode.
3. At any time $V_{i}=V_{z}+V_{R 1}$, where $V_{z}$ is the breakdown voltage $V_{R_{1}}$ is voltage across R 1 , and $I_{z}=I_{\mathrm{z}(\mathrm{min})}+I_{\mathrm{L}}$, where $I_{\mathrm{L}}$ is the maximum load current stipulated.
Using Zener diode 1 N 753 where $V_{z}=6.5 \mathrm{~V}, I_{z(\text { min })}=$ $100 \mu \mathrm{~A}$ (from Fig. 2).

Maximum $I_{z}=100 \mu \mathrm{~A}+10 \mathrm{~mA}$

$$
=10 \cdot 1 \mathrm{~mA}
$$

Therefore R 1 must conduct $10 \cdot 1 \mathrm{~mA}$ at the minimum supply voltage.

Allowing 1.5 volts minimum across R 1 , i.e. $V_{\mathrm{i}}-V_{\mathrm{z}}$.

$$
\begin{aligned}
R_{1} & =\frac{1 \cdot 5}{10 \cdot 1 \times 10^{-3}} \\
& =148.5 \mathrm{ohms}
\end{aligned}
$$

The nearest preferred value resistor would be 150 ohms.
4. At maximum supply voltage 12 V :

$$
\begin{aligned}
V_{\mathrm{R}_{1}} & =I_{\mathrm{z}} R_{1} \\
I_{\mathrm{z}} & =\frac{(12-6.5)}{150} \\
& =36.7 \mathrm{~mA}
\end{aligned}
$$

Under no load conditions this current flows through the Zener diode when the Zener dissipates most power.
5. The power dissipation for these open circuit conditions is given by

$$
\begin{aligned}
P_{\mathrm{c}} & =I_{\mathrm{z}} V_{\mathrm{z}} \\
P_{\mathrm{c}} & =6.5 \times 36 \mathrm{~mA} \\
& =234 \mathrm{~mW}
\end{aligned}
$$

This is well within the 400 mW rating of the 1 N 753. The results of this circuit are shown graphically in Fig. 4.

## ZENER DIODES IN SERIES

As stated earlier in this article it is possible to add two or more diodes in series (Fig. 5). The calculation is similar but in this circuit $I_{z(\mathrm{~min})}$ must be a minimum value, so that the current passing through any diode is never less than the value at which the breakdown voltage of that diode occurs. If this condition is fulfilled then six different stable supplies can be obtained from three diodes as shown in Fig. 5.

## ZENER DIODES IN PARALLEL

For very good stability at low voltages, especially for supplies containing a.c. ripple, two Zeners can be added in parallel, Fig. 6. The calculation is the same for both diodes, each being treated as a single stage. If a potentiometer is substituted for R2 then a variable stabilised supply can be obtained although stabilisation is not as good as in the fixed voltage circuit.

If large load circuits are required or the supply varies by more than 2 to 3 V the Zener diodes can easily be over loaded; to overcome this a transistor is usually added.

## VOLTAGE STABILISE WITH <br> TRANSISTOR

In this type of circuit the Zener diode is used as a constant voltage source on the base of a transistor. The transistor is operated in the emitter follower configuration, with the load acting as the emitter resistor (see Fig. 7).

The roltage at the emitter follows the voltage at the base. In this circuit, the latter is constant so the output across the load is also constant and the maximum current through the load is the maximum emitter current of the transistor. The gain of the transistor $h_{\mathrm{t}} \mathrm{e}$ should be high (e.g. 50) to ensure that under maximum load conditions the base current is not large enough to load the Zener diode circuit.


Fig. 4. Load characteristics for given inputs of Zener dioda stabiliser


Fig. 5 Three Zener diodes connected in series provide six
stabilised outputs.
stobilised outputs.

Fig. 6. Zener diodes connected in parallef


| $v_{1}=13.0$ volts |  |
| :---: | :---: |
| $V_{0}($ volTs $)$ | $I_{0}(\mathrm{~mA})$ |
| 6.10 | 10 |
| 6.05 | 30 |
| 6.05 | 50 |
| 6.05 | 60 |

## STABILISER DESIGN

The procedure for designing a circuit using 1 N753 to give 6.5 volts output, as in Fig. 7, is as follows:

1. Stipulate maximum and minimum load current, (e.g. 50 mA and 0 mA respectively).
2. Stipulate maximum and minimum supply voltage; again the minimum supply should be 1 to 2 V above $V_{\mathrm{z}}\left(\right.$ e.g. $\left.V_{\mathrm{i}(\max )}=12 \mathrm{~V}, V_{\mathrm{i}(\min )}=8 \mathrm{~V}\right)$, i.e. $V_{\mathrm{R} 1}=1.5 \mathrm{~V}$.
3. Select transistor for power dissipation $P_{\text {tot }}$

$$
\begin{aligned}
P_{\mathrm{tot}} & =\left(V_{\mathrm{i}(\max )}-V_{\mathrm{o}}\right) \times I_{\mathrm{L}(\max )} \\
& =(12-6.5) 50 \times 10^{-3} \\
& =275 \mathrm{~mW}
\end{aligned}
$$

It is also required that $h_{\mathrm{fe}}=50$ (use transistor type OC84)

Therefore $I_{\mathrm{b}}=\frac{I_{\mathrm{L}(\max )}}{1+h_{\mathrm{fe}}} \simeq \frac{I_{\mathrm{L}(\max )}}{h_{\mathrm{fe}}}$

$$
I_{\mathrm{b}}=1 \mathrm{~mA}
$$

4. For good regulation the current through the Zener diode should be much larger than the required base current, i.e. by a factor of 5 .
The value of $R_{\mathrm{L}}$ is then calculated as in the simple stabiliser.
i.e. $R_{\mathrm{L}}=\frac{V_{\mathrm{i}(\mathrm{min})}-V_{\mathrm{Z}}}{I_{\mathrm{z}}}$

$$
=\frac{8-6 \cdot 5}{5 \times 10^{-3}}
$$

$=300$ ohms
The nearest preferred value resistor is 300 or 330 ohms.
5. The power dissipation is given by

$$
P_{\mathrm{c}}=\left(\frac{\left(V_{\mathrm{i}(\max )}-V_{\mathrm{z}}\right)}{R_{1}}-I_{\mathrm{b}(\min )}\right) V_{\mathrm{z}}
$$

If the minimum load current is zero, $I_{\mathrm{b}}=0$

$$
\begin{aligned}
P_{\mathrm{c}} & =\left(\frac{12-6 \cdot 5}{330}\right) 6 \cdot 5 \\
& =112 \mathrm{~mW}
\end{aligned}
$$

For the results of this circuit, see tables in Fig. 7.
It is interesting to note that as this circuit behaves as an emitter follower the output voltage is smaller by the $V_{b e}$ of the transistor. It is also worth noting that $V_{\text {be }}$ increases slightly as the emitter current increases.

Fig. 7 (left). Circuit diagram and table of results of voltage stabiliser using a transistor connected in series.


Fig. 8. Use of Zener diodes in meter circuits


Fig. 9. The results of adding a Zener diade and parallel resistor to a ImA meter


Fig. 10 a. Zener diode used to protect a voltmeter against overload


Fig. 10b. Two Zener diodes used "back to back" to give protection against positive and negative transients


Fig. Il. A free running square wave generator using a Zener diode

## ZENER DIODES FOR METER CIRCUITS

One of the most useful applications of Zener diodes to the amateur constructor is its addition to voltmeter circuits (Fig. 8a).
For this application a 1 mA meter was used, with a series resistor R1, as a voltmeter. Sensitive meters should be shunted to measure at least 1 mA f.s.d. to ensure that f.s.d. is not reached by Zener diode leakage current (Fig. 2).
The Zener diode presents a very high impedance (about 50 kilohms) until the voltage across it reaches the breakdown voltage, so from zero volts to the Zener breakdown voltage the meter reads only the Zener leakage current, i.e. Fig. 2 (A - B). After breakdown the Zener resistance is very small (e.g. in the region 10 to 100 ohms ) so the meter now behaves as a normal voltmeter. The scale is compressed up to the Zener breakdown voltage, whereafter it is linear. If it is required to measure a voltage of 7 V accurately then using a Zener diode of 6.5 V and a 1 mA meter, the meter will read 7.5 V full scale with a deflection range of 1 V if $\mathrm{R} 1=1$ kilohm.
If a resistor is placed in parallel with the Zener diode (Fig. 8b) to form a potential divider then the lower part of the scale can be increased, while still being compressed, to give a reasonable reading outside the required rarge. Fig. 9 shows the results of adding a Zener and parallel resistor to a 1 mA meter.

## CIRCUIT PROTECTION

A Zener diode can be used very effectively to protect a voltmeter against overload or a reversed polarity signal (Fig. 10a). When the input reaches the breakdown voltage the diode conducts and short circuits the meter. In practice the diode used has a breakdown voltage 2 to 3 times that which would overload the meter. Also, if a voltage is applied which is of reversed polarity the Zener diode behaves like a normal diode, forward biased, thus again short-circuiting the meter. This method of protection can also be used to protect instruments from transients through supplies. In this case it is usual to place two Zener diodes back to back as in Fig. 10b, thus protection is gained against positive and negative transients.

## SIMPLE TIMING CIRCUIT

In the circuit of Fig. 11 the capacitor C 1 is charged through the resistor R 1 until the voltage across it reaches the Zener breakdown voltage plus the $V_{\text {be }}$ of the transistor.
The Zener diode then conducts allowing a base current to flow which switches the transistor hard on. The time taken for this to occur is given by

$$
t \simeq \frac{0.7 C_{1} R_{1} V_{z}}{V_{\mathrm{cc}}}
$$

Unfortunately the value of R1 has a practical maximum. This resistor must be able to conduct the $I_{z(\min )}$ of the Zener diode and a base current large enough to switch on the transistor. This means that large values of capacitance must be used for long time delays.
The repeatability of the time delay depends on the capacitance and the shape of the knee of the Zener diode. Thus this circuit can never be used in applications which require a high degree of accuracy.
If a relay is used in the collector circuit with one set of contacts used to discharge the capacitor then the circuit becomes a free running square wave generator.


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By P. R. HINCHCLIFFE B.Sc.


#### Abstract

ASTUDY of the acean involves the accurate determination of such parameters as pressure, temperature, salt content and current flow in regions of the earth's surface where man could never live, without the aid of bulky and expensive protective vessels. The following article describes how the oceanographer enlists the aid of electronics to penetrate depths otherwise forbidden to him, and to study them both in situ and in the laboratory.


## MEASUREMENT OF DEPTH

The eatliest measurements of depth were, of course, taken by the technique of throwing a weighted rope over the side of a ship. In later years, a blob of candle grease on the weight also provided early marine geoch $\epsilon$ mists with a convenient sample of the sediment from the sea floor.
However this procedure was inaccurate, and especially in deep waters, very lengthy. Paying out and winding in 3,000 fathoms of wire is a very tedious procedure, and a survey of a small area could take months using this procecure.
A modern day technique is to use sonar. A high frequency acoustic pulse (about $10-15 \mathrm{kHz}$ ) is released from a transducer below the ship, at the same time that a pen recorder begins to sweep across a paper. The reflected echo is received and causes the pen to mark the paper.
The time pulse and echo gives the depth accurately, assuming that the velocity of sound in water is known. This value is taken to be 800 fathoms per second, but tables are available to correct for different parts of the ocean.
Accurate measurement of sound velocity is also useful for determining current flow or water densities under certain circumstances. The method usually used to measure velocity is based on the "sing-around" system (Fig. 1). A pulse emitted from the transmitting transducer travels an accurately known path and is received by a second transducer, which feeds the signal back to the transmitter via shaping and amplifying circuits, causing electrical pscillations to occur. The frequency of oscillation is dependent on the velocity of sound in the water between the transmitter and receiver.
This transmission of data as a frequency modulation is very convenient, requiring only one probe-to-ship cable to carry many different channels of information. Considerable modification is often incorporated into a sensor system so that a modulated frequency signal can be obtained, as we will see later.

## DEPTH RECORDER

Knowing the velocity of sound enables accurate depth measurements to be made using the system mentioned above. Fig. 2 shows in block diagran form a typical depth recorder based on this principle.

Use of the "sing-around" principle would cause loss of definition of the structure of the sea bed as the interval between pulses may be several minutes in deep waters, and in this time the boat may have thavelled quite a cistance. To overcome this a string of Fulses is given out and this is gated in a particular code to determine which echo carne from which pulse.


Fig. 1. Scund velocity meter using "sing-around" frinciple


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[^5]

Fig. 2. Block diagram of typical depth recorder


Fig. 3. Helical stylus used on depth recorders

Sing-around type depth recorders, however, have the advantage that loss in signal strength by absorption in water can be compensated for automatically by using a time-variable gain on the receiver.

The recorder uses a pen which sweeps across the paper at a constant rate-often a helical stylus is used to achieve this effect (Fig. 3). A pulse is emitted as the stylus is at zero and the pen marks the paper again on receipt of the echo. Different rotating speeds of the stylus give different depth ranges.

The transducers used are usually multiple ceramic devices, but for some cases nickel/iron magnetostrictive rings or quartz crystal devices may be used.

Sonar systems are also used for giving the depth of probes or sampling devices below the sea surface. A "pinger" is fitted on to the probe, which gives out a string of sonar pulses. Two main pulses are received onboard the ship, one direct and one reflected from the sea bed. When the two traces on the recorder coincide, the probe or sampler will be on the sea bed.

## VIBROTRON

Depth is closely related to pressure, to such an extent that a pressure recorder is often used to indicate the depth of an instrument. A useful sensor, which directly gives a signal whose frequency is dependent on
pressure, is the vibrotron, in which the pressure is used to tighten or slacken a vibrating wire, hence varying the frequency of vibration of the wire. Devices in which pressure varies the capacitance of the capacitor in an LC oscillator are also useful.

The vibration and the sonar device described may be used in wave measurement, which may become important in the future for predicting storms. However, these have to be resting on the sea bed and cables must run from the instrument to shore or ship, so they are only really useful as near-shore, shallow water devices.

For shipborne wave analysis a very elegant system is used. An arbitrary reference depth is fixed below the ship. Shipborne transducers measure the pressure (and hence the height of the wave) and compare this to the reference level to allow for the motion of this ship in both vertical and rolling motion. The reference level is fixed by measuring the vertical acceleration due to the wave motion at each side of the ship. Distance is found from acceleration by double integration.

The transducers used are variable inductance transformers (Figs. 4 a and 4 b ). A voltage of 10 V at 10 kHz is fed to the fixed coil, and $10 \mathrm{~V} \pm 10$ per cent is induced into the other, dependent on its height relative to the



Fig. 5. Shipboard wave recorder
core. This is compared to the standard 80 V reference to give an output $V_{o}$ (Fig. 4c) dependent on either acceleration or pressure.
The acceleration signal is integrated twice (Fig. 5) and mixed with the pressure signal. The resultant is fed via a filter to eliminate long term drifting in the previous network into a pen recorder. The trace may then be analysed at leisure on land.

## MEASUREMENT OF TEMPERATURE

There are two quite separate problems involved in temperature measurement of the ocean-the thin skin of the surface is often at a quite different temperature to that of the mixer layer immediately below. Measurements of the surface skin are usually carried out using an airborne radiation thermometer (a.r.t., see Fig. 6) which, as the name suggests, is usually carried in an aeroplane.

This device compares the infra-red radiation of the surface layer of the sea with the radiation reflected from a shutter in front of the thermostatically controlled container. The shutter rotates to give a chopped 20 Hz signal in the thermistor; the signal is then filtered, analysed and recorded. This method was used to plot the Gulf Stream very accurately, as it can measure surface temperatures of large areas rapidly to within $\pm 0 \cdot 2$ degree $C$.

For sub-surface measurements, reversing thermometers and isothermal sampling bottles are often used, but for the fast temperature/depth contouring a device known as a bathythermograph (b.t.) is useci. This is in the form of a fish which is towed behind the ship.

On paying out the hawser the weight at the end of the b.t. causes it to sink, and on stopping the winch and then winding in the hawser the b.t. rises to the surface by means of its fins. Pressure drives a smoked glass plate in one direction, whilst the expansion of a liquid in a copper tube drives a stylus over the plate to indicate temperature against depth.

However, a more modern procedure is to use the expendable bathythermograph (e.b.t.) which is in the form of a streamlined plumb carrying a thermistor. An extremely fine 3-core cable connects the thermistor to a compensated bridge circuit on board the ship. As the cable is reeled out from both the e.b.t. and the shipboard unit simultaneously, it will float along the sea surface allowing the plumb to fall freely at a known speed whilst the ship steams away from the spot where it dropped the e.b.t.

The temperature is recorded against time, and hence against depth. These e.b.t.'s cannot be recovered, as the cable is far too fine to stand the strain of rewinding the instrument. It is possible to record the temperature to within $\pm 0.1$ degree $C$.

Contouring temperature recorders (strings of 20 or more thermistors on a single hawser) are often used to plot the variation of the depth of an individual isotherm. The thermistors are wired individually into circuits such as that shown in Fig. 7.

## MEASUREMENT OF SALINITY

Water masses in the ocean can be accurately defined and their movements plotted by measurement of their depth, temperature and salt content. However, variations of salinity in the major oceans are fairly small, and measurements to within $\pm 0.01$ per cent would be required.

The most reliable and accurate method of salinity measurement is by the chemical procedure of titration, but this requires isolated samples of sea water, and a good deal of time and care. Rapid laboratory techniques usually measure the resistance of a standard vessel of sea water. The resistance is inversely proportional to the salinity, but also varies with temperature and pressure of the water.

In the laboratory pressure presents no problems-if the sample has been taken from a great depth, the pressure may be allowed for by use of tables. However, temperature must be controlled to a very fine degree, and this is done by immersing the conduction "cells" in an accurate thermostat.

Fig. 8 shows the circuit of a simple conduction bridge. The circuit is a Wheatstone bridge compensating

## Conductivity salinometer

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Fig. 8. Simple conduction bridge circuit



(d)

Fig. 10. Detalls of induction salinometer
Fig. 10a. Coll system of inductive conductivity sensor
Fig. IOb. Theoretical circuit of sensor
Fig. 10c. Circuit that will give approximately the correct compensation for a temperature change over a limited range
Fig. 10d. Pressure compensator


Fig. Ila. Paralog system


Fig. I/b. Vector diagram showing that phase $\phi$ varies with magnitude of $E_{0}$


Fig. I/c. Salinity, temperature, depth probe


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carefully converted finto a workshop, fo just ia rafefulyy convertor in the winter-it can't be used. because it never gets warm until it is time to finish. The answer is RADIANT ZONF
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circuit, two arms being the cells containing the sample and a reference standard, the third the balance control and zero setting trimmer, and the fourth having a capacitance zero trimmer to allow for the capacitance in the cells, across the reference resistor. To avoid electrolytic dissociation of the sea water damaging the electrodes, an a.c. source is used, between 500 and $5,000 \mathrm{~Hz}$. The cells must be of high resistance to avoid electrical heating.

The main difficulty of this circuit is the problem of measuring the variable resistor to 1 part in 50,000 as would be necessary to obtain the required accuracy. This may be overcome by using a transformer bridge. Other advantages of this form of circuit are that it has a negligible temperature coefficient and very little capacitance to earth. The number of turns must be controllable to 1 part in 100,000 , and this is overcome by using a system of tapped coils shown in Fig. 9a.

Fig. 9 b shows the complete bridge which will give an output as an oscilloscope trace indicating both resistive and reactive null-points.

## INDUCTION CELLS

For in situ measurements of salinity the conductance bridge is quite unsuitable. The electrodes would be fouled very quickly with insoluble matter and the temperature would be difficult to control accurately or compensate for. These problems are not inherent, however, in the induction or electrodeless salinometer.

Figs. 10a and 10b show the principle of the induction cell. Two magnetically shielded toroids are wound on a non-inductive former with the water sample coupling them. On to each former a second coil is wound, one of which is in opposition to its primary, so that it may be used to give a null-reading at the balance point of the bridge, i.e. when the voltage induced in the secondary by the water link is balanced out by the voltage induced by the second coil system.

However, the salinity of a given sample is dependent on temperature and pressure in a rather complex way, and whilst some induction cells record pressure and temperature along with conductance for subsequent compensation, a more convenient way is to compensate within the instrument. Typical temperature and pressure compensating circuits are shown in Figs. 10c and 10 d .

As mentioned earlier, the most efficient way of transmitting analogue data from a probe to a ship is in the form of a modulated frequency. This salinity system can be made to vary a frequency by use of positive feedback through a variable phase amplifier (Figs. 11a, 11b). In the paralog system positive feedback occurs through amplifiers A1 and A2. For this to occur the total phase shift around the circuit must be zero or 360 degrees, and the frequency of oscillation will change until the variable phase amplifier (Q2) can create this condition. The phase shift due to $E_{\mathrm{r}}$, the combination of $E_{0}$ and $E_{1}$ shifted through 90 degrees, depends on the magnitude of $E_{0}$, which in turn, is dependent on the state of the sensor.

In a typical survey probe three such systems may be incorporated using respectively an induction cell, a thermistor and a vibrotron unit, giving out three different main frequencies modulated to give the variation in salinity, temperature and pressure of the water surrounding the probe.

Fig. 11c shows how this information can be received on board the ship and analysed by use of narrow bandpass amplifiers and frequency modulation detectors to give a voltage dependent on frequency, and subsequently recorded as a plot of temperature and salinity against depth (equivalent to pressure) on an $\mathrm{Xl}, \mathrm{X} 2, \mathrm{Y}$ pen recorder.

To be continued

Close-up of the probe, showing inductive salinity sensorupper of two cylinders near the base of the probe

Salinity, temperature, depth probe
(Plessey)



## UNLIMITED!

A selection of readers' suggested circuits. It should be emphasised that these designs have not been proven by us. They will at any rate stimulate further thought.
This is YOUR page and any idea published will be awarded payment accord. ing to its merit.

## SUPPRESSED ZERO CAR VOLTMETER

THE voltage appearing across the terminals of a car battery is a useful indication of the state of the battery and charging circuit. If a conventional voltmeter (say 0 to 15 volts) is connected across the battery it will be found that only a small portion of the scale is used, centred on the nominal battery voltage of 12 volts and extending a maximum of about 3 volts on either side, as the battery charges and discharges (see Fig. 1).

A Zener diode has the property of not conducting until a certain voltage, the Zener voltage, appears across it. The voltage drop across the diode then remains constant provided its wattage rating is not exceeded.

Consider now the circuit of the improved battery voltmeter (Fig. 2). A 9 volt Zener diode is connected in series with a conventional voltmeter circuit (moving coil meter plus multiplier resistance) which measures 6 volts full scale. If the voltage across the terminals of the instrument is less than 9 volts then the diode will not conduct and the meter will read zero. Suppose the voltage now rises to 12 volts. The diode will conduct and a voltage drop of 9 volts will appear across it, 3 volts will then appear across the voltmeter circuit which will then indicate half full scale deflection. If the voltage now increases to 15 volts, 9 volts will again appear across the diode and 6 across the meter, which will indicate full scale deflection. The meter will, therefore, only indicate voltages between 9 and 15 volts, thus spreading the useful part of the readings on a conventional voltmeter along the full scale length of the meter used.

In the prototype a 9 volt, $\frac{1}{4}$ watt Zener diode was used and the 0 to 6 volt voltmeter, M1, consisted of an ex-Government 5 mA moving coil meter in series with a 3 kilohm potentiometer, VR1.

## CONSTRUCTION

Construction consisted of mounting the two components on a small piece of Veroboard which was bolted to the back of the meter by the nuts on the meter terminal bolts. Connection to the meter terminals was by solder tags which were soldered to the board. A red lead and a black lead from the board formed the input connections to the instrument. Care was taken to observe the correct polarity and one lead was taken to the car chassis and the other to the ignition switch so that the voltmeter continuously monitored the battery voltage when the ignition was turned on.

## CALIBRATION

Calibration was carried out by setting the potentiometer VR1 to its maximum resistance and connecting an accurate 12 volt supply to the instrument. The potentiometer was adjusted for half full scale deflection on the meter. The potentiometer was then sealed with a blob of wax. The original scale on the meter which read 0 to 5 mA was covered with white paper, leaving the scale itself uncovered. The figures 9 to 15 were substituted for 0 to 5 . The figure 12 appeared at the centre of the scale in place of the original 3.

A very neat and professional looking job was obtained with the aid of "Letraset" dry transfer letters. The legend VOLTS was also added.

This type of instrument is known as a suppressed zero voltmeter and this technique could be adapted for uses other than the one described. Care must be taken not to exceed the manufacturer's published wattage ratings of the diode. The prototype has been in use in the author's car for some months and has proved very useful.
P. A. Graves, Westcliff-on-Sea, Essex.


Fig. I. The small segment of the scale that is used in conventional car voltmeters


Fig. 2. Circuit diagram of the improved car battery voltmeter

Sinclair launch their System 2000 range with coils. They have become an integral part of provision for plug-in remote and switched the amplifier, tuner and speaker shown here. the printed circuit and never need adjust- tuning units (available separately). The amplifier and loudspeaker are equally outstanding and well worth comparing for yourself. Ask your dealer for a demonstration or write or ring us for a leaflet.

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