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23401 ITRESPEAKERCABMETS Z88CPUHEXKEYPAD

MIOINTERFACING TECHNIOUES
412 LGGIT COUNTER
WEATHER SATELLITE DOWNCONVERTER

## C(0) NTENTg

## EDITORIAL

I'Maplin Projects Book Twenty Two is a compilation of the pfojects from 'Electronics - The Maplin Magazine, Issue $22^{\prime}$, which is now out of print. Other issues of 'Electronics - The Maplin Magazine' will be replaced by projects books as they go out of print. For kit prices, please consult the latest Maplin catalogue and ifree price change leaflet, order as CA 99 H .


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## PROJECTS

## $2 \mathrm{HI}-\mathrm{FI}$ <br> LOUDSPEAKERS

ITwo excellent speaker designs offering 40-litre and 23-litre enclosures.


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- A versatile all-purpose counter in one compact module.


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A keypad and ROM allowing direct programming of and control over the Z80


19 WEATHER DOWN CONVERTER
I Describing the aerial, preamp and down converter enabling the Maplin 'MAPSAT' system to receive the Meteosat 2 satellite


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## 24 BOB'S MINI

Audio Level Tester, Sound Triggered Flash, In-Circuit Resistance Meter, Audio Isolator.


## 30 MID <br> INTERECCIING TECHNIOUES

- Describes a MIDI interface module and its use with 6502 based micros, such as the VIC20 and CBM64.



## HIFI LOUDSPEAKERS AND ENCLOSURES

## by Dave Goodman

Loudspeakers generally appear to be simple devices, where electrical signals are applied to one end with sound eminating from the other! In fact their design and application is very complexnot just a question of 'bashing up a quick box' hoping it will sound O.K.

This series will be featuring the very latest range of High Fidelity Loudspeakers in the Maplin catalogue, explaining manufacturers' specifications, and how to use them in your own designs. In addition, kits in the form of pre-shaped baffles with speakers, Xover, etc., will be available to accompany the series.

The three main drive units available at this time are:

Bass Driver YN24B
50W Extended Bass YN4TB
Fibreglass Driver YN25C
Each unit has a high temperature aluminium voice coil former allowing good heat dissipation with very low distortion characteristics.

The six inch, square bass driver (YN24B) has a flat diaphram constructed from microfoam damped polymer foam that has been sandwiched between aluminium sheets. This design offers good vibration control with excellent spatial dispersion making it ideal for use with digital recordings (CD). Cabinet


## BASE DRIVER - YN24B

## Chassis

Baffle
Fixing
Flux Density
Frequency Response
Power Handling
NOM Impedance
Sensitivity
Coil Diameter
Free Air Resonance
Moving Mass
Suspension Compliance
Mechanical Q
Electrical Q
Total Q
Equivalent Air Load
Effective Surface
$150 \times 150 \mathrm{~mm}$
$133 \times 133 \mathrm{~mm}$. Corner radius 20 mm $134 \times 134 \mathrm{~mm}$. 5 mm clear
(B)

7800 Gauss
$65 \mathrm{~Hz}-4 \mathrm{kHz}$
60W peak, 30W RMS
8 ohm
87dB (IW - 1M)
25 mm
(Fs) $50 \mathrm{~Hz} \pm 8 \mathrm{~Hz}$
(Mmd) 0.0107 Kg
(Cms) 0.0009
(Qms) 2.26
(Qes) 0.61
(Qts) 0.48
(Vas) 22 Litres
(Sd) $\quad 0.0131 \mathrm{~m}^{2}$

mounting is from the outside using the 170 mm square plastic trim supplied.

The eight inch extended bass driver (YN47B) is a more traditional looking unit constructed with a paper cone and dust cover, and a foam rolled surround. The chassis finish is cosmetic, for outside cabinet mounting and the quality of reproduction is excellent.

The six and a half inch bass/mid range driver (YN25C) has a yellow, varnished fibre glass cone with velbex dust cover and rolled surround. This unit has excellent bass response and superbly defined mid range reproduction, producing a clear, airy overall sound with a high degree of realism.

## Designing a System

Loud speaker performance characteristics are of great importance when looking for optimum performance in a system. Cabinet dimensions can be calculated quite accurately for a particular drive unit, although the final performance may not always be what the constructor expects. For instance, a relatively small (8in.) woofer may require
a 250 litre cabinet for optimum performance. 250 litres of volume is approximately 8.83 cubic feet, which if imagined as a cube would measure over 2 feet high by 2 feet wide bv 2 feet deep. Today's 'midi system' popularity in hi-fi systems favours the small book case size cabinet, and it would take some book case to hold a cabinet of these proportions! Therefore, one must decide on a cabinet size suitable for the room available and also the cost and complexity of construction.

For this series, we will be considering the closed box and ported box (reflex) type of design orly, using the mathematical models introduced by an Australian Engineer, $\mathbb{A} . N$. Thiele. Speaker manufacturers supply data for use in these calculations, which makes life a lot easier for the constructor.

A closed box means just that! A completely sealed and air tight box, whereas the ported box has a tuned port which may be a simple hole or slot cut in the front panel. Closed boxes are usually smaller in size than reflex types and much favoured by the system manufacturer. They exhibit a gradual roll off or
slope at low frequencies, and particularly small cabinets can sound lacking in bass. The ported box is larger in size with much greater bass response and a more pronounced cut-off below the tuning point or resonance. However, such a cabinet still requires to be designed and made carefully or it too will have only a poor performance!

## Speaker Parameters

The first step in optimising a cabinet design requires information based on the driver specification. Figures 1 and 2 stow graphical responses of the square bass driver calculated from the given daia. On the vertical X axis, Vb is the box volume shown as both cubic feet and litres, the horizonal $Y$ axis shows the box resonance ( Fb or Fc ), and the -3 dB cut-off frequency (F3). Two plots on each graph are shown for the free air resonance frequency (Fs) of 51.5 Hz $\pm 8 \mathrm{~Hz}$; one curve for 44 Fz and the other for 59 Hz . These are hcwever extreme variations, and random tests on various samples have produced Fs figures of 44 Hz quite consistantly If an average


FIBRE GLASS DRIVER - YN25C

Chassis
Baffle
Fixing
Flux Density
Frequency Response
Power Handling
NOM Impedance
Sensitivity
Coil Diameter
Free Air Resonance
Moving Mass
Suspension Compliance
Mechanical Q
Electrical Q
Total Q
Equivalent Air Load
Effective Surface
$173 \times 173 \mathrm{~mm}$
145 mm
$124 \times 124 \mathrm{~mm} .5 \mathrm{~mm}$ clear 13000 Gauss
$35 \mathrm{~Hz}-5 \mathrm{kHz}$
45W peak, 22W RMS
8 ohm
91dB (IW - 1M)
25 mm
(Fs) $\quad 44 \mathrm{~Hz} \pm 7 \mathrm{~Hz}$
(Mmd) 0.0114 Kg
(Cms) 0.00017
(Qms) 1.01
(Qes) 0.22
(Qts) 0.18
(Vas) 30 Litres
(Sd) $0.0113 \mathrm{~m}^{2}$

point between both curves is taken, then from Figure 1:
(1) Box volume $\mathrm{Vb}=57$ litres

Resonance $\mathrm{Fc}=60 \mathrm{~Hz}$
Cut off $\mathrm{F} 3=80 \mathrm{~Hz}$
(2) Box volume $\mathrm{Vb}=28$ litres

Resonance $\mathrm{Fc}=69 \mathrm{~Hz}$
Cut off $\mathrm{F} 3=76 \mathrm{~Hz}$
(3) Box volume $\mathrm{Vb}=18$ litres

Resonance $\mathrm{Fc}=75 \mathrm{~Hz}$
Cut off $\mathrm{F} 3=75 \mathrm{~Hz}$
The smallest practical size of a closed box for this speaker is just over 0.5 cubic feet internal volume, but as can be seen, the low frequency response is not very good. From Figure 2:
(1) Box volume $\mathrm{Vb}=57$ litres

Resonance $\mathrm{Fb}=38 \mathrm{~Hz}$
Cut off $\mathrm{F} 3=32 \mathrm{~Hz}$
(2) Box volume $\mathrm{Vb}=28$ litres

Resonance $\mathrm{Fb}=48 \mathrm{~Hz}$
Cut off $\mathrm{F} 3=45 \mathrm{~Hz}$
(3) Box volume $\mathrm{Vb}=14$ litres

Resonance $\mathrm{Fb}=58 \mathrm{~Hz}$
Cut off $F 3=63 \mathrm{~Hz}$
Using a tuned port in a 0.5 cubic feet box extends the low frequency response below 63 Hz , which is a significant improvement over the closed box. To realise the full potential of this speaker, but keeping the cabinet size down, a ported box of 24 to 40 litres internal volume is required.

Predetermined equations are used to calculate box volumes. For a ported box: $\mathrm{Vb}=\operatorname{Vas} \times \mathrm{Ots}^{2} \times \mathrm{S}$
Where:
Vas is the equivalent air load.
Qts is the total speaker $Q$.
$S$ is the peak or dip in response at resonance (sensitivity).
Vb is the box internal volume.
The $S$ factor is related to active port surface area in a reflex cabinet, which determines the amount of increase or decrease in output at resonance. Values of $S$ range from 4 to 16 where, $S=4$ is -3 dB down and $\mathrm{S}=16$ is +3 dB up at the cabinet/driver resonant point ( Fb ). As an example of determining the effect of $S$ in designs, a value of 8 offers a relatively flat response at Fb with smooth roll off to F3. As a matter of interest classical music tends to favour a slight drop in output at $\mathrm{Fb}, \mathrm{S}=5.7$ (approx. 1 dB down), and pop music $=11$ (approx. 1 dB up).

Using data from the Bass Driver specification and $S=5.7$ in the equation as follows:

$$
\begin{aligned}
& \text { Vas }=22 \text { litres } \\
& \text { Ots }=0.48 \\
& \mathrm{~S}=5.7
\end{aligned}
$$

$\mathrm{Vb}=22 \times 0.48^{2} \times 5.7$ i.e. $\mathrm{Vb}=28$ litres.
With $\mathrm{S}=8$ :
$\mathrm{Vb}=22 \times 0.48^{2} \times 8$ i.e. $\mathrm{Vb}=40$ litres.
With $S=11$ :
$\mathrm{Vb}=22 \times 0.48^{2} \times 11$ i.e. $\mathrm{Vb}=55$ litres.


Figure 1. Bass Driver closed cabinet response graph.


Figure 2. Bass Driver vented cabinet response graph.

A larger box could be designed in this way, but then the driver will be under-damped, thus reducing its power handling capabilities. Similarly, smaller boxes will over-damp the driver, thus lifting the Fb with subsequent loss of bass output.

With values of S equal to eight, Vb is the optimum value. Cabinet resonance can be calculated from $\mathrm{Fb}=(0.39 \times$ Fs)/Qt and the -3 dB cut-off point by squaring Fs , multiplying this by Fs squared, dividing the result by Vb and finding the square root of the total, i.e.
$\mathrm{F} 3=\sqrt{\frac{\mathrm{Vas} \times \mathrm{Fs}^{2}}{\mathrm{Vb}}}$

Therefore, for Qts $=0.48, \mathrm{Fs}=44 \mathrm{~Hz}$ and $\mathrm{Vb}=40$ litres:


The next step is to find port dimensions to suit Vb and Fb by referring to Figure 3 and 4 for tube length and diameters.

Each of the curves represents a different cabinet volume, Vb , and cabinet resonance, Fb , is on the vertical X axis. If a line is drawn horizontally from $\mathrm{Fb}=$ 40 Hz to a point between both 28 litre and 43 litre curves, corresponding to $\mathrm{Vb}=40$
litres, then tube lengths can be read along the horizontal scale. Either 50 mm internal diameter or 75 mm internal diameter tubes are suitable for use as a port on low volume systems and for this design a $50 \mathrm{~mm}(\mathrm{~d}) \times 63 \mathrm{~mm}(\mathrm{l})$ or $75 \mathrm{~mm}(\mathrm{~d})$ $x$ 167mm(l) tube is called for. Larger diameter tubes are always longer in length and may be too long to fit inside the cabinet. When this happens with a 75 mm tube, the 50 mm tube used instead will be shorter for the required resonance. At extremely low frequencies, or when speaker/cabinet design offers a high SPL (output sensitivity), air movement through the tube can produce a chuffing noise which can be very disturbing. Always try to use the largest port possible ensuring a clearance between inside back panel to tube of at least 75 to 100 mm .

## Cabinet Dimensions

Producing an enclosure that adds to the sound from a loud-speaker is to be avoided, or kept to an absolute minimum. Any added resonances will 'colour' and distort the original sound. A cabinet made from thin materials will audibly vibrate and absorb low frequency energy, to the detriment of bass response. The enclosure shape can also affect performance by lumping air resonances at different frequencies and from sound energy reflecting off internal wall panels through the speaker cone. Designing an enclosure with non-parallel walls minimises internal reflection problems, but is extremely difficult to build. The finished triangular cabinet shape may also look like anything but a speaker system!

To keep construction simple with the minimum of panel resonances and reflections, heavy duty or industrial grade chipboard of 19 mm ( $3 / 4 \mathrm{inch}$ ) thickness is best employed. Flakey chipboard that looks like a slab of compacted straw should be avoided. Each of the three cabinet dimensions have a relationship in the ratio of $1: 1.6$ : 2.3, thus ensuring that no side is an exact multiple of any other and thereby spreading resonances instead of lumping them. Figure 5 shows Vb the internal volume) three dimensionally as (w)idth 1 , (d)epth 1.6, and (h)eight 2.3. With volume $\mathrm{Vb}=40$ litres $(40,000 \mathrm{cc})$ the equation used for calculating each dimension is the third root (cube) of Vb divided by the ratio product, and multiplied by each side ratio in turn.

Thus from $3 \sqrt{\mathrm{Vb} /(\text { ratio product) }}$ we get:
$\mathrm{w}=3 \sqrt{40000 /(1 \times 1.6 \times 2.3)} \times 1$
$d=3 \sqrt{40000 /(1 \times 1.6 \times 2.3)} \times 1.6$
$h=3 \sqrt{40000 /(1 \times 1.6 \times 2.3)} \times 2.3$
Therefore: $\mathbf{w}=22.15 \mathrm{~cm}, \mathrm{~d}=35.44 \mathrm{~cm}, \mathrm{~h}$ $=50.95 \mathrm{~cm}$.


Figare 3. 50 mm I/D tabe length graph.


Tweeters.


Figure 4. 75mm I/D tube length graph.

If these dimensions are simplified to integers, then cabinet internal sizes are w $=23 \mathrm{~cm}, \mathrm{~d}=35 \mathrm{~cm}, \mathrm{~h}=50 \mathrm{~cm}$. The volume Vb is now $40,250 \mathrm{cc}$ or 40.25 litres, a small increase of $0.6 \%$.
For $\mathrm{Vb}=23$ litres $(23,000 \mathrm{cc})$,

$$
\mathrm{w}=19 \mathrm{~cm}, \mathrm{~d}=29 \mathrm{~cm}, \mathrm{~h}=42 \mathrm{~cm} .
$$

## Construction

It must be stressed that calculations used in this article are based on theoretical principles and do not necessarily reflect the perfect design. In practice, parameters like volume are reduced by speaker metalwork, crossover modules, bracing, port rube volumes and stuffing material. Jointing methods affect volume and all of these factors should be considered before cutting any panels. In this particular design, the 40 litre Vb figure allows for approximately $5 \%$ over volume to accomodate the extras. Butt joints should be used for assembling panels, fixing with white wood glue and 1.5 inch chipboard screws.

Only a front baffle is supplied in these kits so the constructor must arrange to have five side/back panels made to the cutting lists shown. Most timber suppliers have facilities for cutting chipboard quite accurately to customer requirements and the material costs should not be very high.


Figure 5. Relationship between width, depth and height.


Figure 6. 40 Litre cabinet panels.


Inside the $\mathbf{4 0}$ litre cabinet and front baffle layout.


Figure 7. 23 Litre cabinet panels.


23 litre cabinet frent baffle.


Inside the 23 litre cabinet.


## CUTTING LIST (40 LITRE CABINET)

|  | Material | High Density Chipboard 19mm thick |
| :---: | :---: | :---: |
| (1) | Back Panel | l off $-500 \mathrm{~mm} \times 230 \mathrm{~mm}$ |
| (2 \& 3) | Side Panels | 2 off -500mm x 388mm |
| ( 4 \& 5) | End Panels | 2 off - $268 \mathrm{~mm} \times 388 \mathrm{~mm}$ |
|  | Material | Soft wood 15 mm square prepared. |
| (6 \& 7) | Hor. Frame | 2 off - 200 mm long |
| (8\&9) | Vert. Frame | 2 off - 500 mm long |
| RESIN 38 mm 25 mm | W' wood adh 1.5") No. 8 chip wire nails. | sive. <br> board screws. |

## CUTTING LIST (23 LITRE CABINET)

|  | Material $:$ High Density Chipboard 19 mm thick |  |
| :--- | :--- | :--- |
| (1) | Back Panel $:$ | 1 off $-420 \mathrm{~mm} \times 290 \mathrm{~mm}$ |
| $(2 \& 3)$ | Side Panels $:$ | 2 off $-420 \mathrm{~mm} \times 228 \mathrm{~mm}$ |
| $(4 \& 5)$ | End Panels $:$ | 2 off $-228 \mathrm{~mm} \times 328 \mathrm{~mm}$ |
|  | Material $:$ | Soft wood 15 mm square prepared. |
| $(6 \& 7)$ | Hor. Frame $:$ | 2 off -260 mm long |
| $(8 \& 9)$ | Vert. Frame : 2 off -420 mm long |  |
| RESIN 'W' wood adhesive. |  |  |
| 38mm (1.5") No. 8 chipboard screws. |  |  |
| 25mm wire nails. |  |  |

Figure 8. Assembling the panels.
19 mm or $3 / 4 \mathrm{in}$. chipboard is a common variety and dimensions given are based on this. If thinner or thicker board is used then change the end panel ( 268 mm - 40 litre, 328 mm - 23 litre) size accordingly. See Figure 6 and Figure 7.

Figure 8 shows assembly details for the panels. Either large 'sash' clamps or chipboard screws can be used for holding panels together while the glue dries, but do not apply glue to the back panel (1). The use of screws is not recommended on pre-finished or laminated panels unless screw heads appeal to the constructor! Also note that plastic laminated edges butting up together cannot be glued successfully, although wood veneer will be O.K. On side panels (2) \& (3), drill 5 clearance size holes along the front edge only, approximately 9 mm in from the edge, and counter sink each hole. Apply a liberal amount of glue to the long edge of the front baffle on the right hand side and offer up panel (3). Insert screws and repeat the procedure for panel (2). Temporarily place back panel (1) between the side panels to space them apart evenly and tighten all screws. Remove excess glue exuding from the front joints with a damp cloth and while the assembly is drying, drill and counter sink end panels (4) \& (5) as before. Do not drill along the back edge, only along the front and sides. Apply glue to the assembled side and front panels along the top edges, but not the back panel and offer up panel (4). Fit all screws and remove the back panel which may now need cleaning, and wipe away excess glue from all but joints. Turn the assembly over and fit panel (5) as before. Figure 9 shows a completed cabinet.


Figare 9. Completed cabinet.

## Mid-range Driver YP13P

 pleated paper surround.Specification
Flux density Frequency response
Enclosure type Power handling

Impedance
Coil diameter
Chassis size
Fixing centres
Baflle cut-out
Free-air resonance
(infinate baflle enclosure)
Acoustic response

A mid-range loudspeaker with ferro-fluid cooled voice coil for increased power handling, and an enclosed steel chassis, requiring no special enclosure and can be fitted directly in bass speaker's cabinet. The speaker has a paper cone and dust cover with a

9500 Gauss
$500 \mathrm{~Hz}-12 \mathrm{kHz}$
Infinate bafile 70W @ 1kHz (DDN 45573) 35W RMS
$8 \Omega$
16.5 mm
$100 \times 100 \mathrm{~mm}$
$70 \times 70 \mathrm{~mm}, 4.5 \mathrm{~mm}$ clear
75 mm diameter
$850 \mathrm{~Hz} \pm 128 \mathrm{~Hz}$
91dB (IW @ lm)



Figure 10. Back panel framework.
Figure 10 shows a simple wood framework which is glued and pinned inside the cabinet, and holds the back panel (1) in place. Both longer ( $8 \& 9$ ) verticals are the same length as panels ( 2 \& 3) but horizontals ( 6 \& 7 ) must be cut to lengths determined by their thickness. Dimensions given are for 15 mm square prepared and should be the thinnest material used; thicker material will mean shorter lengths for ( $6 \& 7$ ) accordingly.

Draw a reference line inside the cabinet either 19 mm in from the back edges or to suit the back panel thickness. Glue both verticals ( 8 \& ) to this reference line and use wire nails to hold in position. Do the same for ( $6 \& 7$ ) spreading glue over each cut end. Wipe away any excess glue and re-measure the recess between frame and panel ediges, make any adjustments to ensure the frame is square.

## Beffie Mountings

Figure 11 shows the port tube fitted in the baffle hole cut-out below the square woofer. Spread a thin layer of glue around the inside of the baffle port hole and insert the tube from behind. Run a thin filler of glue around the tube on the inside face of the baffle keeping the tube flush with the front face and leave to dry.

Before mounting loudspeakers, solder connecting wires to each +V and $-V$ terminal on all units; this can be an awkward job to do with the speakers in place! Insert speakers into the baflle from the front and secure with $4 \mathrm{BA} \times 1.5^{\prime \prime}$ bolts, shakeproof washers and 4BA nuts.

## 40 Litre Beffile - <br> 3-Way System

Refer to Figure 12. Fit the rectangular tweeter FD95D into the
topmost position and the mid-range speaker YP13P into the centre hole. Fit the bass driver YN24B into the square cut-out position. and mount the plastic trim cover over the top. Be careful not to force any cones during installation and take precautions while tightening up with a screwdriver. It may be found advisable to cover the speakers with thick card whilst doing this, just in case the screwdriver should slip!

## 23 Litre Beffie -2-Way Systom

Refer to Figure 12. Fit the dome tweeter YN43W into the topmost position and insert the bass driver YN24B into the square cut-out. Mount the plastic trim cover over the top and follow the same precautions during installation as before. If required, a protective grille can be fitted over the dome tweeter to prevent dust and damage from spoiling its performance. The dome is very soft and sticky by design and should not be handled. To fit the grille, very carefully remove all 4 star-head screws, place the grille over the hole positions and replace the screws.

## Cross Over and Wiring

When wiring speakers to the crossover module, use a separate cable pair to each unit, connecting terminal 'W' to bass speaker positive, ' $T$ " to tweeter positive, and if used, $M$ ' to mid-range positive. The positive terminal on each speaker will be marked either by a red dot or a + symbol stamped into the plastic housing. Speaker negative or return cables should be terminated to the crossover module terminals marked ' $C$ '.

On the 2-way module only, it is necessary to remove the bass speaker


Figure 11. Fitting the port tribe.
by-pass capacitor. This component is not marked, but can be identified by placing the module with input terminals ' IN ' and 'C' facing to the left and output terminals ' N , ' C ' and ' T ' facing to the right. The capacitor to be removed is then the botom one which is soldered to the track areas marked with a ' $W$ ' and ' $C$ ' on the copper side. De-solder both capacitor leads and remove carefully. If required, the capacitor can be left in position, but this has the effect of emphasising the mid-range response on the bass speaker which sounds quite raucous in this particular set-up.

## Cabinet Wadding

Cut five pieces of fibre wadding to fit inside the cabinet and fix to the walls with adhesive. Both sides, top and bottom panels and the back panel should be covered, but not the baifle panel. Keep the port tube clear of any obstructions and do not cover the crossover module. Depending on where the module is fitted in the cabinet, cut out a section of wadding so that it fits around the module and not over it. Also, allow clearance for the wood frame when cutting the back panel piece; the wadding should not be sandwiched in the recess when the panel is screwed in place.

Finally drill a small hole in the back panel for the connecting cable to your amplifier. If the speaker is to be sited some distance from the amplifier, then use a fairly thick cable, such as 2 -wire mains cable, and not the thin bell-llex variety. On the input side of the crossover module, 'IN is the positive terminal and ' $C$ ' the return terminal. Ensure correct polarity connections to the amplifier on stereo speakers, to keep speakers in phase with each other. If you are not sure about polarity, connect a


Figare 12. Baffle moanting.
1.5V battery with positive to ' IN ' and negative to ' $C$ ' on the module. The woofer cone-panel should pop outward, if not then the polarity is incorrect and the woofer (or crossover) wining should be reversed. Figure 13 shows the two types of cabinet discussed in this article.

Amplifiers vary in their power output specifications and often 'MIDI' systems rated at 40 Watts per channel refer to peak output power. Both of these designs have a 50 W peak continuous sine wave rating which should be adequate for most domestic listening environments.


Figure 13. 40 and 23 Litre cabinets.

## FINAL SPECIFICATIONS

23 Litre Cab, 2-Way System, Reflex Port

Cross Over Power Rating Maximum Signal Frequency Response Impedance
2.75 kHz (Modified) 50W peak, 25W RMS Continuous Sine Wave 40V peak, 14.2V RMS Sine Wave 50 Hz to $16 \mathrm{kHz}, 4 \mathrm{~dB}$ peak at 60 Hz 82

40 Litre Cab, 3-Way System, Reflex Port

Cross Over
Power Rating Maximum Signal Frequency Response Impedance

1 kHz and $6 \mathrm{kHz},-6 \mathrm{~dB}$
50W peak, 25W RMS Continuous Sine Wave 40 V peak, 14.2 V RMS Sine Wave
45 Hz to 23 kHz
$8 \Omega$

## 23 LITRE CABINET KIT PARTS LIST

MISCELLANEOUS

| Hi-Fi Bass Driver | 1 | (YN24B) |
| :--- | :--- | ---: |
| Hi-Fi Dome Tweeter | 1 | (YN43W) |
| Grill for Dome Twtr | 1 | (FD93B) |
| Plastic Pipe 130 | 1 | (YP15R) |
| Crossover 2-Way | 1 | (WF02C) |
| Baffle 2W23L | 1 | (XJ09K) |
| 4BA $\times 1 / 1 / 2 i n$. Bolt | 1 Pkt | (LR52G) |
| 4BA Nut | 1 Pkt | (BF17T) |
| 4BA Shake | 1 Pkt | (BF25C) |
| No. $4 \times 1 / 2 \mathrm{in}$. Self Tap | 1 Pkt | (BF66W) |
| Acoustic Wadding | 2 m | (RY06G) |
| Hi-Fi Loudspeaker Cable | 1 m | (XR72P) |
| Instruction Leaflet | 1 | (XT62S) |
| Constructors' Guide | 1 | (XH79L) |

The above items are available as a kit: Order As LM21X (23 Litre Cabinet Kit)

## 40 LITRE CABINET KIT PARTS LIST

## MISCELLANEOUS

| Hi-Fi Bass Driver | 1 | (YN24B) |
| :--- | :--- | ---: |
| 70W 3.25in. Speaker | 1 | (YP13P) |
| Mini Dome Twtr Rect | 1 | (FD95D) |
| Plastic Pipe 180 | 1 | (YP14Q) |
| Crossover 3-Way | 1 | (WF03D) |
| Baffle 3W40L | 1 | (XJ08J) |
| 4BA $\times 11 / 2 i n$. Bolt | 2 Pkts | (LR52G) |
| Nut 4BA | 2 Pkts | (BF17T) |
| 4BA Shake Washer | 2 Pkts | (BF25C) |
| No. 4 Self Tap $\times 1 / 2 i n$. | 1 Pkt | (BF66W) |
| Acoustic Wadding | 3 m | (RY06G) |
| Hi-Fi Loudspeaker Cable | 2 m | (XR72P) |
| Instruction Leaflet | 1 | (XT62S) |
| Ccnstructors' Guide | 1 | (XH79L) |



Particular projects from the Maplin range have proved themselves to be very popular, but technology and component specifications have a habit of changing, with the result that some of these projects are in danger of becoming obsolete. Even if this were not the case, it may be equally worthwhile improving the project in question in order to increase its versatility and usefulness, and bring it up-to-date.' The 2nd Time Around' series is dedicated to reviewing and improving original, popular Maplin projects by republishing them with the necessary updates and improvements, ensuring their continued availability. This time it is the turn of the $41 / 2$ Digit Counter.

# $41 / 2$ DIGIT LCD 

 COUNTERby Mike Holmes and Dave Goodman based on an original project by Mark Brighton

## FEATURES

ڤ General purpose counter

* Low power consumption
$\star$ LCD display
$\star$ Buffered inputs
* Schmitt trigger count input
$\star$ Will display from 0 up to 19999
« Can be flush fitted behind a display panel

This is a simple $41 / 2$ digit counter intended for general purpose applications, which was first published in the March 1987 issue of 'Electronics'. It can be used either as it stands or in the form of a 'building block' module where a counter function is required as part of a larger system. Please note however, that it is not a complete frequency counter or a timer in its own right, but it can be used in these applications if provided with the necessary external gating signals as supplied by a separate timebase of some description.

The counter is built around the ICM1224 IC, which actually contains all the essential circuitry to operate as an incremental counter and simultaneously drive a $41 / 2$ digit LCD. The chip includes a

19 kHz oscillator and a divide by 128 divider producing a 150 Hz signal for the display's FC backplane, making it very easy to use, and requiring only one +5 V DC regulated supply and the display to function.

## Circuit Description

The 7224 is shown as ICl in Figure 1. This is a minimum circuit configuration for this device, and makes use of just three main inputs to the chip for the functions COUNT, COUNT/INHIBIT and RESET. Inverter stages TR1 to TR3 have been added to the minimum configuration to provide a measure of immunity to overvoltage signals, by buffering the chip from the board inputs, and enabling the counter
to operate over a wide range of input vcltage peaks from approximately +2 to +20 V . This is a desirable precaution, given that 'general purpose' covers uses that may require signal voltages to be arything but standard logic levels.

To reset the counter to zero, the RESET input at TB9 is taken to $>+0 \cdot 7 \mathrm{~V}$, which also blanks the display if TB14 is connected to +5 V or left floating. In this configuration ICl is normally in leading zero blanking mode. For all other functions the RESET input TB9 should be grounded or left open circuit to ensure normal counting operations. The COUNT input, TB8, is negative going, that is it requires a positive voltage falling to zero for a count of one to be incremented in the counter. This input need not neces-


Figure 1. Circuit diagram.
sarily be a hard edged pulse or square waveform, since the COUNT input of ICl incorporates a Schmitt trigger input stage, which, together with the gain of TR2, will allow slower voltage variations. For example, a low level sine wave signal (less than 5 V peak) alternating about 0 V can be used to trip the counter on each falling positive half-cycle. Moreover, the Schmitt trigger operation offers some immunity to interference and noise injection, thus preventing erroneous and erratic behaviour of the counter. This flexibility allows a wide variety of COUNT input sources, including various types of sensors

The COUNT/INHIBIT input at TB7 is used to defeat normal counting operation so that the display remains static without the need to remove the signal at the COUNT input. To do this, COUNT/ INHIBIT is taken to $>+0.7 \mathrm{~V}$. In this condition the last count will be retained and displayed indefinitely, or can be cleared to zero with RESET. COUNT/ INHIBIT must be grounded or open circuit at TB7 to enable, or resume, normal counting. In this way the counter can be 'gated' by a means based on a time period or a specific condition (e.g., counting allowed provided condition is true, etc.).

Some additional terminals are provided on the board, which are direct


Figure 2. PCB layout.

connections to both the LCD array and ICl , and brought out and made available to the user. These include the four decimal point positions TB3 to TB6, the plus sign TB1, and the minus sign TB2. If the counter module is to be incorporated into a system for a particular task, then these can be hard-wired to 0 V as required, or temporarily connected for some general purpose use to clarify the display. TB15 is provided as a second convenient earth terminal for this purpose. The decimal points may be switched if the module is to form the basis of some sort of frequency or period counter, over several switched ranges. If these terminals are not to be used, they should be left floating.

## Construction

With reference to the Parts List and the board layout and legend shown in Figure 2 , insert and solder all six resistors, then the 15 veropins from the bottom (nonlegended) side of the board at the positions TBl to TBl5 using a hot iron to push them home, then solder in place. Insert and solder TR1 to TR3, making sure orientation is correct by aligning their package style with the ' $D$ ' shaped legends. Carefully fit the LCD array DY1 to the underside of the board, ensuring that the glass pip or similar marker at one end aligns with the rectangular marker on the legend shown in dotted lines on the top side, and that all the pins line up with their respective holes. Be very careful when soldering that the display does not get hot; if it does, wait between soldering operations until it is cool. It may be helpful to solder each corner pin first to ensure the display is seated properly before soldering all the pins. In fact the pins may be quite long, and if so then it is possible to gain some extra height above the board by slipping a small, rectangular piece of corrugated cardboard underneath the display, which can then be removed after soldering. This will make the top surface of the display approximately 5.5 mm above the PCB, and the longer lead length will add extra resistance to heating during soldering.

The same soldering precautions can be applied to the 40 -pin DIL socket, which is fitted at ICl position with its end notch adjacent to the rectangular marker on the legend. Upon completion, check your work for possible short circuits and incomplete solder joints. In particular closely examine the areas between the pins of the DIL socket and the display. When you are satisfied that all is well, carefully insert ICl into its socket, ensuring that its end notch aligns with that of the socket, and commence testing the module.

Pins TBl to TB6 on the board access the 'plus' and 'minus' signs and the decimal point symbols of the display. Earthing each in turn to TB10 should cause these to appear.

## Using The Module

How the module is used is entirely up to the constructor, one of the most obvious applications being of the event counting vanety. Means of electrical input to the COUNT pin have already been described, and a mechanical switch can also be used between TB10 and TB8 with a pullup resistor between TR3 and TB11, to cause an increment of one whenever the switch is closed. The switch may be a thumb operated push-button for counting items by hand, or a micro-switch on a machine. Note however, that some form of 'debouncing' is desirable and a minimum requirement would be a 100 nF capacitor connected across the switch terminals. Figures 3 and 4 show alternative arrangements utilising a magnetic hall effect device (with a magnet) and an optocoupler respectively as sensors. Either of these latter two methods can produce a counter for a hand operated wire winding machine, or for use with a wheel to make an odometer etc.

Some more sophisticated functions are available if desired. TBl2 provides access to the CARRY outpat of ICl (active low), which can be used to drive an overflow indicator or even a second, cascaded counter module.

Connecting TB13 to ground via a wire link to TB10 defeats the leading zero blanking mode, causing the display to show zero on RESET.

TB14 provides access to the chip's STORE input (active low). The STORE function controls internal latches which transfer decoded display data from the count decoders to the display drivers; whilst TB14 is low the latches are 'transparent' and the display reflects real time counting. On TB14 being high, the last count value is latched, causing the display to hold this value, even though counting is continuing. Pulsing TB14 low will show updates of the count value, remaining static between times. RESET does not effect the display. For continuous real time counting TB14 should be wired to 0 V at TB15.


Figure 4. Opto-coupled input.
A simple frequency counter can be made using a suitable external clock and logic timebase producing, in sequence,

COUNT/INHIBIT (TB7) low for the gating (sampling) period, followed by STORE (TB14) low to update the display, then RESET (TB9) to clear the counters for the next gating period. Signal input is to COUNT continuously. If a gating period of 1 second is used, you will have an AF frequency counter with a resolution of 1 Hz and an upper display limit of $19,999 \mathrm{~Hz}$, covering the entire AF band with no range switching necessary.

Other applications of this sort include machine rpm indicators, engine rpm and maybe even mph counters using suitable sensors. The LCD is deliberately placed on the underside of the PCB so that it can be incorporated into a front panel display design with the actual LCD mounted flush behind its display window, but leaving all connections accessible.

## 4 $1 / 2$ DIGIT COUNTER PARTS LIST

RESISTORS: All 0.6 W 1\% Metal Film

|  |  |  |
| :---: | :---: | :---: |
| Rl-3 10k | 3 | (M10K) |
| R3-6 4k7 | 3 | (M4K7) |
| SEMICONDUCTORS |  |  |
| ICl ICM7224IPL | 1 | (FP62S) |
| TR1-3 BC548 | 3 | (QB73Q) |
| MISCELLANEOUS |  |  |
| DY 1 41⁄2 Digit LCD | 1 | (FP61R) |
| PCB | 1 | (GD44X) |
| DIL Socket 40-pin | 1 | (HQ38R) |
| Pin 2141 | 1 Pkt | (FL21X) |
| Instruction Leaflet | . | (XT55K) |
| Constructors' Guide | 1 | (XH79L) |

The above items are available as a kit Order As LM19V (41/2 Digit Countr Kit)

The following item (which is included in the kit) is also available separately.
41/2 Dig Countr PCB Order As GD44X

# Keypad for the * Type in your own code easily Visual display aids checking simple cont * Simple construction by Graham Dixey c.Eng., M.I.E.R.E. 

## Introduction

The Maplin Z80 CPU card which was published in Issue 15 of 'Electronics' offers an inexpensive way to get to grips with computerised control systems for those who can write their own control programs and put them into an EPROM. The provision for 8 K of on-board memory is generous for such applications, and input/output decoding for peripheral chips is also provided. Unfortunately the module is totally devoid of any kind of resident software and is completely inaccessible to the 'user' in its basic form. Provision is made for a keyboard to be added however, the suggested device is the 8279 programmable keyboard/ display interface IC, which can look after a variety of input sources (keypads, full

## PLEASE NOTE THAT THIS PROJECT WILL SHORTIY BE REVISED AND RE-DESIGNED

keyboards, sensor arrays, etc.) and can control up to sixteen 7-segment displays if required. Thus, it is obviously possible to produce a small computer which can be programmed directly from a HEX keypad, with a 7 -segment display to monitor addresses, status and data, both in and out.

In the design presented here, there are eight 7 -segment displays, from left to right, the first four form the 'address field', the next two the 'status field' and the two on the right, the 'data field'. What is needed to achieve this simple objective is the module described here, a monitor resident in EPROM (which has been developed and is available - see Parts List), and some knowledge of Z 80
machine code or the desire to acquire it, which can be assisted by my current series on the subject.

Note that it is advisable to obtain a copy of Issue 15 of 'Electronics' to be sure exactly what is provided in the Z80 CPU kit and what is otherwise 'optional'.

## The Keyboard Display Circuit

The circuit is shown in Figure l, and is quite straightforward, largely due to the built-in sophistication of the 8279 IC.

The display is multiplexed at a rate determined by the system clock and a control word sent by the monitor. As a result, a binary counter output appears



Figare 1. Circuit Diagram.

## Keypad for the Z8O



## The Monitor EPROM

A simple monitor occupies rather less than half of a 2716 EPROM and provides the following facilities:
(a) Access to any memory location to examine and/or modify data,
(b) Step backwards or forwards through memory to examine, edit or enter program data,
(c) Run a program from a given start location.

No sophistication is claimed for the monitor, but it is easy to use with a little practice. It operates as follows.

On power up, eight dots appear on the display to indicate that the monitor is running. Now press CTRL-A. CTRL-A means 'CTRL key plus A key, press down together'. OA now appears in the 'status field' to indicate 'address mode". Type in an address, which you'll notice goes into the display in 'typewriter' mode, i.e. left entry. If you now enter CTRL-D, the status field changes to Od (data mode), though the dots remain in the data field. However, anything typed on the keypad now will appear in the data field and is entered into memory at the location stated in the address field. Entering CTRL-F takes you forward to the next location, while CTRL-B takes you back to the previous location. CTRL-F and CTRL-B only operate in data mode and always refresh the display, in other words, an address must be entered first before you can move forward or backward from that location. You can use this facility to only examine or edit memory rather than entering a program. Instead of typing in the actual start address, type in the one 'before it'; enter data mode and then use CTRL-F. This takes you to the location you want and brings up the data actually at that location on the display. Now as you step forward or back through memory, you will have a simultaneous display of memory and data. You can look anywhere in ROM or RAM in this way. CTRL-A and CTRL-D allow you to 'toggle' back and forth between the two modes, so you can nip about in memory quite niftily.


Figure 3. Connections to Z80 CPU.
Note that the data display is only refreshed by CTRL-F or CTRL-B, which means that whenever you toggle back to 'data' mode from 'address' mode the data shown is arbitrary. To see the correct data at the new location, type CTRL-F followed by CTRL-B (or vice-versa).

Thereafter, any memory locations examined by use of either of the latter control keys is correct.

To run a program that you have entered, type CTRL-E and OE appears in the status field. Enter the start address of the program, and operate CTRL-C. The program will now run and eight dashes on the display is the sign that it is doing so. If you put a HALT instruction at the end of your program, it will wait and you can then leave the program by using the 'reset' keys, which will take you back into the monitor. This, of course, allows you to examine any memory locations that might have been modified by the program simply by re-entering the sequence via CTRL-A.

## Scratchpad RAM

As with all monitors, this one requires a small amount of RAM for its own use. This it uses for the storage of variables and also as a 'stack' during the running of certain monitor routines. The monitor ROM occupies the addresses $\& 0000$ to 807 FF ; the following area of the memory map is normally occupied by RAM, starting at address $\$ 0800$. The monitor claims the bytes from 80800 to 8085 F for its scratchpad. This means that the lowest address at which user programs should be stored is 80860 .

| Summary of Control Functions |  |  |
| :---: | :---: | :--- |
| Command | Status <br> Code | Mode \& Action <br> Power-on |
| Reset | 8 dots | Monitor ready |
| CTRL A | 0 A | Monitor ready |
| CTRL D | Od | Adress mode. Enter address. <br> Data entry mode, following Address mode <br> only. Enter data. <br> Step forward one location and refresh <br> display. Data mode only. |
| CTRL F | 0d | $0 b$ |
| CTRL B | OE | Step backward one location and refresh <br> display. Data mode only. <br> Enter start address of program to execute. |
| CTRL E | 8 dashes | Execute program. |
| CTRL C |  |  |

## Z80 CPU MODULE HEX KEYPAD KIT PARTS LIST

| RESISTORS: All 0.6W 1\% Metal Film |  |  |
| :---: | :---: | :---: |
| Rl-8 22, | 8 | (M22R) |
| R9-16 470, | 8 | (M470R) |
| SEMICONDUCTORS |  |  |
| TR1-8 BC327 | 8 | (QB66W) |
| ICl ULN2803A | 1 | (QY79L) |
| IC2,3 74LS138 | 2 | (YF53H) |
| MISCELLANEOUS |  |  |
| DY1-8 1/2in. Display Type 1 | 8 | (FR39N) |
| PCB | 1 | (GD40T) |
| S1-19 Keyboard Switch | 19 | (FF61R) |
| Pin 2145 | 1 Pkt | (FL24B) |
| Keytop 1 Position | 18 | (FF62S) |




## by Robert Kirsch Part 1

PLEASE NOTE THAT THIS PROJECT AND THE WEATHER SATELLITE SERIES WILL BE UFDATED IN THE NEAR FUTURE!

A system for receiving and decoding data from polar orbiting weather satellites in the 137 MHz band has been described in previous issues of 'Electronics'. This article describes a Down Converter that may be connected di-
rectly to this receiving system to enable it to receive $S$ band signals from Meteosat 2, a geostationary weather satellite.

## The Mefeosat System

Meteosat forms one of a chain of five geostationary weather satellites located above the equator that cover the entire world, see Figure 1. Meteosat 1 was launched during November 1977 by the

European Space Agency (E.S.A.) and continued to work until November 1979, being replaced in June 1981 by Meteosat 2 , which is still in service.

This satellite is located on the Greenwich meridian, nearly $36,000 \mathrm{~km}$ above the equator ( 0 degrees $\mathrm{N}, 0$ degrees $E$ ) and orbits on the same axis as the Earth at a speed that maintains it in a fixed position above the Earth (geostationary orbit).


Figure 1. Weaticer Satellite system.

The Meteosat satellite system differs from the polar orbiting satellites in that the higher orbit of Meteosat enables it to see the whole disk of the Earth at once, thus it can make constant observations of the area covered. The satellite looks at the Earth through a device called a multispectral radiometer which provides four separate images, two visible and two in different infra-red spectral bands. One infra-red channel gives a thermal image of the Earth, the other responds to the water vapour absorption band giving an indication of the levels of atmospheric humidity.

The raw data from the satellite's radiometer is transmitted to the Earth for processing in the $S$ frequency band $(1670-2110 \mathrm{MHz})$. The receiving station for these signals, called the DAATS (Data Acquisition, Telemetry and Tracking Station), is located in the Federal Republic of Germany, about 50 km from Darmstadt. Signals received are fed to the Meteosat Ground Computer System at the European Space Operations Centre in Darmstadt, see Figure 2.

The data from the satellite is processed by a pair of large mainframe Siemens computers from which information is fed by land-line to world wide users and also back to the DAATS for re-transmission to the satellite in WEFAX format with coastline added. The satellite re-radiates this information on two S band frequencies, channel A at 1694.5 MHz and channel B at 1691 MHz ,


The Down Converter.
these are the signals received by the Down Converter described in this article. These signals are then translated to 137.5 MHz by the Down Converter ready for detection and decoding by the Mapsat receiving system.

## The Complete System

Figure 3 shows a block schematic of the complete receiving system, details of the Pre-amplifier, Down Converter and Channel Switching Unit are shown. Information on the Mapsat receiver and decoder can be found in issues 18 and 20


Figure 2. Meteosat 2 receiving station.
of 'Electronics' (XA18U and XA20W), which are available as back issues, see inside back cover.

## Aerial

The aerial used is of the loop-yagi type which is fairly unusual for this kind of application, but it has several advantages over more conventional satellite receiving aerials. The relatively small size of this aerial makes mounting it a simple exercise as it can be attached to most types of standard television aerial masts, its small surface area producing a low wind resistance. The beam-width of this system is fairly broad and aligning is quite easy. The complete receiving systerm may be operated from batteries in the vicinity of the aenial during adjustment and this enables the aerial to be aimed for the best signal from the satellite. This type of aerial is equivalent to a small dish and has a sharp horizontal polarisation. Due to the low level and high frequency of the signals received it is important to keep feeder losses as small as possible and for this reason a high gain low noise pre-amplifier is mounted directly below the receiving element of the aerial.

## Pre-amplifier

Figure 4 shows the essential circuit diagram of the aerial pre-amplifier, which, due to the nature of its application, is only available as a complete ready-built module. The circuit is a straightforward three stage design, having for the first two stages low noise GASFET transistors, TRI and TR2, which combine both high gain with a low noise level of less than ldB. The third stage is a moderately low noise MMIC (Monolithic Microwave Integrated Circuit), ICl, to provide sufficient overall gain in the region of 35 dB . This combination ensures that the gain of the twin GASFET front-end will be enough to completely overcome the noise contribution of the MMIC and deliver something very close to the specified noise figure of the input GASFET, TR1, for overall noise, namely 0.5 dB .

TR1 and TR2 are source biased for
simplicity and device protection, and are powered from a 5 V regulator IC. The MMIC is powered from the 'raw', 12 V supply via its biasing resistor R4. Series inductors are used in the matching network at each gate and simple moulded chokes are used for gate DC return. Interstage impedance matching is provided by the stripline ' $L$ ' networks which combine both L3 and C4, and L6 and C 8 respectively.

As with any microwave circuit, this schematic is only part of the design since the physical dimensions of the construction are significant fractions of the electrical wavelengths handled. For this reason it is not possible to copy the circuit from scratch, nor should the assembled and aligned module be 'fiddled with'!

## Down Converter

The incoming signals from the pre-amp are coupled via Cl (see Figure 5) to the input stripline of TRl; this forms an amplifier virtually identical to that of the pre-amp. The output from this stage is coupled to LT, the input stripline of the mixer GaAs FET TR2. This line is tuned by VC4 and VC5. The dual gates of this FET are also fed via C5 with signals from the oscillator/multiplier stages. The resultant output from the mixer at 137.5 MHz is tuned by L6 and VC6 and fed to the output socket via C9.

Two crystals are provided to enable both channels of the satellite to be received without re-tuning the 137.5 MHz receiver. The switching of these crystals is accomplished by interrupting the power to the Down Converter for set periods. The logic that detects these pulses and switches the crystals is formed by IC2 and its surrounding components. TR5 is the crystal oscillator whose collector circuit, VLl and C23, is tuned to the overtone frequency of the selected crystal. The output from this stage is fed to the multipliers formed by TR4 and TR5 producing an output of 18 times the crystal frequency. This signal is filtered by L9 and L10 before being fed to the gate of the mixer via C5.

A 9 volt supply is fed from the Mapsat receiver via the channel switching unit and downead coax and is isolated from the 137.5 MHz signal by L 5 . This supply feeds the oscillator stage direct and the rest of the Down Converter via the 5 volt regulator RGI . The negative supplies for the two GaAs FETs is generated by ICl . The 5 volts required by the pre-amp is fed to SKl via LKI and L14 which isolates the signal path from the power supply.


Figure 3..Block schematic of the system.


Figure 4. Pre-amplifier circuit.

## Power Supplies

The power for both the Down Converter and the Aerial Pre-amplifier comes from the power unit in the Mapsat receiver via the same coaxial cable that carries signals to the receiver from the Down Converter. This simplifies the installation of the system as only one cable connects all the units together. The Channel Switching Unit (to be described in the reat issue) is connected in series

## The aerial showing the attached pre-amplifier.




Figure 5. Down Converter circuit.

with this cable at any convenient point near the Mapsat receiver.

## Sighting the Aerial

The aerial must be positioned in such a way that it can point at clear sky in a due south direction (from the British Isles), see Figure 6, at an angle of 30 degrees above the horizon. There should also be no major obstructions below about 10 degrees of the aerial axis, see Figure 7. No mast or other aerial should be within 1 metre of the aerial other than directly behind, where there is no restriction. The angle of elevation is set by the special clamping bracket supplied with the aerial when the support mast is mounted vertically. Small changes in elevation may be affected by setting the mast off vertical in the appropriate direction. The heading and
elevation of the aerial when used in other countries may be calculated from the position of the satellite above the Earth and the known height of $36,000 \mathrm{~km}$. Note this may require special mounting arrangements to be made. Further details of the aerial assembly and installation will be given in Part 2.

## Kits

There are 3 main kits available for this project. The Down Converter kit, an Aerial and Pre-amplifier kit, and the Channnel Switching Unit kit. The Down Converter and Pre-amplifier are supplied ready-built, tested and aligned. The Channel Switching Unit and the Aerial are all that can be constructed by the hobbyist. In addition, other items will be required to complete the installation, these inciude a mast for the aerial, a
water proof box for the Down Converter (if it is to be sited out of doors), various connectors and cable for connecting the Down Converter to the Mapsat receiver via the Channel Switching Unit. The aerial must be coated with a good quality polyurethane clear varnish before installation, and this can usually be obtained from your local DIY shop.

The Down Converter may be used with other aerial systems provided they produce a sufficient signal level, and do not have $D C$ continuity across their output terminals as the Down Converter supplies power to the Pre-amplifier via the coaxial cable inner and outer conductors.

## WEATHER SATELLITE DOWN CONVERTER KIT

All of the items making up the Meteosat aerial, aerial preamplifier and down converter as described above are only available as one complete kit, and none of them are available separately.

Similarly, none of the individual components from either the aerial preamplifier or the down converter modules are available separately, as these are ready-built and pre-aligned. This is
especially true for the PCBs, so please don't try to order the PCBs alone or ask us for their order codes, as you will still not be able to build the modules from scratch without specialist equipment.

The complete down converter system is available as one kit. Order As LM74R (D Con/P Amp/Aerl Kit)

# MINIFIRCUITE 

from Robert Penfold

## Audio Level Tester

This device is intended as an aid to checking audio equipment, and it is a form of audio signal tracer. However, rather than the usual audio amplifier feeding an earphone or a miniature loudspeaker, this design has a ten LED bargraph display with the LEDs at 3dB intervals on the scale. The unit therefore provides a fairly accurate indication of the audio level present in the circuit, provided it is within the measuring range of the unit of course. When used in conjunction with an audio signal generator or function generator this makes the unit suitable for such things as voltage gain measurement and frequency response testing. In other words, it is in many ways more like a very basic audio millivolt meter than a conventional audio signal tracer, and it can often provide more meaningful results than an ordinary signal tracer. The user should be aware of the devices's weaknesses though, and these are mainly that it will not detect very low level signals, and it does not give any idea of the waveform that it is measuring. It is therefore useless trying to test something like a low impedance dynamic microphone (with its sub 1 millivolt RMS output level) using this

tester, or searching for the stage in an amplifier which is producing clipping. Like virtually any simple piece of test gear, it can be very helpful and worthwhile if used thoughtfully and in the right context, and worse than useless if it is not.

## Circuit Operation

The circuit is similar to that of a LED audio level indicator, and it could actually be used in this role if desired. Basically the unit comprises a precision half wave
rectifier driving a smoothing circuit and a bargraph driver.

Looking at the circuit in more detail, ICl is the active device in the precision rectifier, which is a simple half wave type. For reasonably accurate results with low level signals, it is essential to use some form of active circuit in order to overcome the inherent non-linearity of semiconductor diodes. This lack of linearity is most severe with silicon devices which require a forward bias of about 0.5 volts or so before they will start

to conduct significantly. In this circuit a germanium type is usecu, and although these offer much improved performance on low level signals, they still need some external assistance in order to give really good results.

In this circuit, the standard approach of including the diode in the negative feedback look of an amplifier has been adopted. This boosts the output voltags of the amplifier by an amount which is equal to the voltage drop through the diode, and this exactly compensates for the forward voltage drop. The gain of the amplifier has been made variable by means of RV1 to permit calibration of the unit. R1 and R2 form a simple $0 \mathrm{~dB} /-20 \mathrm{~dB}$ attenuator at the input of the unit, and this enables the sensitivity of the unit to be cut by a factor of ten so that relatively high level signals can be accommodated.

The bargraph driver is an LM3915N logarithmic type which has 3 dB increments between the LEDs. Here it is wired in the 'dot' mode and it does not
provide a true bargraph display. This gives slightly less clear results, but the current consumption is substantially reduced so that the unit can be powered from a small 9 volt battery if desired. C2 smooths the input signal to the bargraph driver, and this is important as the signal would otherwise be varying at a very fast rate making the display dim and blurred. R1 sets the LED current at approximately 10 milliamps.

## Construction

There are two basic constructional forms that can be used with a device of this type. The first method is io build it as a conventional instrument with a set of screened test leads which connect to JKl. The second approach, and perhaps the better one, is to build the unit as a probe, with Cl connecting to the probe tip and an earthing lead fitted with a crocodile clip connecting to the negative supply rail ( Kl then being omitted). The gain of the circuit is not particularly high, and at
lllk the input impedance is only moderately high, and the component layout is not all that critical, although reasonable care needs to be taken with the layout of any fairly sensitive audio circuit. On the prototype the LEDs are ten separate 5 millimetre types. A proper bargraph can be used if perferred and will give the unit a neat appearance, albeit at a greater cost.

RV1 is adjusted to give the desired ful scale sensitivity, which can be arything from under 100 millivolts peak to peak, to over 800 millivolts peak to peak with Sl in the ' H ' position. With it set to the low sensitivity 'L' position the full scale voltage is boosted by a factor of ten. The best setting to use is to some eztent a matter of personal preference, and also depends to some degree on the equipment that will be tested, but a full scale value of about 200 millivolts peak to peak with Sl at the ' H ' setting probably gives coverage of the most useful dynamic range.

## Sound Triggered Flash

With the aid of an automatic flashgun trigger, it is possible to take a variety of action shots that would be practically impossible by any other means, and which would take innumerable attempts in order to catch just the right moment. The most popular form of automatic trigger is probably the sound triggered type. These are suitable for shots such as balloons in mid-burst, water splashes, champagne corks 'popping', etc., and the very short duration of the output from an electronic flashgun usually 'freezes' the action well enough to give a sharp image. Of course, shots of this type must be taken under quite dark conditions as the camera's shutter must be set to 'Bulb' and left oper for a few seconds while the exposure is made, and shots really need to be carefully set up and rehearsed if a lot of wasted film is to be avoided.

## Circuit Operation

This unit is designed primarily for use with a low impedance dynamic

microphone (the type used with inexpensive cassette recorders), although it will function reasonably well with crystal, high impedance dynamic types, or types which have similar output characteristics to the latter. With any of
these types of micropione the output level is only going to be something in the region of 1 millivolt, and a considerable amount of amplification is needed in order to boost the signal to a level that can drive a switching device of some

kind. In this case a two stage common emitter amplifier is used, and this provides over 80 dB of voltage gain. RV1 is the gain control, and it is generally advisable to have the gain no higher than is really necessary as this would almost certainly result in frequent spunious triggering of flashgun, and possibly a lot of wasted frames.

The output from the amplifier is used to trigger a monostable based on 555 timer device ICl. This gives a nominal output pulse duration of 1.1 seconds, and it activates LEDl to show that the unit has been triggered properly. This is of little value in normal use when the flashgun will obviously fire if the unit is triggered properly, but it is very useful for 'dry' runs when preparing to take a shot. The output pulse from ICl is also used to trigger a thyristor which in turn activates
the flashgun. RV2, R8 and C6 provide a variable delay before triggering occurs, and this is useful for taking a sequence of shots to show (say) a balloon at the instant it starts to burst and the first tears that start to appear in it, through to the point where it has fully collapsed. Note that the distance from the sound source to the microphone also introduces a small delay, and for rapid triggering this distance should be as short as possible. If very fast triggering is required it would also be advantageous to include a switch to permit C 6 to be cut out of circuit.

## Construction

The amplifier section of the unit has quite a high level of voltage gain, and consequently the component layout for this section of the unit needs to be
designed with suitable care. An output socket to suit the miniature coaxial plugs fitted to flashgun leads might be obtainable from a large camera accessory stockist, but a simple altemative is to use a socket and short piece of lead cut from a flash extension lead. The lead must be connected through to CSRI with the correct polarity, and this is something that can be checked using a multimeter set to a high $D C$ voltage range. CSR1 will not be damaged if the lead is connected with the wrong polarity, and it is therefore quite alright to determine the correct method of connection using trial and error. Note that the circuit will only function properly with a sensitive thyristor such as the C106D, which will trigger from a gate current of well under a milliamp, which precludes most other devices from use in the unit.

## In-Circuit Resistance Mefer

Accurately measuring the value of a resistor that is out of circuit usually presents no great problem, and even the most basic of multimeters usually has a number of resistance measuring ranges that cover most likely values. Measuring the resistance of a component that is in-circuit is a very different proposition, and there is the problem of other components in the circuit placing resistance in parallel with the component that is being measured. This gives a lower reading than the true value of the component.

In modern circuits this problem is largely due to the semiconductors in the circuit rather than other resistors.


Semiconductor junctions (which can be in diodes, transistors, integrated circuits, or practically any semiconductor component) are often forward biased by the voltage source in the multimeter, and this results in a very low reading. Sometimes simply reversing the test leads will cure the problem, but although this might eliminate the original junction by reverse biasing it, it may well forward bias a second junction and re-introduce the problem.

The most simple solution to the problem is to use a resistance measuring circuit that operates with a maximum voltage across the test resistance that is too low to bring a silicon semiconductor junction beyond the threshold of conduction. This means keeping the voltage down below about 0.4 volts, which is substantially less than that used by most resistance measuring devices. It must be stressed that this system should eliminate the shunting effect of semiconductor junctions, but it will not cut out any pure resistance in parallel with the component under test. Very often there will be no significant resistances of this kind, but if a low reading is obtained one lead of the resistor should be disconnected from the board to isolate the component from any parallel resistance, and the check then repeated. Of course, if an excessive resistance reading is obtained this certainly means that the device under test is a 'dud' (or you have forgotten to switch off the supply before starting work on the equipment!).

## Circuit Operation

The unit operates on the well established principle of sapplying a constant current to the test component, and then measuring the voltage developed across it (which is proportional to its resistance value). A point in favour of this system is that it gives a forward reading linear scale rather than the reverse reading logarithmic type of a conventional analogue instrument.

In this circuit TRI operates as a standard constant current source, and the five switched emitter resistances provide five output currents. These give the unil measuring ranges having full scale values of $1 \mathrm{k}, 10 \mathrm{k}, 100 \mathrm{k}, 1 \mathrm{M}$ and 10M. Normally with circuits of this type low resistance ranges cause problems as they require quite high test currents. In this case the currents are relatively low, and it would be acceptable to add a $100 \Omega$ range if cesired by using the otherwise unused position of Sl to switch a $150 \Omega$ resistor into circuit. This still only gives a test current of about 4.3 milliamps. An advantage of the low test currents and voltages is that there is little risk of damaging anything in the circuit under test. On the other hand, it does mean that the volmeter section of the unit must have an extremely high input impedance in order to give good accuracy, particularly on the higher ranges. This requirement is fulfilled by using an operational amplifier having a MOSFET input sage to provide buffering and a certain amount of voltage amplification.

C3 rolls off the frequency response of ths stage to avoid problems with stray pick-up of mains 'hum' and other noise. There is a separate preset resistor for each range in the voltmeter circuit (switched by Slb) so that each range can be individually calibrated.

With no test resistance the meter will be driven beyond full scale deflection. In order to avoid this, IC2 is used to detect an excessive output voitage from ICl and to switch on TR2 which diverts the output current from the meter. This could cause confusion, where it is unclear whether the meter is showing a low and valid reading, or an overload is being suppressed. LEDl avoids this by lighting up when a valid reading is present.

## Construction

The unit offers little that is difficult as $f a r$ as construction is concemed, but bear ir mind that both integrated circuits are MOS types and consequently require the usual antistatic handling precautions. For calibration purposes five $1 \%$ resistors are required, and these should have values equal to the full scale values ( 1 k , 10 k , etc). It is just a matter of connecting the appropriate resistor for the range that is being calibrated, and then adjusting the correct preset for precisely full scale reading on the meter. RV6 is given any setting that permits a full scale reading to te obtained, but which also suppresses tiee meter with no test resistor connected, and its exact setting will probably not be critical.

## Audio Isolator

It is sometimes necessary to couple an audio signal from a piece of equipment without making any direct connection to the equipment. The most common example of this is where a headphone or tape output is required from a television or other item of gear which has a 'live' chassis, and the isolation is required to avoid problems with electric shocks and short circuits to Earth. Another use for an audio isolator is where (say) a number of musical instruments are connected together and there are problems with 'hum' loops. the easiest solution to the problem is to use an isolation transformer, but in practice suitable components seem to be unobtainable. Where an output is required from a circuit that has a 'live' chassis, even if a transformer with the right input/output characteristics could be obtained, that is not to say that it would necessarily guarantee to withstand a few hundred volts without breaking down.

What is generally a more satisfactory solution to the problem is to use an opto-isolator plus some simple electronics to provide the signal transfer, and that is precisely what this circuit does. It has to be emphasised that if the unit is to be used to provide an isolated

output on equipment which has a 'live' chassis, it is essential that the person installing it knows exactly what they are doing and have the requisite experience to undertake this task. This is quite definitely not a beginners project, and is potentially very dangerous if used incorrectly with 'live' chassis equipment.

## Circuit Operation

The obvious method of using the audio input signal to vary the input current to the LED in the opto-isolator, and then taking the audio putput from a load resistor on the transistor side of the device does not generally work well in
practice. The two problems that compromise results are rather poor linearity through the system, and a vulnerability to the pick-up of 'hum' and other electrical moise. Better results are usually obtained using an ultrasonic carrier wave having either pulse width or frequency modulation. In this circuit trequency modulation is used.

On the input side of the unit ICla acts as a buffer amplifier which gives an input impedance of about 50 k , and IClb is the buffer stage in an active third order lowpass filter. This prevents high frequency signals from reacting with the sarrier signal to give distortion products

## AUDIO LEVEL TESTER PARTS LIST

| RESISTORS: All $0.6 \mathrm{~W} 1 \%$ Metal Film (Unless specified) |  |  |  |
| :---: | :---: | :---: | :---: |
| RI | 100k | 1 | (M100K) |
| R2 | 11 k | 1 | (MllK) |
| R3 | 47k | 1 | (M47K) |
| R4,5 | 1k | 2 | (M1K) |
| R6 | 10k | 1 | (M10K) |
| R7 | 1k2 | 1 | (M1K2) |
| RV1 | 10k Hor Encl Preset | 1 | (UH03D) |
| CAPACITORS |  |  |  |
| Cl | 330nF Poly Layer | 1 | (WW47B) |
| C2 | $10 \mu \mathrm{~F} 50 \mathrm{~V}$ PC Electrolytic | 1 | (FF04E) |
| C3 | $100 \mu$ F 10V PC Electrolytic | 1 | (FF10L) |
| SEMICONDUCTORS |  |  |  |
| ICl | CA3140E | 1 | (QH29G) |
| IC2 | LM3915N | 1 | (YY96E) |
| Dl | OA91 | 1 | (QH72P) |
| D2-11 | Red LED | 10 | (WL27E) |
| MISCELLANEOUS |  |  |  |
| JK1 | 3.5 mm Jack Socket | 1 | (HF82D) |
| Sl | SPDT Min Toggle | , | (FH98G) |
| S2 | SPST Min Toggle | 1 | (FH97S) |
| B1 | 9V PP3 Battery | 1 | (FK62S) |
|  | PP3 Battery Clip | 1 | (HF28F) |
|  | 8 -pin DIL Socket | 1 | (BL17T) |
|  | 14-Pin DIL Socket | 1 | (BL18U) |

## IN-CIRCUIT RESISTANCE METER PARTS LIST

| RESISTORS: All 0.6W 1\% Metal Film (Unless specified) |  |  |  |
| :---: | :---: | :---: | :---: |
| R1 | 3 k 3 | 1 | (М3К3) |
| R2 | 1 k 5 | 1 | (M1K5) |
| R3,11 | 15k | 2 | (M15K) |
| R4 | 150k | 1 | (M150K) |
| R5 | 1M8 | 1 | (M1M8) |
| R6 | 10M | 1 | (M10M) |
| R7 | 8M2 | 1 | (M8M2) |
| R8 | 100k | 1 | (M100K) |
| R9,13 | 10k | 2 | (M10K) |
| R10 | 39k | 1 | (M39K) |
| R12 | 5k6 | 1 | (M5K6) |
| R14 | 1 k | 1 | (M1K) |
| RV1-5 | 47k Hor Encl Preset | 5 | (UH05F) |
| RV6 | 10k Hor Encl Preset | 1 | (UH03D) |
| CAPACITORS |  |  |  |
| Cl | $100 \mu \mathrm{~F} 10 \mathrm{~V}$ PC Electrolytic | 1 | (FF10L) |
| C2 | 100 pF Ceramic | 1 | (WX56L) |
| C3 | 220nF Poly Layer | 1 | (WW45Y) |
| SEMICONDUCTORS |  |  |  |
| ICl | CA3130E | , | (QH28F) |
| IC2 | CA3140E | 1 | (QH29G) |
| TR1 | BC559 | 1 | (QQ18U) |
| TR2 | BC549 | 1 | (QQ15R) |
| TR3 | BCl79 | 1 | (QB54J) |
| D1,2 | IN4148 | 2 | (QL80B) |
| LD1 | Red LED | 1 | (WL27E) |
| MISCELLANEOUS |  |  |  |
| Sl | 6-Way 2-Pole Rotary Switch | 1 | (FH45W) |
| S2 | SPST Sub-Min Toggle | 1 | (FH97F) |
| ME1 | $100 \mu$ A Panel Meter | 1 | (RW92A) |
| SK1,2 | 2 mm Socket | 2 | (HF42X) |
| Bl | 9V PP3 Battery | 1 | (FK62S) |
|  | PP3 Battery Clip | 1 | (HF28F) |
|  | 8-Pin DIL Socket | 2 | (BL17T) |

SOUND TRIGGERED FLASH PARTS LIST

RESISTORS:All 0.6W 1\% Metal Film (Unless specified)

| R1 | 470s, | 1 | (M470R) |
| :---: | :---: | :---: | :---: |
| R2,5 | 4k7 | 2 | (M4K7) |
| R3 | 1M8 | 1 | (M1M8) |
| R4 | 2M2 | 1 | (M2M2) |
| R6 | IM | 1 | (M1M) |
| R7 | 1k | 1 | (M1K) |
| R8 | $390 \Omega$ | 1 | (M390R) |
| R9 | 100k | 1 | (M100K) |
| RV1 | 10k Log Pot | 1 | (FW22Y) |
| RV2 | 22k Lin Pot | 1 | (FW03D) |
| CAPACITORS |  |  |  |
| C1,7 | $100 \mu \mathrm{~F} 10 \mathrm{~V}$ PC Electrolytic | 2 | (FF10L) |
| C2,4,5 | $1 \mu$ F 100 V PC Electrolytic | 3 | (FF01B) |
| C3 | $2 \mu 2 \mathrm{~F}$ 100V PC Electrolytic | 1 | (FF02C) |
| C6 | $220 \mu \mathrm{~F}$ 16V PC Electrolytic | 1 | (FF13P) |
| SEMICONDUCTORS |  |  |  |
| ICl | NE555 | 1 | (QH66W) |
| TR1,2 | BC549 | 2 | (QQ15R) |
| LD1 | LED Red | 1 | (WL27E) |
| CSl | Cl06D | 1 | (QH30H) |
| MISCELLANEOUS |  |  |  |
| Sl | SFST Min Toggle | 1 | (FH97F) |
| B1 | 9V PP3 Battery | 1 | (FK62S) |
| SK1 | 3.5 mm Jack Socket | 2 | (HF82D) |
| SK2 | Min Coax Socket |  | (See text) |
|  | 8-Pin DIL Socket | 1 | (BL17T) |
|  | Microphone |  |  |

(M470R) (M1M8) (M2M2) (M1M) (M1K) (M390R) (FW22Y) (FW03D)
(FF10L)
(FF01B)
(FF13P)
(QH66W)
5R)
(QH30H)
(FH97F)
(HF82D)
(BL17T) Microphone

As req.

## AUDIO ISOLATOR PARTS LIST

| RESISTORS: All 0.6 W 1\% Metal Film |  |  |  |
| :---: | :---: | :---: | :---: |
| R1,2 | 100k | 2 | (M100K) |
| R3,4,5,7,11,12,13,14,15 | 10k | 9 | (M10K) |
| R6 | 4k7 | 1 | (M4K7) |
| R8 | 1k2 | 1 | (M1K2) |
| R9 | $470 \Omega$ | 1 | (M470R) |
| R10 | 2k2 | 1 | (M2K2) |
| R16,17 | 47k | 2 | (M47K) |
| R18 | 2k7 | 1 | (M2K7) |
| R19 | 5k6 | 1 | (M5K6) |
| CAPACITORS |  |  |  |
| Cl,15 | $100 \mu$ F 10V PC Electrolytic | 2 | (FF10L) |
| C2 | $1 \mu$ F 100V PC Electrolytic | 1 | (FF01B) |
| C3 | 2n2F Poly Layer | 1 | (WW24B) |
| C4 | 3n3F Poly Layer | 1 | (WW25C) |
| C5 | 220pF Polystyrene | 1 | (BX30D) |
| C6 | 330pF Polystyrene | 1 | (BX31J) |
| C7,9,14 | $\ln F$ Poly Layer | 3 | (WW22Y) |
| C8 | 680pF Polystyrene | 1 | (BX34M) |
| Cl0 | 4n7F Poly Layer | 1 | (WW26D) |
| Cll | 180pF Ceramic | 1 | (WX59P) |
| C12 | $4 \mu 7 \mathrm{~F} 63 \mathrm{~V}$ PC Electrolytic | 1 | (FF03D) |
| Cl3 | $10 \mu \mathrm{~F} 50 \mathrm{~V}$ PC Electrolytic | 1 | (FF04E) |
| SEMICONDUCTORS |  |  |  |
| ICl | 1458C | 1 | (QH46A) |
| IC2 | NE555 | 1 | (QH66W) |
| IC3 | 4001BE | 1 | (QX01B) |
| IC4,5 | $\mu$ A741C 8-Pin DIL | 2 | (QL22Y) |
| OPl | Opto Isolator | 1 | (WL35Q) |
| MISCELLANEOUS |  |  |  |
| JK1 | 3.5 mm Jack Socket | 1 | (HF82D) |
|  | 8-Pin DIL Socket | 5 | (BL17T) |
|  | 14-Pin DIL IC Socket | 1 | (BL18U) |

on the output. The carrier oscillator is a 555 astable circuit which is modulated by the normal means of coupling the audio input signal to pin 5 . The carrier frequency is around 100 kHz incidentally. IC2 drives the LED in the opto-isolator via current limiting resistor R8.

Unfortunately, ordinary opto-isolators are not particularly fast in operation, and the output signal across load resistor R9 is a fairly weak triangular waveform. This is amplified by TRl though, which gives a virtual square wave output signal having quite fast rise and fall times. The F.M. demodulator is a monostable circuit which is formed by two of the 2 input NOR gates of IC3. A monostable might seem to be an unlikely form of F.M. demodulator, but it can work very well in this roll. Under quiescent conditions the output waveform of the monostable is a squarewave signal, as shown in (a) of the


## Waveform

waveform diagram. If the input frequency is increased, the output pulse length remains the same, and the gap between the pulses narrows (as in (b)). With reduced input frequency the duration between output pulses increases, giving a waveform of the type shown in
(c). The point here is that the average output voltage varies in sympathy with changes in the input frequency, and there is a linear relationship between the two.

In order to recover the demodulated audio signal a lowpass filter is needed, and it must give a high degree of attenuation in order to give an output signal that contains an insignificant carrier content. In this circuit a 24 dB per octave active filter is used. This is followed by a low gain amplifier based on IC5 which gives a further stage of filtering. This also boosts the output signal slightly so that there is approximately unity voltage, gain through the unit.

## Construction

The main point to check when constructing the unit is that there is no path of conduction through the unit. It is worthwhile checking the finished unit with a multimeter set to a high resistance range to ensure that no connection through the unit has been inadvertently left. It may be possible to tap off the supply for the input side of the circuit from the equipment which supplies the input signal, but the output side of the unit must, of course, have an entirely separate and safe power source.

As far as audio quality is concerned the unit is not up to true hi-fi standards, and it has something less than the fuil audio bandwidth. It provides a very accep:able level of performance though, and is more than adequate for most purposes.

Aadio Isolator


# $M \cdot \mid \cdot D$ NTERFACING TECHNQUES 

## by R.D. Ball

## The System

MIDI is the acronym for Musical Instrument Digital Interface, MIDI is now the universal standard for connecting and controlling electronic musical instruments.

Originally, synthesisers were controlled using two signals; 'gate' and 'CV', the 'CV' signal is simply a DC voltage corresponding to the pitch of the note, a change of IV would give a change in pitch of one octave. The 'gate' signal is used to control the sample and hold circuitry, i.e. it gates the control voltage. It is also used to trigger the envelope generator. This system suited mon-
ophonic analogue synthesisers but is not really practical for modern polyphonic digital synthesisers.

Fortunately, synthesiser manufacturers adopted a single standard, this system was developed by Roland in conjunction with other manufacturers. MIDI was the result.

The outline of MIDI is that all synthesisers and drum machines (or any other MIDI compatible device) use the same set of codes to initiate and stop notes. If certain information does not apply to a particular machine (i.e.


## A Milli interface box

system runs at this speed to avoid apparent gaps between simultaneous events. The data word format is 8 bits, 1 stop bit and no parity.

A full implementation of MIDI utilises three sockets on the equipment. These are MIDI IN, MIDI OUT and MIDI THRU. MIDI IN is the serial data into the equipment. MIDI OUT is the serial data from the equipment. MIDI THRU is a buffered version of the data that appears at MIDI IN. The connection between equipment is similar to the way that Commodore printers and disk drives may be chained together.

To prevent possible hum and earth loop problems all MIDI inputs are Opto Isolated. Connection between equipment is usually via 5 pin $180^{\circ}$ DIN leads (with some equipment intended for use 'on the road' XLR connectors may be found), only two cores and screen are used on 5 pin leads though. Ready made MIDI leads can be purchased or leads may be made up using good quality screened cable and connectors. MIDI leads should not be longer than 50 metres.

## What can MIDI be used for?

(a) Producing a 'thicker' sound

This means connecting two or more synthesisers together so that notes played on the MASTER synth will also be played on the SLAVE synth(s). This can be very effective, especially if the attack rate of the envelope generator on the SLAVE is considerably slower than that of the MASTER. This effect is generally termed layening or doubling.
(b) Allowing computer control

By using MIDI, electronic musical instruments can be controlled by a home computer. This means that
synths can 'alk' to computers and vice-versa. This opens up possibilities of using the computer as a sequencer or to produce effects such as appregiation of chords or even to act as an extra modulation generator. This does however require the computer to be MIDI compa:ible, this article also includes a circuit for a MIDI interface and it is hoped that this will provide a stepping stone into computer controlled electronic music.
(c) Reducing equipment space and cost

By using a master keyboard a number of slave synths can be driven, since the slave synths do not need keyboards, the keyboards can be cmitted!! Keyboardless synths are much cheaper and occupy a lot less space than full synths. The master keyboard may be a special MIDI MASTER KEYBOARD which has no sound generation circuitry; just MDI sockets. Master keyboards generally feel and respond more like a real piaro keyboard and normally include good facilities for program and parameter changes on slaves. Sorre also allow the keyboard to be 'split' so that different octaves transmit on different MIDI channels. A cood master keyboard will transmit velocity and aftertouch information as well as note information.
(d) Reducing obsolescence

All MIDI equipment is compatible, so that old equipment will quite happily work along side the very latest equipment and that applies to all equipment yet to be designed!! Even old analogue synths can join the MIDI revolution as MIDI to CV corverter boxes are now available.

## (e) The MIDI studio

By using a sequencer, different instruments can be recorded into memory, just like multi-track recording on tape. Each 'track' is assigned to a different MIDI channel allowing simultaneous playback, and because the information is in memory it can easily be modified or edited. It is also possible to change instruments once tracks have been recorded. One great advantage is the total absence of recording medium noise. Once multiple tracks have been laid down, the slave devices can now be mixed onto conventional tape.

## MIDI Modes

Mode 1: OMINI ON/POLY (OMINI MODE)

In OMNI MODE, the MIDI device receives data transmitted on all 16 MIDI cinannels, note data is assigned to the device's voices polyphonically.

This means that whether the transmitting device sends data down channel 1 or 16 (or any channel for that matter), the device in omni mode will receive it. Data corresponding to notes will turn on and off the devices voices polyphonically, i.e. a number of different notes simultaneously sounding; as in a chord. The degree of polophony will cepend on the device and is usually around six or eight notes.

## Mode 2: OMNI ON/MONO

In this mode, data is received from any channel as above, but note data is assigned to only one voice (monophonically).

If multi note information is sent, the device will either respond to the highest, lowest or last note to be received, this depends on the device and some manufacturers allow the assignment to
be changed.
Simply, regardless of how many notes are played, only one will sound

## Mode 3: OMNI OFF/POLY (POLY MODE)

In POLY MODE, the device only responds to data being sent down a particular channel and note data will be assigned polyphonically (as in Mode l). With POLY Mode the receiving device has to be assigned to a channel, obviously the receiving and transmitting devices must be on the same channel otherwise nothing will happen.

## Mode 4: OMNI OFF/MONO (MONO MODE)

In MONO MODE each voice is assigned to its own channel, i.e. if a six voice synth was assigned so the basic channel is 1 , then successive voices would be assigned to channels 2 through to 6 . Synths with multi timbral facilities allow each voice to have a different sound, for these reasons MONO mode is very powerful but needs careful use to get good results. Facilities in this mode can vary and the owners manual should outline how to use this mode fully.

## MIDI Dafa Messages NOTE ON

The following sequence of data will initiate a note. Exchange consists of a three byte transfer.
BYTE 1 Channel No. (0 to 15) and NOTE ON command.
BYTE 2 Key No. (0 to 127) $0=$ lowest, $127=$ highest. MID ' C ' $=60$.
BYTE 3 Key Velocity; how hard key is pressed, ( 0 to 127) $0=$ no velocity, $127=$ max velocity. Non-velocity keyboards use 64.

## NOTE OFF

The following sequence of data will stop a note. Exchange consists of a three byte transfer.
BYTE 1 Channel No. and NOTE OFF command.
BYTE 2 Key No.
BYTE 3 Key Release Velocity; how quickly key is released, (0 to 127).

## POLYPHONIC KEY PRESSURE <br> (AFTERTOUCH)

Some keyboards also respond to the pressure applied to the key after the key has been pressed. Exchange consists of a three byte transfer.
BYTE 1 Channel No. and KEY PRESSURE command.
BYTE 2 Key No.
BYTE 3 Key Pressure ( 0 to 127) $0=$ no pressure, $127=$ max pressure.

## OVERALL PRESSURE (AFTERTOUCE)

 Similar to above, but is the overall pressure of all the notes down. Exchange consists of a two byte transfer.BYTE 1 Channel No. and OVERALL PRESSURE command.
BYTE 2 Overall Pressure ( 0 to 127) $0=$ no pressure, $127=\max$ pressure.


Inside the box

## CONTROLLERS

These are used to transmit information corresponding to operation of such things as portamento, damper and operation of modulation wheels or joysticks. This means, for example, that when the damper is operated on the master, it is also operated on the slave.

There are two main types of controller cornmand:
CONTINUOUS - these correspond to turning a por on the control panel or operating a modulation wheel, i.e. anything that needs to have a continuous value corresponding to position, to be transmitted.
ON/OFF - these correspond to switch operations such as the damper pedal being pressed.

For the different types of controllers a set of sub-channels or controller numbers are used:

0 to 31 for continuous controllers (low and high resolution).
32 to 63 for continuous controllers (high resolution).
64 to 95 for on/off controllers.
96 to 121 undefined.
122 to 122 for channel mode messages.

Controller messages consist of a three byte transfer.
BYTE 1 Channel No. and CONTROLLER message.
BYTE 2 Controller No. (0 to 127).
BYTE 3 Controller Value ( 0 to 127) $0=$ full off, 127 = full on.

## PITCH BEND

Pitch bend is considered such an important controller it is assigned as a separate message. Pitch bend is a three byte transfer.
BYTE 1 Channel No. and PITCH BEND command.
BYT'E 2 Pitch Bend value ( 0 to 127) LSB.
BYTE 3 Pitch Bend value (0 to 127) MSB.

## PROGRAM CRANGE

This enables program or patches corresponding to different sounds to be selected, this is a two byte transfer.
BYTE 1 Channel No. and PROGRAM CHANGE command.
BYTE 2 Program No. (0 to 127).

## MODE MESSAGES

This group of messages affect the modes of operation and are under the class of controllers.

## LOCAL/REMOTE

This is used for selecting whether note data for voices within the synth is obtained from the keyboard and MIDI or just MIDI. In the second option, operation of the keyboard just sends out MIDI data. This option is useful for adding such features as appregiation, auto chordir.g or other MIDI effects and is achieved by using a MIDI effects box or micro computer.

## ALL NOTES OFF

Causes all notes on that channel to cease sounding. This command should be used with some care as not all devices respond to it!

## OMNI/POLY/MONO SELECT

This allows selection of the channel and note assignment mode as previously discussed.

## SYSTEM EXCLUSIVE ( $\star$ )

This allows totally non-standard information to be exchanged between compatible equipment. Such as data for a particular patch or even data for a sampled sound.

The exclusive message was provided for manufacturers different needs and is one facility that means MIDI cannot become obsolete.

The exclusive message is a multi byte transfer and the length depends on the application.
BYTE 1 SYSTEM EXCLUSIVE Command. BYTE 2 MANUFACTURERS ID No.


Figore 1. Block diagram.


BYTE $n+1$ END OF EXCHANGE Command.

## SYSTEM RESET ( ${ }^{( }$)

Puts all the equipment into the reset state, as if it had just been powered up. There are many other messages but only the main ones have been covered here.
N.B. $(\star)=$ Any 'SYSTEM' message that is tansmitted, is received on all channels

## MIDI Requirements

INTERFACE HARDWARE

Data rate 31.25 K 3AUD $\pm 1 \%$, Asymchronous.

1 start bit, 8 data bits, 1 stop bit.
Total 10 bits, one complete word takes $320 \mu$ s to transmit.

Circuit operates using 5mA current locp. The receiver must be opto-isolated. Opto isolator must require less than 5 mA to turn on.

## MESSAGES

## i) Channel messages

The status byte contains a four bit number corresponding to the MIDI channel required ( 1 to 16 ); the number transmitted is 0 to $15\left(000 \mathrm{O}_{2}\right.$ to $111 l_{2}$ ).
ii) System messages

These are not given a channel


Figure 2. Circuit.
number and are received on all channels.
COMMON - for all devices connected.
REAL TIME - for timing and synchronisation purposes.
EXCLUSIVE - manufacturers exclusive data exchange.

## Block Diagram

Figure 1 centres around the 6850 Universal Asynchronous Receiver Transmitter; this device is used to convert parallel data from the computer to serial MIDI data and also to convert serial MIDI data to parallel data for the computer.

The data from the 6850 UART is not suitable to directly drive a MIDI device. The data has to be buffered to drive the MIDI bus. The buffer has quite a low output impedance so as to prevent problems caused by induced noise or a low cable capacitance. The output is current limited to protect the equipment.

As previously mentioned, MIDI inputs need to be Opto Isolated, this gives added safety and prevents troublesome (and annoying!) hum loops from being formed through cables and equipment. After data is passed through the Opto Isolator it follows two routes; into the 6850 UART and via an inverter into another buffer (duplicate of MIDI OUT buffer) to provide MIDI THRU data.

The data rate is derived from a 2 MHz crystal oscillator, this is subsequently divided by 64 in the 6850 to give the 31.25 K BAUD data rate used in the MIDI system.

Connection to the computer data bus is via the tri-state buffers in the 6850, data transfer is controlled by read/write, enable and select signals.

Register selection is achieved by using read/write and register select lines.

## Circuit Description

In Figure 2, ICla, IClb, R1, R2, R3 and Xl form a conventional two inverter crystal oscillator which runs at 2 MHz . The output of which is fed to IC2's transmit and receive clock inputs, the 31.25 KHz data clock is obtained by an internal device by 64 circuit in IC2. In the interface, IC2 does most of the work; parallel/serial and serial/parallel conversion, as well as interfacing to the computer expansion bus. Bus control is achieved using ' $E$ ' (enable) and ' $R / W$ ' (read/write). The E line is normally connected to the processors ' $\varnothing 2$ ' (phase 2) system clock. Data transfer takes place when the $\varnothing 2$ line is high. The R/W line controls the data direction. With R/W high data is read from the 6850's registers and with R/W low data is written to the 6850's registers. Since the 6850 has two pairs of registers, a selection signal is required, this is the 'RS' (register select) line, and is connected to the least significant bit of the address bus. Data is exchanged via 8

| Data Bus | $\text { Addr + } 1 \text { (TX) }$ <br> Transmit | Addr+1 (RX) Receive | Addr+0 (WR) Control | Addr +0 (RD) Status |
| :---: | :---: | :---: | :---: | :---: |
| D0 | Data bit 0 | Data bit 0 | CK divide | RX reg full |
| D1 | Data bit 1 | Data bit 1 | CK divide | TX rees orint |
| D2 | Data bit 2 | Data bit 2 | Word form | *not**used* |
| D3 | Data bit 3 | Data bit 3 | Word form | *not**used ${ }^{\text {a }}$ |
| D4 | Data bit 4 | Data bit 4 | Word form | Framing err |
| D5 | Data bit 5 | Data bit 5 | (TX ctrl) | RX overun |
| D6 | Data bit 6 | Data bit 6 | (TX ctrl) | Parity err |
| D7 | Data bit 7 | Data bit 7 | (INT en) | (INT requ) |

Table 1. Register contents.

|  |  |  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  |  |  |  |  |  |  |  |  |  |
| Function | D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 | Decimal |
| Reset | 1 | 1 | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | 3 |
| Divide by 64 | 0 | 1 | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | 2 |
| Divide by 16 | 1 | 0 | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | 1 |
| Divide by 1 | 0 | 0 | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | $\star$ | 0 |

Table 2. Control register (Reset and Divide).
bi-directional lines (D0-D7), when IC2 is de-selected D0-D7 are in the tri-state mode. To insert the 6850 into the computer's memory map, 3 select lines are provided; two active high (CSO and CS1) and one active low (CS2), these lines may have to be fed from a separate address decoder if suitable memory map decode lines are not available on the computer.

Serial data transmitted from the 6850 is not suitable to directly drive the MIDI bus, so it has to be buffered. This is achieved using IC3e, since this is an inverting buffer, the data has to be first inverted using IC3d. By taking the MIDI output from between the +5 V line and IC3e, IC3e is sinking rather than sourcing current. R8 and R9 limit the maximum current that can be drawn under possible fault conditions, protecting the computer, interface and MIDI device. IC3e forms the MIDI OUT buffer. Data received drives the LED half of IC4. R5 serves to limit current and D1 affords reverse bias protection for the LED. When the LED tums on, the transistor half of IC4 is biased on, and pulls the input to IC3a and the receive data input of IC2, low. When the LED is off, the transistor is also off, the input to IC3a and
the receive data input of IC2 are pulled high via R4. IC3a, IC3c, R6 and R7 form the MIDI THRU buffer, operation is identical to the MIDI OUT buffer except that data is obtained from the opto isolator instead of the transmit data output of IC2.

## 6850 UART Registers

Table 1 shows the registers contents on all 8 data lines. Table 2 shows the reset and divide settings in the Control register and Table 3 gives the various settings required for various word formats. Bit D7 of the Control register is the Receive Interrupt Enable bit and if bit D7 is set, an interrupt will be generated when RX Register Full bit goes high. If bit D7 is not set, receive interrupts are disabled. To RESET 6850 UART; set bits DO and Dl to 1 (i.e. decimal 3). To ENABLE 6850 UART; set bit D0 to 0, bit D1 to 1 , bit D2 to 1 , bit D3 to 0 , bit D4 to 1 (i.e. decimal 22). (This corresponds to Divide by 64 and 8 bits, No Parity, 1 Stop Bit). To TRANSMIT data; check that the TX Register Empty bit, (Dl; Control Register) is high, if not wait until bit Dl is high, then place transmit data into bits D0-D7 of TX Register. To RECEIVE data; check that

| Word format | D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 | (Decimal |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 7 bits, even parity 2 stop bits | * | * | 0 | 0 | 0 | * | * | * | 0 |
| 7 bits, odd parity |  |  |  |  |  |  |  |  |  |
| $7 \begin{aligned} & 2 \text { stop bits } \\ & 7 \text { bits, even parity }\end{aligned}$ | * | * | 1 | 0 | 0 | * | * | * | 4 |
| 7 bits, even parity | * | * | 0 | 1 | 0 | * | * | * | 8 |
| 7 bits, odd parity 1 stop bit | * | * | 1 | 1 | 0 | * | * | * | 12 |
| 8 bits, no parity |  |  |  |  |  |  |  |  |  |
| 2 stop bits | * | * | 0 | 0 | 1 | * | * | * | 16 |
| 8 bits, no parity 1 stop bit | * | * | 1 | 0 | 1 | * | * | * | 20 |
| 8 bits, even parity |  |  |  |  |  |  |  |  |  |
| 1 stop bit | * | * | 0 | 1 | 1 | * | * | * | 24 |
| 8 bits, odd parity 1 stop bit | $\star$ | * | 1 | 1 | 1 | * | * | * | 28 |

Table 3. Control register (word format)
the RX Register Full bit, (D0; Control Register) is high, if not wait until D0 is high, then retrieve data from bits D0-D7 of RX Register.

## MIDI Inferface Software Writing

Sequencing can be accomplished from BASIC quite easily. All that is required is that the appropriate data be output to the 6850 in the correct order. This data may be stored in an array or in data statements.

For recording or processing of MIDI data received, it is really necessary to use machine language, either entirely or using subroutines which can be called from BASIC; this should not be too much of a problem to anyone who has an understanding of assembly language, however guidelines will be given.

Before the 6850 is used it has to be RESET and the CLOCK DIVISION rate set. This is achieved by writing decimal 3 then decimal 22 to the control register of the 6850 .

Data being exchanged between devices is passed through the TX and RX data registers. To achieve correct data transfer, the STATUS register must be checked; i.e. that the TRANSMIT DATA REGISTER is empty before sending more data; TDRE bit of STATUS REGISTER will be high when it is OK to send more data, and similarly for receiving data; data must be present in the RECEIVE DATA REGISTER before it can be read; RDRF bit of STATUS REGISTER will be high when it is OK to read the data.

The Registers will be configured in the computers memory map as shown in Table 4; the actual locations will depend on the machine and the address decoding used, but addresses for the VIC 20 and the Commodore 64 are shown. Figures 3 and 4 show the pin functions of VIC 20 and Commodore 64 edge connectors respectively, and Table 5 shows connections from the two edge connectors to the circuit.

To illustrate how simple the software can be, a few examples are shown in Listings 1 to 5 , these show how to RESET the 6850 and READ and WRITE data, examples are given in both BASIC and 6502 assembly code. In the examples:
'base' means base address,
'base+1' means base address+1 location,
'data' means either variable or memory location containing data.

Please note that BASIC should not be used to directly get data from the 6850 since BASIC is slow. However BASIC may be used to send data without problem. If data needs to be fetched in BASIC, a machine language subroutine must be used.

Table 6 is intended as a guide to what data should be sent and in what order. Table 7 gives Controller assignments and Table 8 describes Mode Messages.

| Base address |  | (read) | Status Register |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Base address |  | (write) | Control Rocister |  |  |
| Base address +1 |  | (read) |  | ve ${ }^{\text {b }}$ | ister |
| Base address +1 |  | (write) | Transmit Data register |  |  |
|  |  | $\begin{aligned} & \text { VIC-20 } \\ & \text { (dec) } \end{aligned}$ | (hex) | CBM-64 (dec) | (hex) |
| Base address | $=$ | 39936 | \$9C00 | 57088 | \$DF00 |
| Base address +1 | = | 39937 | \$9C01 | 57089 | \$DF01 |

The base address is the position in memory where the 6850 has been placed.
Table 4. Register Memory Map
(Viewed looking into expansion slot)


Figure 3. VIC 20 Edge Connector.
(viswed looking into expansion slot)
TOP



| Top |  |  |  | Bottom |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | GND | L | ROML | A | GND | L | A10 |
| B | D0 | M | 102 | B | AO | M | A11 |
| C | D1 | N | EXROM | C | A1 | N | A12 |
| D | D2 | 0 | GAME | D | A2 | 0 | A13 |
| E | D3 | P | 1/01 | E | A3 | P | A14 |
| F | D4 | Q | Cot clock | F | A4 | Q | A15 |
| G | D5 | R | RW | G | A5 | R | SO2 |
| H | D6 | S | IRQ | H | A6 | S | NMI |
| 1 | D7 | T | +5V |  | A7 | $T$ | RESET |
| J | DMA | U | $+5 \mathrm{~V}$ | J | A8 | U | ROMH |
| K | BA | V | GND | K | A9 | $V$ | GND |

Figure 4. Commodore 64 Edge Connector.

| MIDI Circuit | VIC 20 | CBM 64 |
| :---: | :---: | :---: |
| OV | GND | GND |
| D0 | CDO | D0 |
| D1 | CD1 | D1 |
| D2 | CD2 | D2 |
| D3 | CD3 | D3 |
| D4 | CD4 | D4 |
| D5 | CD5 | D5 |
| D6 | CD6 | D6 |
| D7 | CD7 | D7 |
| RW | C RW | RW |
| RS | CAO | AO |
| E | SO2 | SO2 |
| CS2 | I/O 3 | I/O2 |
| +5V | +5V | +5V |
| Note the CS0 and CS1 lines should be tied to +5 V , as only CS2 is used. |  |  |

Table 5.

| Example 1 |  |  |
| :---: | :---: | :---: |
| 10 REM * $\begin{gathered}\text { RESET } 6850 \star \star\end{gathered}$ |  |  |
| 20 POKE 'base', 3 |  |  |
| 30 REM * SET DIVIDE RATE \& WORD FORMAT |  |  |
| 40 POKE 'base',22 |  |  |
| Listing 1 |  |  |
| Example 2 |  |  |
| @ reset | LDA\#\$03 | load acc with hex 03 |
|  | STA\$'base' | store acc at 'base' |
|  | LDA\#\$16 | load acc with hex 16 |
|  | STA\$'base' | store acc at 'base' |
|  | RTS | return from subroutine |

Listing 2

Example 3

| @ get data LDA \$'base' | load acc with STATUS |
| :--- | :--- |
| AND\#\$01 | AND with bit 0 |
| CMP\#\$01 | test |
| BNE\$@ get data branch if no data |  |
| LDA\$'base+1' | get data |
| STA\$'data' | store acc at 'data' |
| RTS | return from subroutine |

Listing 3

## Example 4

10 REM **IS TXREG EMPTY? **
20 IF (PEEK ('base') AND 2) $=0$ THEN 20
30 REM $\star \star$ SEND DATA to $6850 \star \star$
40 POKE 'base +1 ','data

## Listing 4

## Example 5

| @ send data LDA\$'base' | load acc with STATUS |
| :--- | :--- |
| AND\#\$02 | AND with bit 1 |
| CMP\#\$02 | test |
| BNE\$@ send | branch if not ready |
| data |  |
|  |  |
| LDAS'data' | get data to send |
| STA\$'base +1 1' | send data |
| RTS | return from subroutine |

## Listing 5

## MIDI INTERFACE PARTS LIST

| RESISTORS: All 0.6W 1\% Metal Film |  |  |  |
| :---: | :---: | :---: | :---: |
| R1,3 | 2k2 | 2 | (M2K2) |
| R4 | 1 k | 1 | (M1K) |
| R2,5,6,7,8,9 | 220s2 | 6 | (M220R) |
| R10 | 15k | 1 | (M15K) |
| CAPACITORS |  |  |  |
| Cl | 100nF Polyester | 1 | (BX76H) |
| SEMICONDUCTORS |  |  |  |
| ICl | 74LS04 | 1 | (YF04E) |
| IC2 | MC6850P |  | (WQ48C) |
| IC3 | 74LS14 | 1 | (YF12N) |
| D1 | 1N4148 | 1 | (QL80B) |
| OP1 | Opto Isolator 6N139 | 1 | (RA59P) |
| MISCELLANEOUS |  |  |  |
| XTl | 2MHz Crystal | 1 | (FY808) |
| SK1-3 | PC DIN Skt 5-pin A | 3 | (YX91Y) |
|  | 24-pin DIL Skt | 1 | (BL20W) |
|  | 14-pin DIL Skt | 1 | (BL18U) |
|  | Box AB10 | 1 | (LFIIM) |


| First byte | Second byte | Third byte | Description |
| :---: | :---: | :---: | :---: |
| 1001 mmmm | Onnn nnnn | Owrown | NOTE ON (Velocity $0=$ note off) [1] |
| 1000 mmmm | Onnn nnnn | Owrown | NOTE OFF [1] |
| 1010 mmmm | Onnn nnnn | 0ppp pppp | POLYPHONIC KEY PRESSURE (Aftertouch) [2] |
| 1101 mmmm | Oppp pppp |  | OVERALL KEY PRESSURE (Aftertouch) [2] |
| 1011 mmmm | Occc cccc | Oddd dddd | CONTROLLER CHANGE [3] |
| 1110 mmmm | Oddd dddd | Oddd dddd | PITCH BEND [4] |
| 1100 mmmm | OPPPPPPP |  | PROGRAM CHANGE [5] |
| 11110000 | 0 ililiii | 0******* | SYSTEM EXCLUSIVE [6] |
| 11110111 |  |  | EOX (End of exchange) [7] |
| 11111111 |  |  | SYSTEM RESET |
| Key |  |  |  |
| mmmm | = | MIDI channel No. (0 to 15) |  |
| Onnn nnnn | $=$ | NOTE on kəyboard (0 to 127) |  |
| Own wrw | $=$ | VELOCITY (0 to 127) |  |
| Oppp pppp | $=$ | PRESSURE (0 to 127) |  |
| Occc coce | = | CONTROLER No. (0 to 127) |  |
| OPPP PPPP | $=$ | PROGRAM No. |  |
| Oddd dddd | $=$ | DATA (0 to 127) |  |
| Oililii | i | I D Code (0 to 127) |  |
| 0****** | = | Undefined number of data bytes (as Oddd dddd) |  |
| Notes |  |  |  |
| [1] | Onnn nnnn | $\begin{aligned} & =60 \text { is middle }{ }^{\prime} C^{\prime} \\ & 0=\text { off, } 1=\text { pppp }^{\prime} 64=\text { between }_{\text {mp }} \text { and }{ }_{m t}, 127=\ldots \\ & =64 \text { in non velocity sensitive devices } \end{aligned}$ |  |
|  | Own wry |  |  |
|  | Ownw |  |  |
| [2] | Oppp pppp | $0=$ no pressure, $127=$ max pressure |  |
| [3] | Occc cccc | = controller number; see list |  |
|  | Oddd dddd | = data for con | controller |
| [4] | Oddd dddd | $\begin{aligned} & \text { 2nd byte }=L S B \text {, 3rd byte }=\text { MSB } \\ & \text { 2nd byte }=0 \text {, 3rd byte }=64 \text {, gives no bend } \end{aligned}$ |  |
|  |  |  |  |
| $\begin{aligned} & {[5]} \\ & {[6]} \end{aligned}$ | OPPP PPPP | = program or patch number |  |
|  | Oiiiiiii | = manufactu | rers ID code |
|  | 0****** | = undefined | number of data bytes, terminate by sending [7] |
| [7] |  | Use to return to normal use after EXCLUSIVE data exchange |  |

Table 6.

## Controller assignment

Controller No.
0
2 to 31
32
33
34 to 63
64 to 95
96 to 121
122 to 127

## Description

Continuous controller 0 MSB
Modulation wheel MSB
Continuous controllers 2 to 31 MSB
Continuous controller OLSB
Modulation wheel LSB
Continuous controllers 2 to 31 LSB
Switch controllers
**undefined ${ }^{*}$ *
MOCE MESSAGES (see Table 8)

## Controller data

For continuous controllers 0 to 127 (min to max).
Fore switch controllers $0=$ off, 127 $=$ on.
For continuous controllers; if only 7 bits of resolution required, send only MSB. If full resolution is required send MSB first, then LSB. If only LSB has changed in value, LSB can be sent without MSB,

Table 7.

## Mode Messages

| Controller No. | Description | Data |
| :--- | :--- | :--- |
| 122 | Local control | $0=$ off, $127=$ on |
| 123 | All notes off | 0 |
| 124 | Omni off (all notes off) | 0 |
| 125 | Omni on (all notes off) | 0 |
| 126 | Mono on [poly off] (all notes off) | number of channels |
| 127 | Poly on [mono off] (all notes off) | 0 |
|  |  |  |

All messages which can be sent successively (e.g. NOTE ON) under the same STATUS byte, can be sent without STATUS byte until a different STATUS is required.

