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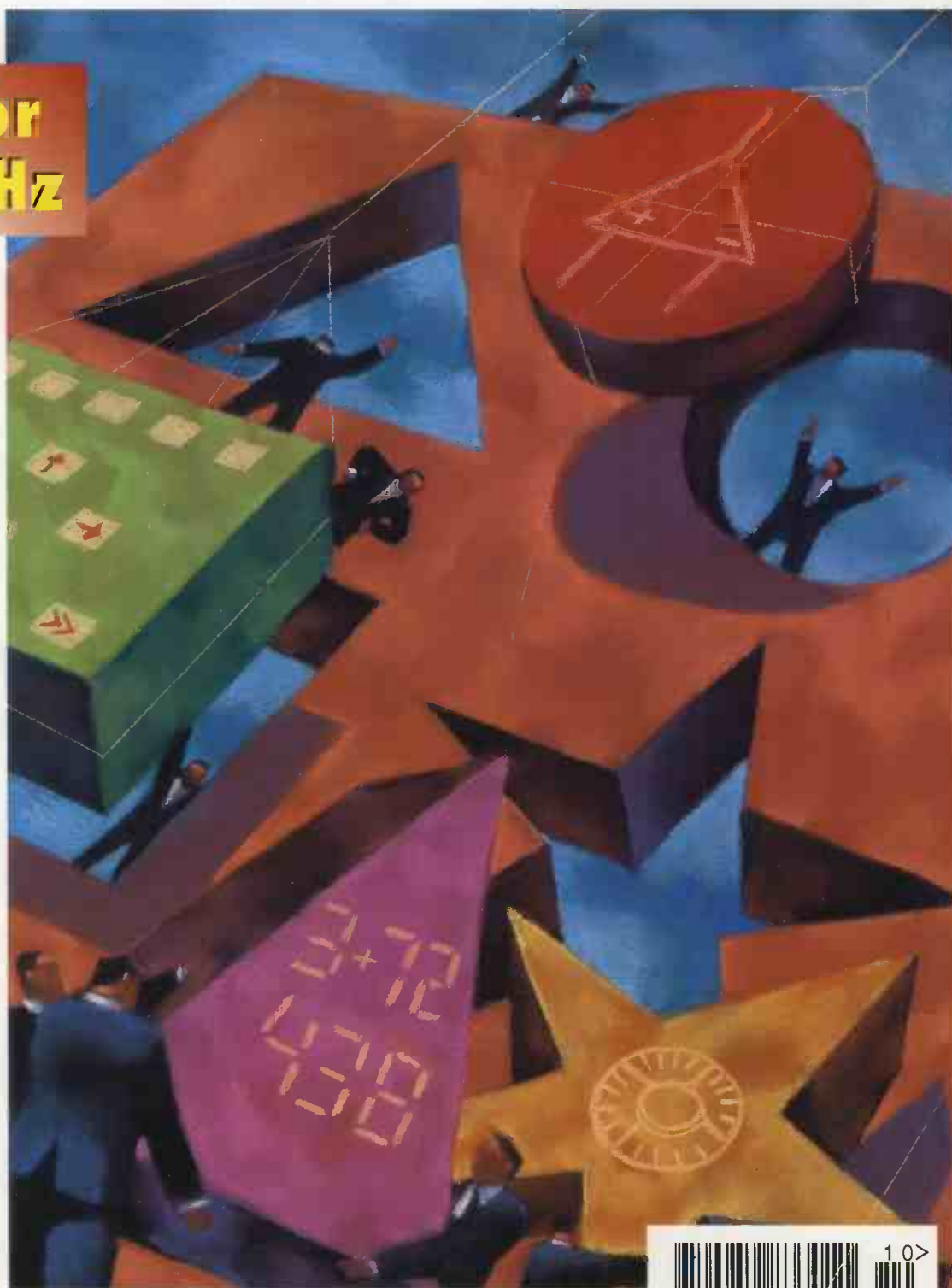
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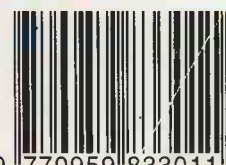
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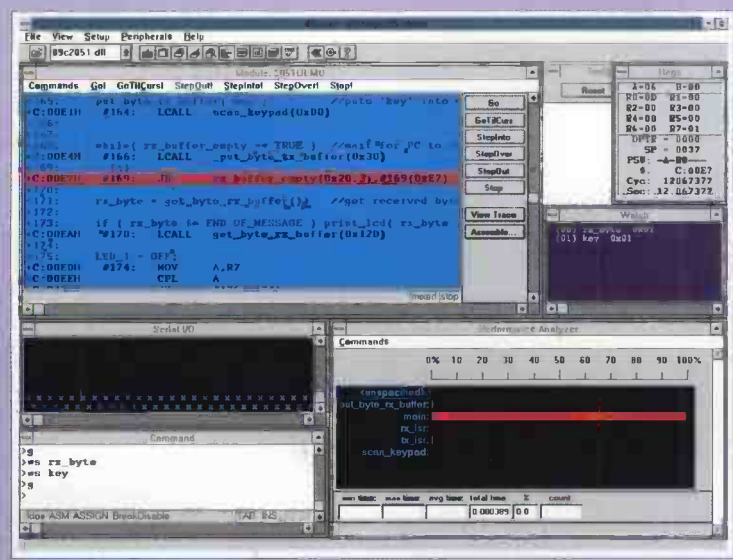


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952-ST	X	X					X		£185

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Cover – Jamel Akib

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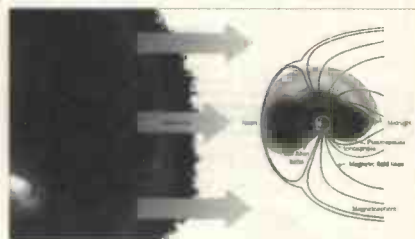
Prom and logic array in harmony, Measuring low frequencies, Non-invasive timer for battery equipment, Synchronising a crystal, Half-duplex from a pc's COM port.

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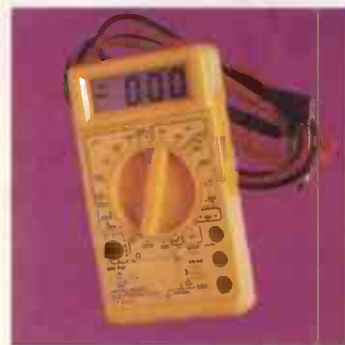
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## EW+WW READER OFFER



Digital multimeter including transistor gain tester and capable of measuring 10A dc for just £14.95 – fully inclusive. See page 864.

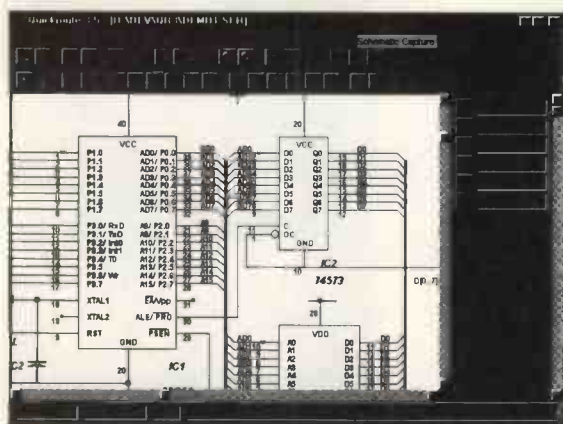
**Next month:** Applying a new 3-terminal magnetic detector with digital output, MathPlus reviewed, developing a C++ library for geometric transforms, using the Zetex 78L05.

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**NEW**

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"moving from schematic to layout could not be easier"  
review of Quickroute 3.0 in EW&WW Jan 1995

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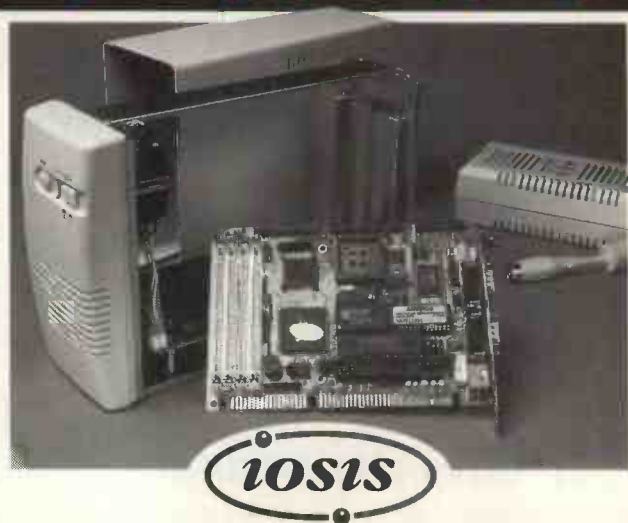
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The Americans talk about a 'Cultural Shift' towards electronics as US college students nowadays badger their parents not for their first car, but for their first pc. Moreover, apparently they want that pc, like they used to want that first car, to be the 'hottest', in terms of raw power, they can wangle.

In the UK, if any 'Cultural Shift' is occurring, it was undoubtedly spurred along by last month's shenanigans which saw *The Times* being given away free – courtesy of Microsoft – and the Rolling Stones being paid a reported £8m to provide the backing music for the launch of Microsoft's new pc operating system.

Even chips are scoring high in the awareness stakes with ubiquitous Pentium advertising, with readers of the serious papers learning that Siemens and Fujitsu are spending a billion dollars apiece on UK-based chip plants, and with Alan Clark (of Diaries and ladykilling fame) recently telling the readers of his *Mail on Sunday* column that Japanese industrial prowess came from its strength in 'very high speed integrated circuits' which, he said, are the basis for every type of manufactured equipment.

Such events are bringing it home to many that high-tech is a big, rich industry that has spawned the 39 year-old 'richest man on the planet' – Bill Gates – and is growing like weeds.

There's only one thing wrong with all this public awareness – the companies involved, Intel, Microsoft, Siemens, Fujitsu etc are all foreign. There's no chance – since Sir Clive Sinclair's regretted departure from the forefront of the electronics scene – of any Brit or British company doing anything sufficiently stunning in electronics to capture the public imagination.

Why? It seems to be an accepted truth among the British political establishment that we simply can't hack it in electronics. Last month, the deputy prime minister Michael Heseltine was asked if he was satisfied that the UK Government spent 25 times less than the German government on collaborative European chip R&D. Heseltine replied: "We can't be a winner in every field. If you look at some areas, UK companies have a disproportionately larger share, like pharmaceuticals – a high-tech area where we are winning in the market." Clearly Heseltine did not see the high-tech electronics area as one where we are winning in the market.

Why not? If we can make the investments to

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**"We can't be a winner in every field..."**

succeed in pharmaceuticals and have world class companies like Wellcome-Glaxo, why can't we have world class companies in electronics?

Looking back over the years we can chart the milestones along the route to our present position: the closure of our leading edge chip firms in the 70s – Marconi Elliott Microelectronics and Elliott Automation; the neglect of the GEC-Philips joint venture in chips – Associated Semiconductor Manufacturers; the abandonment by our largest electronics company of the flagship R&D project of Alvey – the chip process technology research programme.

All these decisions were made by one company – GEC – whose boss Lord Arnold Weinstock is generally seen as being unenthusiastic about developing the building blocks of electronics products – chips. Can the UK's relative failure in electronics really all be put down to the attitude of one man?

**David Manners**

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## New hope for HD-CD standards

The first steps to avoid potentially bruising battle over standards in high density CD disks have been taken with Sony and Philips agreeing to talk with rival Toshiba about creating a single standard.

Sony and Philips have been promoting their jointly developed Multimedia CD (MMCD) format but have few supporters outside of pc and cd-rom drive manufacturers. The rival Toshiba Super Density (SD) has garnered the largest amount of support with major electronics firms and Hollywood

studios choosing the format in the hopes of creating a large video disk industry.

At a recent demonstration of Multimedia CD technology at the Internationale Funkausstellung trade show in Berlin, Philips and Sony officials said that they would work toward a single standard.

Representatives of major US computer firms, organised as the Technical Working group, have been appealing for a single standard.

"We understand and share their belief that a single format should be

in the best interest of the consumer," said Henk Bodt, executive vice-president Philips Electronics.

"Toward this end, recently, Philips and Sony have written to the Technical Working group expressing our desire to work toward creating a single format that ideally combines the best features of the currently proposed Multimedia CD and SD formats," he continued.

Bodt added that discussions have begun with the SD Alliance on developing a single standard.

Fig. 1. New CCD read-out structure incorporating an electrode in the centre of the photo diode gives successive transfer electrodes for eight-phase driving, as opposed to conventional four-phase.

## CCD advance offers ten times the brightness levels

Matsushita Electric has developed the world's first hyper-dynamic range charge-coupled device, for which it has applied for two Japanese and two overseas patents. The device is capable of reproducing 10,000 perceptible brightness levels – 10 to

20 times that of conventional ccds. It is also capable of high resolution reproduction, either independently of or in combination with the hyper-dynamic range.

The ccd incorporates both a new read-out structure and a new 8-phase

driver. The new structure involves a signal charge read-out electrode transecting the central part of the photo diode. This has not been practical before because it mixes electrical charge signals from left and right, and produces low illumination images by requiring a long reading distance. In this ccd the problems have been solved by implanting boron in an optimal pattern in the photo diode area under the reading gate, and by a one-time heat diffusion process to produce a gently sloping potential

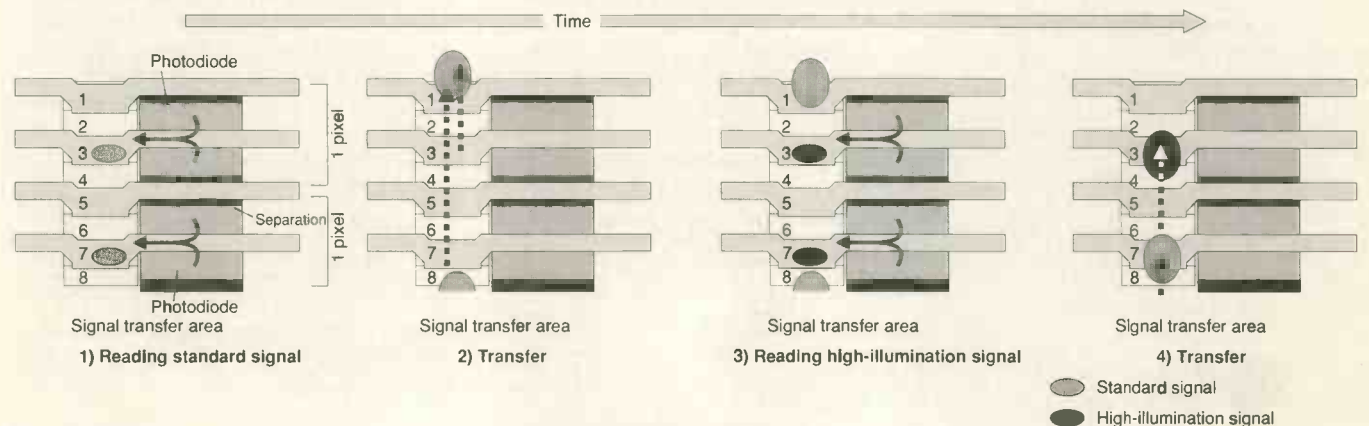
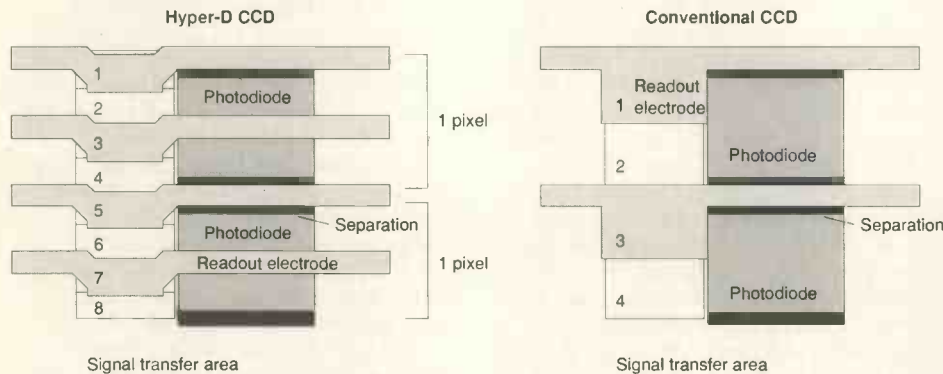


Fig. 2. Eight-phase driving enables the hyper-dynamic range – standard brightness signals are read (1) then transferred (2), followed by the brief exposed high-illumination signals (3&4), allowing up to 10,000 brightness levels.

# Breakthrough heralds 16 times more transistors on an IC

Researchers at Harvard University are claiming a breakthrough in a new semiconductor production technology that will let IC designers increase the transistor count 16-fold.

Professor Mara Prentiss says the team has developed a lithography process that has produced test samples with circuits 50nm wide. The technology can be refined to produce 10nm circuit features.

The researchers are pioneering atom lithography, a technology that manipulates a stream of atoms to etch out circuits. Current lithographic

techniques are starting to approach physical limits, needing new kinds of lithography.

Prentiss and other researchers working on atom lithography claim the technology offers significant benefits compared with other approaches such as e-beam and x-ray lithography. In theory, atom lithography can be used to achieve much finer resolutions.

Prentiss did not say when atom lithography technology will be ready for commercial use. The technique can create chips with transistor counts

in the 100 million range and be used to speed up current designs by making them more compact.

Prentiss' team used a laser to control a well-collimated atomic-beam that etches the circuit design on the substrate. The research is funded by the US National Science Foundation and involves six research groups organised as the Consortium for Light Force Dynamics.

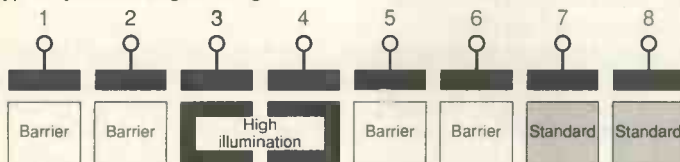
For more information on the Consortium for Light Force Dynamics point your web browser at <http://atom-sun.harvard.edu/lightforce/>

to smoothly read-out the charged signals. The 8-phase driving method applies different driving pulses independently to the eight successive transfer electrodes. This enables the capture of 10,000 brightness levels by reading out a standard signal with an independently variable exposure time of 1/60-1/15,000 second to a signal transfer area, transferring the signal to the next transfer area, and reading out the high illumination signal which has an independently variable exposure time of 1/800-1/200,000 second. High resolution is achieved

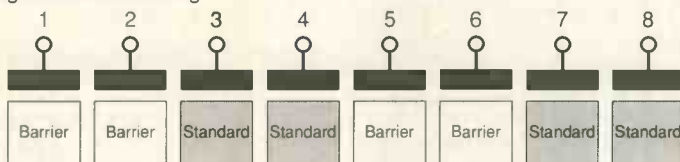
by combining two sets of the eight layers of the transfer electrodes, then driving the electrode independently when transferring the electrical charge.

Potential uses for this 1/4in ccd are an electronic still camera, a 'pocket-sized' camcorder that is more advanced than current professional cameras, a camera for car navigation systems which would be robust due to the elimination of a mechanical iris, a mobile videophone, and various applications in medical and basic research.

### 1) Hyper-dynamic range driving



### 2) High-resolution driving



### 3) Combination of both methods

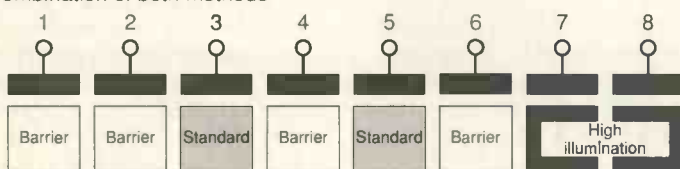


Fig. 3. Exploiting eight-phase driving to cope with differing requirements – hyper-dynamic range driving with high-illumination and standard signals (1), high-resolution driving with standard signals combined (2) and a combination of both (3).



## A shrink for lcds

Full colour on a miniature lcd display has been demonstrated by the display and 2-D Optics Team at GEC Marconi led by Dr Mike Worboys. Aimed at headup displays for fire fighters and other professionals, the display combines a c-mos chip backplane with ferroelectric liquid crystals. Colour is obtained by sequentially illuminating the display with red, green and blue leds. The proof-of-concept display is 176 by 176 pixels, but the team is about to start work on a 768 by 576 pixel PAL version measuring only 8mm by 6mm. Plans for a 1280 by 24 version exist. The image has been superimposed for the benefit of the photograph.

## Ghosts laid to rest

A device for rejecting tv ghost signals has been announced by Oren Semiconductor. Ghosting is the degradation in a television picture caused by the transmitted signal arriving via multiple paths.

Oren Semiconductor has been set up by the dsp company Zoran and the Singapore based Goldtron Group, and has its headquarters and design centre in Israel.

Its ghost cancelling device, the OR43100, consists of an adaptive

equaliser controller and a large, 576-tap digital filter. The device exploits a ghost cancelling reference, gcr, signal which, in countries such as the US and Japan, is transmitted with the tv signal. Using the received gcr signal, the device calculates the necessary filter coefficients for the tv receiver to effect the necessary ghost cancellation. With its 576-tap filter, the device can reject ghost signals occurring up to 7.5µs before, and

42.5µs after the main received tv video signal.

Spencer Horowitz, Oren's director of marketing, said the device has already been adopted by Philips Consumer Electronics for its Magnavox tvs. He also confirmed its adoption "by other major consumer manufacturers".

Oren Semiconductor is developing further devices to address the emerging digital cable and terrestrial Tv standards.

### IN BRIEF

#### Windows 95 compounds component supply problems

US pc makers are fighting for market share with major price cuts in the wake of the launch of Microsoft Windows 95, which could boost demand for semiconductors, exacerbating shortages of key components.

Over recent weeks, at least six major US pc makers have cut pc prices. Hewlett Packard is the latest company to do so - reducing prices by as much as 19%. Other companies cutting pc prices include IBM,

Compaq Computer, Dell Computer and IPC Technologies.

Many pc buyers are upgrading to more powerful systems needed to run Windows 95, which requires faster microprocessors, larger hard drives and system memories. The demand is likely to worsen a shortage of d-rams.

#### Speed up for automotive electronics

Market analysis company BIS Strategic Decisions is predicting the total demand for automotive electronics will rise to \$16.1bn by the year 1999.

Its report states that the implementation of automotive electronic systems has significantly increased in recent years. This, combined with the general recovery in key world car markets last year, resulted in a 1994 automotive electronics growth of 24 per cent.

The 1999 demand is expected to split into: power train \$7.5bn, body \$3.8bn, chassis \$2.7bn, security \$1.1bn and driver information \$1bn.

Safety, security and convenience features are the main factors driving demand.

#### Battery boost

Mitsubishi Chemical has developed an anode material that is claimed to improve the performance of lithium ion cells.

Mitsubishi says that cell capacity is raised from 300-500mAh in more conventional cells to over 600mAh. This will allow longer battery life in mobile phones and pdas. The material is also said to improve output current capability.

The company has already established a line at its Sakaide plant in Kagawa to produce 30 tonnes of

batteries per year on a test basis.

Mass production will begin in spring 1996.

#### Video server brings greater immediacy to news reporting

ITN is to be the first in Europe to employ Sony's digital video server technology for its news production.

The pilot system will replace current video tape systems with video servers controlled by a Sony/Oracle computer system.

The system will enable a journalist seated at a workstation to script, edit and caption stories in a form ready for broadcasting, dramatically reducing production times.

#### Korea boosts lcd production

The first major challenge to the Japanese monopoly in large colour flat panel displays is swinging into gear with the three Korean electronics majors, Samsung, Hyundai and Lucky Goldstar, sharply increasing production.

#### Car navigation system is a game

Japanese consumer electronics giant Hitachi is one of the first companies to start developing car navigation devices that can be used as game machines when taken out of the car.

The announcement of a 32bit car navigation system based around the Sega Saturn game machine was expected from Hitachi last week.

Matsushita Electric Industrial is also pursuing this line of development and its similar product is expected before the end of the year at prices around £500.



#### Ring with more than a hole

Dallas Semiconductor and US jewellery maker Jostens have created a ring containing a 64kbit memory chip that can hold personal information such as credit card numbers and photographs.

The \$60 ring could be used like a smart card. The user would touch the ring to a data reader to download credit card or automated teller machine payment data.

Dallas is talking to potential users. Applications areas might include security, where a combination of the ring and password could be used to limit access to computer files or sectors of a building.



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### Micromaster LV £625

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### 16 bit Emulator card £195

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# RESEARCH NOTES

Jonathan Campbell

## Bringing solar wind down to earth

Scientists at AT&T Bell Labs are investigating how solar activity affects long cable systems in practice, by using transoceanic cables to sense how geopotentials change with time and solar activity.

Elsewhere, much research is going into analysing solar activity and unravelling the secrets of the solar wind. But even if we can eventually understand – and predict – solar storms, their interaction with electrical and electronic systems on Earth is so complicated that we still can not be sure what actual effect a solar storm will have.

Part of the problem is that the magnitudes of earth potentials that might occur across specific power line or cable routes in a set time interval cannot be determined from first principles.

Variations in Earth's background magnetic field caused by the charged particle solar wind can induce electrical currents to flow. Normally, changes in magnetic field often appear to propagate from east to west

– and magnetometers at both ends of a cable can predict the induced voltages. But in other cases the field appears to change almost simultaneously over a larger area.

The Earth's surface consists of layers of conducting and insulating materials, so the electric potentials that can be induced by a time-varying magnetic field can be quite different in different areas. The sea too, as a good conductor, has a significant effect. At the same time, statistics of magnetic fields and induced voltages all vary with local time, date and solar activity, and researchers say there appears to be more than one type of magnetic storm.

Yet these effects are vitally important to the design of long conducting systems such as power feed equipment for oceanic cables.

In the AT&T study (*AT&T Technical Journal*, Vol 74, No 3, pp.73-84), computer-based instruments are continuously monitoring cable voltages relative to the local ground, digitising the values

at two second intervals and writing the data to hard disk.

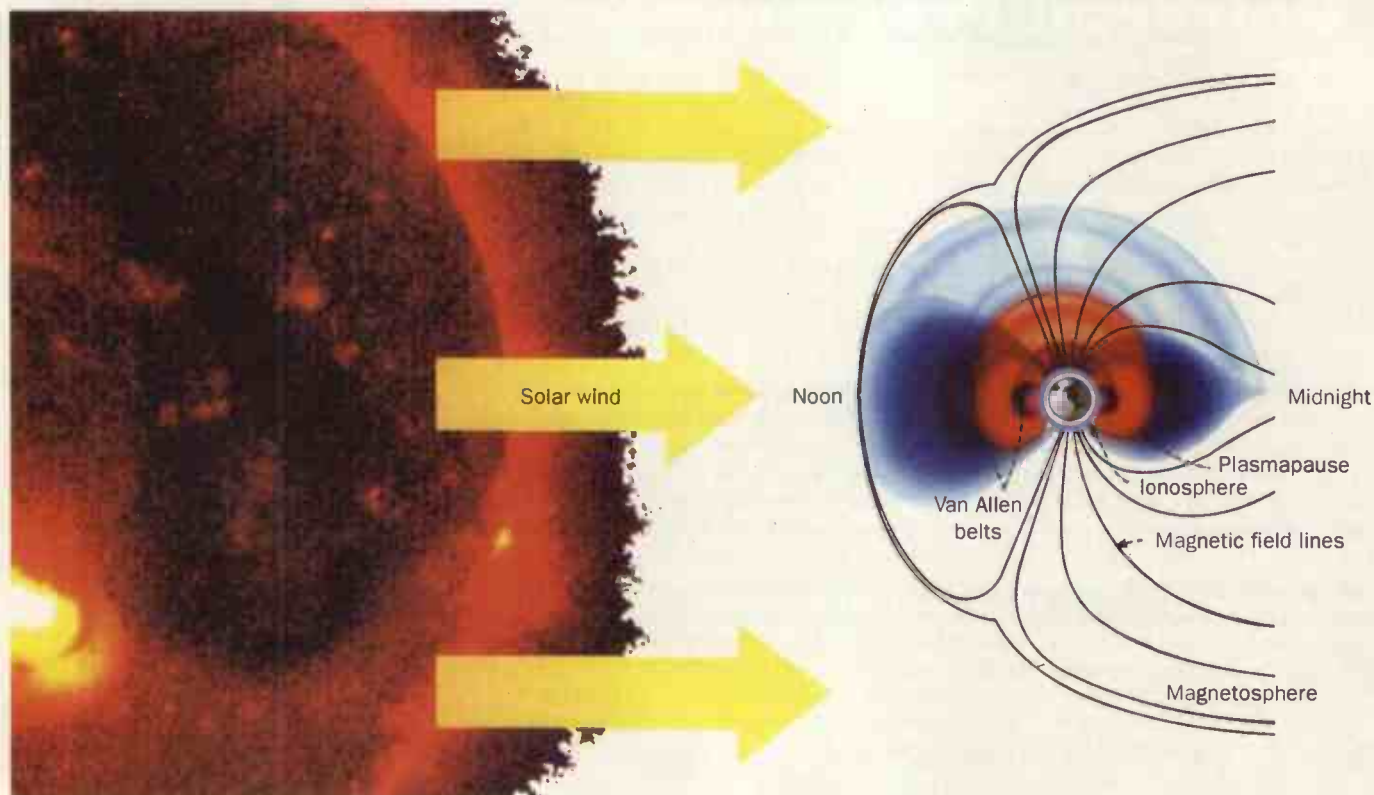
So far scientists have been finding that theoretical predictions are not able to match actual results – particularly in the case of the largest excursions.

However, the researchers say their eventual aims are both to improve predictability of observed signals and also to gain a better understanding of the mathematics of predicting non-stationary processes.

One interesting related discovery is that two of the AT&T cables being monitored, parallel and separated by about 75km in the Pacific Ocean, effectively form a large elongated Faraday loop. The net potential around the loop is induced only by variations in the component of the geomagnetic field perpendicular to the current loop.

Measurements show that this large cable loop is much more sensitive than a magnetometer to magnetic field fluctuations, giving a convenient way to measure small variations in ionospheric currents.

*Emissions from the sun constantly affect the Earth. This x-ray image of the sun, made by the Japanese Yohkoh spacecraft, shows the solar corona expanding into the interplanetary medium as an ionised gas.*



## Now walls really do have ears

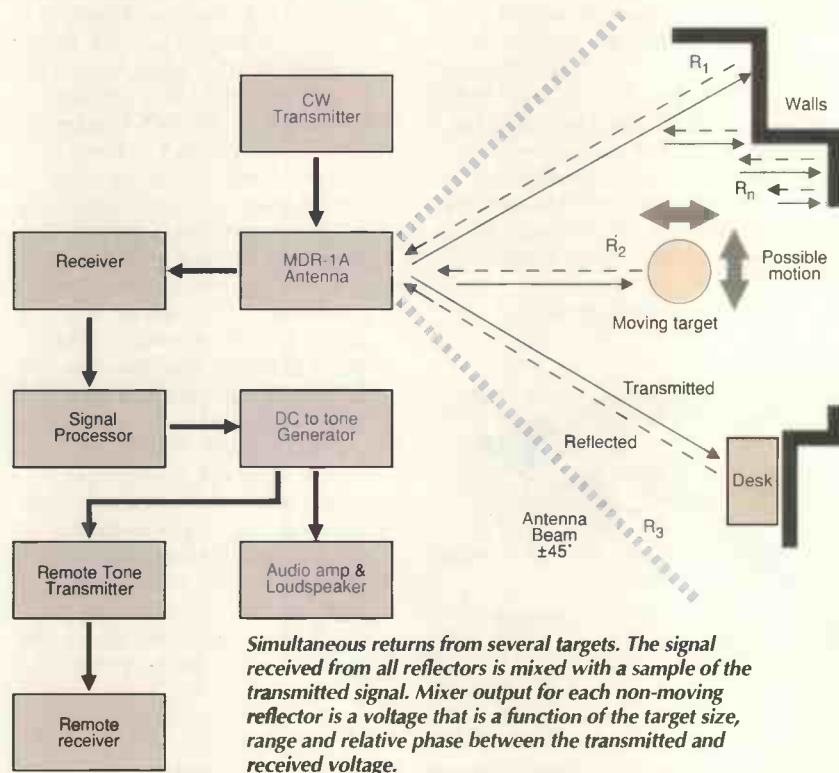
In this security-preoccupied age, cctv and infra-red sensors attached to buildings are no longer an unusual sight. Unfortunately, because they must be located in the open, they can be disabled too. But a low-power radar movement detection system that can 'see' – or should it be hear? – though walls and so be built into brickwork, suffers no such drawbacks. In addition the technology could also be useful in collecting information in terrorist attacks where hostages have been taken.

Developer of the system is Hughes Advanced Electromagnetic Technologies Center, and its workings were recently described in an interview with Lawrence Frazier, Senior Engineering Staff Specialist at Hughes Missile Systems Company (Surveillance through Walls and Other Opaque Materials *OE Reports*, No. 140, published by SPIE).

Frazier explained that, unlike conventional radar, the MDR-1A motion detection radar uses very low power. Less than 2mW goes to the antenna, as opposed to a high powered airport radar which uses 100,000W. It doesn't provide any range or angle information, only indicating if there's motion. However, it is extremely simple – the tone pitch changes when a person is moving or constant if there is no motion.

To operate, a signal is transmitted into a room, and the radar receiver senses a net voltage, representative of all the things in view. Any change, such as a movement brings about a change in the dc signal.

In the open the range is around 30m, dropping down to 10m through 15cm



of steel reinforced concrete, such as a standard slab wall. Operating frequency is 915MHz and the wavelength is approximately 0.3m.

The system is not Doppler based. Instead a cw signal is transmitted. The phase of the energy coming back off each fixed reflector is summed together into a constant vector voltage. This has a voltage and phase relative to the signal that was sent out. Small phase changes of the received signal are produced by any one of the reflectors changing position.

The system is said to be so sensitive that it is capable of detecting even finger movement in a room. For all those *EW+WW* readers who might be going into their bedrooms a little more self-consciously than before, Frazier says that Hughes is very careful to whom it sells. But he does admit that some unscrupulous person could, if they wanted, obtain one.

Do you think you had better get one to check your neighbours out, before they start checking you out?

## Pivotal microtechnology

Microscale conducting bilayer hinges that can rotate and hold plate-like limbs at any position could form the basis for the design of a powerful microrobot, according to three physicists at the Linköping Institute of Technology in Sweden.

The hinges, made of a layer of polymer and a layer of gold (Controlled Folding of Micrometer-Size Structures, *Science*, Vol 268, pp.1735-1738), are used to connect rigid plates to each other and to a silicon substrate. The bending of the hinge occurs because the polymer, sitting in an electrolyte, can be tuned

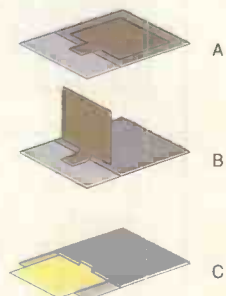
from insulating to metallic, with the resulting movement of ions producing a change in volume. This change translates into a rotation in the hinge in a process that can be controlled by application of a small voltage.

The Linköping team says that electrically controlled bending of the hinges allows the precise three-dimensional positioning of the plates in space. Other systems tilt the position of plates relative to a base substrate. But the Swedish approach allows controlled folding of 180° at each joint.

The hinges can apply real power

too. To give an idea of their strength, the researchers say the process is equivalent to a person under water trying with one arm to move a 30m square plate – but conducting polymers are able to deliver stresses of hundreds of megapascals.

Several rigid elements can be connected with the hinges, allowing a microstructure to fold and assemble into a predetermined shape. Valves that close automatically in response to a chemical signal or change in pH is one application. But more complex folding structures could form the basis for microrobot limbs too.



*Square plate paddles 90µm square being accurately positioned by 30µm hinges. Could it form the basis for a powerful microrobot?*

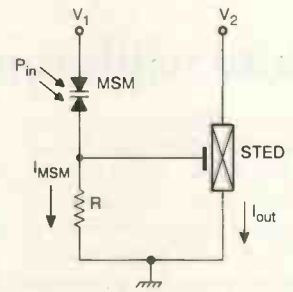
## Integrated receiver boosts optical performance

Low cost, low parasitic components that deliver high reliability and performance will be vital for the next generation of optical communications. Now work supported by DBP Telekom in Germany, has led to development of a new integrated optical receiver that could offer high current gain and inherent pulse shaping, while its structure is simpler and more bandwidth efficient than anything before proposed. The receiver integrates an InGaAs transferred-

electron device having a Schottky-gate-electrode (sted), with an InGaAs metal-semiconductor-metal detector, msm - 'A new photoreceiver concept using InGaAs transferred-electron devices', D Hahn *et al*, *IEEE Journal of Quantum Electronics*, Vol 31, No6.

In the circuit for the integrated photoreceiver, the msm detector is biased by a voltage,  $V_1$ , chosen to yield maximum sensitivity of the device, while  $V_2$  is set to a level just below the threshold of the sted - see diagram. An optical input pulse generates a photocurrent leading to a gate-trigger voltage for the sted across the load resistance  $R$ . The whole circuit requires only two epitaxial layers and relatively few fabrication steps.

The researchers report a dark current density of  $3 \times 10^{-5} \text{ A/cm}^2$  - comparable to the best values so far reported - while responsivity is  $0.4 \text{ A/W}$  and capacitance at  $10 \text{ V}$  is  $20 \text{ fF}$ . Rise time of the temporal photoresponse is smaller than  $70 \text{ ps}$  which also gives an indication of the capability to trigger the sted in the



Simple circuit forms the basis of the integrated photoreceiver.

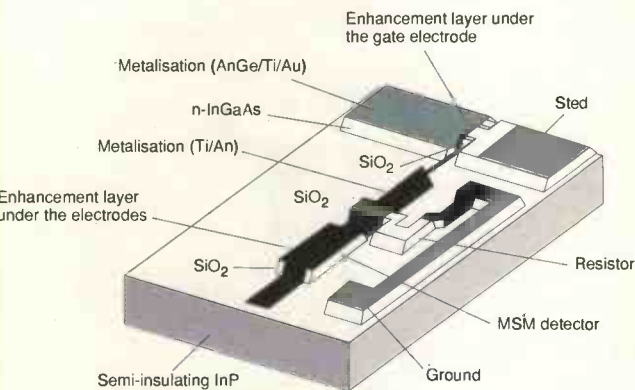
Gbit/s range.

According to Hahn and colleagues, the device should be able to demonstrate a high current gain up to  $60 \text{ dB}$  and a sensitivity of  $-24 \text{ dBm}$  at data transfer rates of  $10 \text{ Gbit/s}$ .

The circuit also, naturally carries out some pulse shaping of the input signal, as rise and fall times of the current drop result from domain formation and dissolution.

Because circuit implementation is so simple, it should become very easy to add a laser diode in order to realise an optical repeater.

Only two epitaxial layers and relatively few fabrication steps are required.



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# PROTEUS

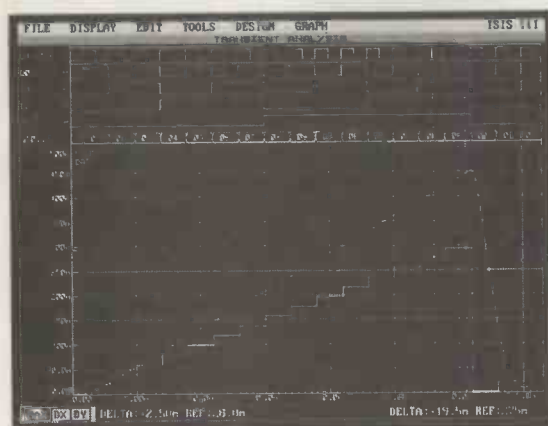
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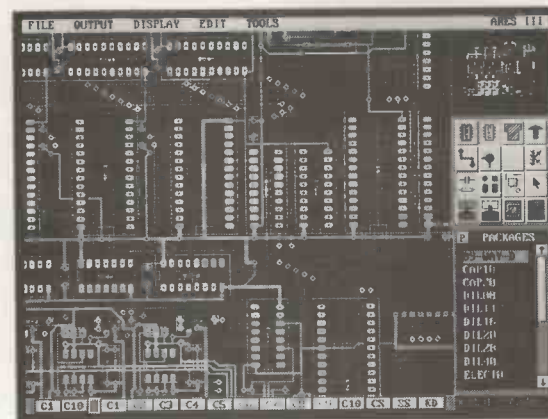
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## Musicians need more fingers

Our well-known sequence of octaves, tones and semi-tones is not the only musical system in the world. Oriental and Arabic musicians have a choice of 36 notes in the same period occupied by our octave scale of twelve notes. Now, engineers at the National Technical University of Athens have combined both systems in a single keyboard electronic instrument for Byzantine music (eibm).

In the eibm, the semitone, normally the smallest musical partition, is divided into three new notes. To enable musicians to play these notes a keyboard has been designed (Study and Design of an Electronic Musical Instrument which Accurately Produces the Spaces of Byzantine

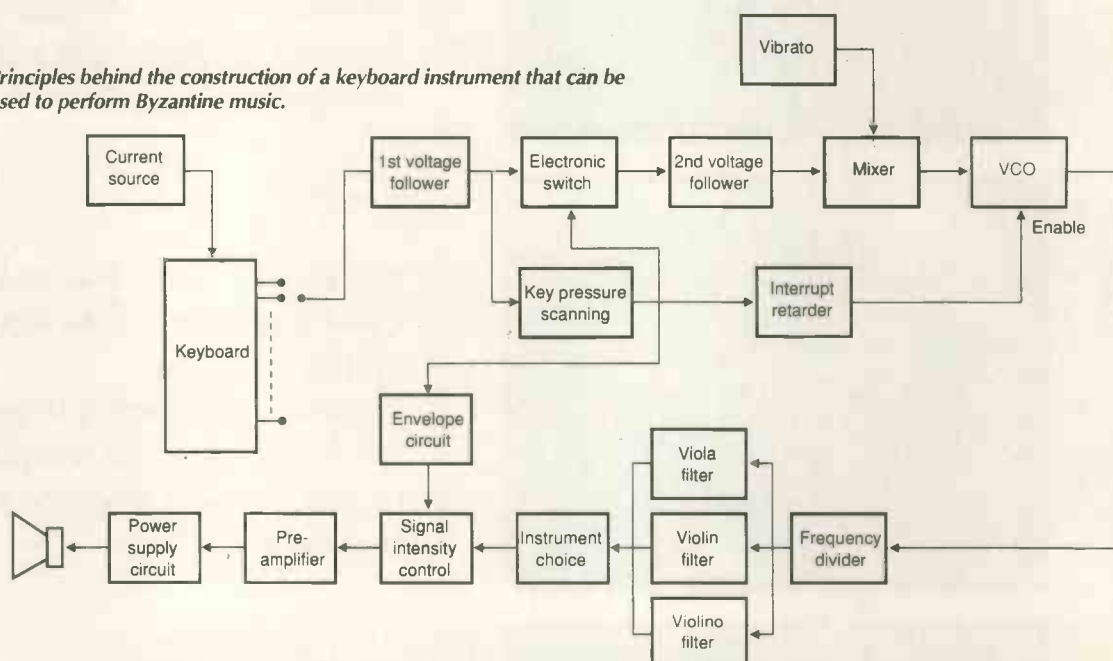
Music, *IEEE Transactions on Consumer Electronics*, Vol 41, No 1, pp118-124) that looks like the European harmonium, except that every key has three segments. The result is that both Western and Byzantine music can be played on the same instrument.

Electronic components in the instrument include a circuit to create the envelope of the note (the shape) and a pre-amplifier – a voltage amplifier having a high input resistance – for driving the modulated output signal. There is also a power amplifier with a low output impedance for driving a speaker, various filters to simulate the sound of different instruments, and circuits to produce vibrato.

But the heart of the instrument is the oscillator, producing a regulated frequency that depends on the position of each key. A voltage-controlled oscillator is used so that when a key is pressed, equivalent to a voltage, the oscillator frequency is defined. The power supply must of course be particularly stable to provide a solid base for the reference voltages defining the frequency of the vco, but the researchers report success with their instrument and say they expect the eibm to enable creation and study of polyphony in oriental music scales and allow experimental recording of the auditory impression of different scales.

They also hope the eibm will open up the study of new sounds.

Principles behind the construction of a keyboard instrument that can be used to perform Byzantine music.



## Organic development pleases tv and telecoms

A prototype opto-electronic modulator that can convert the equivalent of 15 million simultaneous phone conversations from electronic to optical form – reliably and over an extended time – has been developed following work at the University of Southern California-UCLA.

Currently, the best opto-electronic modulators have only one-third as much capacity as the new device. The tripling of conversion capacity results from substituting a newly synthesised polymer for the crystalline materials previously used in such devices.

Cable television companies, which convert electronic signals captured

from satellites into the signals carried over optical cable, are large users of opto-electronic modulators. Telephone companies too, which prefer to carry signals long distance over low-loss fibre-optic cable, but which switch and process signals electronically, are potential customers.

High-capacity devices that can process up to 20GHz electronic signals conventionally use lithium niobate. But the USC-UCLA device uses an organic polymer and operates at 60 GHz – successfully converting a much wider band of electronic signals to optical ones. The 60GHz range is the equivalent of some 15,000 television channels.

The USC researchers say the wide bandwidth capacity of the device will offer advantages in design and operation of advanced 'smart' radar systems, which use arrays of antennas to transmit extremely high radio frequencies for tracking multiple targets and to avoid enemy radar jamming signals.

Tacan Corp, a manufacturer of fibre-optic components for cable television and other markets, is collaborating with USC and UCLA to develop the new devices commercially.

The USC-UCLA team is working now on a 100GHz device, and a 200GHz model might ultimately be built. ■

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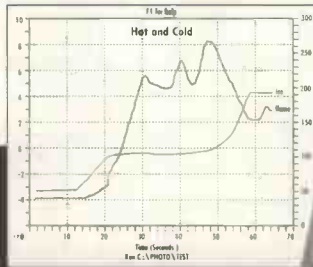
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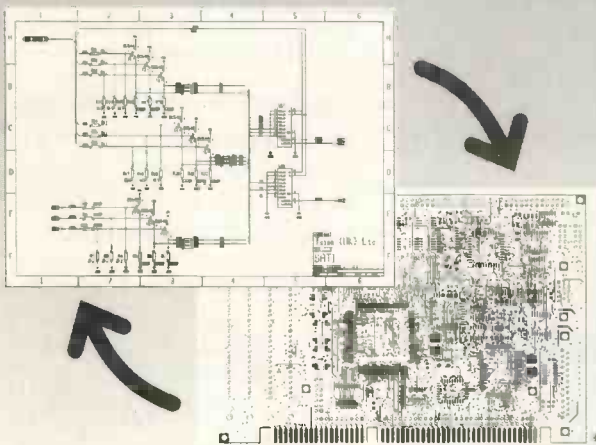
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# Sweeping to

Spanning 0 to 200MHz in one range, this stand-alone sweeper provides an output level of  $+3\text{dBm} \pm 1\text{dB}$  into  $50\Omega$  with very low harmonic and spurious content. **Ian Hickman** designed it in response to the recent scarcity of commercially-available alternatives.

# vhf



Fig. 1. Construction of the screened enclosure for the rf unit. A die-cast box would have been more convenient, except that it is difficult to create the three partitions needed

Swept frequency generators – sweepers – were once commonplace in electronics labs. Earlier types, wobulators, used a vibrating capacitor and consequently produced an output whose frequency varied approximately sinusoidally with time.

Nowadays, stand-alone sweepers are rare, but a sweeper is incorporated in most modern spectrum and network analysers. This design covers 0-200MHz, or any desired narrower sweep within this range. It provides an output level of  $+3\text{dBm} \pm 1\text{dB}$  into  $50\Omega$ , with very low harmonic and spurious content.

An external attenuator may be used to reduce the level as required. Sweep speeds of 25ms for the full 200MHz sweep, down to 250s, are provided. Faster speeds are suitable for use with a basic oscilloscope, while slower speeds are provided for use with a digital storage oscilloscope.

Additionally, the sweep may be turned off. The unit may then be used to provide a cw output. Alternatively, an external audio input up to 15kHz may be connected to provide fm output. Maximum deviation available is the

full 200MHz, and as the external input is dc coupled, the unit may be used as a 0-200MHz vco in a phase locked loop.

## Sweeper details

A sweeper is basically a beat frequency oscillator, bfo, comprising a fixed frequency oscillator, ffo, a mixer and a voltage-controlled oscillator, vco.

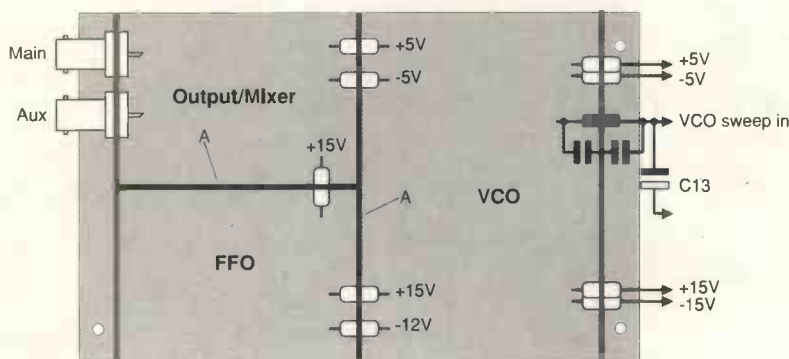
The vco can be swept from the same frequency as the ffo to some higher maximum frequency. Outputs of both oscillators connect to the mixer. Mixer output, suitably low-pass filtered and amplified, forms the sweeper output.

In addition to covering the full range 0Hz to  $f_{\text{vco(max)}} - f_{\text{ffo}}$ , arrangements are always included to permit the output to sweep as small a range as desired, centred anywhere within the full range.

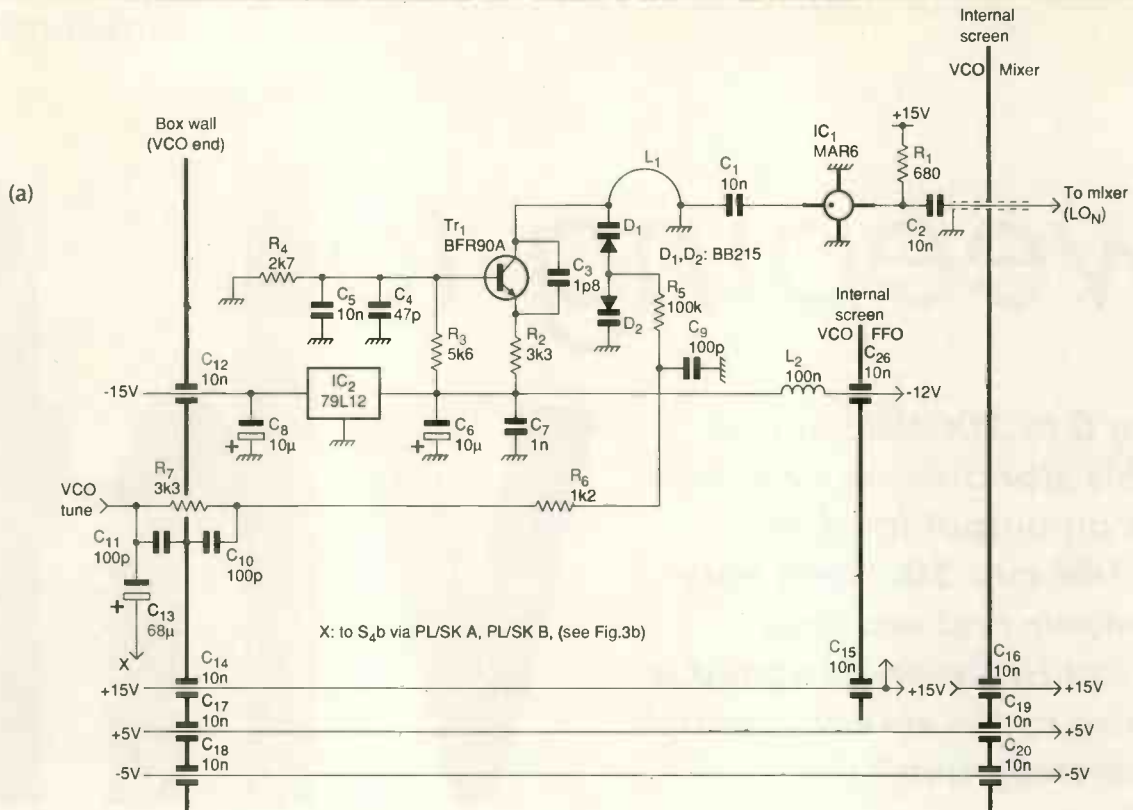
Implementing a sweeper is greatly simplified if extensive use is made of rf ics, as opposed to discretes. Associated low-frequency circuitry, sweep generator, linearisation etc. will use ICs anyhow – the universal quad op-amp being the obvious choice.

I was prompted to develop this instrument by the appearance of the Analog Devices AD831 low distortion mixer. Being an active mixer, it has the advantage that conversion gain can be set to any desired figure. The usual value is 0dB, compared with the typical 6.5dB conversion loss of a passive Schottky diode ring mixer.

The data sheet for this active mixer, housed in a 20 lead plastic leaded chip carrier, claims a 500MHz bandwidth for both the rf and local oscillator ports. Some of the graphs show typical performance extending to 600MHz. Knowing that the larger more reputable IC manufacturers tend to give conservative fig-

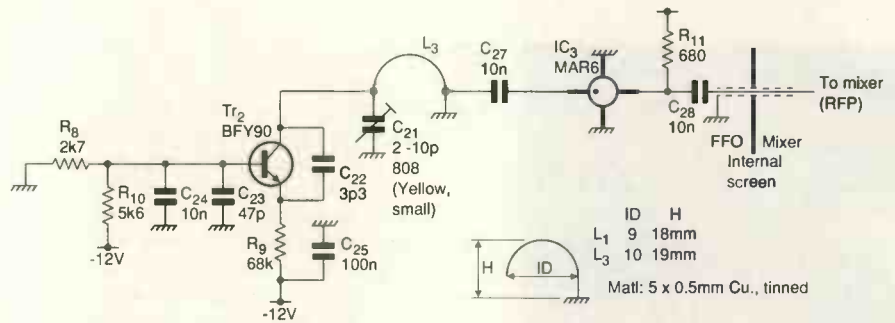


A - double-sided copper clad board.  
Base, sides, ends and lids - single sided

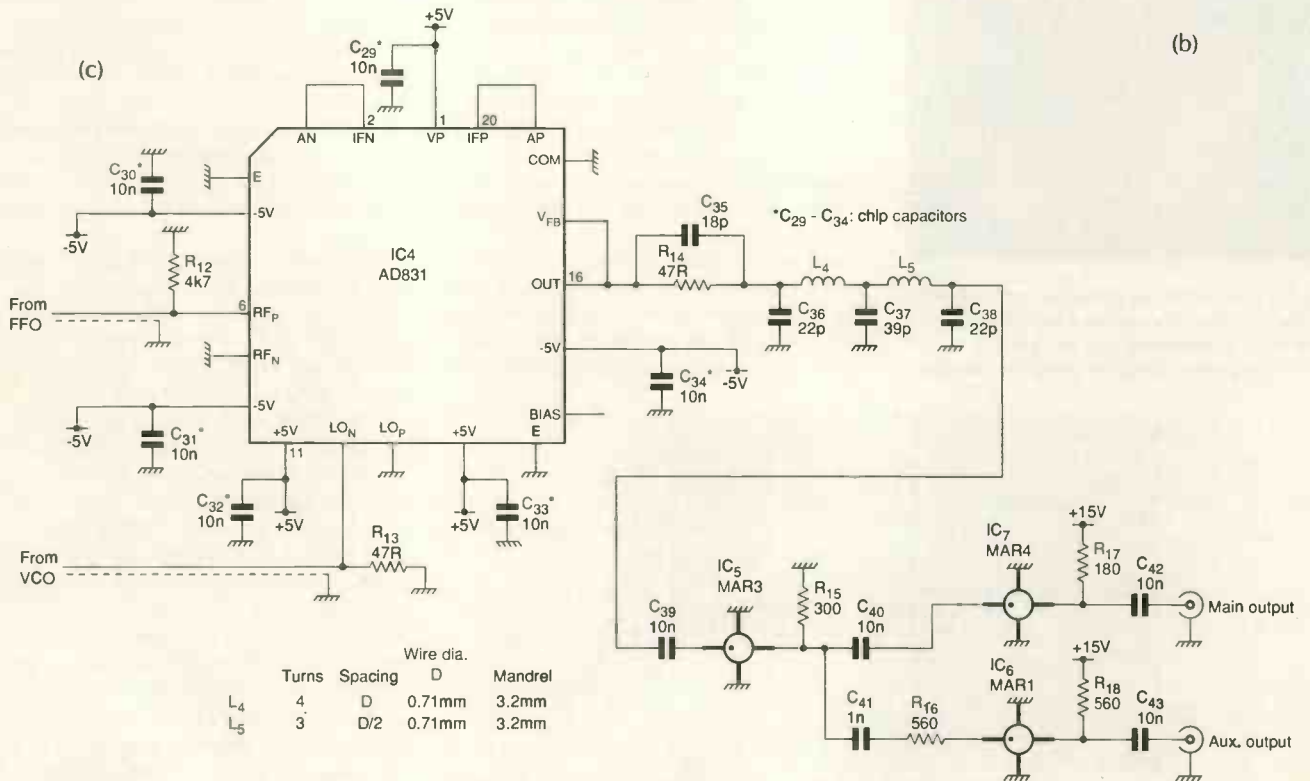


**Fig. 2. Rf circuitry unit.**

- a) vco circuit and supplies decoupling
- b) fixed frequency oscillator
- c) mixer and output stages.



(b)



ures in their data sheets, I decided to aim at a design providing an output frequency range covering 0-200MHz. For reasons explained later, this implies a fixed-frequency oscillator at just over 400MHz and a vco covering from there up to just over 600MHz.

**Rf criteria**

As the rf section was clearly going to be the most tricky, design and construction of this section was tackled first. A fully screened construction was obviously essential, so I initially considered a die-cast box. However, I soon abandoned the idea since three separate and completely screened internal compartments were required for the two oscillators and the mixer/output. Partitions are not easily implemented in a die-cast box.

For the prototype, an enclosure was fabricated out of copper-clad pcb. Single sided was used for the base, four sides and three lids, while double sided board formed the internal partitions, Fig. 1.

Each lid sits just below the top of its compartment, supported on stops soldered to the compartment wall. With its copper side uppermost, when the rf unit is complete, each lid is soldered to the walls of its compartment. I recommend that you tack it at one central point only on each of its four sides, as you are sure

to want to remove it again sometime.

Enclosure seams, by contrast, are soldered along their whole length. For ease of access, initially, just the base, sides and central partition were assembled. The short partition and ends were fitted only when the three sections were all basically functional, the lids only when final testing was complete.

This approach illustrates one of the differences between general analogue circuit design and rf design, which tends to be overlooked by the newcomer to rf. At rf, mechanical design has to be considered before the circuit is worked out in detail

**VCO details**

The vco was designed first, accommodated in the largest of the three compartments. It, and the remaining rf circuitry, are shown in Fig. 2. In my prototype, the vco used the same inductor as the fixed-frequency oscillator. As a result, it covers the required tuning range, but with not much to spare at the top end. The bottom end however extended down to around 300MHz – hence the different vco inductor specification shown in the diagram.

Output from each oscillator was taken from a tap near the earthy end of the inductor, to ensure low loading on the oscillator. This does not significantly affect its operating Q, which,

especially in the case of the vco, is much lower than I would like.

Outputs are amplified to about -10dBm, and connect to the mixer via miniature 50Ω coaxial cable. Each cable is taken into the mixer compartment under a notch in the intervening partition. At this point, the outer sleeve was locally stripped, and the coax screen soldered to the partition.

Similarly, to prevent leakage of rf energy from one compartment to another, power rails are taken through 10nF feed-through capacitors. These are also used to bring the supplies into the vco end of the rf unit. But a different arrangement – shown in Fig. 2 – is needed to bring in the vco tuning voltage.

Drive from the vco is applied to the local-oscillator input. In the AD831, this input feeds an internal limiting amplifier before being applied to the mixer cell. This results in the mixer performance being unaffected by variations in the level of local-oscillator drive as the vco sweeps across the band.

Since the rf port is a linear input, the waveform of the mixer if output reflects that of the rf input. The fixed-frequency oscillator is designed for low output harmonics. The following amplifier also operates way below the 1dB compression point. As a result, the mixer intermediate frequency output also exhibits a

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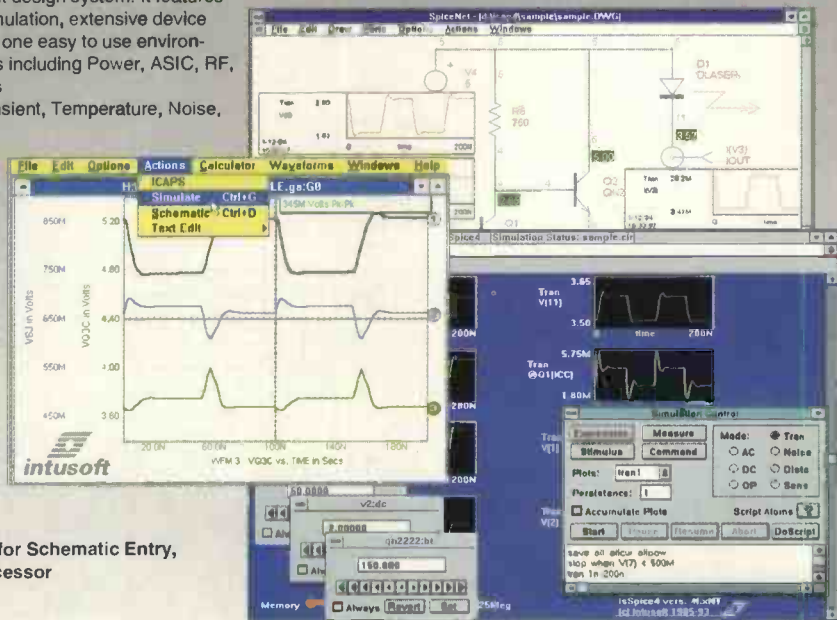
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low harmonic content. An MAR4 IC, with its +11dBm, 1dB compression point, ensures that at the sweeper's modest +3dBm output, the harmonic content remains low.

Extensive use is made of convenient and reasonably priced 'Mini-Circuits' rf amplifier ICs, available from various distributors. In addition to the MAR4 providing the main +3dBm output, an MAR1 provides a -13dBm auxiliary output. This is well buffered from the main output and can be used to drive a frequency meter.

**Low-frequency circuitry**

Compared to the rf section, the sweeper's low frequency circuitry is not critical. In the case of the prototype it was all accommodated on a stripboard. This was supported horizontally on brackets at the rear of the case, above the mains transformer and the rf unit.

Connections between this board and the rf unit were made via an 8-way 0.1in pitch Molex plug and socket. These were mounted at the left rear of the stripboard, above the rf unit.

In front of the 8-way connector, a 15-way Molex plug and socket provided all the necessary connections to the front panel controls

and the 'bnc ext' fm input and 'trigger' output sockets. The whole instrument was mounted in a case 305x159x133mm high.

The suite of stabilised supplies were mount-

ed at the right hand side of the stripboard, above the 20V+20V 25VA mains transformer.

Figure 3a) shows the centre frequency setting circuitry IC<sub>9B</sub>, with 10kΩ coarse and fine

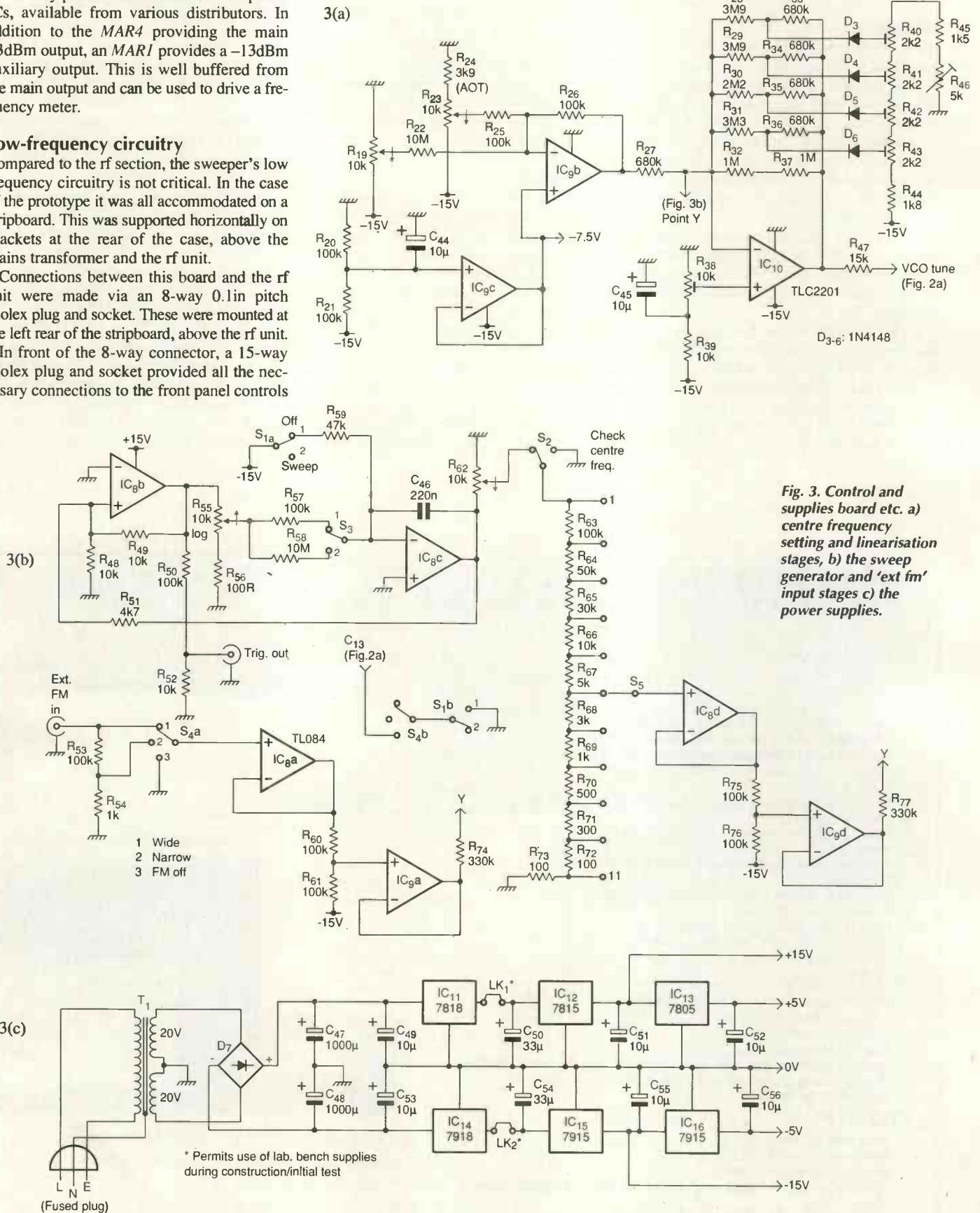


Fig. 3. Control and supplies board etc. a) centre frequency setting and linearisation stages, b) the sweep generator and 'ext fm' input stages c) the power supplies.

controls, together with the vco drive amplifier  $IC_{10}$ . A TLC2201 was chosen for  $IC_{10}$ , since its cmos output stage can swing to within millivolts of either rail. This allows the vco to be tuned up to a top frequency just in excess of 600MHz.

For ease of adjustment, both the 10kΩ potentiometers were ten-turn types. A select-on-test resistor  $R_{24}$  was fitted at the earthy end of the coarse tuning control  $R_{23}$ . Its tuning range – in cw mode with sweep disabled – was limited to just below 0Hz, in fact about –10MHz.

Note that both  $IC_9$  and  $IC_{10}$  operate between 0V and –15V rails. This ensures that the varactor diodes in the vco can never become forward biased.  $IC_{10}$  incorporates a linearisation network around its feedback resistor.

Figure 3b) shows remaining control circuitry. Principally, this comprises the sweep generator  $IC_{8B}$  and  $c$  – operating on +15 and –15V rails – sweep-width buffer  $IC_{8D}$  and level translator  $IC_{9D}$ .

The sweep generator produces a triangular voltage, not a sawtooth. Note also that the highest vco frequency results when the tuning voltage is at –15V and that  $IC_{10}$  is inverting. Consequently, when displaying the discriminator characteristic of an fm receiver on an oscilloscope for example, the oscilloscope should be triggered at the start of the positive-going half of the triangular waveform.

**Signal selection and i/o**

A suitable timebase speed should be selected to display the whole of the frequency sweep. With the values shown in Figure 3b), the wiper of sweep speed variable control,  $R_{55}$ , at the top of its travel and sweep speed switch  $S_3$  in position 1, the sweep time is about 25ms. Thus a sweep speed of 2ms/division, adjusted to 2.5/div with the oscilloscope's timebase speed VAR control, will be suitable.

Alternatively, a front panel output socket can provide a suitably scaled version of the sweep voltage from  $IC_{8C}$ , for application to the oscilloscope's X input, in XY mode. Progressively slower sweep speeds may be set by means of  $R_{55}$ . Position 2 of  $S_3$  provides sweep speeds slower by a factor of 100, for use with digital storage oscilloscopes in 'roll' mode.

A buffer and level translator are provided for the 'ext fm' input,  $IC_{8A}$  and  $IC_{9A}$ . Figure 3c) shows the power supplies, which uses 78/79XX series IC regulators, fitted with heatsinks.

**Non-linear ramp for a linear output**

A linear sweep voltage or ramp is produced by the sweep generator, but applied directly to the vco this will not result in a linear frequency sweep. The sweep linearisation network around  $IC_{10}$  shapes the ramp into a non-linear form which is the inverse of the vco tuning voltage/frequency characteristic, resulting in a linear frequency sweep.

Necessary adjustments to the linearisation are most conveniently made while displaying the full 0-200MHz sweep, with the aid of 10MHz 'birdie' markers. These can be pro-

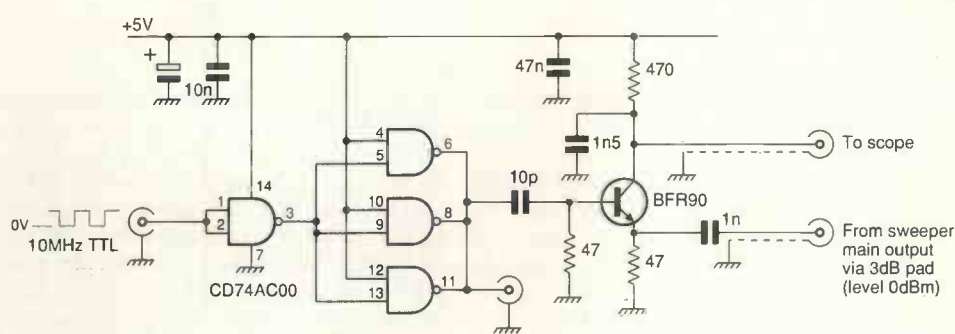


Fig. 4. Harmonic mixer used to provide birdie markers, to facilitate the adjustment of the frequency sweep linearisation.

duced by feeding the output of the sweeper to one input of a passive schottky diode double balanced mixer, and narrow 10MHz spikes to the other.

Mixer output is then displayed on an oscilloscope. I tried this arrangement, but it proved unsuccessful, partly due to the poor risetime of the 10MHz squarewave obtained from a 10Hz-10MHz video generator. But even below 100MHz, where birdie markers were obtained, their amplitude was very small.

In principle, the linearisation network could be set up without the sweep, using the centre frequency setting control  $R_{23}$ . But in practice this proves impossibly cumbersome. So I set up a special harmonic mixer, Fig. 4. This used the 10MHz squarewave already mentioned, but with its edges sharpened up by a test circuit that I had used previously<sup>1</sup>.

**Checking the sweep**

Figure 5a) shows output of the harmonic mixer, lower trace, during a positive-going varactor tuning voltage sweep, upper trace, corresponding to a falling frequency sweep. The sweep extends from 10MHz to –80MHz, i.e. to the point where the vco frequency is 80MHz below the fixed-oscillator frequency.

Note the absence of a birdie marker at 0Hz. This photograph was taken before fitting any linearisation around  $IC_{10}$ , and the decreasing tuning sensitivity with increasing frequency is clearly visible.

In the design stage of the sweep circuitry I felt that five breakpoints should be ample to achieve an acceptable degree of linearisation. Provision for these were made in the circuit of Fig. 3a). When it came to setting up the linearisation, never one to use two resistors where one will do, I tried the effect of using just three of the breakpoints. Adequate linearity was not achievable, but the addition of a fourth breakpoint as in Fig. 3a) achieved a good degree of linearity, Fig. 5b). This shows the vco tuning voltage, top trace, and the output of the harmonic mixer, bottom trace, showing birdie markers at 10MHz intervals from 0Hz-200MHz.

As viewed on the oscilloscope, the fit is not quite perfect, though this does not show too clearly in the diagram.

However, as the trace is expanded from the 20MHz/div shown, by means of coarse sweep width control  $S_5$  to say 1MHz/div, any deviation from perfect linearity becomes more noticeable. For this, fine sweep width  $R_{62}$  is wound fully clockwise.

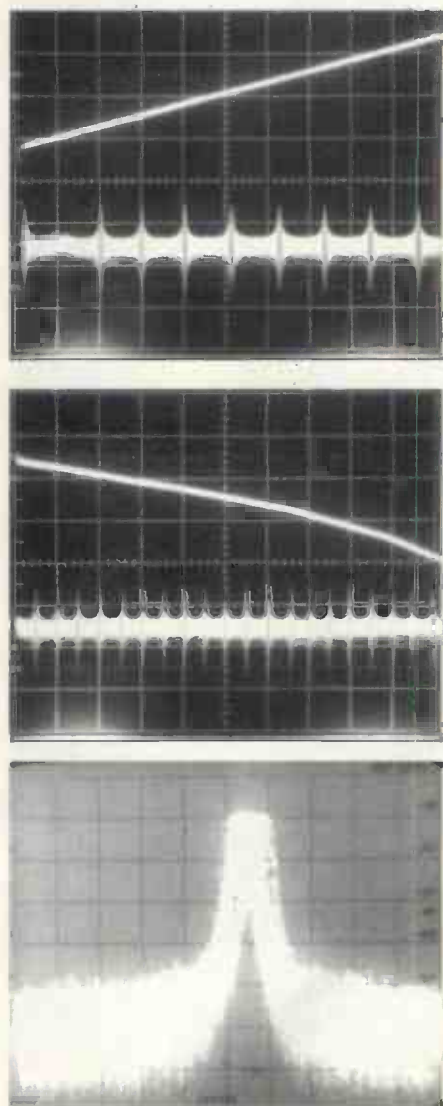


Fig. 5a) Sweep voltage (upper trace) and birdie markers output from the harmonic mixer (lower trace) as frequency sweeps from 10MHz through zero to –80MHz. 5b) Linearised sweep from 0 to 200MHz. 5c) Output in 'cw' mode, ref.level +10dBm, 10dB/div. vertical, centre frequency 100MHz, 5kHz/div. horizontal, I.F. bandwidth 1kHz.

It is therefore worthwhile taking the extra trouble to use all five breakpoints. Resistors  $R_{40-43}$  set positions of the individual breakpoints, while  $R_{46}$  permits adjustment of where the first breakpoint occurs. Resistors  $R_{28-31}$  set the strength of each breakpoint. These were  $5M\Omega$  potentiometers during the setting up, being replaced with the nearest E12 values when linearity had been achieved. A final tweaking of the positions of the breakpoints was then carried out.

Owing to the non-linear gain of  $IC_{10}$ , following linearisation, if its non-inverting input were returned to  $-7.5V$ , its output would swing further in the negative-going direction than in the positive when a sweep voltage was applied. To counter this, it is returned to a

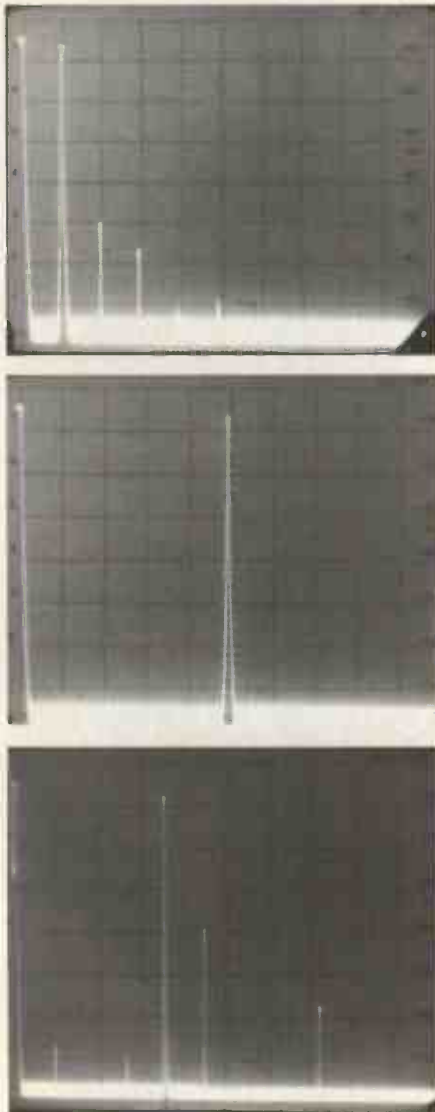


Fig. 6a) Showing the sweeper main output, in CW mode, set to 20MHz. Note the low level of the second and third harmonics. (Span 0-200MHz, ref.level +10dBm, 10dB/div. vertical.), b) As a), but output set to 98MHz. c) Main output at 180MHz, showing spurious responses, see text. Settings as a) except span 0-500MHz.

somewhat lower voltage, set by  $R_{28}$ . Whatever sweep setting is selected, the sweep may be turned momentarily by means of the check centre frequency switch  $S_2$  – a biased toggle.

Again, due to the non-linearity of  $IC_{10}$ , the 'centre frequency' does not correspond to midscreen on the oscilloscope display, but to a point about two divisions to the right of this. As the sweep width is reduced, the display will expand off each side of the screen, points to the right of 'centre frequency' moving to the right, points to the left moving leftwards.

**Switching functions**

Switch  $S_1$  provides a choice of sweep on – position 2 – or sweep off, and serves two purposes. In the off position,  $C_{13}$  is switched into circuit, preventing any ac noise on the vco tune line reaching the varactor – provided 'ext fm ln' is set to off at  $S_4$ . This reduces linewidth of the rf output substantially, Fig. 5c).

Towards the end of the six seconds exposure required by the oscilloscope camera the frequency starts to wander up by a few kilohertz. Apart from this it can be seen that the linewidth is not greatly in excess of the analyser's 1kHz intermediate-frequency bandwidth, although noise 'shoulders' appear at only some 40dB below the output. For this, a centre frequency of 100MHz, 5kHz/div horizontal with a reference level of +10dBm, 10dB/div vertical were used.

Broadband noise floor is about  $-70dB$ , compare Fig. 5c) with Fig. 8a), which covers a 1MHz span, as against a 50kHz span.

Note that to use cw mode, it is necessary to set 'fine display width'  $R_{62}$  to minimum and  $S_5$  to position 11. This is because in the 'sweep-off' position,  $S_1$  causes  $IC_{8C}$ 's output to rise to +15V, which would set the vco to maximum frequency.

The other use for  $S_1$  is when using a dso in 'roll' mode as the display. Setting  $S_1$  to 'sweep off', i.e. cw mode, sets the vco to maximum frequency and the trigger-out level positive. The digital storage oscilloscope may now be set to 'roll' mode, with positive triggering.

On returning  $S_1$  to the sweep position, output will sweep down to minimum frequency and then back up to maximum. At this point the trigger output goes positive again, halting the dso acquisition. If a suitable sweep speed was selected with  $R_{55}$  and  $S_3$ , to match the dso's time/div in 'roll' mode, the screen will display the result of one complete frequency sweep from minimum to maximum.

The linewidth or close-in noise of the instrument is much worse than that of a signal generator, but this is normally the case with sweepers, or any other bfo type instrument. In other respects, performance is very good.

**Performance at rf**

Figure 6a) shows output at 20MHz with a span of 0-200MHz, ref.level +10dBm, 10dB/div. From this, you can see that the second-harmonic is over 40dB down and the

third-harmonic nearly 50dB down on the output, which is around +3dBm. Even at 100MHz, Fig. 6b), second-harmonic is nearly 40dB down, while higher harmonics are of course out of band. Level flatness of the main output is within  $\pm 1dB$  over 2-200MHz.

These photographs were actually taken with the unit in 'cw' mode, but the harmonic performance is of course identical in sweep mode.

Another important criterion of any signal generator, including sweepers, is the level of spurious outputs. These are an unfortunate fact of life, wherever two or more oscillators and a mixer are involved. They can be explained with the aid of Fig. 6c), which covers 0-500MHz, ref. level +10dBm, 10dB/div. vertically.

Output at 180MHz is the main output at a level of +3dBm nominal. The output at 360MHz is its second-harmonic, which is still considerable, despite being in the stopband of the post mixer low-pass filter. The three other outputs are spurious responses, or spuri.

To simplify the explanation, assume the fixed oscillator is at 400MHz, so that its second harmonic is at 800MHz. To produce a 0-200MHz output, the vco must cover 400-600MHz. As the vco frequency approaches 600MHz, it will beat with the second harmonic of the fixed oscillator. This beat frequency falls to 200MHz as the vfo frequency reaches 600MHz.

This is a 'two/one' response, i.e. twice the fixed-oscillator frequency, ffo, minus the vco frequency. The other two/one response,  $2 \times vco - ffo$  is out of band at all times. For this reason, the fixed frequency is set very slightly higher than 400MHz. In the prototype, measurement showed it to be 406.6MHz.

With the aid of a frequency meter and spectrum analyser, this is easily deduced from the measured frequency  $f_x$  of the wanted output. Here, it coincides with the two/one response so  $vco - ffo$  and  $2 \times ffo - vco$  both equal  $f_x$ . This means that the two/one response never goes below 206.6MHz, i.e. it remains out of band at all times.

The two smallest outputs are higher order spurious responses; fortunately such responses get smaller the higher the order. The output at 133.4MHz is a three/four response, as can be deduced from the fact that it moves 30MHz for every 10MHz that the wanted output moves. As a result, it must involve the third harmonic of the vco.

Given the main output is 180MHz, the third harmonic of the vco will be  $3(180+406.6)$ , which is 1759.8MHz. The fourth harmonic of the fixed oscillator is 1626.4MHz, so the 133.4MHz output – which is over 60dB below the main output – is the  $3 \times vco - 4 \times ffo$  response. As the main output is tuned up or down, this spurious response moves in the same direction. Eventually, as the frequency is reduced, it disappears out of band below 0Hz, never to return.

The other spurious response, at 46.6MHz, moves at twice the rate of the main output,

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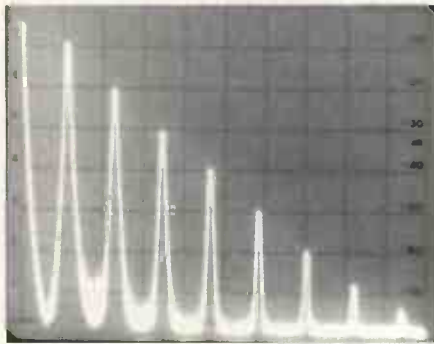
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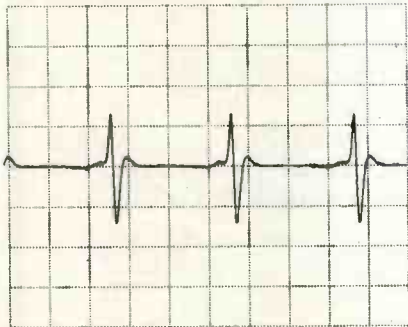


Fig. 7a). Main output of the sweeper in 'cw' mode, set to 600kHz. ref. level +10dBm, 10dB/div., span 0-5MHz. b) Waveform of the main output of the sweeper in 'cw' mode, set to 65kHz. The two oscillators run at the same frequency except when slipping a cycle every 16 $\mu$ s.

and in the opposite direction. Similar reasoning to the above identifies it as a two/three response, namely  $3 \times f_{fo} - 2 \times v_{co}$ . Since it travels in the opposite direction from the main output, their frequencies can coincide: this happens at a frequency of 135MHz.

A spurious output that can occur very close to an expected response can be most embarrassing, but fortunately its level is around 60dB down on the main output.

**Low-frequency performance**

Another criterion of a good sweeper is how low a frequency it can be tuned to before the two oscillators begin to affect each other, eventually locking up to the same frequency. Figure 7a) shows the main output set to 600kHz with a reference level of +10dBm, 10dB/div vertical scaling and a span of 0 to 5MHz.

Note the numerous harmonics, the second being only 10dB below the main output. This is due to the two oscillators affecting each other, as a result of the inadequate isolation between them.

One oscillator tends to keep in step with the other until the disturbance from its undisturbed phase becomes excessive, when it suddenly slips a cycle. This is shown in the time domain in Fig. 7b), where the frequency has been reduced still further to about 65kHz. At this frequency the power in the fundamental was

greatly reduced, and a host of harmonics up to very high orders present. Forgetting the frequency for the moment, electronic organ buffs will recognise the waveform as similar to that of a trumpet stop. Any attempt to tune the two oscillators closer together simply resulted in their locking up to the same frequency.

Figure 7 results were taken during the development of the rf unit, before all of the screening was fitted. In the finished instrument, lock-up did not occur until the offset was below 25kHz, or a mere 125 parts per million of the frequency of the two oscillators. This attests to the efficacy of the screening arrangements, the rf to local-oscillator port isolation of the AD831 mixer and the reverse isolation of the MARx series amplifiers. As a result, the sweeper is usable down to 1MHz, even at 500kHz, the harmonics are still over 25dB down, though the output level has fallen to just over 0dBm, due to the size of the various coupling capacitors.

**Modulating the sweep**

Figure 8a) is an example of the sweeper output when used in the external fm input mode with the fm input switch set to 'narrow'. The centre frequency is 100MHz and the horizontal scale 100kHz/div. Reference level and vertical scale are +10dBm and 10dB/div, as usual. It shows the output frequency modulated at 1.5kHz with a peak deviation of  $\pm 75$ kHz, the maximum deviation in the broadcast fm standard.

Individual fm sidebands are visible but blurred. This is due to the lack of an exact relationship between modulating frequency, maximum deviation and sweep repetition frequency of the spectrum analyser, over the exposure time required by the oscilloscope camera.

Figure 8b) is another example of the instrument in use, this time in 'sweep' mode. The top trace shows the discriminator output of a portable transistor radio - an ITT KB Junior Super am/fm model dating from the 1970s. The oscilloscope settings were 0.2V/div and 2ms/div. The sweeper was set to sweep 3MHz during 20ms. No direct connection between the set and the sweeper was necessary, stray pick-up sufficing.

Resultant output is shown on the upper trace. The discriminator characteristic is about the width one would expect for a mono receiver, but the peaks are slightly asymmetrical and the central portion of the characteristic is not quite a linear as it might be - clearly after so many years, the set is in need of realignment. The lower trace shows the effect of excessive sweep speed.

The fm broadcast standard allows for a  $\pm 75$ kHz deviation at a maximum modulating frequency of 15kHz, and this corresponds to a maximum rate of change of frequency of 7139Hz/ $\mu$ s.

In the lower trace of Fig. b), the 3MHz sweep width has been increased to 75MHz, and the oscilloscope timebase speed to

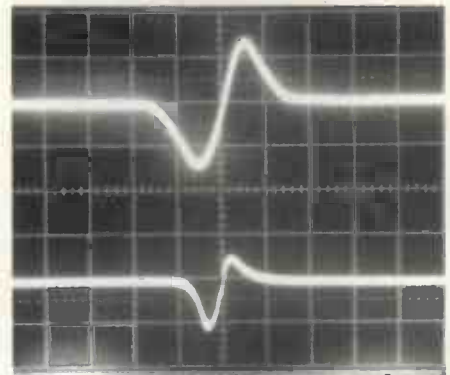
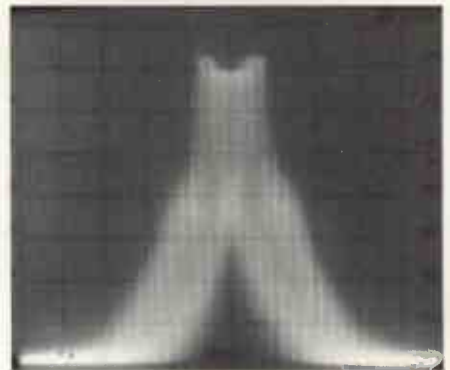


Fig. 8a) Main output of the sweeper in fm mode, showing modulation at 1.5kHz with a peak deviation of 75kHz. ref. level +10dBm, 10dB/div., centre frequency 100MHz, 100kHz/div. b) Output of the discriminator circuit of a transistor radio, upper trace (0.2V/div., 2ms/div.) with a suitable sweep rate. Distorted output, lower trace, when sweep speed is excessive (0.2V/div., 200 $\mu$ s/div.)

200 $\mu$ s/div. The rate of change of frequency is now over 9.3kHz/ $\mu$ s, or over ten times the maximum specified rate. The result is that the discriminator output is grossly distorted and reduced in amplitude. This demonstrates that when using a sweeper, the sweep rate selected must be appropriate to the bandwidth of the circuit under test.

There are many other uses for a sweeper. Used with a return loss bridge, for example, it performs much the same job as a scalar network analyser. I hope to be exploring some of these in a future article. ■

**Reference**

1. Hickman, I, 'CFBOs, delivering speed at any gain?', EW+WW Jan. 1993, pp 78-80, reproduced in the 'Analog Circuits Cookbook', Butterworth-Heinemann 1995, ISBN 0 7506 2002 1.



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# Probing for switching losses

**Current probes can be expensive. Developed for analysing switching losses in power mosfets, this alternative solution, designed by A Durrant and G Whitfield, costs next to nothing.**

**H**ere we will show that it is possible to design an ac current probe small enough to fit around the leg of a power mosfet at very low cost. Our design has the advantage that the layout of the circuit under test is not disrupted when the probe is inserted.

### Defining the problem

Power supply designers are forever under pressure to increase efficiency. There are many types of losses associated with switched mode power supplies. When dealing with voltages in the order of 200V, switching losses in mosfets are a major source of dissipation. Designers find that any successful technique for speeding up switching edges should be incorporated into their designs. Circuit layout is particularly important in order to minimise series inductance. Poor layout can cause dangerous voltage spikes.

The unit we developed achieves 20ns edges when switching 200V. Plots in Fig. 1, show the similarity between a measurement made with a Tektronix P6021 probe, and the low cost alternative outlined here. Peak current is in the order of 16.5A. This speed was achieved by very careful layout together with a low-impedance drive circuit.

To measure switching losses it was necessary to insert an ac current probe in series with one of the switching transistors. The only way

to do this was to lift the drain connection to the pcb, and add 5cm of wire to accommodate the Tektronix probe. Unfortunately extra inductance caused by the wire produced large voltage spikes on the drain of the transistor. Had the supply voltage been turned up to 200V, it would almost certainly have destroyed the transistor.

### A probing solution

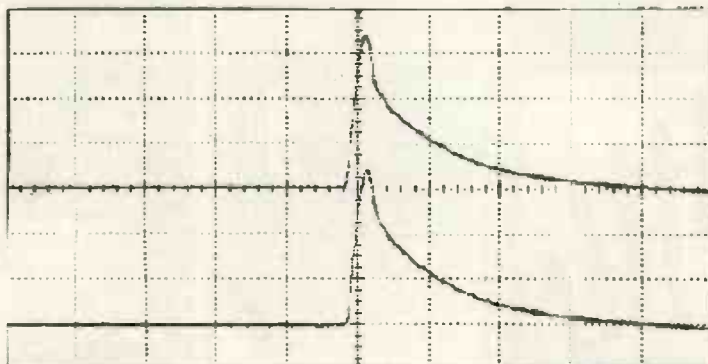
I decided to make the current probe small enough to fit around the leg of the transistor so as not to disturb the layout. This allowed a comparison to be made between the probe and its Tektronix counterpart to ensure that it was performing correctly.

The initial design, Figs 2, 3, used a toroidal ferrite core with an outside diameter of 13mm, an inside diameter of 6mm, and a thickness of 3.5mm, wound with 25 turns. The transistor leg passes through the centre of the core.

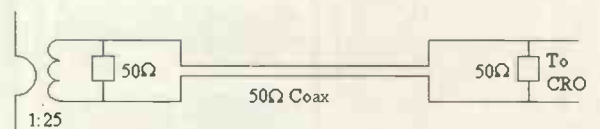
Clearly it was important that proper impedance matching was considered. Two metres of 50Ω coax was used which was terminated with 50Ω at each end. At the oscilloscope end, a simple 50Ω termination was used, while at the toroidal core end, two 100Ω resistors were soldered close to the core.

Current passing through the transistor leg is divided by 25 due to the turns ratio and applied across 25Ω – 50Ω at the current probe

A R Durrant Bsc is with the School of Science, Engineering & Computing, Reading College of Arts and Technology and Dr G R Whitfield is at the Dept of Cybernetics, University of Reading.



**Fig. 1. (left) Comparison between measurements made via Tektronix P6021, top trace, and the low-cost alternative, bottom trace, show little difference in performance with scaling at 100ns/cm versus 5A/cm.**



**Fig. 2. Winding the toroid as prescribed and placing the semiconductor leg through it results in a 1:25 transformer which, in parallel with the 50Ω resistor, feeds the coaxial cable to the oscilloscope.**

end, in parallel with the 50Ω line impedance. Therefore a current of 1A passing through the transistor leg would develop a voltage of 1V at the oscilloscope input. In order to minimise inter-winding capacitance, the 25 turns are wound in one layer.

Characteristics of the toroidal core used were relatively unimportant. Calculations were done to ensure the probe would not saturate. The probe would be unlikely to be subjected to more than 50A, and the switching time would not be greater than 100ns. Since 1A passing through the probe would develop 1V on the secondary winding, then the same could be said for 50A. The peak flux density may be calculated.

$$E = N \frac{d\phi}{dt} = NA \frac{dB}{dt} \therefore$$

$$dB = \frac{E \cdot dt}{N \cdot A} = \frac{50 \times 100 \times 10^{-9}}{25 \times 3.5 \times 3.5 \times 10^{-6}}$$

$$= 16.3mT.$$

Clearly there was no chance of saturation.

Inductance of the secondary winding was 680mH. With 50V developed across the secondary winding, for 100ns, the magnetising current would be,

$$di = \frac{Vdt}{L} = \frac{50 \times 100 \times 10^{-9}}{680 \times 10^{-6}} = 7.3mA.$$

If a conservative 0.25T was used as a maximum before saturation took place, then the maximum volt-second product may be calculated,

$$E \cdot dt = N \cdot A \cdot dB$$

$$= 25 \times 3.5 \times 3.5 \times 10^{-6} \times 0.25$$

$$= 76.5\mu Vs.$$

Since 1V output represents 1A measured then the maximum current second product that the probe could cope with is 76.5mAs.

**Down sizing**

A smaller version of the probe is even more useful where a very tight layout has been used. The probe shown in Fig. 4 was made using 25 turns of 0.2mm wire wound on a 4mm core, namely a Siemens B64290-A36x1. Core dimensions are outside diameter 4mm, inside diameter 2.4mm, and height 1.6mm. The core has an A<sub>1</sub> value of 13nH/N<sup>2</sup> and is made from K1 material, which is reasonably linear up to 0.25T.

Maximum current-second product is therefore 8mAs. Again the probe was unlikely to be subjected to more than 50A, so provided the edge of the waveform is less than 8mAs/50=160ns, then it would not saturate. Inductance of the secondary winding is 13nH×25<sup>2</sup>=8.1mH, so the magnetising current when measuring 50A at 160ns would be 0.98A – about a 2% error in the measurement.

The probe was used to measure current spikes in a mosfet full bridge circuit, and in

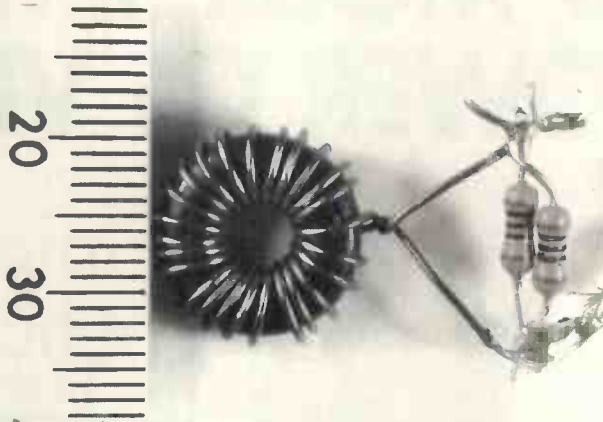


Fig. 3. Prototype probe functioned well, but was a bit on the large side for some applications.

particular to investigate the effect of turning off the built-in diode within the mosfet. Figure 5 shows the circuit arrangement. Duty cycle is arranged so that current in the load moves from left to right, such that when Tr<sub>1</sub> turns off, the output current is maintained by the built-in diode of Tr<sub>3</sub>. When Tr<sub>1</sub> turns back on again, a large current spike appears in both Tr<sub>1</sub> and Tr<sub>3</sub> in an effort to turn off the built-in diode of Tr<sub>3</sub>.

Figure 6a shows a large spike of current in Tr<sub>1</sub> and Fig. 6b shows a similar current spike in Tr<sub>3</sub>. Spikes in both transistors are similar in magnitude, indicating that the built-in diode requires considerable energy to turn off. With a load current and forward diode current of less than 1A, the built-in diode is causing a current to flow of some 16A. Large current spikes caused by the built-in diode can be destructive to mosfets<sup>1</sup>

**Cost considerations**

At the time of writing, the cost of a P6021 current probe is £683. The cost of the home-made current probe is virtually zero. We do not suggest that this design equals the performance or convenience of the Tektronix probe – the P6021 is rated at 500mAs. However we do suggest that if switching edges are to be examined, then a probe can be quickly and easily constructed to give accurate results for a negligible cost – without disrupting circuit layout.

**Further reading**

Pelly, BR, 'The do's and don'ts of using power Hexfets', International Rectifier, Application note 936A, 1983.

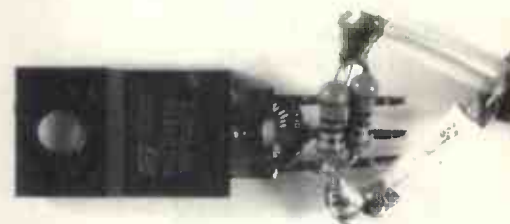


Fig. 4. Redesigning the arrangement using a 4mm core rendered the probe even less disruptive.

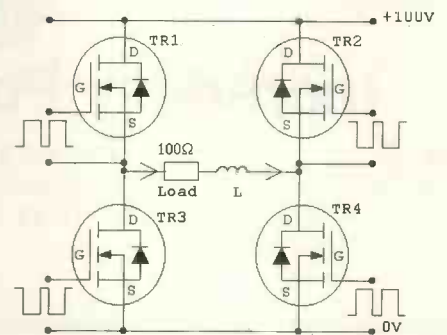


Fig. 5. Power mosfet switching circuit used to test performance of the probe. Duty cycle is arranged so that current in the load moves from left to right.

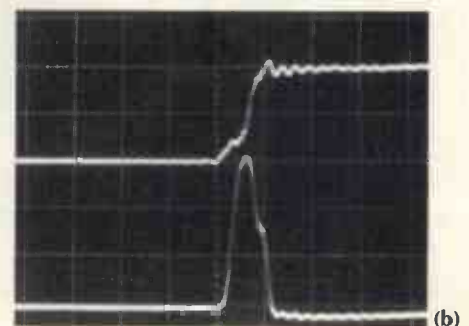
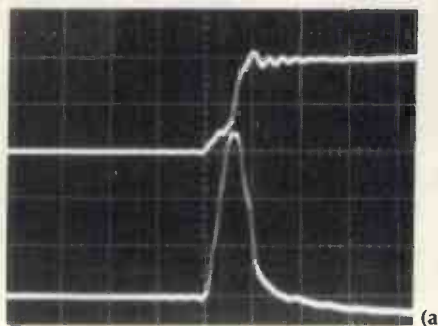


Fig. 6. In a) a large spike of current in Tr<sub>1</sub> of Fig. 5 is evident. Shot b) shows a similar spike in Tr<sub>3</sub>.

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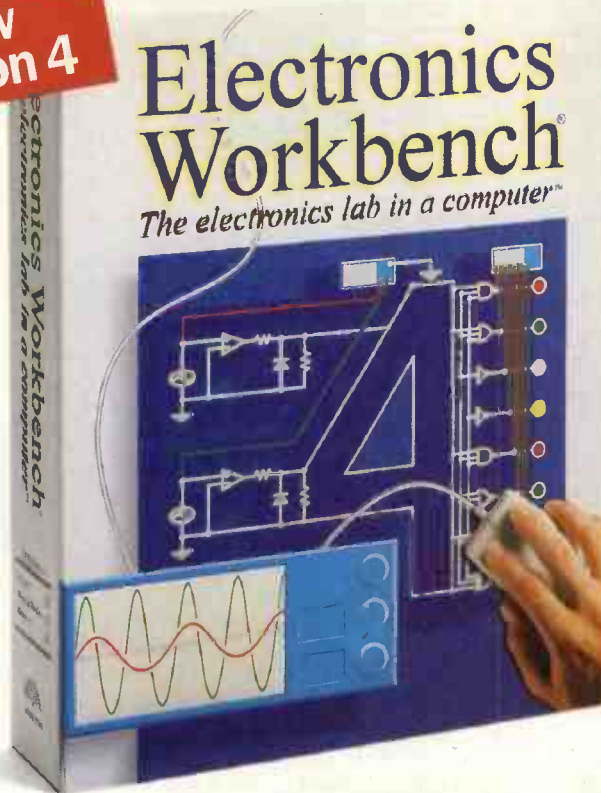
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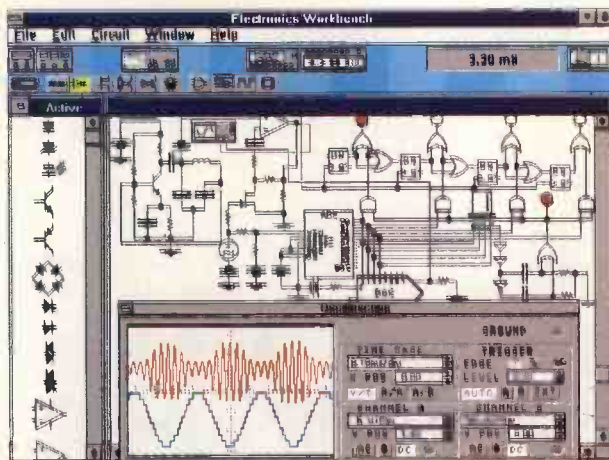
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# Ultra-fast amplifier

This work deals with maximising speed in voltage-feedback amplifier architectures. Nevertheless, most of the arguments and the techniques presented here also apply to current-feedback amplifiers.

When an amplifier is overdriven by a large signal pulse or square wave with a fast enough rise/fall time, the output is generally not capable of following the input immediately, Fig. 1. Output slews at a finite rate, determined by internal currents and capacitances. Note that amplifiers incorporating inductors are not considered in this discussion.

Slew-rate is a term used to designate the maximum rate of change of the output voltage  $V_o$ , formally written as,

$$SR = \left. \frac{dV_o}{dt} \right|_{\max} \quad (1)$$

and expressed usually in volts-per-microsecond, i.e. V/ $\mu$ s.

Generally, the concept of slew rate limiting can be extended to all nodes of the amplifier. In this context, the maximum rate of change of the voltage  $V_n$  at the generic node  $n$  is given by,

$$SR(n) = \left. \frac{dV_n}{dt} \right|_{\max} = \frac{I_n}{C_n} \quad (2)$$

where  $C_n$  is the equivalent total node capacitance to ground and  $I_n$  is the maximum positive or negative current available at the same node for charging or discharging  $C_n$ .

As shown in Fig. 1, positive  $SR(+)$  and negative  $SR(-)$  slew rates are usually different, mainly due to the fact that positive/negative currents available at the node are different.

It is important to stress that a non-linear phenomenon is being considered here. During slewing, the amplifier is overdriven at some point in its circuitry, therefore the output is unable to follow the input.

In Fig. 1, the sinusoidal signal superposed to the step input voltage is not present at the output during slewing times  $T_r$  and  $T_f$ . This phenomenon is the major cause of transient intermodulation distortion in audio amplifiers.

## Slew rate and power bandwidth

Power bandwidth is defined as the maximum

frequency at which full output swing – i.e. close to the supply rails – can be obtained without severe distortion.

Consider a sinusoidal voltage  $V_o(t)$ , which equals  $V_p \sin(\omega t)$ . The rate of change requested to the output is,

$$dV_o(t)/(dt)|_{\max} = \omega V_p \cos(\omega t) \leq 2\pi f V_p.$$

Knowing maximum slew rate of the amplifier, the highest frequency,  $f_{\max}$ , or power bandwidth, that can be linearly reproduced at full swing can be calculated from,

$$f_{\max} = SR/(2\pi V_p) \quad (3)$$

Note that the above relationship between slew rate and power bandwidth applies only to the 'natural slew rate or naturally-enhanced slew rate' of amplifiers. This applies for frequencies where loop gain is sufficiently high and linear. 'Natural' in this instance implies use of linear operation.

Equation 3 is not valid for amplifiers where unnaturally high slew rates are achieved by forcing mechanisms based on non-linear operation. One example is shown in Fig. 2.

Transistors  $Tr_{1,4}$  are connected to form a voltage follower. The natural slew rate is given by  $NSR = I_A/C_A$ . If  $I_A$  is 0.5mA and  $C_A$  is 10pF then  $NSR$  is 50V/ $\mu$ s. Capacitance  $C_A$  is the total equivalent node capacitance seen by sources  $I_A$ .

Transistors  $Tr_{5,6}$  are incorporated to form a fast parallel path which bypasses  $Tr_{1,2}$  when the follower is overdriven. Activated when  $|V_{in} - V_{out}| \geq 2V_{be(on)} \approx 1.4V$ , this parallel path produces forced large-signal slew rates, FSRs, up to 1000V/ $\mu$ s. Due to its high non-linear operation and associated poor high-frequency performance, it does not contribute as effectively as expected to extend linear power bandwidth in accordance with equation (3).

Response of the circuit to a square wave, assuming zero signal source resistance, is also qualitatively shown in Fig. 2. From point a to b and d to e, where  $|V_{in} - V_{out}| \geq 1.4V$ , the circuit responds with its forced slew rate. In all other parts, circuit response is controlled by its natural slew rate.

**Giovanni Stochino demonstrates new techniques for driving up the speed of high-performance 50 $\Omega$  power amplifiers for precision test and measurement.**

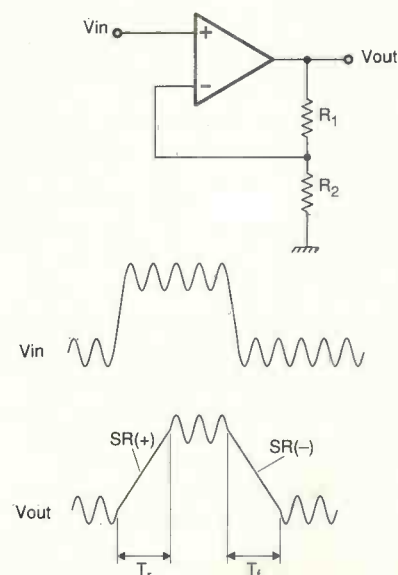


Fig. 1. The phenomenon of slew rate and transient intermodulation in a feedback amplifier.

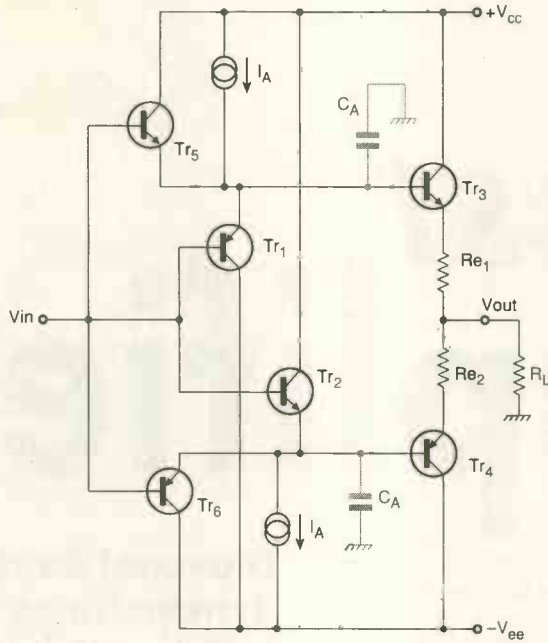
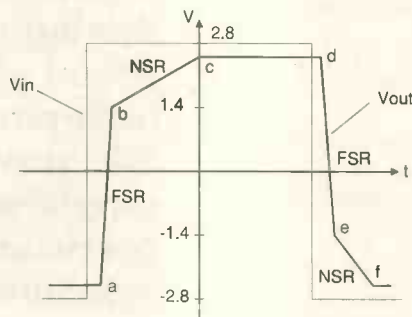


Fig. 2. Slew rate can be enhanced with non-linear mechanisms. Linear power bandwidth, however, is governed by natural slew rate.



**Slew rate in conventional amplifiers**

Slew rate performance of a generic amplifier configuration can be generally worked out by simply inspecting its circuit. The problem of slew rate in conventional integrated op-amps has been treated comprehensively by J.E. Solomon<sup>1</sup>. Since structure of audio and instrumentation amplifiers is similar to that of IC op-amps, most of Solomon's considerations are still valid.

Therefore we will assume, as a good example of a typical conventional amplifier architecture, the configuration of Fig. 3. This is very similar to those used by Solomon<sup>1</sup> for his generic op-amp and by Self<sup>2</sup> for his audio power amplifier. It includes the following three elements,

- A transconductance input stage. This stage provides an output current  $I_a$  proportional, to some degree, to the voltage difference  $V_1 - V_2$ .
- A transimpedance intermediate gain stage, where output voltage  $V_b$  is proportional to input current  $I_a$ .
- A voltage follower output stage - providing power gain needed to drive the load.

Main base-collector transistor capacitances, which contribute to define the node capacitances, are indicated. Capacitance  $C_A$  is from the current generator  $I_A$ . Capacitance  $C_B$  is the

sum of the output capacitances of  $I_B$ , the equivalent collector capacitance of  $Tr_4$  ( $>C_4$  depending on the value of  $R_3$ ) and base-collector capacitance of  $Tr_5$  and  $Tr_6$ .

In Fig. 3,  $Z_b = R_b / C_b$  models the input impedance of the output stage. Analogously,  $C_{5,6}$  result from of output capacitance of  $I_C$  sources and the base-collector capacitance of  $Tr_{7,8}$ , respectively. External capacitor  $C_{dom}$  sets the dominant pole and provides a suitable degree of dynamic stability or phase margin when feedback is applied to the overall amplifier through  $R_{F1}$  and  $R_{F2}$ .

So, open loop gain of the overall feedback amplifier, assuming  $k$  is unity, is given by,

$$A_{OL}(f) = g_m / ([2\pi f C_{dom}] G_o)$$

where  $g_m = I_a / (V_1 - V_2)$  is the transconductance of the input stage, and  $G_o = [R_{F1} + R_{F2}] / R_{F1}$  is the low-frequency closed-loop gain.

The amplifier's stable unity-gain frequency  $f_T$ , with its suitable phase margin, depends on the device technology and circuit architecture used. This can be written as,

$$f_T = g_m / [2\pi C_{dom} G_o] \tag{4}$$

In the lower part of Fig. 3, dynamic range of each stage is represented. This should help comprehension of major slew rate limiting

mechanisms operating within the amplifier.

Consider, for example, that values for the parameters in Fig. 3 are as follows:

- $I_A = 1\text{mA}$        $I_B = 2\text{mA}$        $I_C = 10\text{mA}$
- $C_A = 5\text{pF}$        $C_B = 40\text{pF}$        $C_{dom} = 60\text{pF}$
- $C_{5,6} = 20\text{pF}$        $C_{1,4} = 5\text{pF}$        $R_e = 100\Omega$
- $V_{CC} = V_{EE} = 20\text{V}$ .

It is easy to evaluate the  $SR$  at each node via equation 2. At node  $n_1$ ,  $SR(n_1) = I_a / C_A$ , which is  $200\text{V}/\mu\text{s}$ , due to the limited current available to charge  $C_A$  during the positive input pulse edge. No slew limitation occurs during the negative pulse edge.

At node  $n_3$ , where  $\Delta V_3 \approx 0$ , current in  $C_3$  is virtually null, maximum available current is still limited to  $I_A$  for both positive and negative values. Current needed to charge or discharge  $C_A$  should be subtracted from or added to  $I_A$ .

When slew rate of the amplifier is much lower than  $SR(n_1)$ , this fact can be neglected. You can assume current available for charging and discharging  $C_{dom}$ , at node  $n_3$  is  $I_A$ . At node  $n_4$  which  $C_{dom}$  is connected across, current available to drive  $C_{dom} + C_B$  is limited solely during the positive transition by biasing current  $I_B$ . No limitation practically exists for discharging since  $\beta_3 \beta_4 I_A$  is nominally as high as several amperes.

**Non-slewing amplifier performance results**

With an ambient temperature of  $28^\circ\text{C}$ , a  $50\Omega$  load and  $Tr_{14,16}$  connected to the  $40\text{V}$  supply rails, the final fast power amplifier, Fig. 9, performed as follows.

- Supply current approximately  $\pm 45\text{mA}$
- Input offset voltage\* approximately  $3.5\text{mV}$
- Small signal  $-3\text{dB}$  bandwidth ( $G_o = 15.7$ )  $\approx 5\text{MHz}$ .
- Overload recovery time  $\leq 20\text{ns}$  for  $V_{in} \leq 12\text{V}_{pk}$
- Harmonic distortion, bandwidth  $80\text{kHz}$ :  
 Thd  $\leq 0.004\%$ ,  $f = 2\text{kHz}$ ,  $V_{out} \leq 50\text{V}_{(pk-pk)}$ ,  $G_o = 15.7$   
 Thd  $\leq 0.010\%$ ,  $f = 20\text{kHz}$ ,  $V_{out} \leq 50\text{V}_{(pk-pk)}$ ,  $G_o = 15.7$
- Maximum achievable output rate of change†,  
 a)  $\pm 400\text{V}/\mu\text{s}$  for  $G_o = 15.7$   
 b)  $\pm 600\text{V}/\mu\text{s}$  for  $G_o = 7$   
 c)  $\pm 1000\text{V}/\mu\text{s}$  with  $1\mu\text{F}$  or more across  $R$ .

\* With un-matched transistors and resistors.  
 † Slew rate is mainly limited by available voltage level of the signal source, about  $12\text{V}_{pk}$ . The technique used in case c), useful to 'unnaturally' enhance slew rate, is recommended for pulse amplifier applications only and with low impedance signal sources.

Detailed distortion+noise figures versus output level for the full circuit of Fig. 9 are as follows. Measurement bandwidth is  $80\text{kHz}$ .

$V_{out}(V_{pp})$ $R_L = 50\Omega$	Thd+n a) $f = 2\text{kHz}$	Thd+n b) $f = 20\text{kHz}$
3	0.0061%	0.0068%
6	0.0023%	0.0031%
12	0.0024%	0.0042%
24	0.0031%	0.0065%
34	0.0033%	0.0072%
50	0.0041%	0.0110%

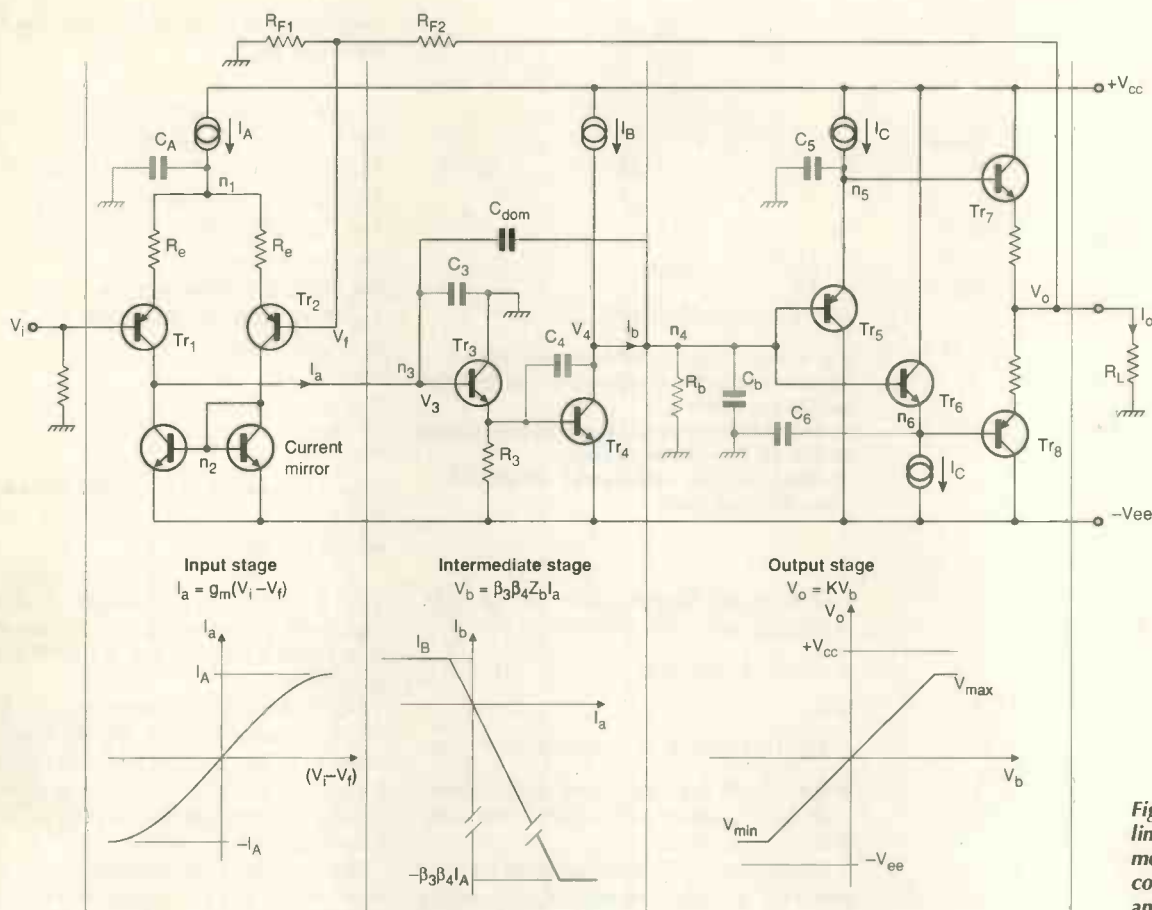


Fig. 3. Slew rate limiting mechanisms in a conventional amplifier.

There are two different slew rate limitation causes at the nodes across which  $C_{dom}$  is connected, namely,

$$SR(n_4)' = I_A / C_{dom} \tag{5}$$

$$SR(n_4)'' = I_B / (C_{dom} + C_B) \tag{6}$$

The lowest of the above values is the actual SR at node  $n_4$ . Considering again the circuit values seen in the above example, equations 5 and 6 yield 16V/ $\mu$ s and 20V/ $\mu$ s, respectively – the actual slew rate value at node  $n_4$  is 16V/ $\mu$ s. Similarly, you can calculate slew rate at nodes  $n_5$  and  $n_6$  as  $SR(n_5) = SR(n_6)$ , which is 500V/ $\mu$ s, assuming base currents of  $Tr_7$  and  $Tr_8$  are much lower than  $I_C$ .

This example shows that in conventional amplifier architectures, the most critical nodes, from a slew rate point of view, are the input and output of the intermediate transimpedance gain stage. This is due to limited currents from the bias sources available at both nodes. Signal voltage appearing at node  $n_4$  is the highest within the amplifier. As a result, full power bandwidth expected will be  $f_{max} = SR(n_4) / (2\pi V_p)$ . Assuming, for simplicity,  $V_p$  equals  $V_{cc}$ , this provides a maximum frequency of 127kHz.

Slew rate limiting sources associated with the problem of driving the frequency compensation capacitance,  $C_{dom}$  are normally defined as ‘first order’ slew rate limitations. In this context all others are defined ‘second order’.

**Improving slew rate**

Slew rate can be improved in the amplifier configuration shown in Fig. 3 by increasing bias current of the input stage and the intermediate stage if required. However, if the overall amplifier stability degree or phase margin has to be preserved, such current changes must be made without affecting unity gain frequency  $f_T$ .

Equation 4 requires that the  $g_m$  of the input stage is kept constant by increasing the  $R_e$  degeneration resistors.<sup>2</sup> Unfortunately, this approach leads to increased power consumption and the deterioration of amplifier input parameters – noise, offset voltage, current, and relevant drifts.

In some applications these side effects can be tolerated, but in others they are intolerable. As a result, the technique of increasing the bias currents for proportionally improving slew rate is only of limited use.

If higher slew rate figures are necessary in specific applications – instrumentation, electrostatic transducer and deflection, test equipment, pulse amplifiers, etc – other solutions which do not compromise stability, noise and offset performance of the amplifier, have to be found.

In conventional amplifier configurations, the primary cause of slew limiting is, as mentioned earlier, the limited current available to drive the frequency compensation capacitance  $C_{dom}$ . An effective way to overcome this limitation is to make use of a class-AB differential amplifier in the input stage. This amplifi-

er should have a low bias current in order to assure good offset and noise performance as well as phase margin. When driven by large and fast input signals, it should also provide high output currents required for the rapid charging/discharging of  $C_{dom}$ .

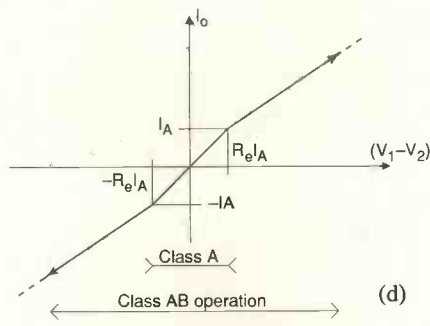
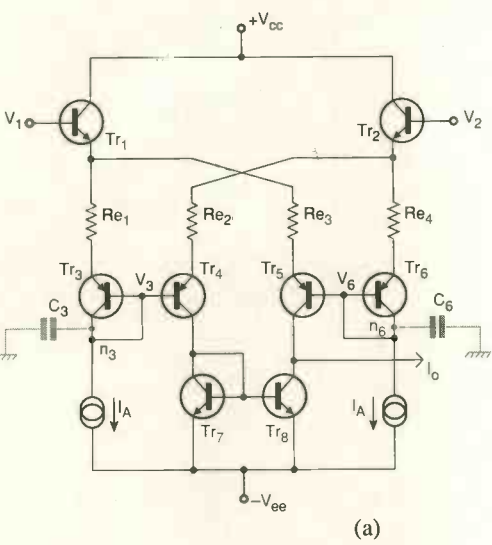
An input stage with such characteristics was first designed by W. E. Hearn<sup>3</sup> for Signetics and used in the monolithic op amps SE/NE530 and 531. These provided slew rate improvement of more than an order of magnitude compared to conventional counterparts – even with worse input parameters. Its basic scheme is shown in Fig. 4a). Figure. 4b) illustrates a different implementation of the same principle. Assuming all transistors are well matched and  $R_{e1-4}$  and  $R_e$  are equal then bias current is the same for all transistors  $Tr_{3-6}$  and equal to  $I_A$ , in virtue of circuit symmetry. However, output current  $I_o$  is no longer limited by bias current. In fact dynamic range of  $I_o$  is largely increased and is always controlled by differential input  $V_1 - V_2$ , as indicated in Fig. 4d).

Operating area can divide into two regions. In region 1, the input stage operates in class-A and the input-output relationship, assuming  $R_e I_A > V_T = kT/q \approx 25mV$ , can be written as,

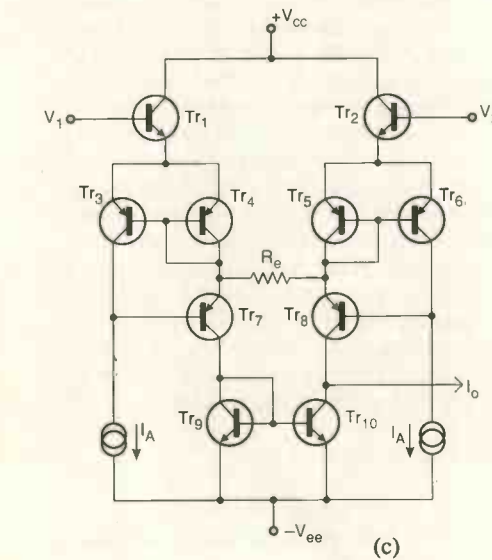
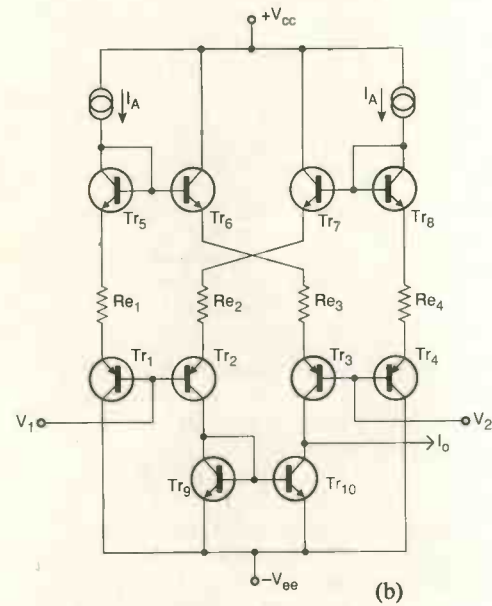
$$I_o \approx 2(V_1 - V_2) / R_e \tag{7}$$

A factor of 2 is present, due to the fact that both left and right halves contribute to the output current.

In region 2, the input stage operates in Class-B. Only half of the circuit contributes



**Fig. 4. Input stages capable of class-AB operation, effectively enhance the natural slew rate of amplifiers:**  
 a) and b) Hearn's input stage implementations.  
 c) Van De Plassche input stage  
 d) Output current versus input voltage of a class-AB input stage.



current to the output, either left or right, depending on the sign of difference  $V_1 - V_2$ , so,

$$|I_o| = (V_1 - V_2) / R_e \leq \beta_p I_A \quad \text{for Fig. 4a) (8)}$$

and,

$$|I_o| = (V_1 - V_2) / R_e \leq \beta_n I_A \quad \text{for Fig. 4b) (9)}$$

Figures 5a,b) show the corresponding current path for a positive and negative step input voltage, respectively.

Implementation depicted in Fig. 4b) is often preferred in integrated technologies, where usually  $\beta_n \gg \beta_p$ . A notable increase in output current and consequently in slew rate can be achieved with the same bias current.

If, for example,  $R_e$  is 100Ω,  $I_A$  is 0.5mA,  $\beta_n$  is 100 and  $\beta_p$  is 50, then output currents as high as 50 and 25mA can be expected for input voltage  $(V_1 - V_2) \leq 5V$ . This translates into potential slew rates of about 800 and 400V/μs, respectively, for a  $C_{dom}$  of 60pF.

It is evident that other sources of slew rate limitation can prove dominant and need to be taken into account. In the input stage, the only remaining factor which can limit slew rate and power bandwidth is associated with capacitances  $C_3$  and  $C_6$  at nodes  $n_3$  and  $n_6$  respectively, Fig. 4a). Assuming  $C_3 = C_6 = C$ , the maximum speed at which  $V_3$  and  $V_6$  can follow  $V_1$  and  $V_2$  respectively is fixed at  $SR = I_A / C$ , which is approximately 100V/μs for an  $I_A$  of 0.5mA and  $C$  of 5pF.

Other SR limitation sources can be identified in the rest of the amplifier by inspecting Fig. 3 and considering first-order slew rate limitations expressed by equations 5 and 6. It should be apparent now that the lowest of the two is equation 6. For an  $I_B$  of 4mA and other values as those of the above example, equation 6 yields 100V/μs and 40V/μs for a  $C_{dom}$  of 0pF and  $C_{dom}$  of 60pF, respectively.

When compared with the maximum slew rate achievable at all other nodes, the above results show that slew rate performance of amplifiers using a Hearn input stage are dominated by limited current available at the output node of the transimpedance intermediate stage. This is the case even when there is no need for external frequency compensation, i.e.

**Component list for the non-slewing amplifier prototype, Fig. 9.**

- $Tr_1, Tr_2, Tr_{10}, Tr_{24} = 2N 5551$
- $Tr_3, Tr_4, Tr_7, Tr_{22} = 2N 5401$
- $Tr_5, Tr_6, Tr_{12}, Tr_{17}, Tr_{21}, Tr_{23} = 2N 2907A$
- $Tr_8, Tr_9, Tr_{11}, Tr_{15}, Tr_{25}, Tr_{26} = 2N 2222A$
- $Tr_{13}, Tr_{16} = 2N 3019$
- $Tr_{14}, Tr_{18} = 2N 4033$
- $Tr_{19} = MJE 15030$  (on heat sink)
- $Tr_{20} = MJE 15031$  (on heat sink)
- All diodes IN 4448
- $R_e = 51.1$
- $R = 330$
- $C = 10pF$
- $R_{F1} = 100$
- $R_{F2} = 1k47$  (function of the closed loop gain  $G_o$ )
- All resistors 1%

when  $C_{dom}$  is 0pF. It is also the case when  $C_{dom}$  is placed between node  $n_3$  and the output as in Hearn's op-amp<sup>3</sup> and in Cherry's audio amplifiers.

Figure. 4c) shows a voltage-to-current converter designed by Van De Plassche<sup>4</sup>. By virtue of its capability of class-AB operation, I have successfully used it as a slew rate enhancing input stage in amplifiers. To my knowledge, this is a first.

Operation of the converter is similar to the Hearn stage. However, it features better input characteristics and greatly improved full range linearity - its  $g_m$ , at  $2/R_e$ , is virtually constant throughout the class-A region of Fig. 4d). These improvements are due to the fact that transistors  $Tr_{1-6}$  are forced through local feedback to operate at a virtual constant-emitter current equal to  $2I_A$  for  $Tr_{1,2}$ , and to  $I_A$  for the remainder.

In my opinion, both Hearn and Van De Plassche input stages can be profitably used in audio power amplifiers using Self's blameless basic architecture. I have used them successfully in 50-80W power amplifiers for instrumentation and test equipment. These feature slew rates up to  $\pm 150V/\mu s$  and power bandwidth in good agreement with equation (3).

**Designing for high slew rate**

Intrinsically high slew rate amplifier architectures - whether current or voltage-feedback - are based on the extension of the concept of class-AB operation to all stages of the amplifier. To the best of my knowledge, the first practical application of this concept, aimed at achieving unusually high slew rates in voltage-feedback amplifiers, was reported for the first time in 1980<sup>5</sup>. Related patent applications appeared in 1977.

In the above publication, a discrete true operational amplifier with a slew rate in excess of  $\pm 400V/\mu s$ , a unity-gain frequency of only 3MHz and a consumption of 4.5mA was presented and discussed. It was stable even in voltage follower configurations. Its basic architecture, with minor differences in the input and output stage, is shown in Fig. 6.

An outstanding feature of this amplifier con-



figuration is that, for the first time all first-order slew rate limitations have been completely removed. At the time of its publication, this feature was a brand new idea, yet the same basic principles are used nowadays in some commercial high slew rate voltage-feedback op-amps<sup>6,7</sup>.

In part, this result has been achieved by developing a complementary-symmetry intermediate gain stage with very high performance. This push-pull class-AB gain stage comprises transistors  $Tr_{9-14}$ . In addition, I have redesigned the input stage, improving its ability to drive the new intermediate stage. It is fully symmetrical and is push-pull class-AB in operation.

This approach greatly enhances the slew-rate capability of the amplifier – even with low standby supply current. The output stage is of the same standard as the diamond voltage-follower used in Fig. 3. An evolution of Hearn's input stage consists of two mirrored diamond voltage followers. They feature a higher degree of flexibility and extended symmetrical input common-mode voltage range.

Transistors  $Tr_{1,2}$  and  $Tr_{7,8}$  perform the level shifting function needed to bias the other transistors. Bias current of transistors  $Tr_{3-6}$  is also controlled mainly by current source  $I_A$ .

Emitter resistors  $R_{2,5}$ , with values in the range 5-20Ω, make no significant contribution to bias current. As a result,  $I_{Q1-8}$  approximately equals  $I_A$ . Instead these resistors are used in discrete implementations for bias stabilisation. Here, input transistors are required to provide high currents during transients and they often suffer from self heating.

Input stage transconductance is controlled via cross coupling resistor  $R_1$ , so that  $g_m \approx 1/R_1$ . Due to its full symmetry, it features better precision and common mode rejection than Hearn's stage. Large-signal performance is similar to that depicted in Fig. 4d), and relationships 8 and 9 are still valid.

In practical implementations, product  $R_1 G_0$  – the closed-loop gain/input-stage-transconductance ratio – should be kept constant and such that a stable  $f_T$  is achieved. This results in best performance in terms of closed-loop bandwidth, slew rate and precision, i.e. low offset and noise.

Figure 7 shows basic details of the new intermediate stage. It can be considered a complementary symmetry implementation of the Wilson current mirror. However, the driving approach is substantially different.

Both upper and lower current pairs  $I_L$  and  $I_R$ , coming from the input stage and suitably directed by current mirrors CM1 and CM2, are employed to drive the upper and lower halves of the intermediate stage respectively. As a result, the base of  $Tr_{11}$ , node A, is driven by  $I_L - I_R$ , while the base of  $Tr_{14}$ , node B, is driven by the antiphase current  $-(I_L - I_R)$ . Due to the balanced driving scheme, quiescent current  $I_Q$  is not influenced by the input stage. It is set by sources  $I_{B1}$  and/or  $I_{B2}$ , and is equal to  $I_{B1} + I_{B2}$ .

Due to the feedback loop inside the Wilson circuit structure, quiescent current is stable

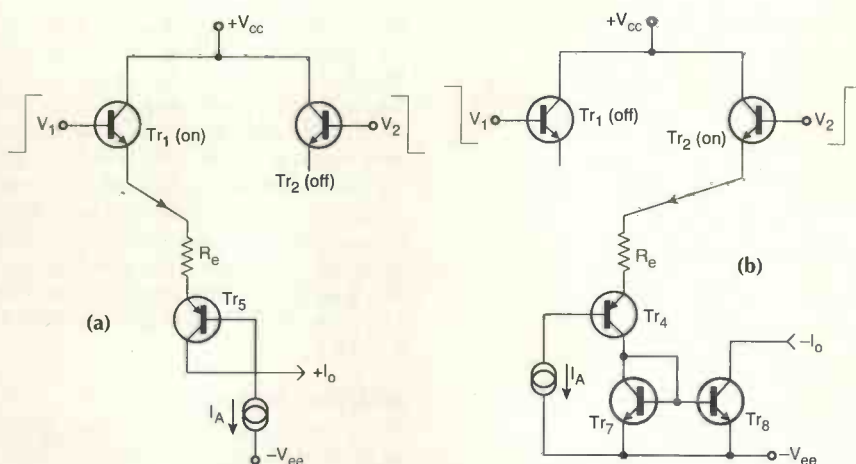


Fig. 5. Current paths in a class-AB input stage under large signal operation.

under all operating conditions – even in the case of severe overload conditions. For the same reason, current gain  $I_b/(I_L - I_R)$  is low but steady over the full dynamic range. As a result, this renders frequency response of the overall amplifier flat and stable.

The simultaneous availability of both wide range currents  $I_L$  and  $I_R$  at nodes A and B is essential for a fast commutation of transistors  $Tr_{11}$  and  $Tr_{14}$ . This helps reduce overload recovery time as well as the dangerous simultaneous conduction of these transistors. Capacitances  $C_{11}$ ,  $C_{14}$ ,  $C_b$  and  $C_{dom}$  can be

charged and discharged rapidly without exposing any intermediate gain stage transistors to risk of overdrive phenomenon.

The remaining still active slew rate limiting mechanisms are second order,

$$SR(n_1) = SR(n_2) = SR(n_3) = SR(n_4) = SR1 = I_A / C_A,$$

assuming  $C_{1-4}$  and  $C_A$  are all equal and,

$$SR(n_5) = SR(n_6) = SR2 = I_C / C_C,$$

assuming  $C_{5,6}$  and  $C_C$  are equal. Considering the example used earlier, i.e.  $I_A = 0.5mA$ ,  $I_C = 10mA$ ,  $C_A = 5pF$  and  $C_C = 20pF$ , results are

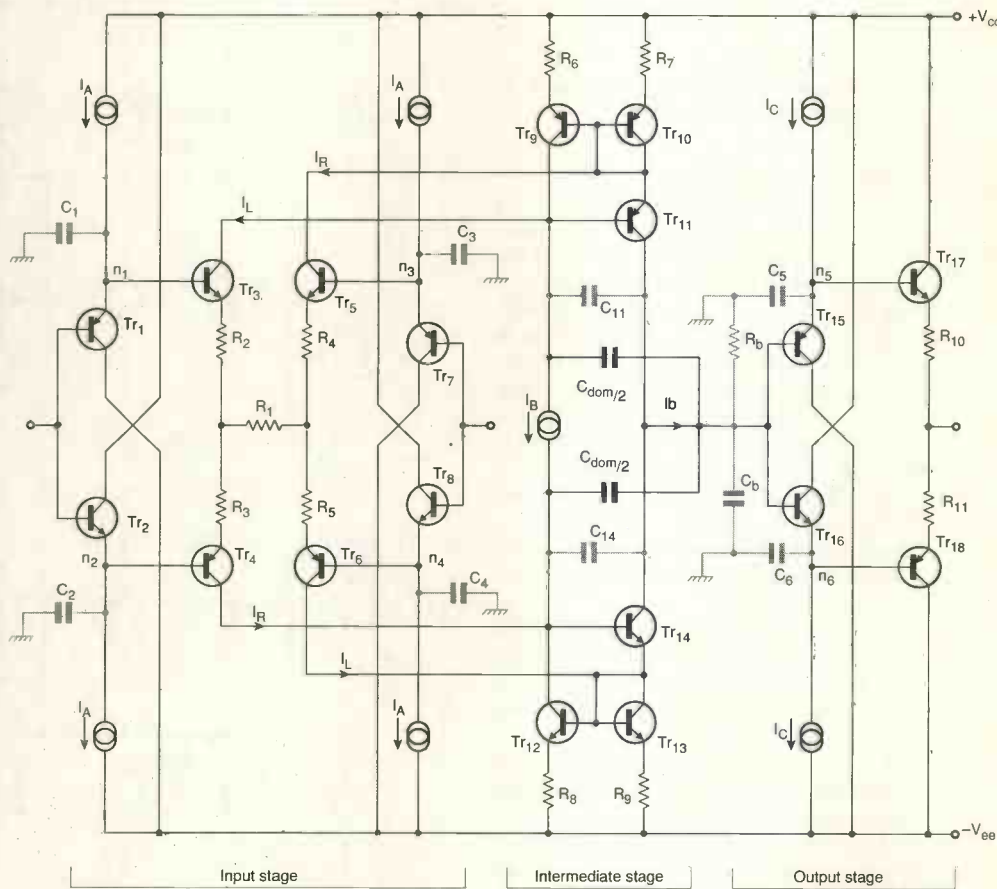


Fig. 6. High slew rate voltage feedback amplifier architecture.

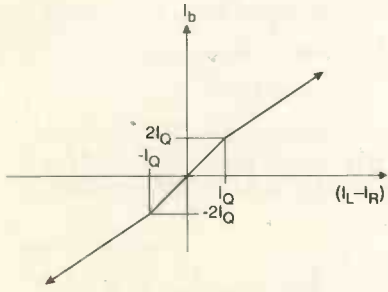
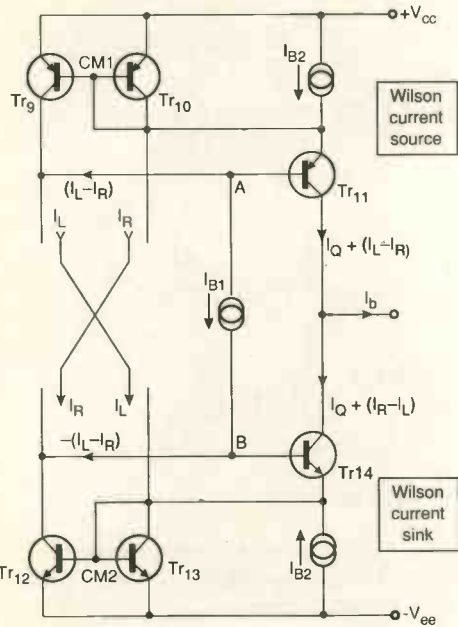


Fig. 7. Details of the class-AB intermediate gain stage and its transfer curve.

100V/ $\mu$ s for SR1 and 500V/ $\mu$ s for SR2. For closed loop inverting gains of 1 or greater and non-inverting gains 5 or greater, SR2 is the dominant slew rate factor for the overall amplifier. Second-order slew rate limiting factors dominant in high slew rate architectures, are counteracted to a large extent in the high slew rate circuit structure of Fig. 8. Instead of being returned to the supply rails, as in Fig. 6, collector currents of  $Tr_{1,2}$ , and  $Tr_{7,8}$  are channelled with the appropriate phase to the inputs of the intermediate stage.

When the input stage is driven by a fast positive step voltage, as in Fig. 8, nodes  $n_1$  and  $n_4$  experience slew rate limitation SR1. An analogous reasoning applies for a fast negative step voltage.

Transistors  $Tr_1$  and  $Tr_8$  are momentarily off, whereas  $Tr_2$  and  $Tr_7$  are forced to conduct the peak currents needed to drive node capacitances  $C_2$  and  $C_3$ , respectively – and they are capable of doing so.

These extra currents, help to drive  $Tr_{11}$  and  $Tr_{14}$  considerably during the transients. In this way, exceptionally high positive and negative slew rates can be obtained at node  $n_5$ . Slew rate of the output stage is enhanced by driving the current sources  $I_C$  by the intermediate stage, Fig. 8.

Improved performance of the intermediate

stage is due to diodes  $D_{1,4}$ . Under all operating conditions they keep all current mirror transistors away from the saturation region, where gain is lower and speed impaired. Forcing the stage to provide peak slew currents results in considerable benefits in recovery time and distortion. Slew rates in excess of 1500V/ $\mu$ s have been achieved in some discrete op-amp prototypes with total supply current of 20-30mA. These use circuit architecture of Fig. 8, and common bipolar transistors, namely an npn 2N2222A and pnp 2N2907A with  $V_{cc}$  and  $V_{ee}$  at 20V.

Adapting for audio

When suitably adapted, principles embodied in the configurations of Fig. 6 and 8 can be successfully incorporated into the design of very high slew rate audio power amplifiers. Slew rates up to 500V/ $\mu$ s can be achieved with the use of modern power devices – bjts or mosfets. However, useful power bandwidth will not be the theoretical one expressed by equation 3. This is mainly due to limited high-frequency gain and power handling ability of most audio power devices.

In the past ten years I have assembled several prototype power amplifiers designed in accordance with these principles, mainly for test and instrumentation purposes. Although distortion was no better than 0.01% within the audio frequency range, slew rates were exceptional, at  $\pm 200$  to  $\pm 400$ V/ $\mu$ s.

A non-slewing amplifier?

High slew-rate voltage-feedback amplifier architectures discussed so far are not without their disadvantages. When compared with conventional configuration, Fig. 3, high slew-rate architectures have worse offset and noise input characteristics. This is often due to the complexity of the input stage.

Low current gain of the intermediate stage reduces low-frequency open-loop gain. This sets a limit on the maximum feedback factor that can be applied to the amplifier and, hence, on the precision and distortion performance when used in instrumentation and audio applications.

Some of these drawbacks can be overcome via the amplifier of Fig. 9. It features the noise and offset performance of conventional amplifiers, yet retains the speed capabilities of high slew-rate architectures. In this design, second as well as first-order slew rate limitations are virtually eliminated. This makes the definition 'non-slewing amplifier' applicable.

In non-slewing architecture, none of the stages of the amplifier experience strong overdrive non-linearities due to too fast and/or too large an input signal. When charging or discharging node capacitance, as well as for driving active devices, current available at all nodes of the circuit is more or less linearly controlled by the differential input voltage – even during the sharpest and highest of transients. This is true, provided that the input sig-

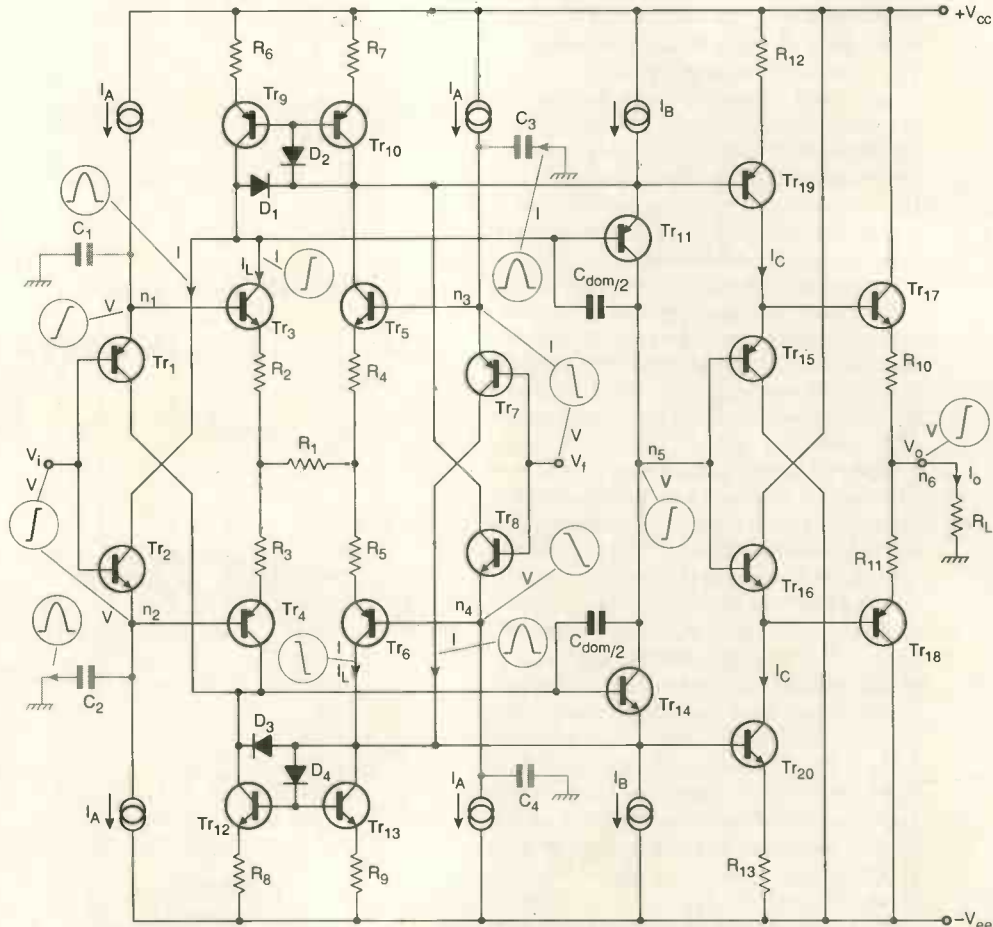


Fig. 8. New improved architecture for high slew rate voltage feedback amplifiers.

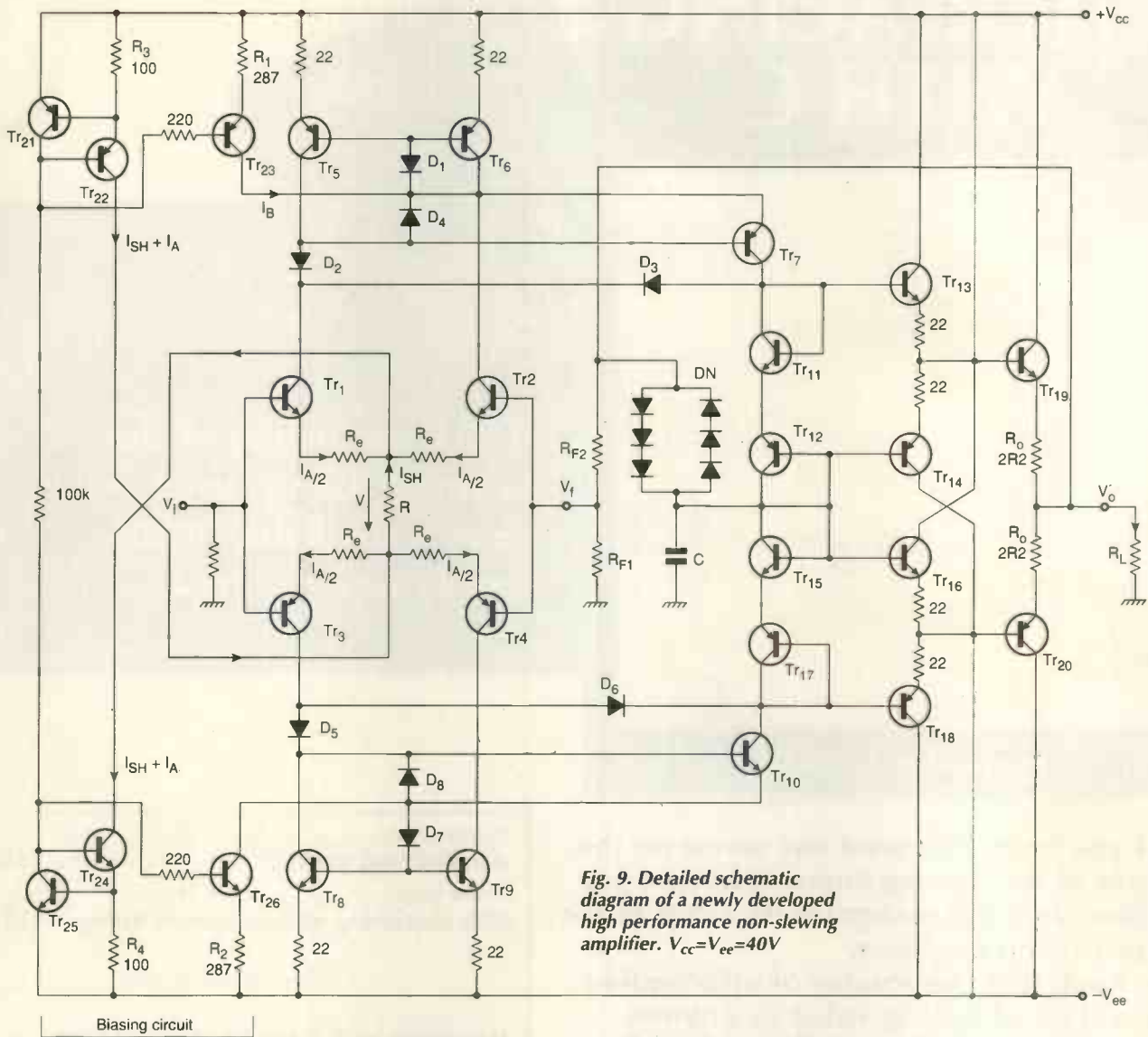


Fig. 9. Detailed schematic diagram of a newly developed high performance non-slewing amplifier.  $V_{cc}=V_{ee}=40V$

nal is within the common-mode input voltage range.

The intermediate gain stage is substantially unchanged. To reduce the recovery time of the whole amplifier further and increase reliability, diodes  $D_{2,3}$  and  $D_{5,6}$  have been added to prevent transistors  $Tr_{7,10}$  from deep saturation.

Input circuitry has been simplified in order to improve noise and offset performance and to avoid the second order slewing mechanisms present in Fig. 6 and 8.

Biasing problems associated with high slew-rate configurations, have been solved by moving and simplifying level shifting circuitry into the emitter loops. Only two current sources,  $I_{SH}+I_A$ , and resistor  $R$  are now needed for this. Current  $I_{SH}$  is used to produce the level shifting voltage  $[V=RI_{SH}=2V_{be(on)}]$  across  $R$ . Excess current  $I_A$  is forced to flow through the emitters of pairs  $Tr_{1,2}$  and  $Tr_{3,4}$ , fixing the operating point of the input stage.

A great advantage of this solution is that all disturbances associated with biasing current sources, such as noise, imbalances and drift, are seen by the input stage as common mode disturbances. As a result, the disturbances are greatly reduced by the amplifier.

Small-signal transconductance is governed by emitter resistor  $R_e$  as well as  $I_A$ . It can be set as low as needed without interacting significantly with the biasing scheme. This results in increased gain and improved dc precision. Large-signal transconductance is equal to  $1/(R+2R_e)$ .

In order to avoid inherent speed limitations of the diamond voltage follower, a self-biasing complementary symmetry darlington configuration is preferred for the output stage. Moreover, output transistors  $Tr_{19}$  and  $Tr_{20}$  are properly driven, which assures the low push and pull driving resistance necessary for rapid commutation and safe high-frequency operation of the output devices.

Circuitry around  $Tr_{11-14}$  serves as a voltage follower to drive  $Tr_{19}$ , while  $Tr_{15-18}$  do the same job for  $Tr_{20}$ . Transistors  $Tr_{14}$  and  $Tr_{16}$  have their collectors connected as shown in Fig. 9 to increase – when necessary – the input impedance of the output stage. This may be needed for instance in high closed loop gain configurations. In power applications however, it is usually safer to connect them to their respective supply rails.

Quiescent current of the output stage devices

is set, by the  $I_B$  sources and by  $R_o$ , to 30mA for  $Tr_{19}$  and  $Tr_{20}$ , and to a value close to  $I_B$  for all other transistors. Diode network  $DN$  limits output current to approximately 800mA – enough to drive 50Ω at full swing. ■

References

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6. Opa 622, Burr-Brown, data sheets, Dec. 1993
7. Smith, D, Koen, M and Witulski, AF 'Evolution of High Speed Operational Amplifier Architectures', *IEEE J. Solid State Circuits*, Vol. 29, No. 10, Oct. 1994, pp. 1166-1179.

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# GPIB

## testing made simple

*This simple, low cost, GPIB tester is useful both as a diagnostic tool for interface troubleshooting and as a learning aid. Designed by Giorgio Delfitto and Andrea Sambo\*.*

IEEE-488 – also known as GPIB and HPIB – is the best known and most widespread parallel instrumentation interface. Many instruments now include an IEEE-488 interface as standard.

Despite its versatility, IEEE-488 can be problematical because the way the hardware and software interact. When failures occur, this interaction can make troubleshooting difficult.

Controller boards for IEEE-488 are provided with driver software comprising a routine library including diagnostic tools. When the diagnostic tools fail, the tester described here

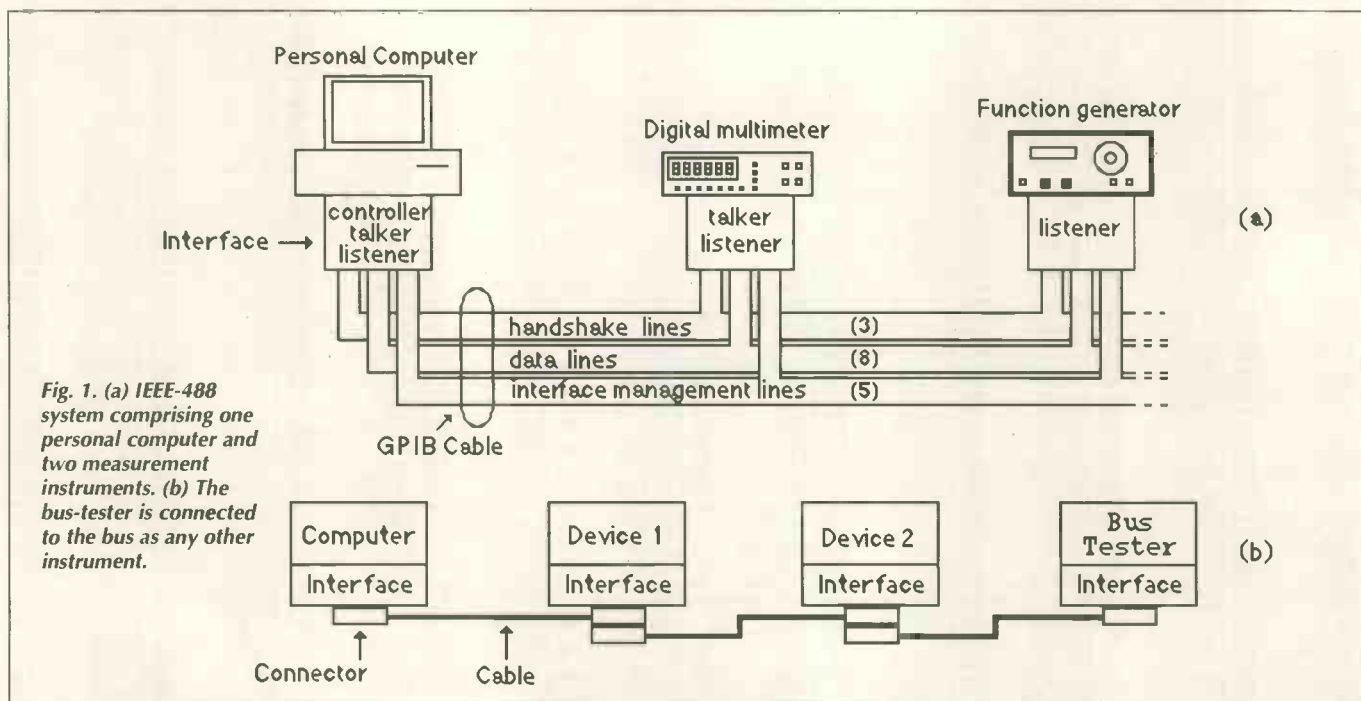
allows the physical evolution of the interface state to be displayed. The tester is useful as a diagnostic aid, for setting up a new instrument and as a learning tool.

Connecting the tester to the computer controlling the interface system allows the routines sending data or messages from the computer to other devices to be tested. This is useful for verifying the transmission protocol. By placing an instrument capable of sending information to the computer on the bus, it is also possible to analyse the reception protocol.

### Communication protocols

Every interface operation occurs in two distinct steps. During the first, where ATN is 1

\*The authors are with the Physics Department of the University of Padua in Italy.



representing command mode, all the devices involved in the communication are addressed, or are sent interface commands. During the second, i.e. data mode, ATN is zero and data transfer takes place.

Note that programming messages, sent to the devices to set the operating condition – device dependent commands – are considered as data and transmitted during the data mode. Library routine **Send** can be used to transmit the programming message to a device. For example, a call to the Basic routine sending a programming string to a function generator is as follows,

**Structure of a Basic command for sending a programming string to a function generator with IEEE-488 interface.**

**CALL Send (Board, Gen, Data\$, Len, TermMod)**

**Board** is an integer variable identifying the controller interface board.

**Gen** is an integer variable storing the generator address.

**Data\$** is a string variable containing the programming message.

**Len** is an integer variable storing the number of characters of **Data\$**.

**TermMod** is an integer variable indicates to the talker – in this case the computer – how to signal end of transmission. Either the EOI line 'End Or Identify' can be asserted, or a particular 'End of Transmission' character can be added to the programming string.

Execution of the **Send** routine lets the bus state evolve following the *IEEE-488.2* transmission protocol, Fig. 2. The controller sets ATN true, i.e. logic low, then sends the computer talker address. For address zero for example, set MTA 0 to  $40_{16}$  or  $01000000_2$ . Next, the interface message UNL, i.e.  $3F_{16}$ , disables all the listeners previously active. Finally, the generator listener address is set. To set MLA 3 to 3 for example, apply  $23_{16}$  or  $00100011_2$ .

Now the controller removes ATN. The talker sends the programming string followed by the termination character, if set, and/or asserting EOI on the last byte of the string. Abbreviations MTA 0, MLA 3, UNL represent interface messages detailed in the standard. 'My talk address' is MTA, 'my listening address' is MLA and UNL tells the device to 'unlisten'

The **Receive** routine can be used to poll an instrument, in the format.

**CALL Receive (Board, Mult, Meas\$, Len, TermMod)**

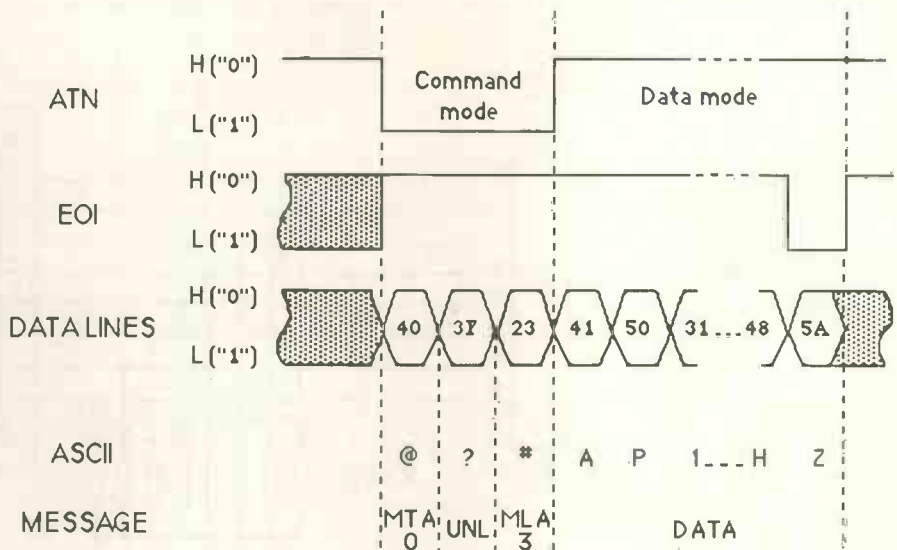


Fig. 2. *IEEE-488.2* transmission protocol. Single bytes values are in hexadecimal form. A possible command string is shown.

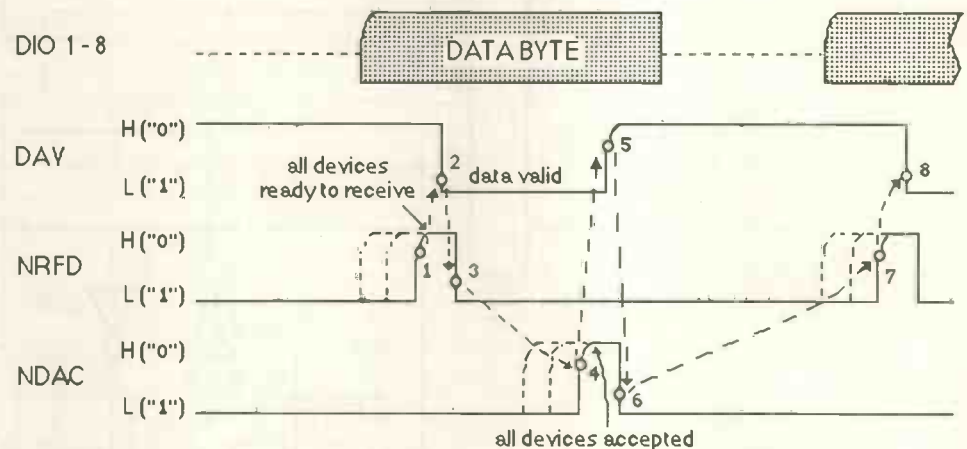


Fig. 3. *IEEE488* uses handshaking so that devices of different speeds can communicate.

Here too the variable **Board** identifies the controller interface board. Variable **Mult** contains the multimeter address while **TermMod** stores, in this case, the termination mode used by the talker – multimeter – when sending data. The **Len** variable specifies the maximum number of characters the multimeter has to read if the termination condition is not detected, for example because of a circuit failure.

After the call of the routine **Receive**, the variable **Meas\$** contains a string representing the result of the measurement. Execution of **Receive** produces the following activity on the bus as established by the following *IEEE-488.2* reception protocol.

The controller sets **ATN** true and sends **UNL**. This disables all the previous listeners.

Next **MLA 0** is set to  $20_{16}$ , which is the computer listener address. The multimeter talker address is sent, and finally, **ATN** is removed.

The computer receives data bytes until the end of message condition is verified;

**The IEEE-488 bus tester**

To understand how the tester works, consider handshake diagram Fig. 3.

Every byte is transferred under control of the handshake lines, named **DAV** (Data Valid), **NRFD** (Not Ready For Data), **NDAC** (Not Data Accepted). Use of these signals is asynchronous, allowing for devices having different response speeds.

An active talker with a byte to send must sample the **NRFD** line. A low level on this line means that one or more listener is not ready to receive data. In this case, the talker must wait until **NRFD** becomes false as shown by point 1 in Fig. 3. In the meantime the listeners keep **NDAC** true to indicate they are not accepting data.

When **NRFD** becomes false, the talker asserts **DAV** to validate the byte it has already put on the data lines, as in point 2 of Fig. 3.

After detecting the falling edge of **DAV**, the listeners immediately assert **NRFD** (point 3), and acquire the data byte (point 4). Obviously,

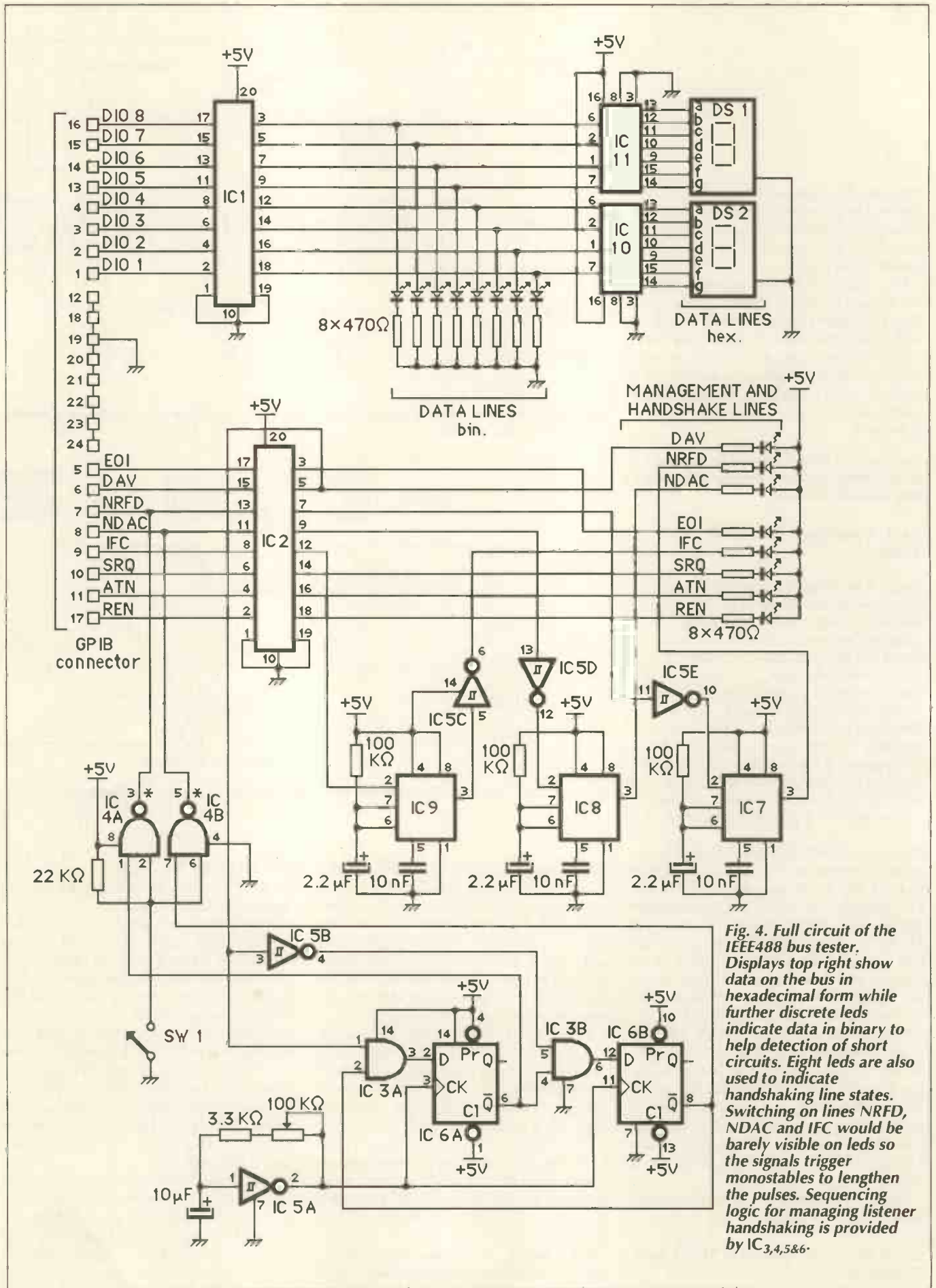


Fig. 4. Full circuit of the IEEE488 bus tester. Displays top right show data on the bus in hexadecimal form while further discrete leds indicate data in binary to help detection of short circuits. Eight leds are also used to indicate handshaking line states. Switching on lines NRFD, NDAC and IFC would be barely visible on leds so the signals trigger monostables to lengthen the pulses. Sequencing logic for managing listener handshaking is provided by IC<sub>3,4,5&6</sub>.



**IEEE-488 overview**

The most important features of the IEEE-488 interface are the bus structure and the ttl negative-logic convention. Open-collector outputs are used to drive the bus, so it is possible to wire-or signals without additional gating.

Figure 1 shows that the bus comprises 24 lines. Eight of the lines are devoted to data transfer, eight are ground connections, detailed in the panel. The remaining eight are control lines grouped as five interface management lines and three hand-shake lines.

Activity on the bus is supervised by a device called the controller, which is usually a personal computer. This device – one of a maximum of 15 – co-ordinates the operation of the whole system via the interface management lines. Communication over the data lines is controlled by hand-shake lines.

When a data transfer takes place, only one device – called the talker – can transmit data whereas one or more devices, known as listeners, can receive them simultaneously.

Each device has its own address, used by the controller to designate the active talker and listeners. There are 31 possible addresses, but each device can have more than one address. Each address is represented by the five least significant bits of a byte (00000 to 11110). The all-ones configuration is mentioned later.

Two other bits, weighted  $2^5$  and  $2^6$ , specify whether a device is a listener or a talker. The possible binary addresses are then x0100000 to x0111110 for the listeners, and x1000000 to x1011110 for the talkers.

Any device can be talker or listener. A digital multimeter, for example, must be able to receive programming data and send the results of the measurement to the computer.

Therefore, in a GPIB communication, active talker and listeners have to be designated before the data can be transferred. To do that the controller sends the addresses over the data lines after asserting the attention line, labelled ATN. Its aim is to inform all the devices that the controller is sending interface messages to the interface circuitry, namely addresses or other commands.

When the attention line is asserted, all the devices must listen to the interface messages. If a device is addressed, it prepares itself for the data transfer that will start when ATN is removed.

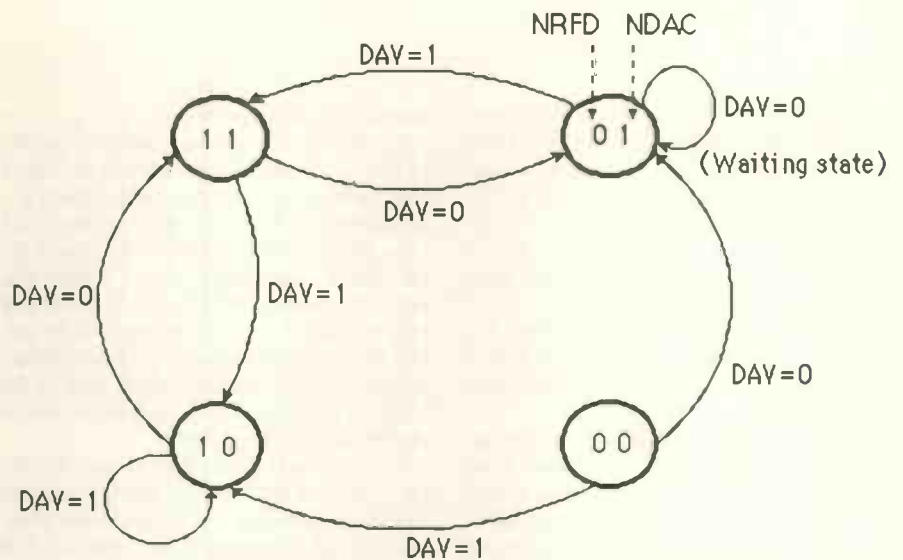


Fig. 5. State diagram of the handshaking logic of Fig. 4. Should the two outputs both be zero on power up, they return to the 'waiting' condition, i.e. NRFD=0 and NDAC=1, within three clock pulses. All these boolean variables are negative logic, i.e. 0V is true.

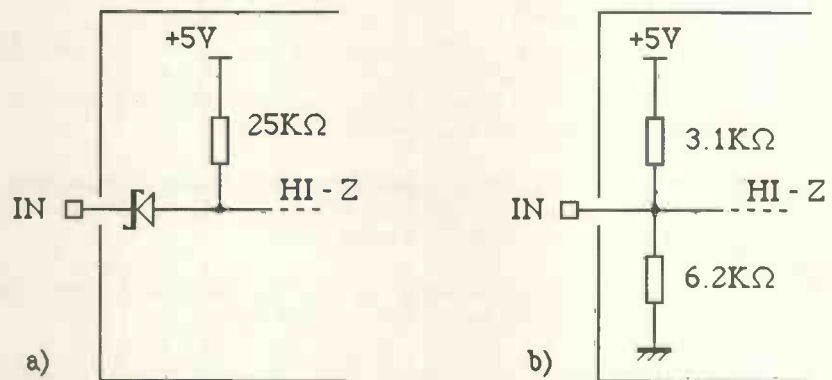


Fig. 6. Comparison between an Is-ttl input circuit, (a) and a typical IEEE-488 input (b).

because of the wired-or connection, the rising edge of this line is set by the slowest device, point 5. Subsequently, the listeners set NDAC to one and NRFD to zero (points 6 and 7), waiting for a new data transfer (point 8).

If one of the devices has a handshaking problem, the system can lock indefinitely. If, for example, a device has NDAC shorted to ground, the talker will wait forever for the rising edge of NDAC shown in point 4 of Fig. 3. To avoid such problems, it is possible to set a timeout by means of the controller board configuration program. When the preset time expires, the controller automatically aborts the i/o operation, activating an error condition.

From the handshake diagram it is clear that the active talker, before modifying the state of DAV, must always wait for NRFD or NDAC going false (see points 1 and 2 or 4 and 5).

As a result, the data transfer rate depends on the speed of the slowest device connected to the bus. This means that, by connecting an always active listener – the tester for example – with adjustable response speed, the system can be made to work very slowly. This allows

the bus state to be displayed in real time.

In order to work so slowly, a long timeout must be set to avoid error conditions. Furthermore, it will be necessary to disable the Automatic Serial Polling function, if present. In this mode, the controller board executes some operations autonomously. Since these operations are outside of the program's control, the bus sequences that result can be difficult to interpret.

**Circuit details**

There are three distinct sections of the tester. In the first, comprising IC<sub>1,10,11</sub>, displays DS<sub>1,2</sub>, show the data lines state, in hexadecimal and in binary notation by means of eight leds. These light when the line is true.

Hexadecimal notation allows rapid decoding of the data, whereas the binary indication can be useful in case of short circuit of a data line to ground or the supply rail. In this case, the corresponding led will be permanently on or off indicating the failure to the operator.

The second section, incorporating of IC<sub>2,7,8,9</sub>, displays the state of the handshake

and management lines via eight further leds.

Signals NRFD, NDAC and IFC can be so fast – even at low transfer rate – as to become almost invisible. For this reason they are not displayed directly: rather they are used as trigger pulses for three one-shot multivibrators that generate long pulses. These are more readily detectable by the human eye.

The third section includes IC<sub>3,4</sub> and IC<sub>6</sub>. These ICs form a sequential circuit capable of managing the listener handshake, whose state diagram is shown in Fig. 5.

The 3.3kΩ resistor, IC<sub>5A</sub>, the 100kΩ potentiometer and the 10μF capacitor make a variable frequency oscillator that clocks the circuit. When SW<sub>1</sub> is open, the handshake circuit is always active with no dependence on ATN. This makes it possible to follow the slowed bus activity both in

data and in command mode.

By closing SW<sub>1</sub>, the open-collector outputs of IC<sub>4A,B</sub> are disabled. The tester no longer contributes to the handshake so the bus activity proceeds as if the tester were disconnected.

An 75452 interface driver was chosen for the NDAC and NRFD output buffer IC<sub>4</sub>. According to the standard, the maximum sink current is 48mA, which is too high for the normal ttl or hc-mos outputs. We have deliberately avoided GPIB transceivers such as the SN75161 or MC3446 since they are expensive and not so readily available.

Inputs of the buffers 74LS240 and 74LS244 (IC<sub>1,2</sub>) present to the bus the equivalent circuit shown in Fig. 6a that can be considered a negligible load compared to the typical GPIB input circuit, Fig. 6b. ■

**Further reading**

**Official documents:**

ANSI/IEEE (New York) Std 488.1 – 1987, *IEEE Standard Digital Interface for Programmable Instrumentation*.

ANSI/IEEE (New York) Std 488.2 – 1987, *IEEE Standard Codes, Formats, Protocols and Common Commands for use with IEEE-488*.

**Handbooks and application notes:**

Hewlett-Packard, *Tutorial description of the Hewlett-Packard Interface Bus* (1983).

National Instruments (Austin TX), *In-depth course on IEEE-488.2 (GPIB)* – (June 1991).

Takeda Riken (Tokyo), *Application note No. 2-1, GP-IB INTERFACE: General Description*.

Texas Instruments (Dallas TX), *Linear and Interface Circuits Applications* (1986).

**IEEE488 bus details**

Front and side views of the GPIB connector are shown here. This type of connector is male on one side, female on the other, allowing cascade connections as shown in Fig. 1b.

Data and handshake lines are detailed in the main text. Management lines are ATN, IFC, REN, EOI, SRQ. Their meaning and principal functions are listed below.

- ATN (Attention). This signal is managed by the controller. Its active state indicates that the controller is sending commands or addresses (interface messages). When not asserted, it says to talker and listeners that the bus is ready to the data transfer.
- IFC (Interface Clear). By asserting this line for a short period of about 100μs the controller can reset the interfaces of all the devices connected to the bus.
- REN (Remote Enable). The controller keeps this line permanently asserted while the interface is working.
- SRQ (Service Request). All devices can use this line to request service from the controller. For example, at the end of the measurement, an instrument can signal that it is ready to be read.
- EOI (End Or Identify). This line is asserted by the active talker while the last byte is sent, to signal the 'end of string' condition. It is used by the controller in a particular service request response mode.

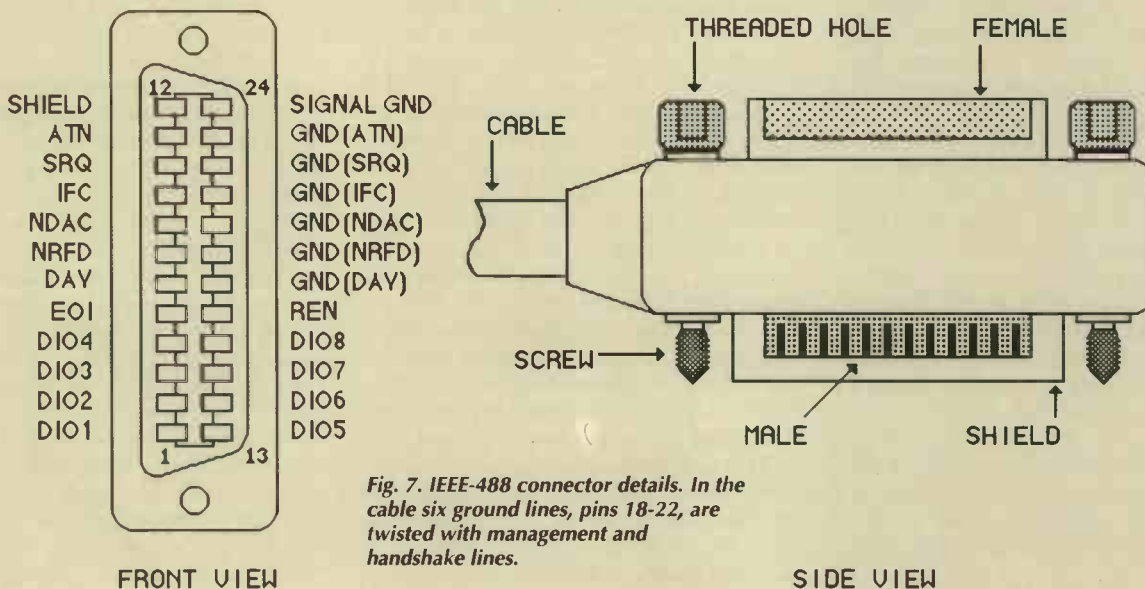


Fig. 7. IEEE-488 connector details. In the cable six ground lines, pins 18-22, are twisted with management and handshake lines.

# Analysing *dc* via SPICE

**Owen Bishop** describes how to get the best from your circuit emulator when performing dc analysis.

At present there are numerous Spice-based circuit simulation packages, most of them based on Spice2 and some on the more recent member of the Spice family, Spice3. In this article I will be working with Spice3, referring where necessary to Spice2 on the few points of difference. References to Spice without a qualifying digit apply equally to Spice2 and Spice3.

*IsSpice4* is a popular implementation of

Spice3, and is used here to illustrate this discussion. Another Spice2-based simulator is *Micro-Cap IV*, which also has some Spice3 capabilities. Naturally, each implementation has its own distinctive enhancements, but the essentials are common to both. Most routines described are also applicable to a wide range of other packages.

The following analysis of a simple RLC circuit, Fig. 1, illustrates the syntax and input/output routines of Spice, using *IsSpice4*. This program is distributed as part of the ICAP/4 package, together with two linked programs, as outlined below. Begin running the ICAP package with a double click on the ICAPS icon. In the Select Project dialogue box, click on Cancel, as this is a new circuit and there is no existing file to select. In the Name New Project box, key in the file name. The next box to appear gives the option of selecting which part of ICAP/4 to use:

- \* Edit Text Files – the *IsEd* program for entering and editing netlists.
- \* Launch *IsSpice* – for simulations.
- \* Launch Scope – for *Intuscope*, an oscilloscope simulator.
- \* Launch SpiceNet – for entering schematic diagrams.

Figure 1 was produced with SpiceNet, but it can be entered as a netlist, using *IsEd*. To enter the netlist, select Edit Text Files and key

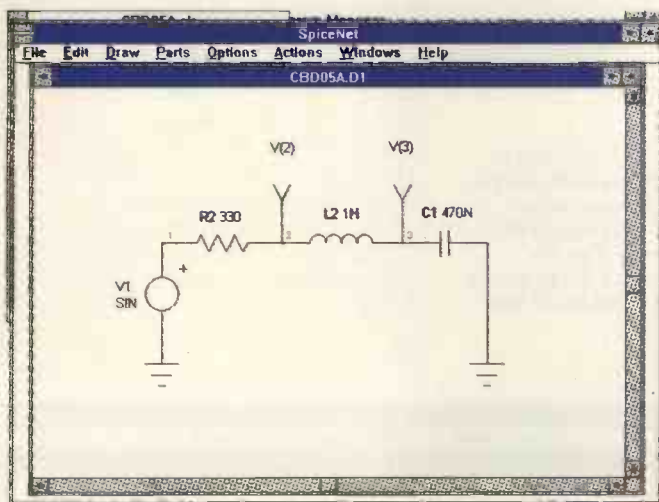


Fig. 1. This simple RLC circuit was drawn using SpiceNet. The Y-shaped symbols represent output or test points. Note the ground symbol on node 0. When the schematic is complete, the program automatically generates the netlist, ready for analysis.

## Spice and beyond

Spice-based simulation packages differ in the degree to which they retain the features of the Spice prototype. Windows-based *IsSpice* uses netlists in the syntax common to Spice2 and Spice3.

Results of analyses are printed out or displayed in the 'teletype' format of the original Spice, but may be displayed more effectively using the linked program, *ruscope*. *Micro-CapIV* and *TopSpice* are similar to *IsSpice*, but dos based. All three are enhancements of Spice in that they allow for subsequent analyses of the same circuit to be called from dialogue boxes. All allow the circuit to be entered as a netlist. The circuit may also be entered as a schematic. All have facilities for displaying output as graphs-on the monitor screen. SpiceAge, though drawing on the principles of Spice in the sense that it uses a netlist with syntax close to that of Spice, has adopted an entirely new approach to initiating analyses and displaying the results.

Although SpiceAge does not include routines for drawing schematics and converting them to netlists, these facilities are available in an linked program known as *GESECA*.

Table 1. This is the Spice netlist for the RLC circuit of Fig. 1. The .PRINT and .PLOT commands of Spice2 are not available in Spice3, but *IsSpice* has retained these commands for convenience.

### CBD05 – series RLC circuit

```
V1 1 0 SIN 0 1V 500HZ1M
R1 1 2 330
L1 2 3 1
C1 3 0 470N
.TRAN 100U 25M
.PRINT TRAN V(2) V(3)
.PLOT TRAN V(2) V(3)
.END
```

in Table 1 on the editing screen. The first line is the title, and is ignored by Spice. The second line defines the voltage source, with its positive connection to node 1 and its negative connection to node 0. It is a sine-wave generator with zero offset and an amplitude of 1V. Its frequency is 500Hz and there is a

delay of 1ms before it begins. All resistors must have a name beginning with R to identify them as resistors. Following the name are their node numbers and values.

Capacitors are similarly listed. The netlist next has a set of command statements. The .TRAN command instructs Spice to calculate

values every 100µs, for a total period of 25ms. The .PRINT command asks for the voltages at nodes 2 and 3 to be listed in the output file. The .PLOT command asks for these voltages to be plotted as a graph. It is obligatory to conclude the netlist with an .END statement. After saving the netlist file, with a .cir

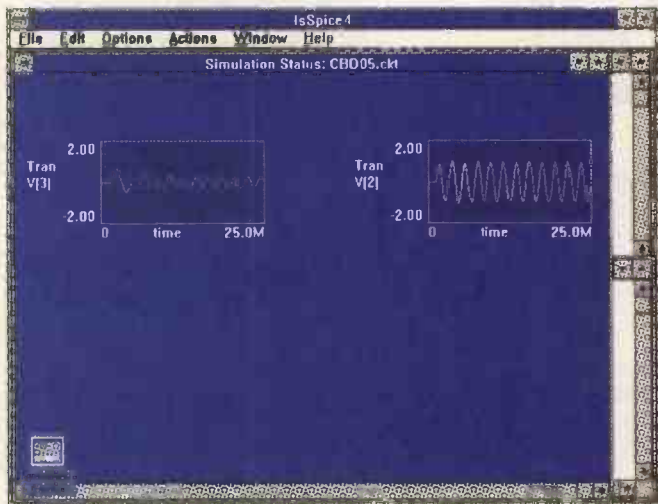


Fig. 2. IsSpice produces this display at the end of the transient analysis of the RLC circuit.

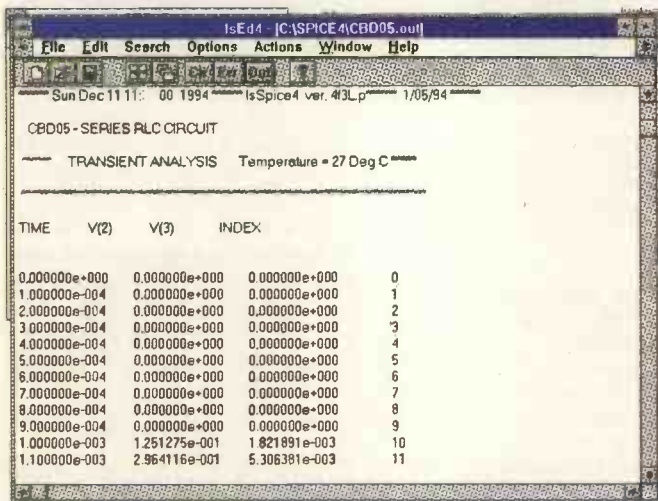


Fig. 3. First part of the transient analysis 'printout' of the IsSpice output file, as viewed on the screen. It can also be printed out as hard copy.

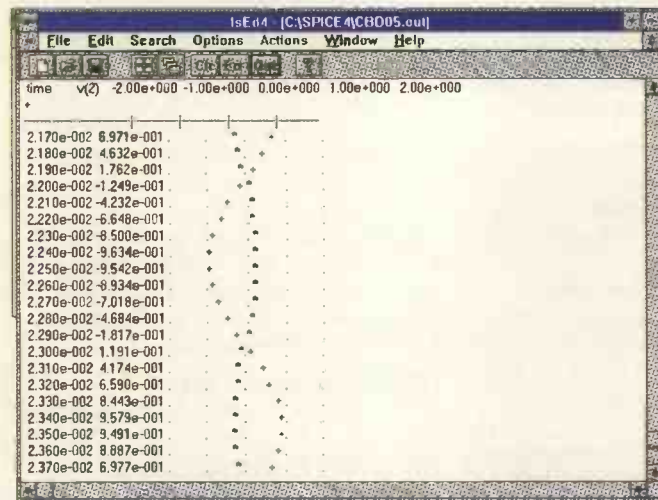


Fig. 4. Part of the teletype-style graphical plot showing the variations in V(2) as pluses, and V(3) as asterisks.

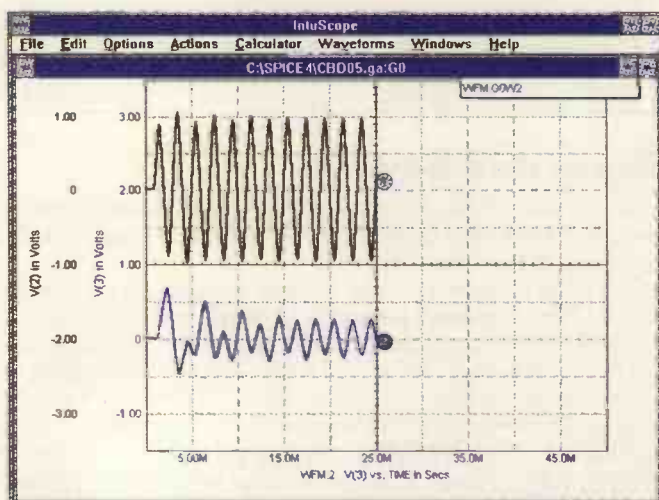


Fig. 5. Another way of looking at the results of the IsSpice analysis is by using IntuScope. This oscilloscope-like display shows the two output waveforms tiled, with V(2) above and V(3) below. Alternatively, they can be shown superimposed on the same set of coordinates.

## Schematic entry - an advantage or not?

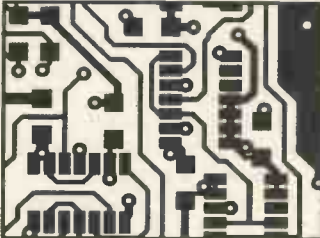
With most of the newer Spice-based simulators, it is possible to prepare the netlist by drawing the schematic on the screen. If the drawing is correct, you can be sure that the netlist generated from it is correct too.

Error messages may point out deficiencies of the schematic such as unconnected component terminals. This advantage is offset by the need to learn, usually, complicated routines for drawing the schematic. There would be some compensation for this if the printed schematic could be used as art-work but, in all the packages I have seen, the graphics quality is inadequate for this. Also, a number of symbols provided by the package may be non-standard, such as the zig-zag resistor, though there may be a facility for drawing your own symbols.

In some programs it is necessary to use the 'ground' symbol to indicate the line that is to be Node 0, but the ground symbol is inappropriate in many contexts. When working on a circuit, it is invariably necessary to revise the circuit, but editing a schematic can be horrendous. Recipe for success? Scribble the design on paper, number or name the nodes, key in the netlist.

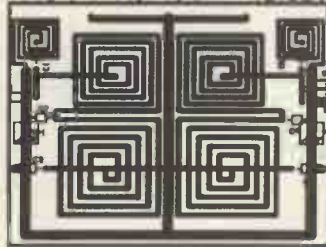
# RAPID, ACCURATE ELECTRONICS CAD

## Schematic and PCB CAD



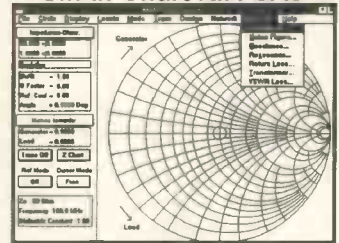
EASY-PC Professional

## Electro-magnetic Simulation



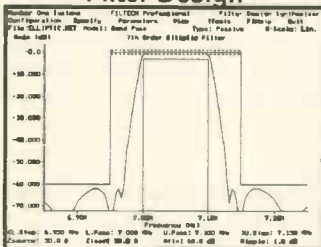
LAYAN

## Smith Chart R.F. CAD



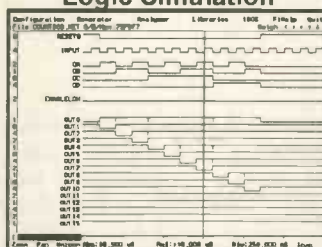
Z-MATCH

## Filter Design



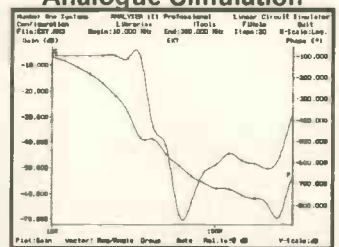
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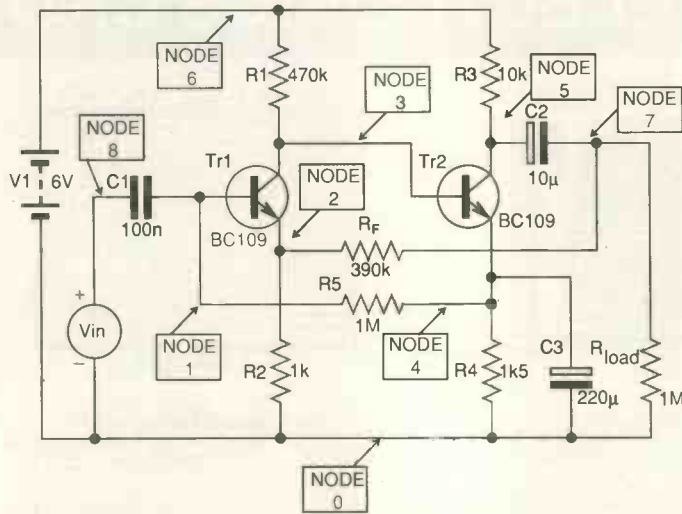


Fig. 6. This low-noise amplifier is used to demonstrate some of the data obtainable from a Spice dc analysis.

extension, select the Actions menu and click on Simulate. Figure 2 shows the screen after the Simulation Control box – not needed here – has been Minimised and the Error File and Output File windows reduced in width.

The small-scale graphs summarise transient analysis of the voltages at nodes 2 and 3, showing how voltages at various nodes vary during the period of analysis. If there are errors in the netlist or analysis, they are reported in an Error File – same name but with .err extension – visible in the Error File Window.

**Generating output files**

To generate the 'output file', select the File menu, then click on Exit/Quit. This causes the results of the analysis to be saved simultaneously in an 'output file' with the same name but with an .out extension. The program then takes you back to the ICAPs menu. Select Edit Text File, which displays the netlist again, then click on the OUT button on the toolbar to see the 'Output file'.

Production of a separate output file harks

back to the early days of Spice, when output was produced on a teletype machine. The routine begins by repeating the netlist, but with models expanded. It then tabulates the initial transient solution of the circuit – the nodal voltages when  $t=0$ .

All nodes are at 0V in this example. The first part of this is shown in Fig. 3. It lists the voltages, which are all zero to start with, owing to the specified delay of 1ms. Later sections of the list could be used as the basis of further analyses with a calculator.

As there is a .PLOT statement in the netlist, the output file is completed by a graphical plot, as might be made on a line printer, part of which is shown in Fig. 4. This is useful for finding the exact location of points such as maxima and minima.

**Using the oscilloscope**

Selecting the Scope option from the Actions menu runs the *IntuScope* program. The Waveform menu lists the waveforms available for display. In this example, select V(2) and then V(3) to obtain Fig. 5. This has the appearance of an oscilloscope screen, the bold lines of the plot helping to enhance the effect. The two traces are identified by a circled number and are plotted in different colours.

**DC analysis**

After that brief summary of the program, I will discuss more systematically what Spice can do and how it does it. The analyses in Spice comprise the dc analysis of steady states, ac analysis dealing mainly with the effects of frequency and transient analysis which shows how voltages vary in time.

Initially in a dc analysis, Spice converts all capacitances to open-circuits and all inductances to short-circuits. It then computes the stable operating point of the circuit with only dc sources applied. Finally, it sweeps the dc source voltages over given ranges and calculates the corresponding node voltages and branch currents. As an example of this, take Fig. 6 – a low-noise amplifier circuit. Figure 7 shows what this looks like to Spice as it performs the dc analysis.

Assuming that this circuit is to be entered as

a netlist, not a schematic, first draw the schematic on paper, numbering the nodes, with the 0V (ground line) as node 0. Using *IsEd*, key in the netlist, Table 2. Note the use of MEG for MΩ. The letter Q is always used for a bipolar transistor, followed by its connections (collector, base, emitter) and its model name – in this instance, BC109.

A definition of the model must appear somewhere in the netlist, after the word .MODEL. This states whether the transistor is npn or pnp, then lists the more important parameters. Any parameters not listed are assigned default values. In this example the model statement defines IS (saturation current), NF (forward emission coefficient), BF (forward current gain), ISE (BE junction leakage current), IKF ( $b_F$  high current rollover corner, and NE (BE junction leakage emission coefficient). Default values are automatically

Table 2. Spice netlist for the low-noise amplifier of Fig. 7.

```

CBD05 – low-noise amplifier
R1      6  3  470k
R2      2  0  1k
R3      6  5  10k
R4      4  0  1.5K
R5      4  1  MEG
RF      7  2  390k
RLOAD  7  0  1MEG
C1      1  8  100N
C2      5  7  10U
C3      4  0  220U
Q1      3  1  2  BC109
Q2      5  3  4  BC109
.MODEL BC109 NPN(IS=18F, NF=1,
BF=400, ISE=0, IKF=0.01, NE=1.5)
V1      6  0  DC 6
VIN     8  0  AC SIN 0 0.005 1K
.DC     V1 0 6 0.2
.OP
.PRINT DC V(7)
.END
    
```

Table 3. Part of the output file from a dc analysis consists of the Small Signal Bias Solution, and currents through the sources.

Circuit: CBD05 – low-noise amplifier  
 \*\*\*\* Small signal bias solution – op

Node	Voltage
V(8)	0.000000e+000
V(7)	6.726277e-003
V(1)	5.283758e-001
V(4)	5.517291e-001
V(5)	2.331081e-003
V(2)	9.349525e-003
V(3)	1.166624e+000
V(6)	6.000000e+000
Source	Current
v1#branch	-3.77176e-004
vin#branch	0.000000e+000

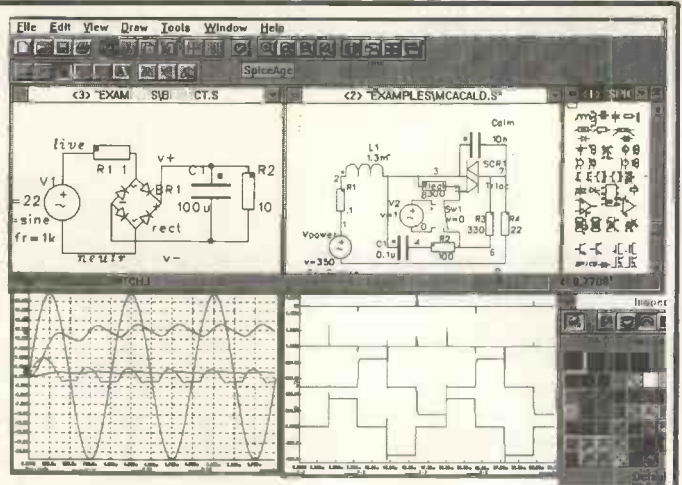
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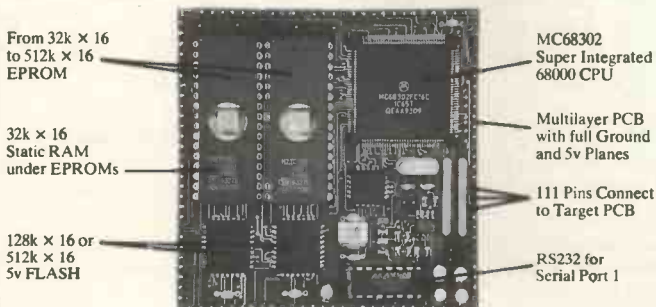


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## Spice command statements for dc analysis

**.DC** initiates a dc analysis with all capacitances open-circuited, all inductances short-circuited, and all ac sources inactivated. Dc inputs (one or two voltage or current sources) are swept over prescribed ranges. Node voltages and branch currents are calculated.

**.OP** Operating point (or bias point). Prints the voltages at all nodes, the current through each source, and the total power consumption. Also prints the currents, terminal voltages and sundry parameters of all non-linear devices. These values are always calculated when a dc analysis is called for, but not printed unless the **.OP** command is given.

**.TF** Calculate the small-signal transfer function (the gain) as a voltage gain, a current gain, a transconductance or a transresistance, depending on the quantities specified after the command. Also calculates input and output resistances.

**.SENS** Calculates the effect of the values of circuit elements and of model parameters on selected output variables. For example, the effect of variations in the base resistance of a modelled transistor on the output current of the circuit, expressed as amps percent. This identifies the quantities which are most critical in determining the performance of a circuit. Sensitivity analysis is usually available only in the more expensive simulation software.

**.TEMP** Specifies the ambient temperature at which the circuit is operating. The default value is 27°C. If several temperatures follow this command, the analysis is repeated at each temperature.

**.NODESET** Sets the initial voltage at one or more nodes to a prescribed value. Used when the Spice algorithms can not find a sensible initial solution.

**.PRINT DC** Calls for the results of the analysis to be printed as a set of tables (not in Spice).

**.PLOT DC** Calls for the results of the analysis to be plotted as a graph (not in Spice3).

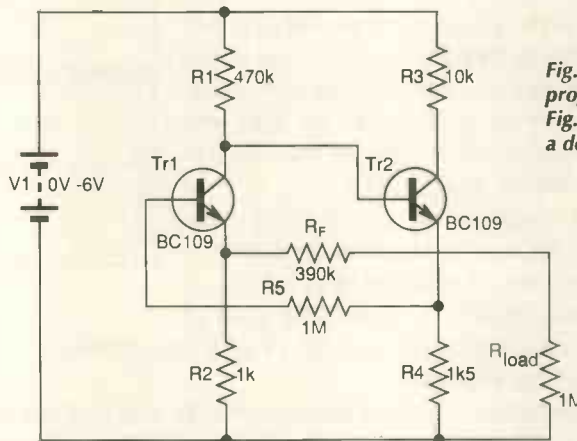


Fig. 7. This is how a Spice program sees the circuit of Fig. 6 when asked to perform a dc analysis.

given to the other 37 parameters – it is up to the user to decide which ones are important enough to warrant specifying.

The two sources are named as V1, the battery, and VIN which is a sine-wave signal source. The battery is shown connected to nodes 6 and 0 with a value 6V dc. The 'DC' is optional in the statement but helps to make the net list more intelligible. Voltage VIN is inactive during the dc analysis but is defined as connected between nodes 8 and 0, with ac output in the form of a sine wave, 0V offset, 0.005V amplitude and a frequency of 1kHz. It is not essential to define its output for a dc analysis. Only the node connections are required. Spice assumes that the voltage is zero.

The **.DC** command instructs Spice to sweep V1 from 0V to 6V in 0.2V steps. The **.OP** command calls for a printout of the operating point voltages (see box) The **.PRINT** command asks for a table of the voltages at node 7 as V1 is swept from 0 to 6V.

### Running the dc analysis

Select the Actions menu and click on Simulate. The preliminary output graph shows no appreciable slope in the curve of Y(7) against VIN because the voltages are too small and the graph is not automatically scaled.

However, the results can be seen later by using *Introscope* and by examining the output file. To generate the output file, select the File menu, then click on Exit/Quit, select Edit Text File, then click on the OUT button. A window then displays the Output file. This first lists the netlist, then the operating point analysis, or small signal bias solution (Table 3).

Voltage at each node is tabulated and shows zero voltage at node 8 because the ac source is inactive, 6V at node 6, the power supply line.

The output also contains information about current through the two sources. Current through V1 is small. Note that the current through V1 is negative. In Spice, a positive current through the source flows from the positive terminal of a voltage source to its negative terminal. Thus a negative current flows from positive to negative through the

external circuit. Spice uses zero-voltage sources as current-measuring devices, as will be demonstrated in a later circuit.

The output file concludes with a print out of the voltage at node 7 as VIN is swept from 0V to 6V. This shows that V(7) does not increase significantly until Vin reaches about 2.4V. The results are given to 7 significant figures (assuming that all seven figures are really significant). The result shows the early near-zero value of V(7) as VIN ramps up from 0V to 3V. This is followed by an exponential increase to 4mV as VIN increases from 3 to 4.2V. Finally there is a linear increase with V(7) reaching 6.73mV as VIN gets to 6V.

The list of command statements shows there are several other aspects to dc analysis. ■

## SI multipliers

Because Spice originated in the days of teletype, when only upper-case letters could be printed, its unit multiples and submultiples may be typed in upper or lower case and it does not distinguish between them. The most frequently used are:

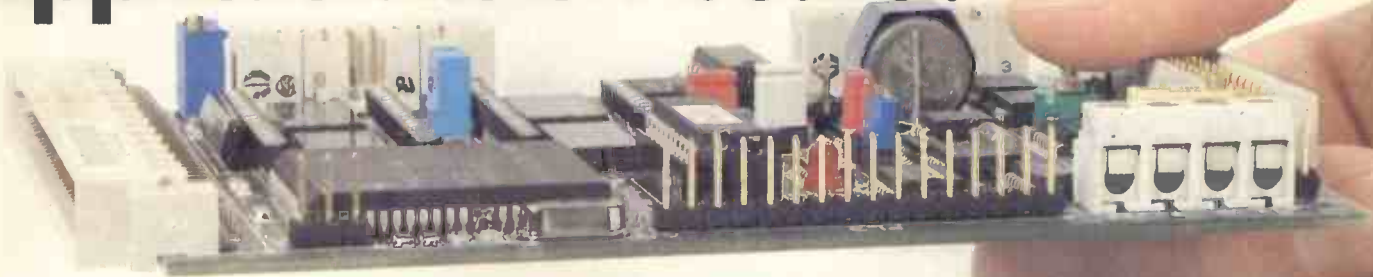
SI mult.	F point	Spice*
p, pico	E-12	P or p
n, nano	E-9	N or n
μ, micro	E-6	U or u
m, milli	E-3	M or m
k, kilo	E+3	K or k
M, mega	E+6	MEG or meg
G, giga	E+9	G or g
T, tera	E+12	T or t

\*Spice ICAP/4

Note the confusion between milli and mega in the two systems. Letters following the multiplier are ignored by *IsSpice*, so that 220UF or 220UFARAD are both read as 220U.



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# Hot audio power



Despite rapid advances in semiconductor technology that have occurred over the past few decades, there are many audiophiles who believe that valves are best. Although at first sight this idea may seem ludicrous, it may not be quite as fanciful as some of the dubious products that the high fidelity industry has come up with of late.

Despite disadvantages of separate heater supplies and the need for high voltage supply lines, valves do have some advantages over their semiconductor rivals.

### Why use valves?

Firstly, valves are easy to drive. At low frequencies the grid of a valve has an impedance approaching 100MΩ, but without the large parallel capacitance of a v-fet. Similarly, being mechanical devices, the characteristics are far better matched between samples than, say, transistors from the same batch. Consequently, a class-AB amplifier output stage built with valves can be far more linear than a solid state equivalent. Most surprisingly of all, for those of us weaned on silicon, is the amazing amount of abuse that valves can take without disappearing in a puff of smoke.

It was in the spirit of curiosity that the design described here was developed. It uses a pair of formerly widely used EL34s driven by a solid state circuit.

There are several reasons why the EL34 is a good choice of output valve. Primarily, it has

Fig. 1a. Simplest form of valve amplifier is Class-A. Relative to the loudspeaker, a valve is very high impedance so a transformer is essential.

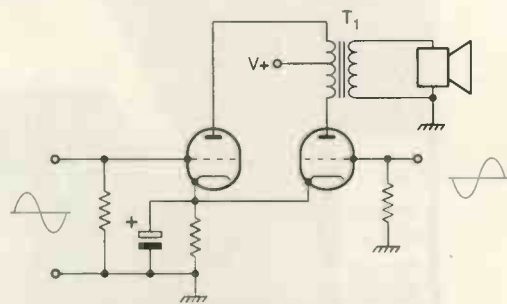
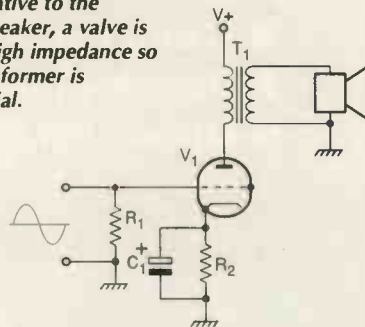


Fig. 1b. In the conventional valve output stage, equal and anti-phase signals are applied to the grids to generate push-pull output.

a high 25W anode dissipation. It also fits into a standard octal relay socket and is relatively cheap. Reasons for the solid state driver will become apparent as the article progresses, but first it will be useful to get to grips with some output stage basics.

The simplest form of valve output stage is the single-ended class-A triode, Fig. 1a). Because valves have limited current handling capacity and rather large internal resistance, anode drive is applied to the loudspeaker through an impedance matching transformer. This system works fine but its maximum theoretical efficiency is only 50%. Usually because of the anode characteristics practical efficiency is more often in the region of 25%. If I had been writing this article a couple of years ago, I could have said that single ended triode output stages were a thing of the past. However audio 'purists' have resurrected them. If you have the money and inclination you can purchase one particular triode amplifier for a cool £30,000.

**Valve output stages**

The conventional valve output stage is shown in Fig 1b) where for simplicity the valves are shown as triodes. Output is fed from the valve

anodes to the output transformer primary. The centre tap of this winding is connected to the positive supply.

When equal and antiphase input signals are applied to the valve grids, push-pull operation is obtained. As with solid state designs the operating class is decided by bias current.

This push-pull stage has the usual advantages of cancelling even-harmonic distortion and increased power output. In addition hum voltages at the anodes cancel, producing an inherently high power supply ripple rejection.

Using EL34s in this type of circuit, it is possible to get outputs in the 20-50W range with reasonable ht voltages. However, the main problem with valve output stages is the output transformer – particularly in terms of frequency response.

A real transformer, as opposed to a theoretical model, requires considerable primary inductance for good bass. Similarly at the high end, leakage inductance and winding capacitance limit response.

A modelled real transformer is shown in Fig. 2. Figure 2a) shows an equivalent circuit at low frequencies. Here primary inductance forms a high-pass filter with the valve's anode impedance. Clearly, the greater the inductance

**Components**

Resistors 1%, 0.5W metal film unless indicated

R <sub>1</sub>	56k	[2]
R <sub>2/5</sub>	10k	[4]
R <sub>3/4</sub>	1k8	[4]
R <sub>12/13</sub>	68k	[4]
R <sub>6/9</sub>	60k	[4]
R <sub>7/8</sub>	220k	[4]
R <sub>10</sub>	470R, 3W ww	[2]
R <sub>11</sub>	6k8	[2]
R <sub>14/15</sub>	470R, 1W	[4]
R <sub>16</sub>	1k, 1W	[2]

**Capacitors**

C <sub>1,2</sub>	100nF, 1000V WKG polyprop	[4]
C <sub>3</sub>	100µF, 100V	[2]
C <sub>4</sub>	220µF, 25V	[2]
C <sub>5,6</sub>	470µF, 400V	[2]
C <sub>7,8</sub>	1000µF, 63V	[4]

**Active devices**

A <sub>1/A<sub>2</sub></sub>	TL072	[2]
V <sub>1/V<sub>2</sub></sub>	EL34	[4]
Tr <sub>1,2</sub>	2SC2547E	[4]
D <sub>1,2</sub>	1N4001	[4]
BR <sub>1</sub>	W08	[1]

**Wound components**

T <sub>1</sub>	Output transformer 20:1 ratio, centre tapped. Primary inductance >8H, Leakage inductance <10mH	[2]
T <sub>2</sub>	Mains, 240V prim. 280V, 700mA second. 6-0-6V 4A second.	[1]

**Transformer availability**

Three transformers especially wound for this design have been produced by Antrim Transformers. The set is available to UK readers for £99.99 plus £8 postage – fully inclusive of VAT. Antrim Transformers Ltd is at 25 Randalstown Road, Antrim, Co Antrim BT41 4LD, tel. 018494 28734, fax 018494 68745. Overseas readers should contact Antrim for export details. Readers who already have the mains transformer can obtain the output pair for £79.50 plus £8 postage, again inclusive of VAT.

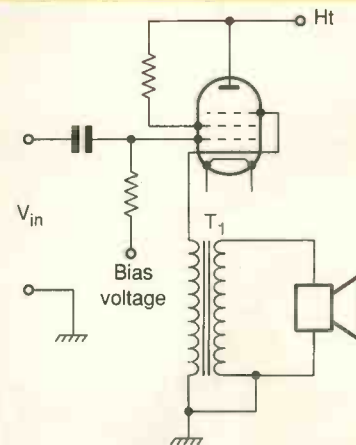
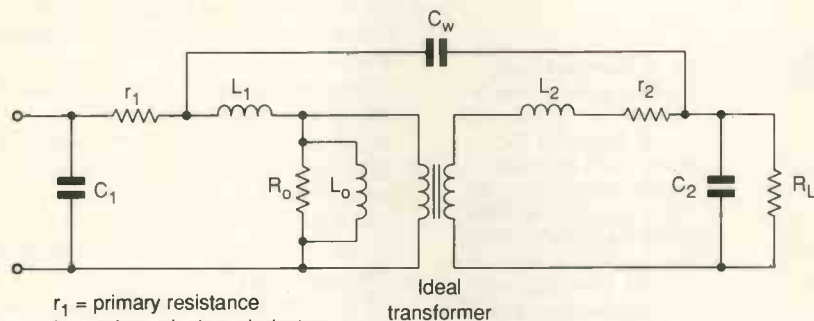
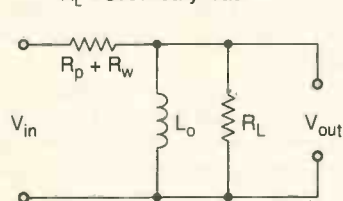


Fig. 3. Valve equivalent of the emitter follower. Putting the transformer in the cathode improves performance but driving the circuit needs a prohibitively large supply rail.



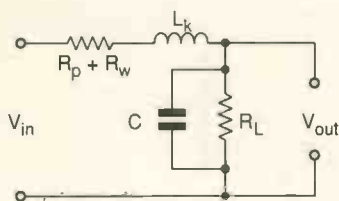
- r<sub>1</sub> = primary resistance
- L<sub>1</sub> = primary leakage inductance
- r<sub>2</sub> = resistance of secondary
- L<sub>2</sub> = equivalent secondary leakage
- R<sub>0</sub> = equivalent core loss resistance
- L<sub>0</sub> = primary inductance
- C<sub>1</sub>, C<sub>2</sub> = equivalent lumped capacitance of primary and secondary
- C<sub>w</sub> = interwinding capacitance
- R<sub>L</sub> = secondary load

Fig. 2. Model of the output transformer, above. For good bass, the transformer requires significant primary inductance. Low and high-frequency equivalents are shown below in a) and b) respectively.



- R<sub>p</sub> = anode resistance
- R<sub>w</sub> = winding resistance
- L<sub>0</sub> = primary inductance
- R<sub>L</sub> = secondary load x turns ratio squared

a) low frequency equivalent circuit



- L<sub>k</sub> = leakage inductance
- C = winding capacitance

b) high frequency equivalent circuit

here the better if good bass response is to be secured.

Figure 2b) shows a corresponding equivalent circuit of the transformer at high frequencies. At these frequencies primary inductance has no influence but leakage inductance in conjunction with the winding capacitance forms a second-order low-pass filter.

Both leakage inductance and winding capacitance are functions of the way the transformer is built. Reducing these factors is usually done by sectioning the transformer windings. Again you can see from the equivalent circuit that leakage inductance needs to be minimised for good ht response.

When calculating values of these inductances for a given anode resistance, calculations show how rapidly required inductance falls when anode resistance is lowered. In fact, if output impedance could be made zero the required primary inductance would also disappear. Similarly, it can be shown that transformer distortion is also highly dependent on anode resistance and similarly drops to zero with zero impedance drive.

One argument which can be put forward in defence of triode output stages is that they

### Supplying ht

Power supply for the circuit is conventional. Ht is derived from the 280V secondary coil of  $T_2$  – full-wave rectified by  $BR_1$  and smoothed by the parallel combination of  $C_5$  and  $C_6$ . Apart from extra ripple rejection this combination of capacitors stores a huge amount of energy – around 68J. This helps maintain supply lines even when feeding awkward loads.

Supply lines for the op-amp circuit are derived from the heater secondaries. For a stereo amplifier, 6V at 3A minimum/channel is required. A 6-0-6V, 50VA transformer is suitable.

Secondaries connect in series and the voltage doubler  $D_1$  and  $D_2$  provides the dual dc supply, smoothed by  $C_7$  and  $C_8$ . Heaters connect in series/parallel across the 12V supply as shown in the schematic overleaf.

Because of the totally balanced operating mode of the amplifier, ripple voltages effectively cancel out – simplifying psu design.

have lower anode impedance than pentodes. Hence, primary inductance can be made lower for a given bass extension. Most practical designs use overall negative feedback to lower effective anode resistance.

Normally the loop is taken from the output winding of the transformer – including it within the feedback loop. However, due to reactive

elements present in an output transformer the amount of feedback that can be employed in this manner is strictly limited.

One of the best ways of solving the problem is to use a cathode follower, Fig. 3. It is analogous to the more familiar emitter follower with similar features. Voltage gain is always less than unity and output impedance is significantly lower than that obtained from a triode used in a normal grounded cathode stage. Distortion is typically an order of magnitude smaller.

These limitations make the circuit more of a laboratory curiosity, since driving it fully would require almost twice the signal swing allowed by the ht voltage. However, the circuit is tantalising and I played with the idea of a push-pull cathode follower output driven by an inter-stage transformer before developing the present circuit.

There is however another way of producing a cathode follower style output stage which possesses all the virtues and few of the vices of a conventional valve output stage. The circuit is an amalgam of a transconductance and a transresistance amplifier, Fig. 4.

I cannot understand why this particular circuit is not used more often since it allows very high performance with a low component count. Figure 4a shows a transimpedance amplifier operating as a conventional virtual earth amplifier.

If open-loop gain is very high then closed-loop performance is determined by the ratio of  $R_1$  to  $R_2$ . If  $R_1$  were to be substituted for a constant current source, Fig. 4b), the amplifier would 'see' 100% negative feedback at its inverting input and voltage gain would be zero.

Replace the current source with a transconductance amplifier and the amplifier will give an output of  $IR_1$ . Distortion generated by the transimpedance stage will be very small because feedback factor B (the proportion of

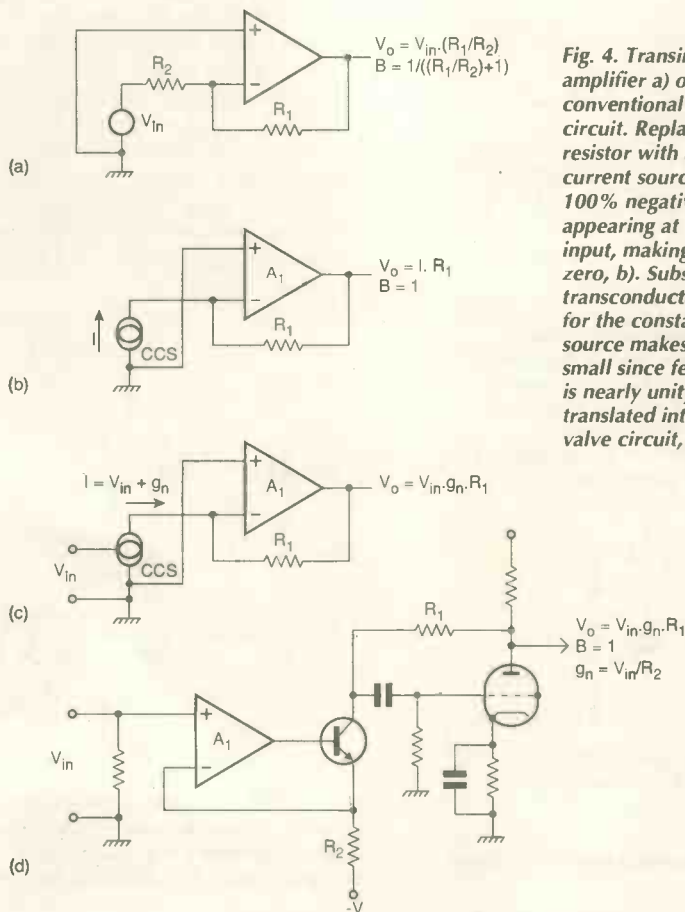
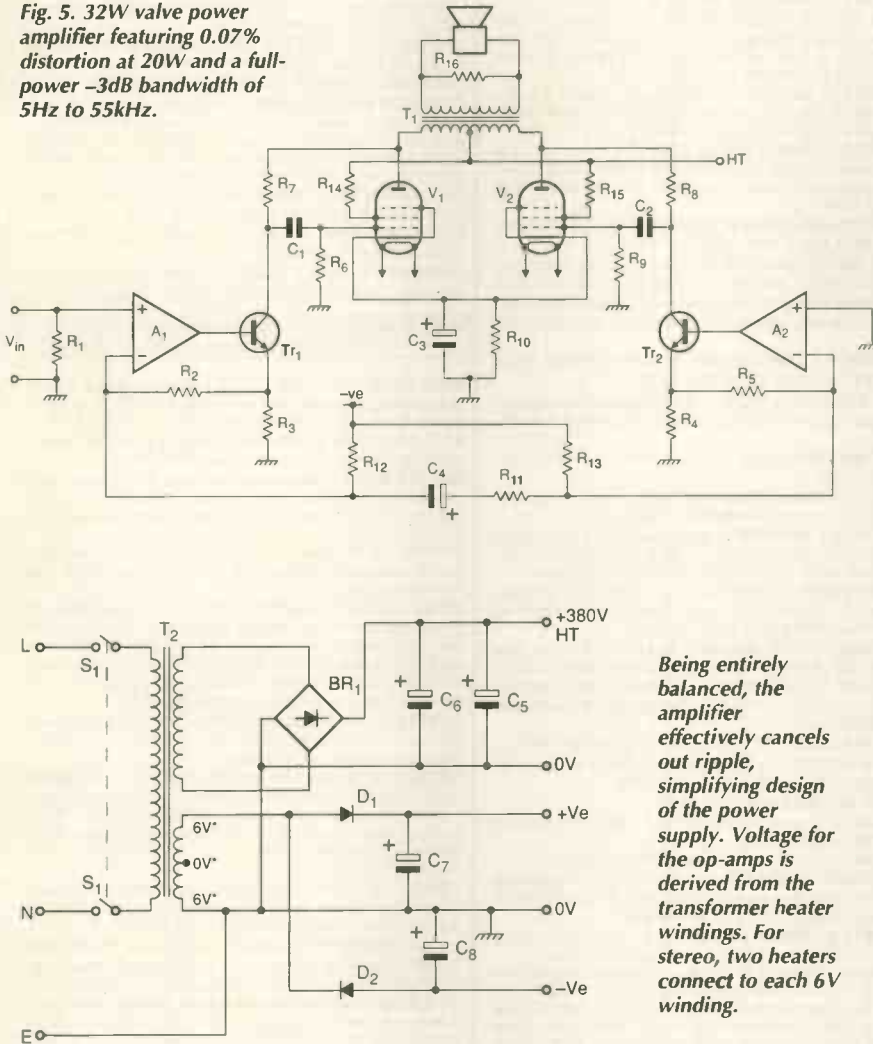


Fig. 4. Transimpedance amplifier a) operating as a conventional virtual-earth circuit. Replacing the resistor with a constant-current source results in 100% negative feedback appearing at the inverting input, making voltage gain zero, b). Substituting a transconductance amplifier for the constant-current source makes distortion very small since feedback factor B is nearly unity c). Circuit c) translated into a hybrid valve circuit, d).

**Fig. 5. 32W valve power amplifier featuring 0.07% distortion at 20W and a full-power -3dB bandwidth of 5Hz to 55kHz.**



*Being entirely balanced, the amplifier effectively cancels out ripple, simplifying design of the power supply. Voltage for the op-amps is derived from the transformer heater windings. For stereo, two heaters connect to each 6V winding.*

the signal fed back) is almost unity. As the transconductance amplifier can also be made with unity gain a very well-behaved circuit is obtained.

In the present circuit the transimpedance amplifier is replaced by the valve. A transistor in the feedback loop of an TL072 op-amp is the basis of the transconductance amplifier. Across the audio band the circuit gives an output impedance greater than 10MΩ.

Required voltage gain can be achieved by altering the transconductance ratio  $R_2$  – both transconductance and transimpedance amplifiers have unity voltage gain. A well balanced push-pull output from the driving circuitry is also required to drive the push-pull output stage. This can easily be obtained by linking inverting inputs of the op-amps via a resistor and dc blocking capacitor.

Unfortunately a valve driver – though possible – is very difficult to design. An EF86 pentode valve is a possibility but the only way of obtaining the requisite high impedance

drive is to use low operating current. High current drive is required from this stage since impedance seen at the output valves grid is low due to feedback employed.

Figure 5 shows the amplifier's complete circuit. Input signals are fed across  $R_1$  to the non-inverting input of  $A_1$  which sets input impedance. Op-amp  $A_1$  in conjunction with  $Tr_1$  form a transconductance amplifier as discussed previously. Feedback is taken from emitter resistor  $R_3$  to the inverting input via  $R_6$ . Resistors  $R_{12,13}$  connect to the supply rail and provides bias for  $Tr_{1,2}$  setting the quiescent current of the stage.

Output current from the collector of  $Tr_1$  feeds into  $R_7$  which connects in shunt between the anode and grid circuit of  $V_1$ . Capacitor  $C_1$  isolates the valve from the dc level present at  $Tr_1$  collector and  $R_6$  returns the grid to ground. At ac,  $R_7$  and  $R_6$  appear as a parallel load to  $Tr_1$ . This impedance is effectively reduced by around 9.2 times by the valve gains.

Biasing of the output stage is effected by

$R_{10}$ , shunted at ac by  $C_3$ . Screen grids are also biased by  $R_{14}$  and  $R_{15}$ .

Both circuit halves are identical. Phase splitting is produced by coupling inverting inputs of  $A_1$  and  $A_2$  together via  $R_{11}$  and dc blocking capacitor  $C_4$ . This results in two antiphase equal amplitude signals at the emitters of  $Tr_1$  and  $Tr_2$  to drive the output stage.

Output voltages from  $V_1$  and  $V_2$  are applied to the primary coil of  $T_1$  while ht is applied to the valves through the centre tap. Audio output signals are taken from the secondary coil of  $T_1$  and applied to the loudspeaker. Resistor  $R_{16}$  keeps the output stage under control in the absence of a suitable load.

Because of heavy negative feedback within the circuit, overall feedback around the output transformer was found unnecessary. However, for those who like to experiment, feedback can be taken from the output side of the output transformer to the non-inverting input of  $A_2$ . If this is tried,  $R_{11}$  should be reduced to increase open-loop gain

**Implementation**

Building this design is straightforward. I used a readily available chassis and tagstrips. For the heater wiring, 5A loudspeaker cable is ideal. It should be laid close to the chassis but need not be twisted together as in low-level valve circuitry.

Potentially lethal voltages are present on capacitors  $C_5$  and  $C_6$  and all ht lines, and there is of course live mains around the transformer primary circuit. Always power up the amplifier with the EL34s *in situ* and make sure that the heaters heat up before turning off again. As long as the valves are in circuit and conducting, they will discharge the decoupling capacitors rapidly after turn off. If they are not installed, high voltage will linger for hours – sometimes days.

The amplifier requires no setting up. Provided it is wired correctly the circuit will operate first time.

**Conclusion**

Was it worth the effort? Yes, I certainly think so. The prototype gives out 32W continuous per channel with a full power bandwidth of 5Hz to 55kHz, -3dB. Measured thd at 1kHz and 20W output is 0.07% while output impedance is a mere 0.6Ω – minuscule by valve amplifier standards.

Mainly though, the amplifier excels at driving awkward loads and can suffer short circuits on the output without complaint. Last, but by no means least, it was fun to design. ■

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**PORTABLE X RAY MACHINE PLANS** Easy to construct plans on a simple and cheap way to build a home X-ray machine! Effective device, X-ray sealed assemblies, can be used for experimental purposes. Not a toy or for minors! £6/set. Ref F/XP1.

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**PULSED TV JOKER PLANS** Little hand held device utilises pulse techniques that will completely disrupt TV picture and sound works on FM too! DISCRETION ADVISED. £8/set Ref F/TJ5.

**BODYHEAT TELESCOPE PLANS** Highly directional long range device uses recent technology to detect the presence of living bodies, warm and hot spots, heat leaks etc. Intended for security, law enforcement, research and development, etc. Excellent security device or very interesting science project. £8/set Ref F/BHT1.

**BURNING, CUTTING CO2 LASER PLANS** Projects an invisible beam of heat capable of burning and melting materials over a considerable distance. This laser is one of the most efficient, converting 10% input power into useful output. Not only is this device a workhorse in welding, cutting and heat processing materials but it is also a likely candidate as an effective directed energy beam weapon against missiles, aircraft, ground-to-ground, etc. Particle beams may very well utilize a laser of this type to blast a channel in the atmosphere for a high energy stream of neutrons or other particles. The device is easily applicable to burning and etching wood, cutting, plastics, textiles etc. £12/set Ref F/LC7.

**MYSTERY ANTI GRAVITY DEVICE PLANS** Uses simple concept. Objects float in air and move to the touch. Defies gravity, amazing gift, conversation piece, magic trick or science project. £6/set Ref F/ANT1K.

**ULTRASONIC BLASTER PLANS** Laboratory source of sonic shock waves. Blow holes in metal, produce 'cold' steam, atomize liquids. Many cleaning uses for PC boards, jewellery, coins, small parts etc. £6/set Ref F/ULB1.

**ULTRAHIGH GAIN AMP/STETHOSCOPIC MIKE/ SOUND AND VIBRATION DETECTOR PLANS** Ultrasonic sensitive device enables one to hear a whole new world of sounds. Listen through walls, windows, floors etc. Many applications shown, from law enforcement, nature listening, medical heartbeat, to mechanical devices. £6/set Ref F/HGA7.

**ANTI DOG FORCE FIELD PLANS** Highly effective circuit produces time variable pulses of accoustical energy that dogs cannot tolerate. £6/set Ref F/DODG2.

**LASER BOUNCE LISTENER SYSTEM PLANS** Allows you to hear sounds from a premises without gaining access. £12/set Ref F/LLIST1.

**CRAWLING INSECT ROASTER PLANS** Harmless high frequency energy pulses destroy pests as they crawl into the energy field. £4/set Ref F/RCR1.

**LASER LIGHT SHOW PLANS** Do it yourself plans show three methods. £6 Ref F/LLS1.

**PHASOR BLAST WAVE PISTOL SERIES PLANS** Handheld, has large transducer and battery capacity with external controls. £6/set Ref F/PSP4.

**INFINITY TRANSMITTER PLANS** Telephone line grabber/room monitor. The ultimate in home/office security and safety! simple to use! Call your home or office phone, push a secret tone on your telephone to access either: A) On premises sound and voices or B) Existing conversation with break-in capability for emergency messages. £7 Ref F/TELEGRAB.

**BUG DETECTOR PLANS** Is that someone getting the goods on you? Easy to construct device locates any hidden source of radio energy! Sniffs out and finds bugs and other sources of bothersome interference. Detects low, high and UHF frequencies. £5/set Ref F/BD1.

**ELECTROMAGNETIC COIL GUN PLANS** Projects a metal object a considerable distance - requires adult.

**ELECTRIC MAN PLANS, SHOCK PEOPLE WITH THE TOUCH OF YOUR HAND!** £5/set Ref F/EMA1.

**PARABOLIC DISH MICROPHONE PLANS** Listen to distant sounds and voices, open windows, sound sources in 'hard to get' or hostile premises. Uses satellite technology to gather distant sounds and focus them to our ultra sensitive electronics. Plans also show an optional wireless link system. £8/set Ref F/PM5.

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**1 WATT FM TRANSMITTER KIT** Supplied with piezo electric microphone 8-30vdc. At 25-30v you will get nearly 2 watts! £12 Ref 1009.

**FM/AM SCANNER KIT** Well not quite, you have to turn the knob your self but you will hear things on this radio that you would not hear on an ordinary radio (even TV). Covers 50-160mhz on both AM and FM. Built in 5 watt amplifier, Inc speaker. £15 Ref 1013.

**MOSQUITO REPELLER KIT** Modern way to keep midges at bay! Runs for about a month on one 1.5v battery. £7 Ref 1015.

**3 CHANNEL SOUND TO LIGHT KIT** Wireless system, mains operated, separate sensitivity adjustment for each channel, 1,200 w power handling, microphone included. £14 Ref 1014.

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**0-5 MINUTE TIMER KIT** adjustable, will switch up to 2A mains. Perfect for alarms, photography, etc. £7 Ref 1020.

**4 WATT FM TRANSMITTER KIT** Small but powerful FM transmitter, 3 RF stages, microphone and audio preamp included. £20 Ref 1028.

**STROBE LIGHT KIT** Adjustable from 1-60 hz (a lot faster than conventional strobes). Mains operated. £16 Ref 1037.

**ULTRASONIC RADAR KIT** Ideal as a movement detector with a range of about 10 metres, automate your cat flap! 12v dc. £15 Ref 1049.

**LIQUID LEVEL DETECTOR KIT** Useful for tanks, ponds, baths, rain alarm, leak detector etc. Will switch 2A mains. £5 Ref 1081.

**COMBINATION LOCK KIT** 9 key, programmable, complete with keypad, will switch 2A mains. 9v dc operation. £10 Ref 1114.

**PHONE BUG DETECTOR KIT** This device will warn you if somebody is eavesdropping on your line. £6 Ref 1130.

**ROBOT VOICE KIT** Interesting circuit that distorts your voice! adjustable, answer the phone with a different voice! 12vdc £9 Ref 1131.

**TELEPHONE BUG KIT** Small bug powered by the phone line, starts transmitting as soon as the phone is picked up! £8 Ref 1135.

**FUNCTION GENERATOR KIT** Produces sinusoidal, saw tooth and square waves from 20-20khz, separate level controls for each shape. Will produce all 3 together. 24vac. £16 Ref 1008.

**3 CHANNEL LIGHT CHASER KIT** 800 watts per channel, speed and direction control supplied with 12 LEDs (you can fit tricolors instead to make kit mains, not supplied) 9-12vdc £17 Ref 1026.

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**INCAR SOUND TO LIGHT KIT** Put some atmosphere in your car with this mini 3 channel sound to light. Each channel has 6 led's. £10 Ref 1086.

**7W HI FI AMPLIFIER KIT** Useful, powerful, ideal for audio systems, Intercoms etc. 12-18vdc £7 Ref 1025.

**PHONE CALL RELAY KIT** Useful device that operates a relay whenever the phone rings. Switches mains at 2A. £10 Ref 1122.

**LEAD ACID CHARGER KIT** Two automatic charging rates, visual indication of battery state, ideal for alarm systems etc, 100mA 12vdc £12 Ref 1095.

**CAR ALARM KIT** Works on voltage drop and vibration, entry/exit delays, adjustable alarm duration. Ideal caravans etc. £12 Ref 1019.

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**METAL DETECTOR KIT** Range 15-20cm, complete with case, 9vdc. £8 Ref 1022.

**SINGLE CHANNEL SOUND TO LIGHT KIT** Mains operated, add rhythm to your party for only £8 Ref 1006.

**SOUND EFFECTS GENERATOR KIT** Produces sounds ranging from bird chirps to sirens. Complete with speaker, add sound effects to your projects for just £9 Ref 1045.

**GUITAR PREAMP KIT** Complete with tone controls, small enough to fit in any guitar, based on TL082 IC. 9-12vdc £8 Ref 1091.

**16 WATT FM TRANSMITTER (BUILT)** 4 stage high power, preamp required 12-18vdc, can use ground plane, yagi or open dipole. £69 Ref 1021.

**TELEPHONE AMPLIFIER KIT** Very sensitive amplifier which uses a pickup coil (supplied) will let you follow a conversation with out holding the phone. £11 Ref 1059.

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**DIVING RODS** Expensive technology cannot challenge the fool proof art of water diving, passed down from generation to generation. Seeing is believing. Use in the home, garden, countryside or desert, it's divinely simple! £4.99 a pair Ref E/3.

**HUGE BUBBLE MAKING KIT** You'll be amazed at the size of the bubbles you can achieve with this bubble making kit. Once you have got the knack it is possible to make bubbles of up to 40 feet long. £11.99 Ref E/9.

**FM CORDLESS MICROPHONE** This unit is an FM broadcasting station in miniature, 3 transistor transmitter with electret condenser mic-fet amp design results in maximum sensitivity and broad frequency response. 90-105mhz, 50-1500hz, 500 foot range in open country! PP3 battery required. £15.00 Ref 15P42A.

**MAGNETIC MARBLES** They have been around for a number of years but still give rise to curiosity and amazement. A pack of 12 is just £3.99 Ref GI/R20

**STETHOSCOPES** A fully functioning stethoscope for all those intricate projects. Enables you to listen to motors, pipes, heartbeats, walls, insects etc. £6 Ref MAR6P6.

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**SHOP WOBBLERS!** Small assemblies designed to take D size batteries and 'wobble' signs about in shops! £3.99 Ref SEP4P2.

**OMRON ELECTRONIC INTERVAL TIMERS.**  
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HY2401S, 240v adjustable from 0-1 secs. £4.99

HY2405S, 240v adjustable from 0-5 secs. £4.99

HY24060M, 240v adjustable from 0-60 mins. £6.99

**DRINKING BIRD** Remember these? Hook onto wine glass (supplied) and they drink, standup, drink, standup etc! £4 each Ref E/1

**SOLAR POWER LAB SPECIAL** You get TWO 6"x6" 6v 130mA solar cells, 4 LEDs, wire, buzzer, switch plus 1 relay or motor. Superb value kit just £5.99 REF: MAG6P8

**BUGGING TAPE RECORDER** Small voice activated recorder, uses micro cassette complete with headphones. £28.99 Ref MAR2P1.

**PLUG IN ACORN PSU** 19v AC 14w, £2.99 REF MAG3P10

**POWER SUPPLY** fully cased with mains and o/p leads 17v DC 900mA output. Bargain price £5.99 Ref MAG6P9

**9v DC POWER SUPPLY** Standard plug in type 150mA 9v DC with lead and DC power plug, price for two is £2.99 Ref AUG3P4.

**13.8V 1.9A** psu cased with leads. Just £9.99 REF MAG10P3

**INFRA RED REMOTE CONTROLLERS** Originally made for hi spec satellite equipment but perfect for all sorts of remote control projects. Our clearance price is just £2 REF: MAG2

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**COMPOSITE VIDEO KIT.** Converts composite video into separate H sync, V sync, and video. 12v DC. £8.00 REF: MAG8P2.

**UNIVERSAL PC POWER SUPPLY** complete with flyleads, switch, fan etc. 200w at £20 REF: MAG20P3 (265x155x125mm).

**GYROSCOPE** About 3" high and an excellent educational toy for all ages! Price with instruction booklet £6 Ref EF15.

**FUTURE PC POWER SUPPLIES** These are 295x135x60mm, 4 drive connectors 1 mother board connector. 150watt, 12v fan, Iec Inlet and on/off switch. £12 Ref EF6.

**VENUS FLY TRAP KIT** Grow your own carnivorous plant with this simple kit £3 Ref EF34.

**TWEETERS** 2" diameter good quality tweeter 140R (ok with the above speaker) 2 for £2 REF: MAG2P5 or 4 for £3 REF: MAG3P4

**6"x12" AMORPHOUS SOLAR PANEL** 12v 155x310mm 130mA. Bargain price just £5.99 ea REF MAG6P12.

**FIBRE OPTIC CABLE BUMPER PACK** 10 metres for £4.99 Ref MAG6P13 ideal for experimenters! 30 m for £12.99 Ref MAG13P1

**ROCK LIGHTS** Unusual things these, two pieces of rock that glow when rubbed together believed to cause rain! £3 a pair Ref EF29.

**3' by 1' AMORPHOUS SOLAR PANELS** 14.5v, 700mA 10 watts, aluminium frame, screw terminals, £44.95 Ref MAG45.

**ELECTRONIC ACCUPUNCTURE KIT** Builds into an electronic version instead of needles! good to experiment with. £7 Ref 7P30

**SHOCKING COIL KIT** Build this little battery operated device into all sorts of things, also gets worms out of the ground! £7 Ref 7P36.

**FLYING PARROTS** Easily assembled kit that builds a parrot that actually flaps its wings and flies! 50 m range £6 Ref EF2.

**HIGH POWER CATAPULTS** Hinged arm brace for stability, tempered steel yoke, super strength latex power bands. Departure speed of ammunition is in excess of 200 miles per hour! Range of over 200 metres! £7.99 Ref R9.

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 Marconi TF2330 - or TF2330A wave analysers - £100-£150.  
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 Marconi mod meters type TF2304 - £250.  
 HP 5065A rubidium vapour FX standard - £1.5k.  
 Systron Donner counter type 6054B - 20Mc/s - 24GHz - LED readout - £1k.  
 Rcal/Dana 9083 signal source - two tone - £250.  
 Systron Donner - signal generator 1702 - synthesized to 1GHz - AM/FM - £600.  
 Tektronix TM515 mainframe + TM5006 mainframe - £450 - £850.  
 Farnell electronic load type RB1030-35 - £350.  
 Rcal/Dana counters - 9904 - 9905 - 9906 - 9915 - 9916 - 9917 - 9921 - 50Mc/s - 3GHz - £100-£450 - all fitted with FX standards.  
 HP4815A RF vector impedance meter c/w probe - £500-£600.  
 Marconi TF2092 noise receiver. A, B or C plus filters - £100-£350.  
 Marconi TF2091 noise generator. A, B or C plus filters - £100-£350.  
 Marconi 2017 S/G 10KHz - 1024MHz.  
 HP180TR, HP182T mainframes £300-£500.  
 Philips panoramic receiver type PM7900 - 1 to 20GHz - £400.  
 Marconi 6700A sweep oscillator + 18GHz PI's available.  
 HP8505A network ANZ + 8503A S parameter test set + 8501A normalizer - £4k.  
 HP8505 network ANZ 8505 + 8501A + 8503A.  
 Rcal/Dana VLF frequency standard equipment. Tracer receiver type 900A + difference meter type 527E + rubidium standard type 9475 - £2750.  
 HP signal generator type 626 - 628 - frequency 10GHz - 21GHz.  
 HP 432A - 435A or B - 436A - power meters + powerheads - Mc/s - 40GHz - £200-£1000.  
 Bradley oscilloscope calibrator type 192 - £600.  
 HP8614A signal generator 800Mc/s - 2.4GHz, new colour £400.  
 HP8616A signal gen 1.8GHz - 4.5GHz, new colour £400.  
 HP 3325A syn function gen 20Mc/s - £1500.  
 HP 3336A or B syn level generator - £500-£600.  
 HP 3586B or C selective level meter - £750-£1000.  
 HP 3575A gain phase meter 1Hz - 13Mc/s - £400.  
 HP 8683D S/G microwave 2.3 - 13GHz - opt 001 - 003 - £4.5k.  
 HP 8660 A-B-C syn S/G. AM + FM + 10Kc/s to 110Mc/s PI - 1Mc/s to 1300Mc/s - 1Mc/s to 2600Mc/s - £500-£2000.  
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 Tektronix 465 - 465B - 475 - 2213A - 2215 - 2225 - 2235 - 2245 - £250-£1000.  
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# Best rf article '95

Following the success of 1994's Writers Award, **Electronics World** and **Hewlett-Packard** are launching a new scheme to run from January to December 1995.

Only articles which have an element of rf design will be eligible for consideration by the judging panel. It is hoped that this year's award will focus writer interest on rf engineering in line with the growing importance of radio frequency systems to an increasingly cordless world.

The aim of the award scheme is to locate freelance authors who can bring applied electronics design alive for other people.

Qualifying topics might include direct digital synthesis, microstrip design, application engineering for commercially available rf ICs and modules, receiver design, PLL, frequency generation and rf measurement, wideband circuit design, spread spectrum systems, microstrip and planer aerials... The list will hopefully be endless.

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Featuring 10A dc measurement capability and transistor  $h_{FE}$  test facility, the DT830B is a compact, 3.5 digit multimeter with six measurement functions. This attractive meter normally sells at £23.44, excluding postage. As a special introductory offer – exclusive to *EW+WW* readers in the UK\* – instrument distributor Vann Draper Electronics is offering this meter for just £14.95, fully inclusive of vat and postage.

In addition to reading dc and ac voltage, dc current and resistance, the DT830B has a diode-test function and a transistor  $h_{FE}$  range indicating gains from 0-1000. Supplied with test leads and battery, this compact multimeter weighs just 170g and measures 70 by 126 by 24 – truly a pocket sized instrument. Its liquid crystal display has high-contrast 0.5in high characters.

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Input impedance	1MΩ
Max input	1000V dc
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Basic accuracy	±1.2%
Input impedance	0.45M
Frequency range	45-400Hz
Max input	750V ac
<b>DC current</b>	200μ/2000μ/20m/200m/10A
Basic accuracy	±1.0%
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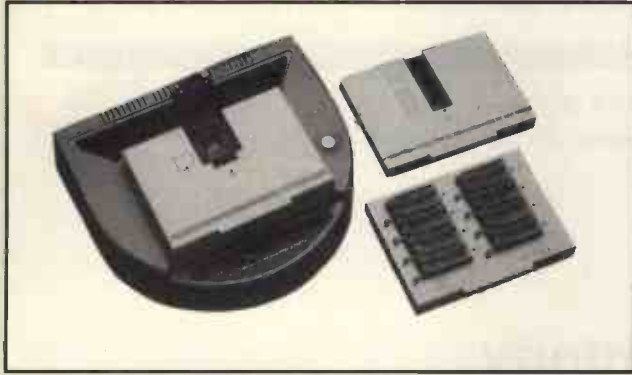
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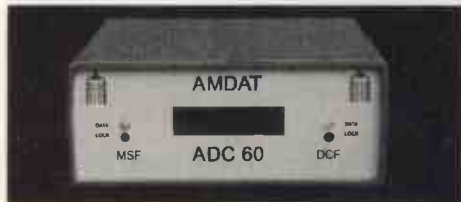
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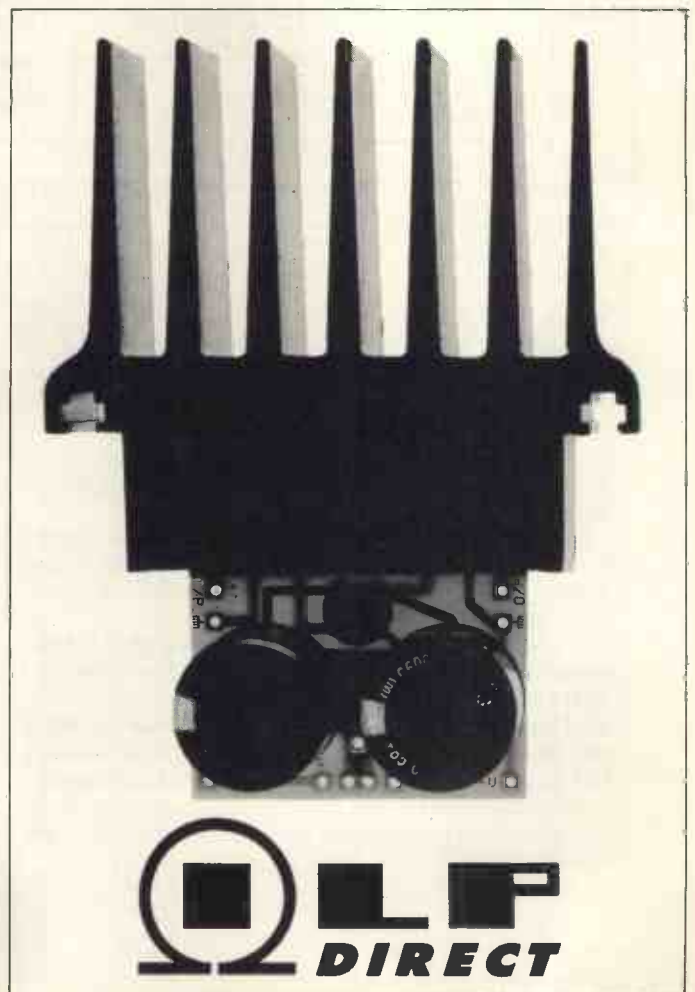
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# CIRCUIT IDEAS

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Do you have an original circuit idea for publication? We are giving **£100** cash for the month's top design. Additional authors will receive **£25** cash for each circuit idea published. We are looking for ingenuity in the use of modern components.

## Prom and logic array in harmony

Once a prom has configured a logic cell array, some of the circuitry configured into the array enables the prom to perform a second function.

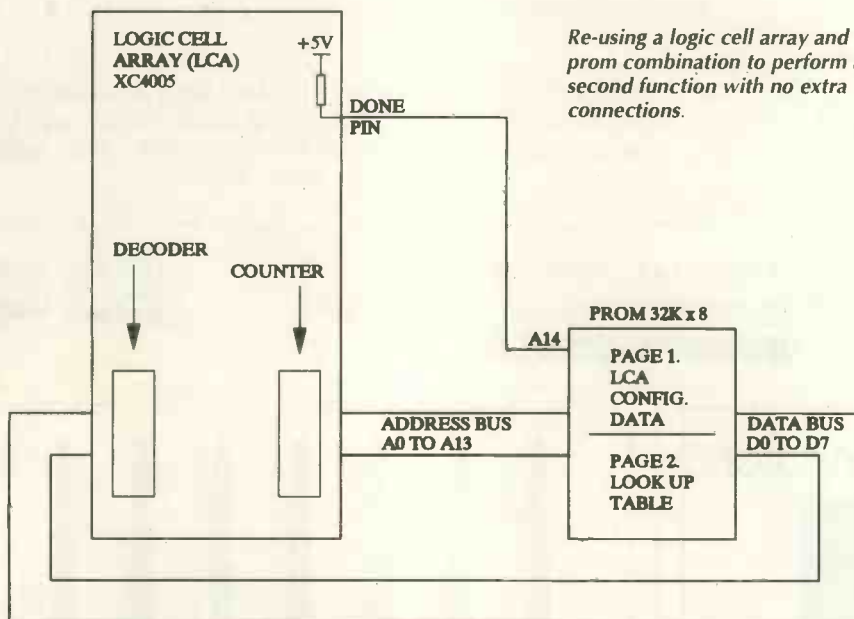
Xilinx XC2000, 3000 and 4000 arrays can be used for this function and the prom is industry-standard, connected to the data and address configuration pins of the array.

Configuration data for the array is in the first page of the prom, the second page carrying data for other functions, in this case a look-up table, the circuits for which are in the array. Connections between the two devices are used for both configuration and, after that is finished, as user-programmable i/o pins.

In the diagram, an XC4005 and a 32K by 8 prom are used together, the first page of the prom taking up 0-16K for the configuration task and 16-32K to form a look-up table to sequence events encoded as data bytes. Counters for prom addressing and a decoder for the table output are in the circuit configured into the array, as is the rest of the circuitry.

A dedicated "Done" pin on the array is low on power up and during configuration, being pulled high by its internal resistor to indicate that the process is complete; it is therefore used to page the second page of the prom at pin 14.

**Brian Wood**  
British Aerospace  
Stevenage  
Hertfordshire



## YOU COULD BE USING A 1GHz SPECTRUM ANALYSER ADAPTOR!

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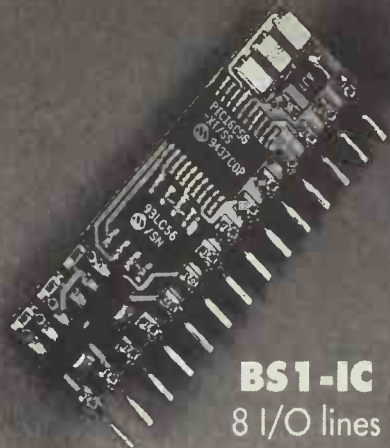
Covering the frequency range 400kHz to over 1GHz with a logarithmic display range of 70dB  $\pm$ 1.5dB, it turns a basic oscilloscope into a precision spectrum analyser with digital readout calibration.

Recognising the importance of good design, TTI will be giving away one of these excellent instruments every six months to the best circuit idea published in the preceding period until further notice. This incentive will be in addition to our £100 monthly star author's fee, together with £25 for all other ideas published.

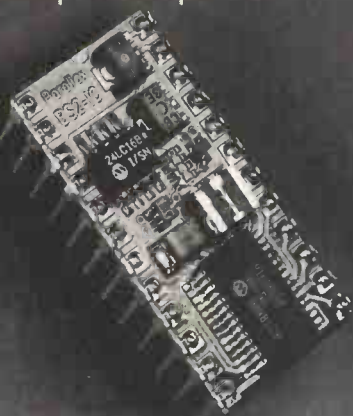
Our judging criteria are ingenuity and originality in the use of modern components – with simplicity particularly valued.



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# Measuring low frequencies using a frequency multiplier

Measuring very low frequencies either takes too long (nearly 17 minutes for 0.1% at 1Hz) or gives the reciprocal, which must be worked out. This circuit provides a high, easily measured frequency directly proportional to the low frequency and only needs one cycle of the input to give the output.

One cycle of the  $f$  input gates a stable clock, divided in frequency by 1000, into a 4040 12-bit binary counter, which then contains a binary

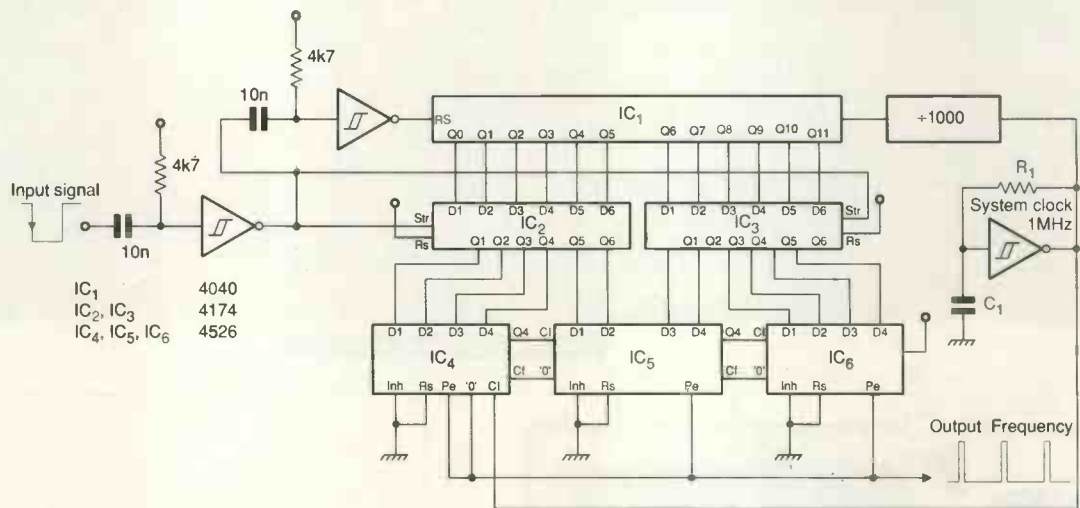
number proportional to the period of the  $f$ . Dividing this by means of the 4526 into the original clock gives an output signal 1000 times higher in frequency than the input.

Negative-going transitions of the input  $f$  are differentiated to give a strobe pulse, which latches the binary result from the counter  $IC_1$  into the 4174s,  $IC_{2,3}$ , this binary number being used by the 4526 dividers  $IC_{4,5,6}$  as divisor. The strobe also forms the pulse to reset  $IC_1$ .

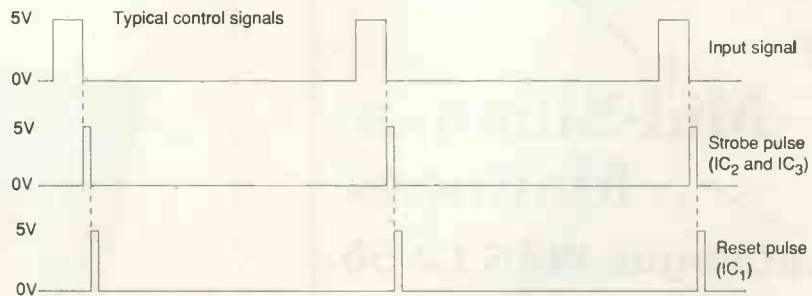
As an example, suppose the input is 2Hz and the clock 1MHz; the binary number in  $IC_1$  will be  $(1MHz/1000)/2=500$  and the output frequency  $1MHz/500=2kHz$ .

If required, overflow circuitry could be arranged on  $IC_1$  to show an out-of-range input.

**Des Keppel**  
Ballon  
County Carlow  
Republic of Ireland



Frequency multiplier for low frequencies surmounts the traditional frequency-measurement difficulties. It needs only one input cycle of low frequency to give a measurable output.



Very simple and elegant circuit acts as both on-off switch and 'snooze' timer for battery-powered devices.

# Non-invasive two-terminal timer

This is a 'snooze' switch for battery-powered radios or other appliances that calls for no modification to the equipment.

A polyester tape coated on each side with copper is simply slipped between two of the cells inside the radio and led out through the lid—there is usually enough of a gap to allow this.

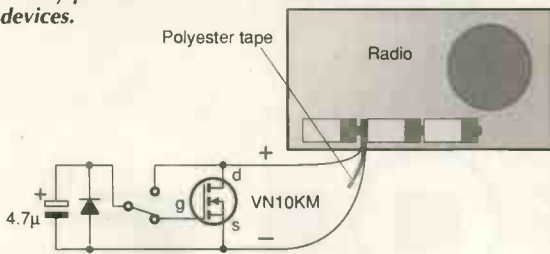
Four components form the circuit, the switch being a sub-miniature, spring-biased microswitch, shown in its rest position. In normal operation in use as an on-off switch, operating the switch once charges the capacitor to the battery voltage which, when the button is released, is applied to the VN10KM vmos fet, turning it on

and completing the battery circuit.

A second switch operation discharges the capacitor through the transistor; when the button is released, the zero voltage on the capacitor turns the fet off.

As a timer, the circuit depends on leakage through the diode. If the radio is left on, leakage slowly discharges the capacitor and, after about 30 minutes, the fet turns off.

**Scott Arnesen**  
Oslo  
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## Phase-synchronising a crystal oscillator

Synchronising the output of a crystal oscillator to an external signal within one cycle is practically impossible, because the  $Q$  of the crystal is so high. You can pull the frequency by capacitance, but that takes several cycles; and if the oscillator is switched on and off, it starts in random phase and may well affect its stability. The circuit shown here allows synchronising to a sync. pulse to

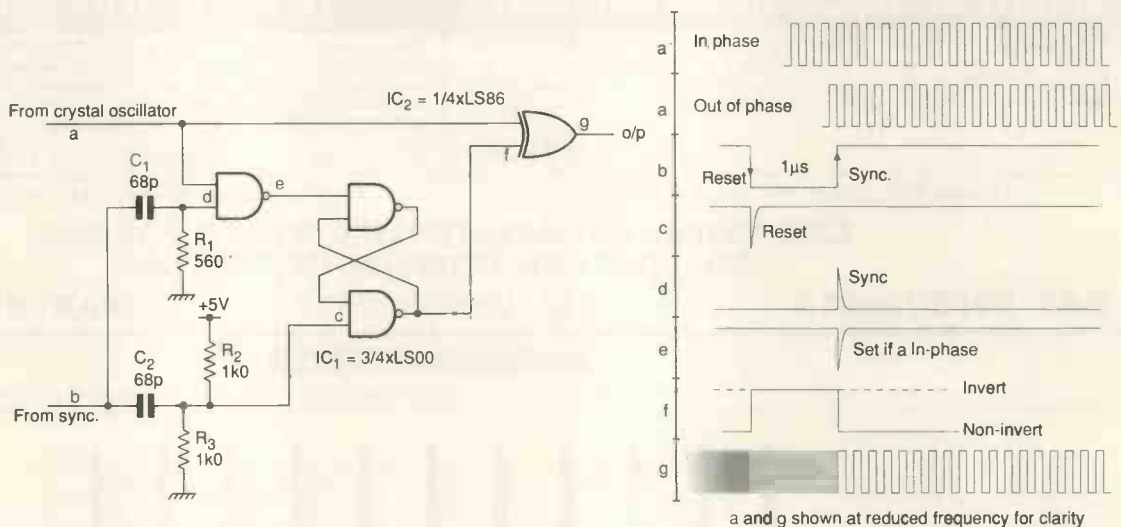
within  $90^\circ$ , close enough in some applications.

The Ex-Or provides the appropriate polarity after comparison in the Nand gate. If the oscillator is in phase with the sync., signal (e) appears and sets the latch; if it is out of phase, signal (e) is absent and the latch is not set, the Ex-Or inverting the oscillator signal or not accordingly. Better resolution could be

obtained by using an oscillator of a frequency  $n$  times higher than the output, dividing it as needed. To select the correct phase, reset the divide-by- $n$  divider counter as the sync. pulse arrives. A presettable counter would allow phase-shifting as well as synchronisation.

**C J D Catto**  
*Elsworth*  
*Cambridgeshire*

*Circuit synchronises a crystal oscillator to an external pulse to within  $90^\circ$  in less than one cycle.*



## Half-duplex from a pc's RS-232 COM port

Using an RS-485 half-duplex driver from the full-duplex RS-232 port of a pc poses the

problem of how to switch the receiver and driver enables in the MAX487 RS485 driver IC. An RTS

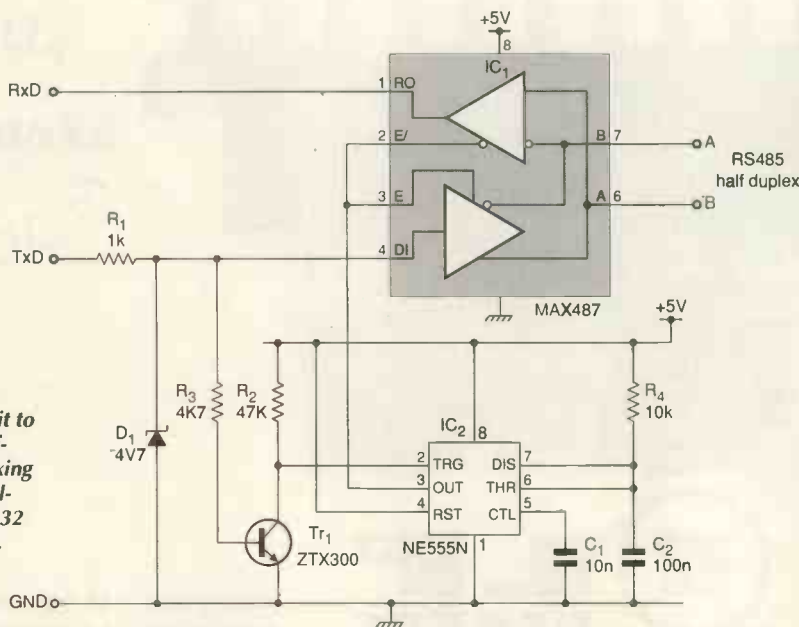
signal from the pc cannot be used, because it is usually permanently asserted. This circuit solves the problem simply and cheaply.

TxD from the pc is clamped to ttl level by  $R_1$  and the zener to drive the 487's driver; ttl output from the receiver will drive most pc RxD inputs directly. The 487 receiver is normally enabled and its driver disabled to allow characters to be sent to the pc. When a character is output, the start bit triggers the 555 astable for exactly one transmission period, set by the values of  $R_4$  and  $C_2$  at 1.1ms, to enable the driver and disable the receiver. These values give almost exactly 9600 baud, but clearly others could be used for different speeds.

Since RS-232 output levels invert ttl inputs, A and B differential outputs of the 487 have to be connected the "wrong" way round.

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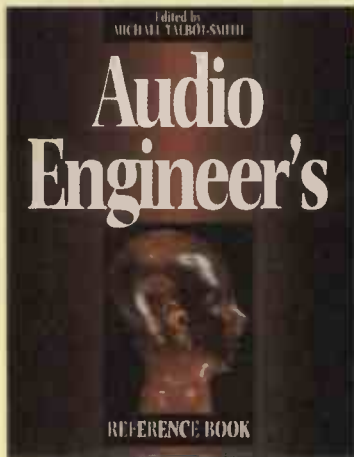
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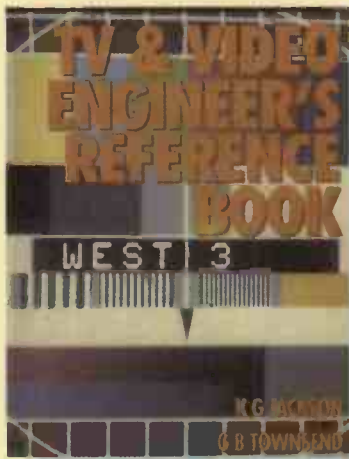
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# Versatile stepper motor controller

Handling three stepper motors together with eight digital inputs and eleven analogue ports, this interface designed by Pei An controls three axis movement while making available a variety of feedback information from sensors.

This versatile triple stepper motor controller board is able to control 3 stepper motors. It also provides an 8-bit input port and an 8-bit output port. Moreover, it incorporates an 11 channel 8-bit a-to-d converter which enables the board to read analogue signals from 11 channels.

The board connects to a pc via a general purpose COM or LPT port i/o card, outlined in the panel.

## System overview

Four blocks make up the stepper driver system – a stepper motor unit incorporating three separate drivers, a data i/o unit, an a-to-d converter unit and a buffer selector. Figure 1.

Each unit has one or several data buffers associated with it, each sharing the same internal data bus. In order to write data to a particular buffer, the clock signal must be supplied to

latch the data as generated by the buffer selector. Figure 2 gives the complete circuit diagram.

## Buffer selection

Chip selection for the buffers is based on a 74LS138 3-to-8-line decoder. Decoded lines Y<sub>0-7</sub> are normally logic high. Applying an address on pins A<sub>0-2</sub> causes one of the output lines to go low.

Address lines A<sub>0-2</sub> connect to C<sub>0-2</sub> of port C of the 8255. Output lines Y<sub>0-4</sub> are used to latch data to the five data buffers. Correspondence between the address applied to the 74LS138 and the data buffers selected is shown in Table 1.

Table 1. Addresses on A<sub>1-3</sub> of the LS138 select the various Y outputs to control the three motors, the output port and the a-to-d converter data buffer.

A <sub>0</sub>	A <sub>1</sub>	A <sub>2</sub>	Output	Data destination
0	0	0	Y <sub>0</sub> low	IC <sub>5</sub> , motor driver 1
1	0	0	Y <sub>1</sub> low	IC <sub>6</sub> , motor driver 2
0	1	0	Y <sub>2</sub> low	IC <sub>7</sub> , motor driver 3
1	1	0	Y <sub>3</sub> low	IC <sub>8</sub> , 8-bit o/p port
0	0	1	Y <sub>4</sub> low	IC <sub>11</sub> , a-to-d buffer

## Stepper motor driving

Working principle of a stepper motor, Fig. 3, shows an imaginary four phase unipolar stepper motor with a single permanent magnet rotor. Its stator comprises two pairs of electro-magnetic poles.

Although this motor only has four steps per revolution, it operates on the same principle as a real motor, which will have more pole pairs.

During operation, terminals A, B, C and D are connected to the negative rail of the power supply individually or in combination to generate a rotational electro-magnetic field inside the stator. As a result, the rotor turns accordingly.

Three methods of driving a stepper motor are 'wave', 'full-step' and 'half-step' drive. Wave, or single-phase drive is the simplest. Each winding is energised in sequence. The rotor turns to align its magnetic poles with the corresponding magnetised poles of the stator. The rotor moves to each position

## Stepper motor controller specifications

### Stepper motor driver

Stepper motors controllable	3
Type of stepper motors	four-phase, unipolar
Modes of stepper motor operation	one-phase, full-phase, half-step
Maximum voltage, with heatsink	35V for stepper motor controls
Maximum current, with heatsink	1.5A

### User i/o ports

Numbers of ttl input lines	8
Numbers of ttl output lines	8

### A-to-d converter, for serial i/o

Conversion accuracy	8bit
Conversion frequency	45,000/second
Multiplexed analogue inputs	11
Input voltage range	0-2.5V

### PC i/o requirements

Centronics 25 line i/o card or RS232 25 line i/o card

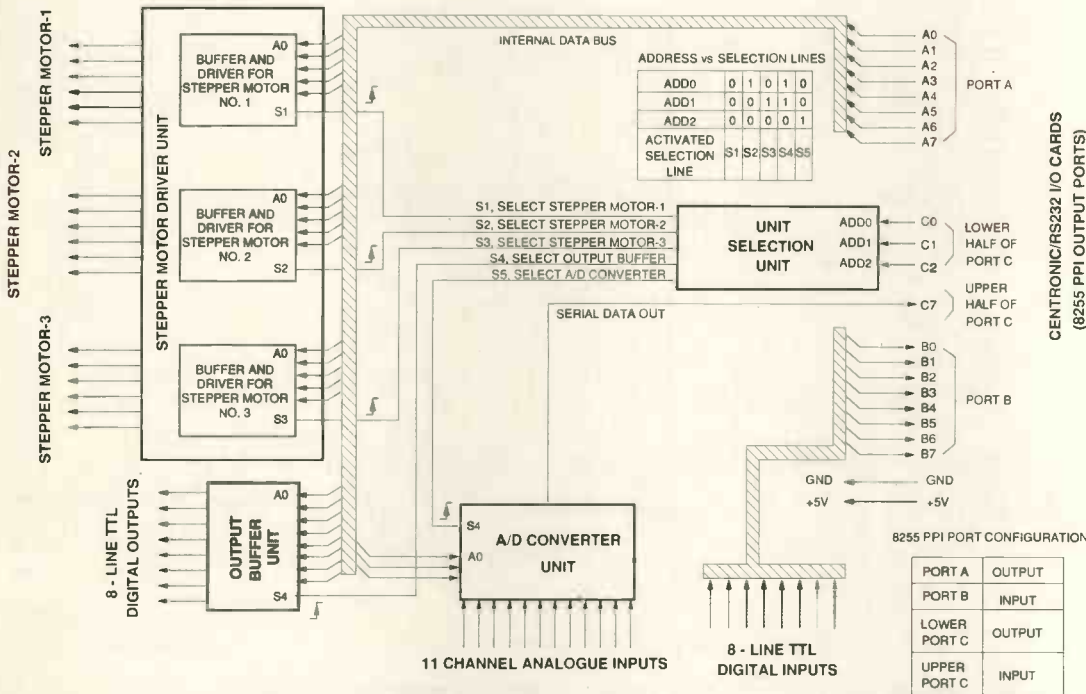


Fig. 1. Stepper motor driver modules, combined, provide control for three motors plus 11 analogue monitoring channels and eight digital outputs.

### Technical support

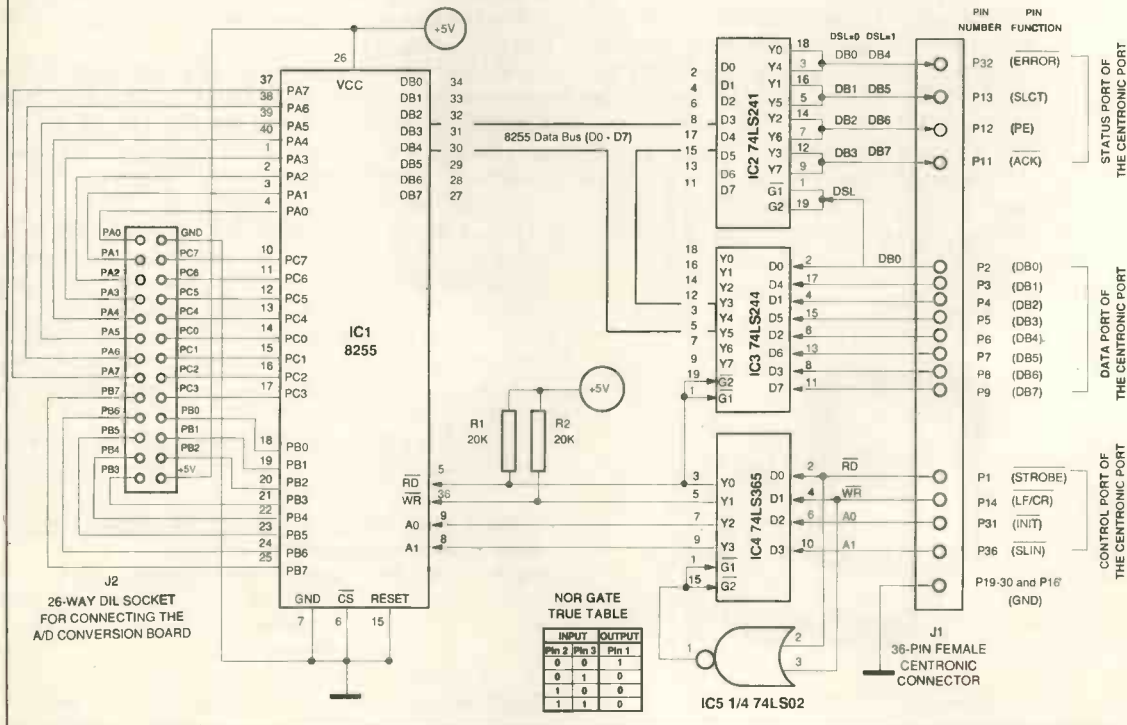
The Centronics 24-line i/o card, the RS232 24-line i/o card and the stepper motor controller board are all available from the author both in the designer's kit form and in assembled and tested form. The designer's kit includes the pcb and all the necessary components. Demonstration control software written in Turbo Pascal 6 - source code and EXE file - are included in the packages. Please contact Dr Pei An at 58 Lamport Court, Lamport Close, Manchester M1 7EG, Tel/Fax/Ans: +44-(0)161-272-8279.

### Input/output for the pc

Two i/o cards are available for connecting the stepper motor controller to the pc. The first interfaces with the Centronics port, and the second with the RS232 port. Details of the Centronics i/o card were presented in 'Catching Data from LPT1' EW+WW, Feb 1995.

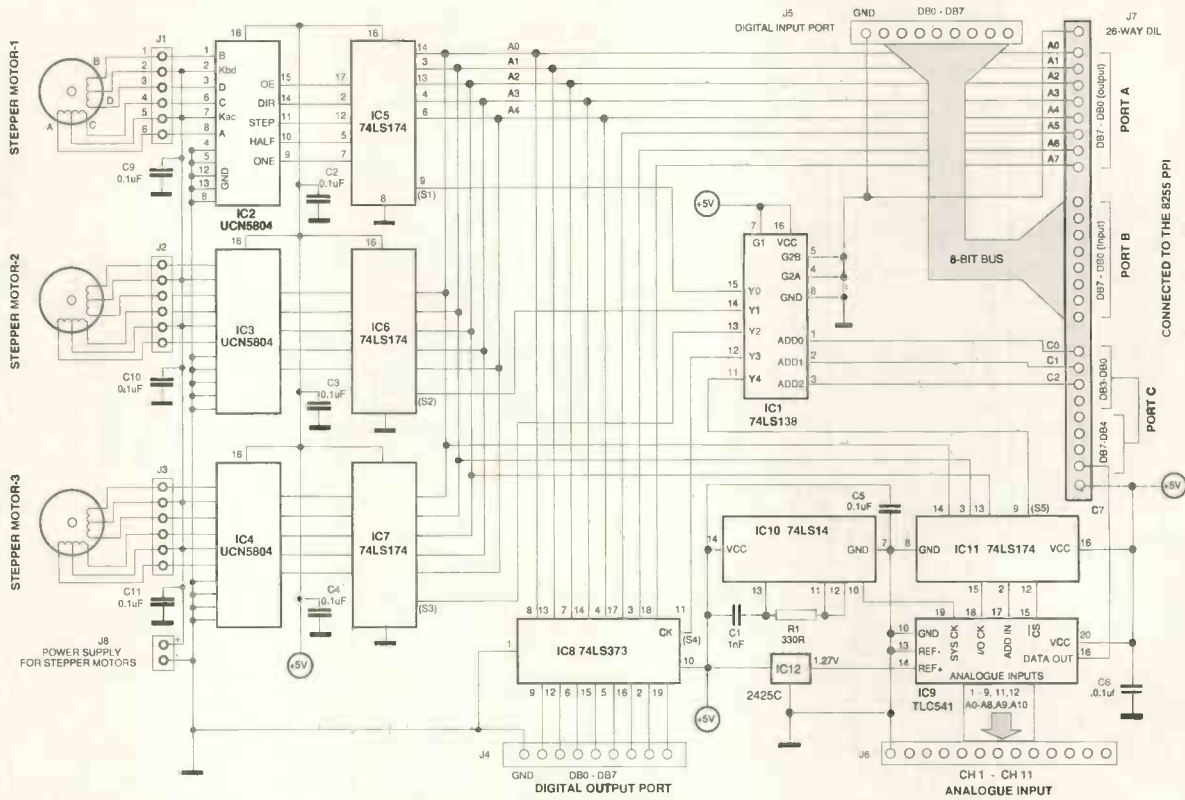
Both i/o cards incorporate an industrial standard interface chip - the 8255 peripheral programmable interface - which provides 24 i/o lines. These lines are organised into four groups: port A, 8 lines, port B, 8 lines, upper half port C, 4 lines, and the lower half port C, 4 lines.

All four ports can be programmed as i/o ports via software. In this application, Port A and lower of half port C are programmed as output while port B and the upper half of port C are programmed as input.



Versatile interface for translating pc i/o from the LPT port to three parallel i/o ports. By adding a uart, a similar interface for connecting to a COM port can be produced.

Fig. 2. With the addition of three eight-bit ports from a common 8255 parallel i/o chip and control software, this analogue, digital and stepper-motor i/o card is controllable from a pc. Note that output of IC<sub>12</sub> is 2.5V, not 1.27V as shown.



in turn, completing a full revolution after four steps. Positions of the rotor and the energising patterns of the winding are shown in Fig. 4a.

By energising the windings in reverse order, the rotor turns in the opposite direction. As only one winding is energised at a time, motor torque is rather low. To improve this 'full step' drive is used.

Full-stepping involves a similar four step sequence as the wave alternative, except that two windings are energised for each step. The rotor aligns its magnetic poles with the strongest magnetic field between the two poles of the stator, Fig. 4b). Because two windings are energised for each step, motor torque is improved.

Half-step drive is a combination of wave and full-step drive. It takes advantage of the rotor's ability to align alternately with the stator's poles and between them - doubling the number of steps available for one revolution, Fig. 4c). Using this method, motor torque varies with steps, but the motor runs more smoothly.

Stepper motors are normally used in precision control applications, which usually require accuracy of movements rather than speed. There is no lower speed limit for stepper motors but there is an upper limit which varies according to motor type.

**Stepper motor driver ICs**

For the stepper motor described, drive sequences are readily accomplished by several industrial standard stepper motor drivers. Stepper motor drivers often use several UCN5804 ICs, which are capable of driving four-phase unipolar stepper motors in full, half and single-phase modes, Fig. 5b).

Two power supplies are required by the IC, one to drive its logic circuits and the other to drive the stepper motors. Pin 16, V<sub>cc</sub>, connects to the positive rail of the logic power supply, with a maximum voltage of 7V. Pins 2 and 7 connect to the power supply for the stepper motors. Pins 4, 5, 12 and 13, are all wired to the ground of the power supplies.

Four outputs, pins 1, 3, 6 and 8 each with internal protection diodes, are connected to four internal Darlington transistors which have a maximum rating of 35V at 1.5A. A heat sink may be needed.

Logic high applied to the output enable pin disables outputs connected to the stepper motor. Pin 14 sets the rotation direction of the motor while pin 11 is the step input. With a negative transition the stepper motor rotates one step. Drive modes are configured by setting the logic levels at pin 9, 'one-phase' and pin 10, 'half-step'.

Control signals to each UCN5804 are supplied by a data buffer based on a 74LS174. This hex D-type bistable device latches data to the outputs on the low-to-high logic clock transition. Data buffers IC<sub>5,7</sub> share the internal data bus. Clock inputs of the buffers are wired to Y<sub>0,2</sub> of the 74LS138.

Data bus lines DB<sub>0,4</sub>, connected to A<sub>0,4</sub> of port A, correspond to output-enable, direction, step input, half-step and one-step signals.

When writing data to a particular buffer, it is sent to the data bus by initially writing the data to Port A of the 8255

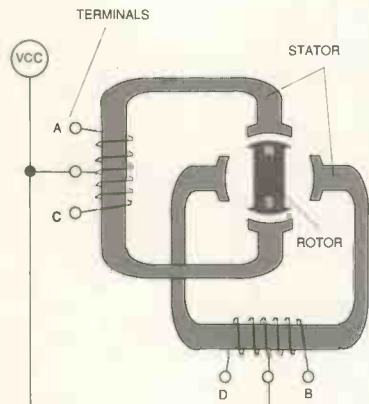


Fig. 3. Stepper motor essentials. This version is simplified - it has only 4 steps per revolution - making it easy to see how energising the coils polarises the stator and causes rotation.

Table 2. Logic signals applied to the stepper motor IC determine the operating mode.

Pin 9	Pin 10	Stepper motor mode
L	L	Two-phase
H	L	One-phase
L	H	Half-step
H	H	Step-inhibit

During operation, pins 9, 10 and 14 should only be changed when the step input is logic high.

PPI. A low-to-high-then-low pulse is then applied to the clock of that particular buffer.

**Output/input unit**

A 74LS373 D-type octal latch, IC<sub>8</sub> performs i/o. Data inputs connect to the internal data bus and the clock is wired to Y<sub>3</sub> of IC<sub>1</sub>. Data to be written to the bus is initially sent by writing the data to Port A and then applying a positive pulse to pin 11 of IC<sub>8</sub>. This pulse is generated by the 74LS138 decoder by sending the corresponding address to the decoder via port C. Lines of the input port are connected directly to port B, configured as an input port.

**Converter analogue to digital**

The a-to-d converter module incorporates a TLC541. This is an eight-bit successive-approximation a-to-d converter that uses switched capacitor technology. It includes on-chip sample-and-hold facilities and a 12-channel analogue multiplexer.

Interfacing between the 40ksample/s a-to-d converter and the peripheral circuits is serial. Figure 5a) shows the pin-out of the TLC541, which has a typical power dissipation of 6mW. An external band-gap voltage reference is used.

The 12-channel analogue multiplexers divide into two groups. The first provides 11 inputs for connecting external analogue signals on pins 1-9, 11 and 12. These correspond to analogue inputs 0-11. In the other group, one input connects internally to a voltage reference for self testing purposes. To select an analogue input, a 4-bit address has to be written into the IC via the serial interface.

Serial interfacing consists of five ttl compatible three-state i/o lines. The system clock controls data conversion and has a maximum clock frequency of 2.4MHz. This results in a data conversion rate of 4kHz. The i/o clock synchronises i/o operations.

Addressing for the analogue multiplexers is provided via serial input 'Add-in' while 'Data out' provides access to the serial data. When the enable pin is high, all i/o is disabled, allowing several such devices to be used on a shared bus.

System and i/o clocks are used independently, so there is no need for special speed or phase relationships between them.

Writing and reading sequences of the IC are as follows, with reference to Fig. 6.

- Chip-select is brought low to start the read/write cycle. To minimise errors caused by noise at the select input, internal circuitry waits for two rising edges. It then waits for a falling edge of the system clock after the high-to-low transition is detected on the select pin, before it is accepted. The most-significant bit of the previous conversion result, DB7, automatically appears on the data out pin.

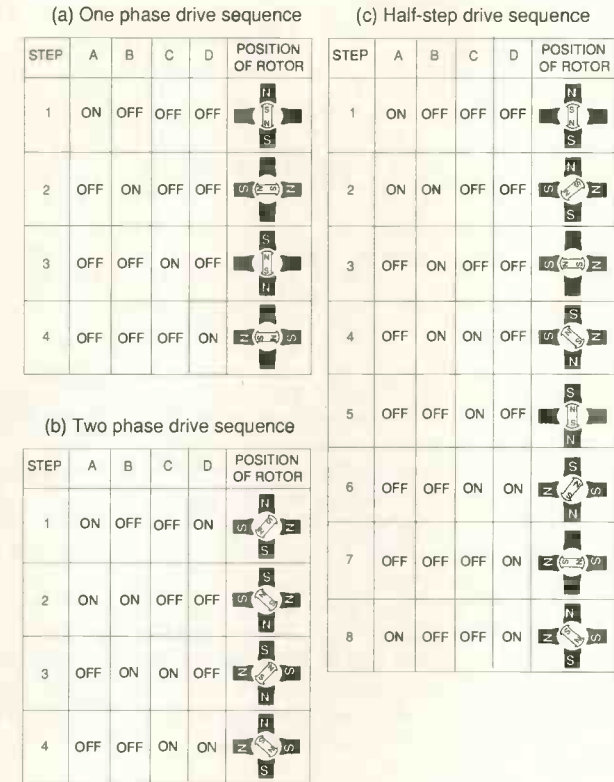


Fig. 4. Energising sequences of stepper-motor windings, comparing single, dual and half-step drive methods.

- A new multiplexer address, A<sub>0,3</sub>, is shifted into the IC on the first four rising-edges of the i/o clock. Level A<sub>3</sub> is shifted in first. Negative edges of the i/o clock shift out DB<sub>6,3</sub> of the previous conversion result. Sampling of the newly addressed analogue input begins after the fourth falling edge of the i/o clock.

- Three further clock cycles are applied to the i/o clock. Data bits DB<sub>2,0</sub> of the previous conversion result are shifted out on each negative edge of the 'i/o clock'.

- On the eighth and final clock cycle applied to the i/o clock, the falling edge completes the sample process and initiates the hold function. Data conversion is then carried out during the next 36 system clock cycles. After this, either the select pin must go high or the i/o clock remain low for at least 36 system clock cycles to allow for the data conversion.

Select can be kept low during periods of multiple conversion. But prevent noise from getting into the i/o clock, since this could cause the device and external interface circuit to lose synchronisation. If the select line is taken high, it must

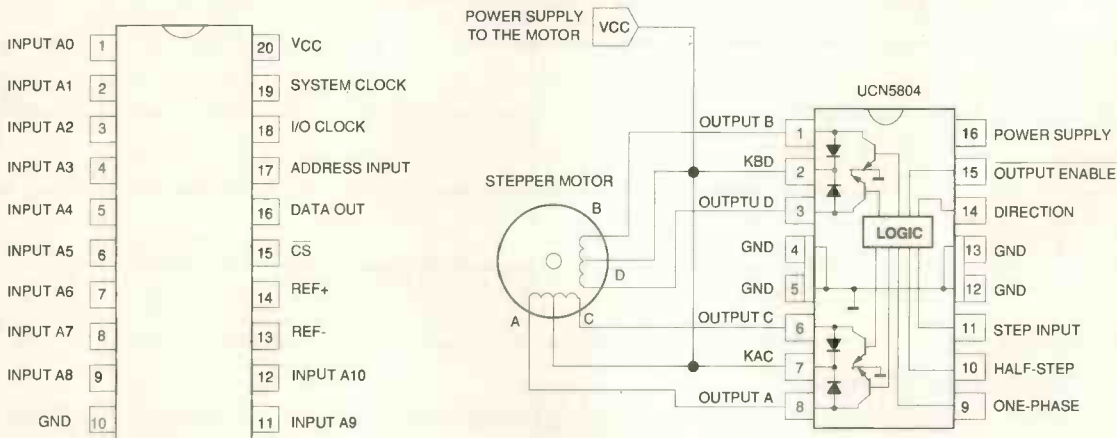
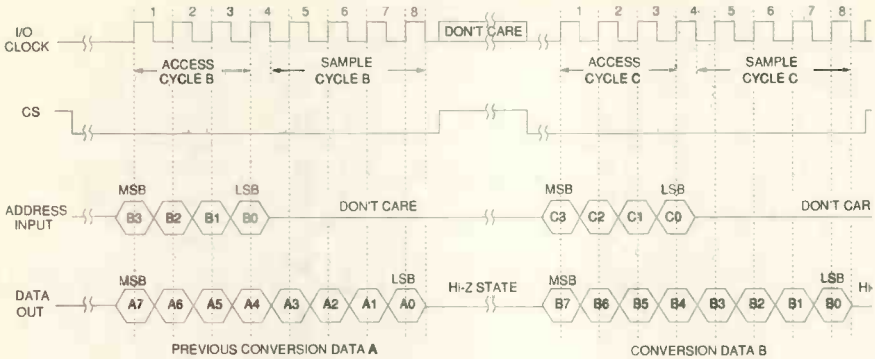


Fig. 5. Analogue input channels of the TLC541 converter, left (a), are arranged in order to simplify pcb design. Pin designations and typical application of the 5804 stepper-motor driver are shown on the right (b).



**Fig. 6. Interfacing of the TLS541 a-to-d converter is greatly simplified since addressing and reading are carried out serially. When deselected, the chip places all i/o in its third state, allowing multiple chips to be connected on a common bus.**

remain high until the end of the conversion – a valid falling edge of the chip-select will cause the device to reset and abort its present conversion.

System clocking is provided by a 2MHz 74LS14 oscillator. The i/o clock, address input and chip select are controlled by A<sub>0-2</sub> of port A via data buffer 74LS174, IC<sub>11</sub>. Its clock pin connects to Y<sub>4</sub> of the decoder. Serial data out, pin 16, connects to DB<sub>7</sub> of port C.

A 2.5V, ±0.8%, reference for the a-to-d converter is provided by the TLE2425CLP. Power supply for the 5V logic circuits is supplied directly from the i/o cards. Power for the stepper motors is supplied from a separate supply, the requirement of which will depend on the types and the number of motors used in the application.

**Programming**

A Centronics or RS-232 24-line i/o card is needed to control the stepper motor board.

**Controlling the stepper motors** – to write control data to a particular stepper motor driver, it is first written to port A. It is then latched to the specified data buffer following a clock signal from IC<sub>1</sub>. Address inputs to decoder IC<sub>1</sub> connect to the lower half of port C, namely C<sub>0-2</sub>.

Initially, address inputs DB<sub>0-2</sub> and selection lines S<sub>1-5</sub> are logic high. When an address is sent to IC<sub>1</sub>, it makes one decoded line go high to low. This results in latching data present on the internal data bus to the data buffer.

**Sending data from the output port** – Data buffering for the output port is controlled by Y<sub>3</sub> of the 74LS138. To send data to the output port it is first written to port A. Next a clock signal is issued to that buffer.

**Reading data from the input port** – Control software reads data from port B directly.

**Controlling the a-to-d converter** – Initially, the select line is brought high to low to start the read/write cycle. The most-significant bit of the new multiplexer address, B<sub>3</sub>, is sent from A<sub>1</sub> of port A. Port C, upper half, starts to read DB<sub>7</sub> of the previously converted result. It is followed by a positive-going pulse via the i/o clock connected to A<sub>0</sub> port A via IC<sub>11</sub>.

At the low-to-high transition of the pulse, the address bit is latched into the a-to-d converter. At the high-to-low transition, the next bit of the conversion result appears on the data out. These procedures are repeated seven times to latch the four-bit address into the IC and to transfer the eight-bit conversion data into the pc.

The chip-select line connects to A<sub>2</sub> port A and is brought high to allow a-to-d conversion. Latching data to the data buffer, IC<sub>11</sub> occurs as described earlier. ■

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# In a lab on a keyboard

This virtual electronics lab – **Electronics Workbench** – was originally intended as a teaching aid but is now being offered as a serious design tool for electronics engineers. Review by **Allen Brown**.

It has become general engineering practice for designers to simulate their designs before any building or construction takes place. To help the electronics engineer in this phase of the design process, a number of software simulation packages have become commercially available.

Spice is now particularly well known for this purpose – especially among analogue designers. A relatively new contender is *Electronics Workbench*, from Interactive Image Technology of Ontario. This package started life as an educational aid and is now attempting to make an impact as a serious design tool for electronics engineers.

Running under Windows, dos or on the Apple Mac, Version 4 is a simulation environment that permits mixed-mode or analogue and digital design simulations. Engineers familiar with the mouse driven graphics user interface, GUI, will experience very few problems in mastering *Electronics Workbench*.

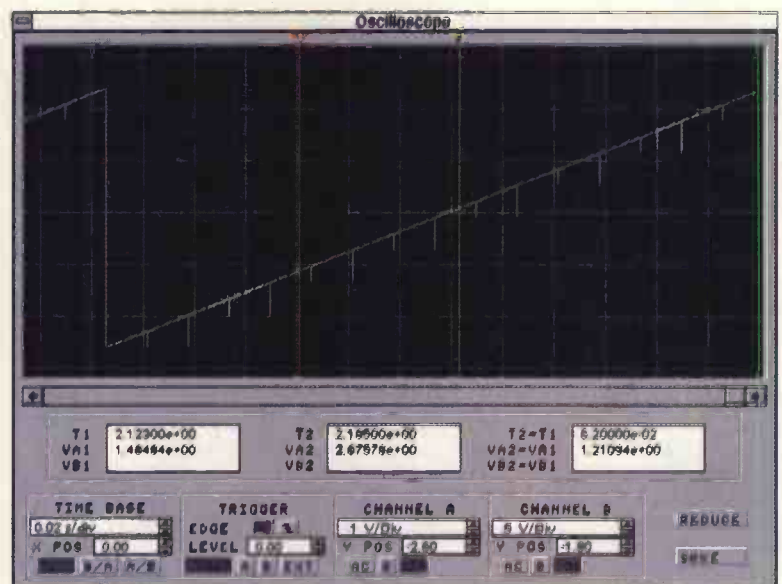
One of the basic ideas behind the design of *Electronics Workbench* was to make it appear like an electronics lab. It is therefore furnished with a number of laboratory instruments and a vast store of electronics components – over 500 op-amps for example. The electronic components are grouped into various categories and accessed via the appropriate icon at the top of the screen. Likewise the general purpose instruments are accessed in the same way. They comprise:

- Digital voltmeter
- Oscilloscope
- Logic word generator
- Logic analyser
- Function generator

There are two other facilities not usually realised in laboratory instrument format – a Bode Plotter and a Logic Converter. These instruments can be used in a variety of modes depending on requirement. However the user must expect limitations that real instruments do not possess, for example the time base on the 'scope is rather basic – no delayed triggering.

## Instruments

The Oscilloscope in the *Electronics Workbench* product is a two channel device with standard triggering. The 'scope icon is dragged into the working area and a double click



produces a miniature scope. The 'scope can then be expanded as shown in Fig. 1. Both channels of the 'scope are updated during the simulation process and have independent gain controls with a common time-base. The instrument can also display Lissajous figures from the two channel inputs.

The logic analyser is an eight channel device, triggerable in one of three ways – trigger terminal, burst mode and pattern definition. It also has an adjustable time-base, but the other options on the logic analyser are pretty meagre so don't expect too much.

The function generator provides three waveforms, sine, triangle, and square wave, all with variable frequency, amplitude, duty cycle and dc offset. By adjusting the duty cycle on the triangle wave both positive or negative sawtooth waveforms can be generated. I would have expected to have seen a provision for adding random noise to the waveforms.

There appears to be no way of injecting noise into a design. Considering the abundance of noise in the real world I would have thought it was essential to have a method of simulating it. Also there does not appear to be a

*Fig. 1. Blow-up of the oscilloscope showing the output of a the d-to-a converter when fed with the outputs of two cascaded generic counters – the effects of the propagation delays are clearly present.*

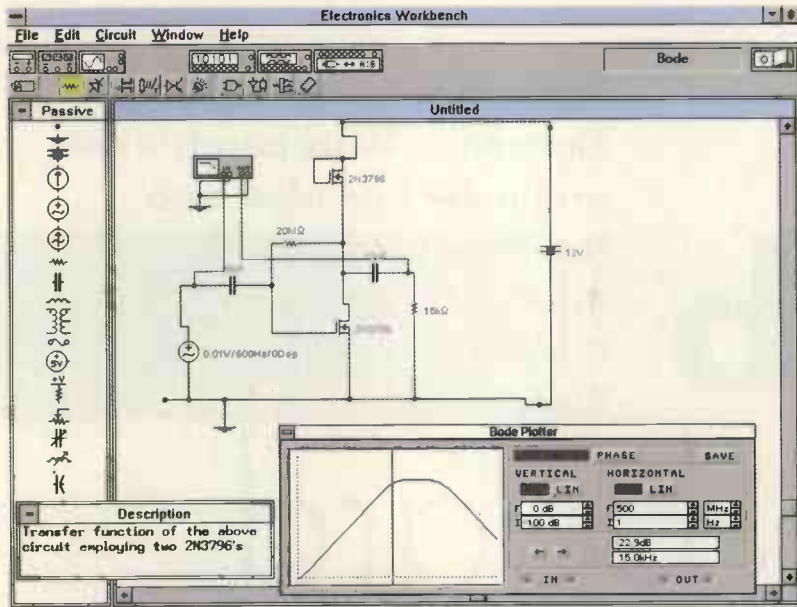
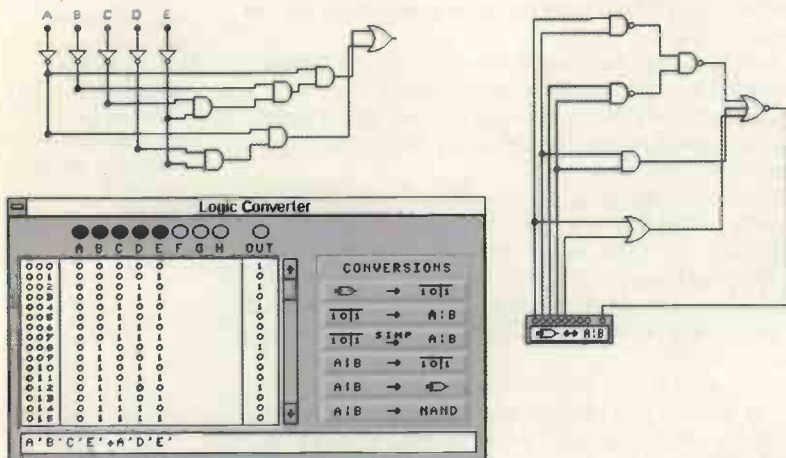


Fig. 2. Determining frequency response of a circuit can be achieved using the Bode Plotter. Unfortunately there isn't a great deal of information on the Plotter graph although the moveable cursor is very useful.

Fig. 3. An example of the logic word generator, the original schematic is on the right and the simplified design is above the pattern table. The reduced Boolean expression can be seen at the base of the table - no need for Karnaugh Maps any more!



method for injecting a signal stored on disk into the circuit. This would be a useful aid for estimating the performance of a circuit with pre-recorded signals.

It would also have been nice to have had a permanent ttl output from the function generator without having to set it up on each occasion. As part of a wish list for the next version of *Electronics Workbench I* would also be pleased to see a pulse generator instrument included.

The logic word generator can generate up to sixteen eight-bit patterns and is useful for verifying performance of small-scale digital circuits. When constructing patterns, a 17in monitor is useful as bits are very small and tend to strain the eyes. However the generator performs its task well and is useful for verifying sequential logic designs.

What is missing though is a device for generating serial data streams. Fortunately the user can fabricate this device using a shift register and logic word generator.

Determining frequency characteristics of a circuit can be accomplished by using the Bode Plotter. The user chooses the initial and final frequencies together with initial and final dynamic ranges.

Once the transfer function is drawn the user can move a cursor line which provides a reading in decibels - or phase angle - with the corresponding frequency. An example of the Bode Plotter is shown in Fig. 2. There is a number of features missing from this instrument. The Bode Plotter cannot be expanded in the same way as an oscilloscope

and the display has no labelled axis. Also it would have been nice to see the phase plot in conjunction with the magnitude plot in order to provide an easier reading of phase margin.

**Digital design**

A design comprising combination and sequential logic can be constructed from generic components that have ideal characteristics. They can then be replaced by equivalent family components - ttl, cmos or others. Alternatively a logic design can be constructed directly from 74 series ICs.

The number of 74 series ICs in the model set ranges from 7400 to 74466 including various family variants. Alternatively there is a large number of cmos 4000 series devices to choose from. Both devices are selected using the sub-window on the left hand side of the screen as seen in the figures.

Wiring up individual components or gates is relatively easy to implement. However behaviour of the wiring algorithm is somewhat strange and sometimes results in contorted wiring schemes which cannot be automatically corrected. Once the design is complete the user can attach coloured monitors to wires to determine their status as the circuit is operating.

The logic converter is a smart idea for combinational logic design, taking up to eight inputs and dispensing with the need for Karnaugh maps. The user can enter the bit pattern and the desired response. Alternatively the user can enter the logic gates and convert the logic to derive the Boolean expression and simplify it. The simplified design is then redrawn via the logic-converted routine.

Although this is a neat idea, there are two problems in this version of the software. Firstly, the logic design generated can overlay the original design location on the screen. Secondly, the logic design generated only uses two-input logic gates instead of multiple input gates which is normal. Figure 3 shows an example of a combinational logic design using the 'logic word generator'.

For sequential logic design *Electronics Workbench* has a choice of flip-flops - RS, D-type and JK - with or without the asynchronous input controls, preset and clear. When cascading a design the user must be careful to keep a substantial distance between flip-flops because of the wiring problem mentioned previously.

As an illustration Fig. 4 shows a design of a Mealy machine for detecting the binary sequence 10110. The input sequence is generated with the word generator - using one bit only. *Electronics Workbench* also provides 8-bit generic a-to-d and a digital converters.

**Analogue simulation**

Via the schematic entry facility, it is very easy to construct an analogue circuit with *Electronics Workbench*.

To begin with the user can enter ideal components and then replace them with actual components from the vast libraries available with the product. Devices come in model sets which are constructed to conform to the Spice syntax model standard. Model set 1 is supplied as part of the product. This comprises diodes, n-p-n and p-n-p transistors, op-amps, j-fets, mosfets, scrs, diacs and triacs. Model sets 2 to 5 are optional and comprise an enhanced range of the above list - all in all an impressive list of components.

If you have your own data base of components conforming to the Spice standard, these too can be imported into *Electronics Workbench*.

There is a large number of components for building analogue simulation and these are accessed from the component windows. Capacitors, resistors and the like, all have adjustable values. However, it would have been nice

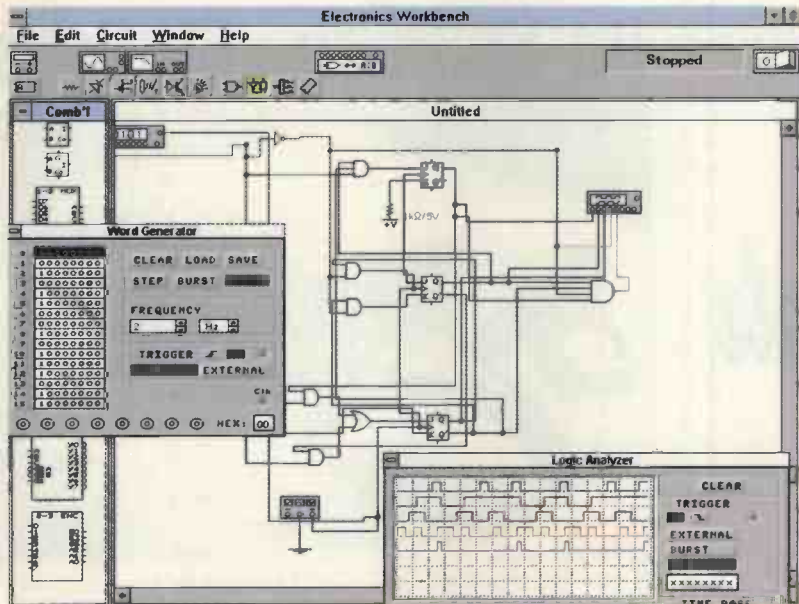


Fig. 4. Logic Word Generator used in combination with the logic analyser can serve as a useful tool for verifying sequential designs. In this example, a detector for the sequence 10110 has been constructed from three JK flip-flops. The output of the circuit is the last trace on the Logic Analyser.

to see tolerance values attached to components as well. This would allow performance envelopes to be derived for a simulation – worst and best case analysis for example.

Electronics Workbench performs its analogue simulation according to the Spice 2 model and is able to perform dc analysis, ac analysis using the Bode Plotter and transient time-domain analysis.

Circuit modelling often involves the use of simultaneous equations. Electronics Workbench employs sparse matrices coupled with the partial pivoting algorithm for finding solutions very quickly. Modelling integral and differential behaviour relies on the trapezoid rule of integration. This is by no means the most effective method of performing numerical integration and it would be useful if future versions employed other integration algorithms such as the Runge Kutta – second and fourth order.

A question often asked of simulation software relates to the size of the model. It appears that Workbench is limited by the amount of available memory in the pc. Normally 10Mbyte is allocated using virtual memory for the modelling environment, i.e. making the pc hard-disk act as ram. This figure can be expanded. However, any computational process that needs to use virtual memory will be greatly impeded. The rule is; if you are likely to require the simulation of circuits containing several tens or even hundreds of components it would be wise to increase the amount of ram in your pc to a minimum of 8MB.

An example of the ac time-response analysis is shown in Fig. 5. This circuit was taken from page 559 of the July edition of EW & WW.

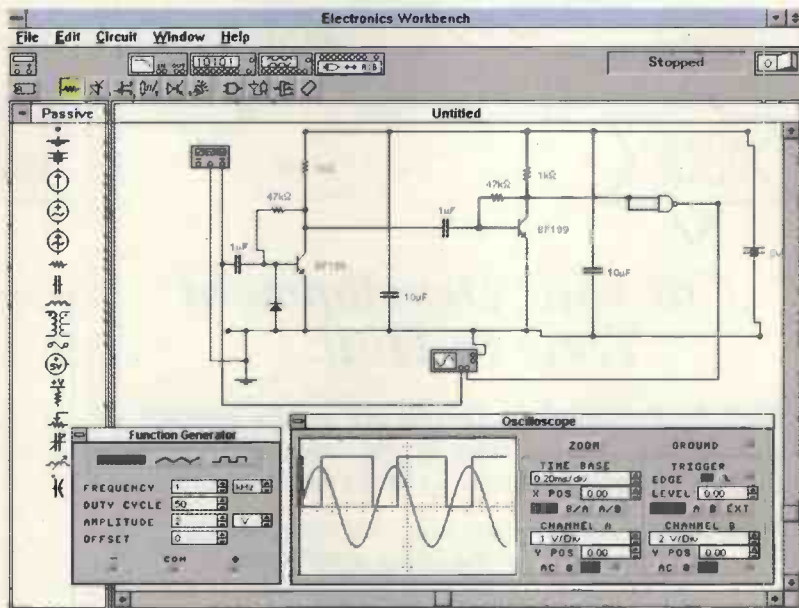


Fig. 5, below. An example of ac Response Analysis of a circuit for converting a sine wave into a digital output.

In this review there are several 'it would be nice to see' comments. If these are taken on-board in future versions I believe that Electronics Workbench will be an acceptable software product for serious design purposes.

**Conclusion**

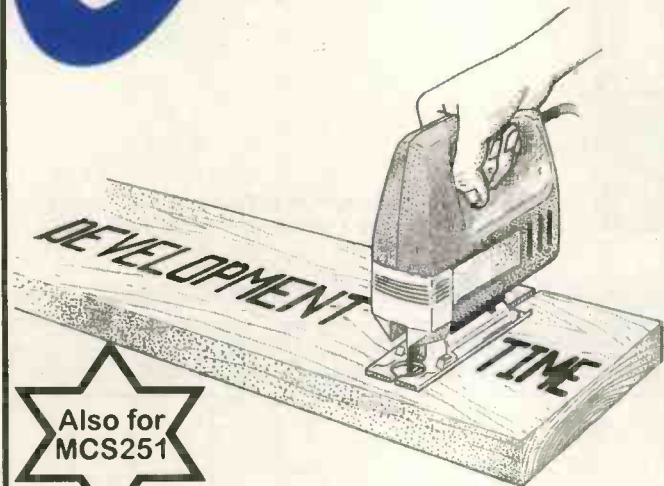
On the whole Electronics Workbench is a good product that is easy to use and has a lot to offer design engineers working on a tight budget. Its ability to mix analogue and digital is an attractive feature. However the a-to-d and d-to-a converter options are somewhat limiting.

The 'technical reference' and 'user guide' supplied with the software are well presented. There is a number of tutorials and model circuits for the new user who will have very little difficulty in getting into the software.

The package represents very good value for money and will serve as an invaluable teaching aid. Its success as a professional engineering tool for large scale circuits will have to be proved somewhat.

**Software availability**  
 Electronics Workbench is produced by  
 Robinson Marshall, Nadella Buildings,  
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# LETTERS

Letters to "Electronics World" Quadrant House, The Quadrant, Sutton, Surrey, SM2 5AS

## Electromagnetic incompatibility

In the course of checking some details points regarding the EMC directive the following has come to my attention.

The issue of harmonic currents drawn from the mains supply is addressed within the generic emissions standard EN 50081-1 by another standard EN 60552. This standard applies only to domestic appliances and similar equipment.

A new standard, EN 61000-3-2 has been prepared to replace EN 60055B 2 covering all electrical and electronic equipment up to 16A per phase. Equipment drawing less than 75W are effectively exempt.

I have been informed that it is currently proposed that EN 61000-3-2 will supersede EN 80555-2 without any of the transitional arrangements that normally apply when a new standard is introduced. At present it is not even possible to buy a copy of EN 61000-3-2 - from BSI Sales at least - yet it is suggested that all equipment should comply with it by 1 January 1996. A similar document IEC 1000-3-2 is also available, though similar problems in obtaining it have occurred.

If your organisation manufactures or indeed imports a product which draws more than 75W from the mains supply then EN 61000-3-2 will apply to you. If you have not already addressed the issue of mains harmonics such as through the use of power factor correction or other means, then it is quite likely that you will not be able to meet the limits set in EN 61000-3-2. In particular those

using 'conventional' as opposed to switchmode supplies may find particular difficulty without radical product redesign.

Representations are already being made by some industry sectors regarding the impossibility of this situation. You may wish to add your own, either through a trade association or directly. Contact points at the DTI and BSI are as follows. David Sutherland, Department of Trade and Industry, 3.120 Red Zone, 151 Buckingham Palace Rd, London, SW1W 955, fax 0171n215 1529/2909. John Baker, Electrical section, Technical help to Exporters, BSI standards, 389 Chiswick High Rd, London W4 4AL. Fax 0181 996 7048.

**Graham Stevenson**  
StuDIomaster  
Luton

## Dynamo turns again

I have been reading with interest the letters concerning cycle lighting systems and would like to refer readers back to the road proven circuit I had published in circuit ideas Dec'94. Note that the upper speed limit of 15mile/h is a misprint. There is no practical upper speed limit.

The circuit operates in a switch mode that matches the generator output into a standard 3W load while incorporating a progressive changeover battery/generator section. Extra power obtained by operating the generator into a higher impedance compensates for any circuit losses.

No modifications are needed to either the alternator or the lamp

systems thus enabling any component part to be easily replaced from most cycle shops. Further, a bypass switch can then be used to instantly switch back to a standard system - in slow moving traffic it saves battery drain.

Backup batteries can be Ni-Cd or dry cells if the charge resistor is omitted. I use a 2.4W halogen headlamp and given that there is no overvoltage at any road speed, a full lamp life is achieved. As previously mentioned high brightness leds give an acceptable rear light with a small power saving. Given the variable output between the makes of generator I recommend that the cheap, readily available Union 'Bottle Dynamo' Cat 6701 is used with my circuit.

Recently introduced by Union is a 26 pole samarium-cobalt magnet hub generator which claims to meet the German standard of 3W at 15km/h. Having no frictional losses this device claims an efficiency of 65% at 15km/h.

However, as a final point, given that some people trundle along at 5mile/h while others cruise for miles at 20, what is the ideal cycle light system?

**PW Fry**  
Holbury  
Southampton

## What's that noise

I read Research Notes 'LF noise', July '95 and 'Mains hum or ear drum' June '94 with interest. I hear a low-frequency sound, comparable with the descriptions given, and those presented in a New Scientist article, - 'Officials eat humble pie

over mystery noise' in 1992.

John Sargent's report mentioned in research notes is known to me. We exchanged information but I do not agree with *all* his conclusions. For example, the conclusion that a sound proof room should be isolated from the acoustical environment is not necessarily true for the low frequencies under investigation. Anyone involved with loudspeaker measurements knows that a sound proof room is only sound proof above a certain frequency.

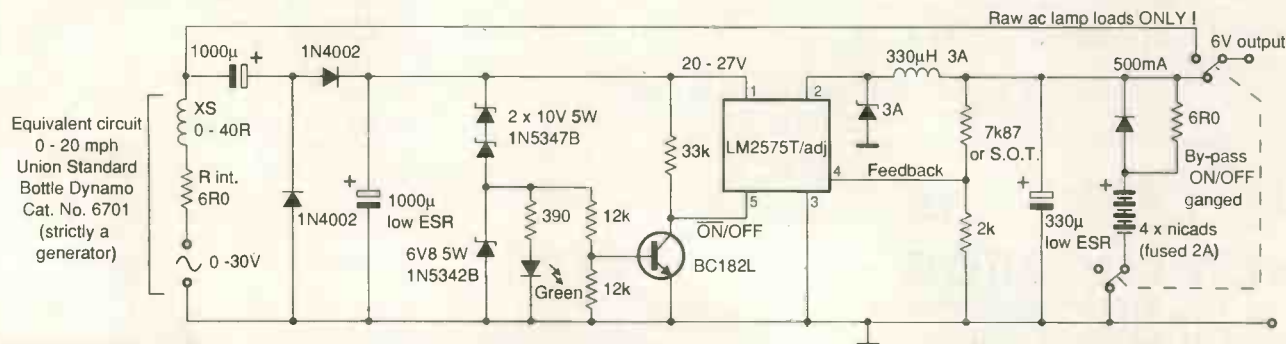
I came across the origin of the sound that I hear by accident. Finding the source was impossible since the human ear cannot distinguish the direction from which sound comes since the frequencies involved are below 200Hz. Even travelling in the direction where the sound level increases failed.

My assumption was that the ground underneath my feet was part of the propagation path. From a point source, the level decreases by 6dB for each doubling of distance; for a line source this is only 3dB and for a flat source, 0dB.

I found the cause of the sound by accident when I was holidaying in France. A sewerage pump went out of order for a few days, and the low-frequency tone disappeared - there was no other pump for several miles around.

Back in the Netherlands I studied the sewerage system and found that the frequency I heard - a noisy signal of about 30dB in the range 8-74Hz just at the hearing threshold - was made by the pumps.

In a village or city the sewerage system is based on the fall of the water in large pipes. At the lowest



Battery-backed dynamo cycle lighting works from walking speed upwards.

point there is a basin and a pump. Outside the built up area, there are thin metal pipes for the transport of waste and dirty water. When the basin is full, the pump starts to run, stopping after about 20 minutes, when it is empty. The next basin is within a few hundred metres.

The sound level of such a pump is almost independent of distance up to about half a mile – maybe reaching people with sensitive ears even after a few miles. All these pumps have the same construction, thus generating sound with the same frequency. The frequency is not constant. It depends on the load, which varies continuously. These interfering sources are summed, resulting in the noisy tone that I hear.

I learned from a researcher in the field of human ear perception that some people observe a tone when low bandwidth noise is fed to the ears. Other people do not hear anything apart from background noise.

I found that it is possible to train people to hear the noise using a 70Hz tone. After gradual attenuation to zero, the tone was still there for some listeners.

I have had a few reactions that are doubtful. Tinnitus aurium for example can cause comparable sounds sensations. So if you have doubts, the most reliable method to identify such sources is a tone comparison. An audio tone generator and a loudspeaker will do. If you find another frequency – it is still possible that you are listening to another sound.

**Ir F. Sessink**  
Nuenen  
The Netherlands

## Field hazards

The coal mining industry was so grateful for the invention of the Davy lamp by the first Director of the Royal Institution that it asked Davy to investigate the violent explosions to which mines were then subject, which typically threw men and machinery high in the air above the shaft mouth. He delegated the job to Michael Faraday who duly implicated coal dust as the prime element. The industry proceeded to ignore this finding for 25 years, one person being sacked for supporting it.

I am confident that alternating magnetic fields cause brain cancer, leukemia, miscarriages and birth deformities such as spina bifida. In this connection I find the letter from J C Williams chief executive of the IEE, obfusatory (Aug. 1995 issue). Nominally it concerns itself with "low-frequency electric fields" and the big point here is that they are recognised as deleterious in the US owing to a legal precedent. There is also concern about power-frequency magnetic fields and about proximity to radio or radar transmitters. The three matters are dealt with in Brodeur's three articles 'The Annals of Magnetism' published in The New Yorker in 1989. The term 'electromagnetic fields' is a new one and until now I was only aware of electric fields, magnetic fields and electromagnetic waves. I understand it has to be qualified in parenthesis if you want to refer to electric or magnetic fields these days. But Mr Williams does not do this. I begin to wonder if the IEE Working Party is going to get even the title of its report right.

And what a shame the IEE has not felt able to speak out against magnetic fields, given the fun we are all going to have proposing splitting live current in 240/440V overhead lines into two equal halves to run on two separate wires above and below the neutral, and so on. (This will boost the economy.) It matters now because companies and computer owners should be asking themselves if it would be better to get computers with screens of a type used in laptops, for health reasons, rather than subject themselves to the leakage field arising from forcing fields across the one-inch gap across the neck of the cathode-ray tube. Of course other screen types will yield desk computers with a smaller 'footprint' which might be economically worth while in some locations.

But do I trust the IEE to embark on research epidemiology? No it's not their business.

**Bernard Jones**  
London

## Arnold Sugden – pioneer

The announcement of Arnold Sugden's death is especially sad, since his pioneering work still remains unhonoured by the Audio Engineering Society despite an initiative taken a considerable time ago by the late Raymond Cooke.

His achievement in single-handedly developing stereo LPs two years before anyone else in the world, alone earns him a prime place in audio history. It also brings closer the extinction of a vanishing breed of

## Switchers for the Masses – free disk

In the August 1995 issue, we omitted to mention that a design software disk is available to support the article 'Switchers for the Masses'. Free to designers, this disk can be obtained, together with an information pack, by faxing National Semiconductor in Germany on 00 49 89 2471 1222. Request National Semiconductor Information Pack Reference No. 700 005.

audio worker, who were involved because of a love of music – not, as is so often the case now, the noise it makes.

I hesitate to suggest it, but could it be he lacked the academic qualifications of those that dominate its membership? I suspect there are many who have never even heard of him.

**Reg Williamson**  
Kidsgrove  
Staffordshire

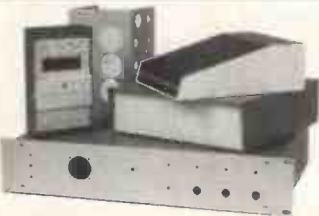
'Arnold Sugden – Pioneer of Single Groove Stereo' Reg. Williamson  
EW+WW June 1994.

## A plea for constructive exploration

Recent articles concerning audio amplifiers and subsequent letters to the Editor have been interesting and stimulating, although it seems that a few personal axes have been ground.

The volume of discussion and disagreement generated provides

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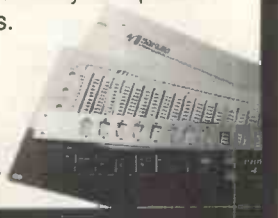
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strong evidence that we still don't fully understand all aspects of audio amplification and sound reproduction relevant to satisfying the human ear/brain. We are not able to define and quantify factors which generate clearly audible effects.

Surely, the best way of improving understanding is for us to continue exploring various avenues of research, to report results and to have these constructively reviewed. In the past some of the most apparently harebrained ideas have resulted in new discoveries. Most importantly, try to remain objective and suppress personal interests.

It is very difficult to remain unbiased and objective in any assessment of a situation. Especially so when we are unable to properly measure or otherwise quantify observations.

My interest relates only to personal satisfaction in developing circuitry for the reproduction of music. I don't have access to sophisticated test equipment as used by 'professionals' but use only a signal generator, oscilloscope and a dmm. I have done measurements of harmonic distortion levels, but no longer do so as results did not correlate with what I heard.

I now limit my bench testing of amplifiers to checking dc conditions, setting quiescent currents and checking square wave response under various loads.

Readers might be interested in some of my own observations relating to the use of bipolar and fet output devices. I have compared the operation of an amplifier using a complementary compound follower output stage consisting of bipolar devices<sup>1</sup> with one comprising bipolar drivers and fet output devices<sup>2</sup>.

The bipolar-only stage could be made to generate crossover spikes when fed with 20kHz sine waves. The spikes could be made smaller by optimal setting of quiescent current, but could not be entirely eliminated.

In contrast, the bipolar/fet stage could not be induced to generate spikes – even when fed with 80kHz sine waves. At low bias settings clean step changes were seen at the crossover point which disappeared totally as bias was increased.

Perhaps this goes a long way to explaining why fets can give such a clear and open sound under normal listening conditions, despite their 'poor linearity'. The very fast response of fets is likely to allow negative feedback to keep better control of the circuit than is possible with relatively sluggish bipolar output devices – especially in the crossover region.

I am currently using amplifiers with class-B complementary source follower fets (2SK134/2SJ49) with complementary class-A bipolar emitter-follower drivers. This has resulted in the cleanest sounding and most stable amplifier that I have yet built. Quiescent current in the output stage is not critical and I cannot detect any audible benefit in increasing this above 100mA or so.

The input stage consists of a conventional emitter degenerated long tailed pair with current mirror, but includes cascode transistors. A cascode voltage amplifier stage is used, buffered from the input stage via an emitter follower stage.

Open loop gain of such an amplifier is high. Despite this, the amplifier is very stable with  $C_{dom}$  at 44pF. It is still stable with  $C_{dom}$  at less than 22pF when driving reactive loads, for example 7.5Ω in parallel with 2μF. The usual output inductor/resistor combination has been omitted. Under these conditions only a very small amount of overshoot is detectable on square wave response. I have not been able to come even close to this using bipolar output devices.

I have learned much from reading papers by Self and others, but have been forced to the conclusion that

conventional circuit analyses do appear to overlook some important aspects. John Linsley-Hood's recent article supports this view<sup>3</sup>. Come on all you bipolar and other buffs – Doug included. Let's have an unbiased (ugh !) exploration of *real life* operation of audio amplifiers.

**Ken Hough**  
Amersham,  
South Bucks

**References**

1. Self, D., 'Distortion in Power Amplifiers', *EW+WW* Nov. 1993, p. 930, Fig. 3(a).
2. Self, D., 'Distortion in Power Amplifiers', *EW+WW*, Nov. 1993, p. 931, Fig. 4(c).
3. Linsley-Hood, J., 'Expert Witness', *EW+WW*, Aug 1995.

**Super symmetry misunderstood?**

Having read Douglas Self's analysis (Sept. '95) of my 'super-symmetrical' amplifier (Dec '94) with great interest, I feel there is a need to clarify two issues – the nature of the 'virtual complement' and the feedback.

Self refers to "...the sharp-eared Batman effect in Fig. 5", describing

it, "They also show sharp changes in gain" and "there are always sharp gain changes" *EW+WW* May 1995. It is obvious that the sharp gain changes are the reason for his analysis of my design.

The odd-looking Batman curves can be traced back to two causes – output clipping, which is not visible in Fig. 5, and gain. The latter is the differentiated characteristic, thus very sensitive to even small curvature changes in the characteristic.

Vertical sides of the Batman profile (g, f) represent constant output current at clipping, when gain immediately drops to zero, (r, q) in Fig. 3. An omission has been made by Self in not entering the implications of these curves.

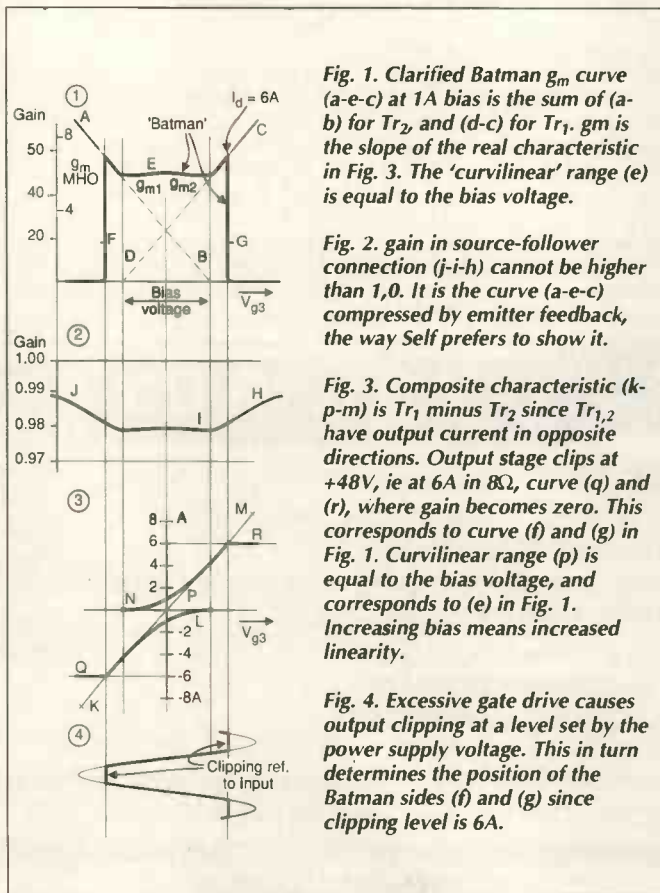
The 'ears' (a, c) will in fact continue up and outward when the power supply voltages are increased. Judgement should be made on the basis of the characteristic in Fig. 3. The sharp gain change at clipping should not be used as an argument, since all amplifiers clip at overload. Self's own amplifier has the same 'sharp changes in gain'. I haven't noticed him mentioning this.

Incidentally, the Batman curves are the same as I pointed out as correct in my letter 'Impossible Curves', July 95, although Self believed that his old 'jagged wingspread' curves were right. It is with great pleasure I note this change of opinion.

Self believes his analysis is fair, but to be fair one needs to be knowledgeable. He knows that the feedback factor is zero at 20kHz. Thus, open-loop gain bandwidth is 0.4MHz. How likely is that? But this statement has one implication – my measured thd 0.015% at 20kHz must be wrong, since there is no feedback at 33kHz, where the third-harmonic is. In fact, the real distortion would be equal to raw distortion.

The clue to this is in Self's remark later. He has placed a 3nF capacitor between the drain and gate of  $T_3$  for compensation purposes. This capacitor is neither necessary, nor desirable. Self will not find it in my schematics, where the proper way to compensate is shown (see  $C_8$  in Fig. 11, used only with the 'twin current mirror', *EW+WW* July '95). One advantage with the 'super-symmetrical' amplifier is that internal capacitances alleviate the need for compensation. Thus it has a high feedback factor and low distortion. The ultimate phase shift is 180° in a two-stage amplifier so it cannot be unstable – a great advantage.

This is in contrast to Self's claim that my amplifier is more complex than the old three-stage topology. It





may be a little harder to grasp, but it is in fact the most easily compensated I have known in my 30 years of amplifier design.

There is more to be said but I will point to one detail – the PSpice shows a slight dissymmetry from the gate of  $Tr_3$  to the output. It is because of the small gate signal voltage required to change the current in  $Tr_3$  (symmetry from gate to output of  $Tr_1$  and  $Tr_2$  is perfect).

Self now focusses on this error in a whole page with four major misleading statements about "...the lack of symmetry about the output line". (The error is insignificant since the gate really belongs to the input stage and is current driven so the error is not visible, only at the voltage drive used in his PSpice). This is to my mind exaggerating and only aimed at discrediting my amplifier since the real cause is never explained.

On the whole my feeling is that Self's analysis is lacking in insight into feedback an stability and the possibility of driving mosfets with unconventional means.

**Bengt Olsson**  
Saltsjö-Boo  
Sweden

## Self on Olsson

Mr Olsson's subject, discussed in the July 1995 issue, is so close to my interests that I am sure he will understand I feel bound to comment on his double mirror article (*EW+WW* July '95) I hope that he will not feel that I am taking on a Judge Self role with respect to his material in particular.

As far as I can see, the whole article is based on the premise that amplifiers usually have insufficient open-loop gain, and so lack enough negative feedback to reduce the distortion adequately. I think that this statement is quite wrong. The amount of negative feedback that can be applied to an audio amplifier is ultimately limited by the need for reliable hf stability, and it is a matter of regret that no-one answered my previous call for an informed debate on the amount of global feedback that could be considered safe. To restate my own experience; given the generic amplifier configuration with dominant-pole compensation and an output inductor, 30dB at 20kHz seems to be reliably stable under all conditions. Will anyone else contribute their view?

To return to double mirrors, if it were necessary to increase the open-loop gain, then why not simply reduce dominant-pole  $C_3$ ? This would increase the gain and the slew

rate at the same time. Alternatively, the input pair degeneration resistors  $R_1, R_2$  could be reduced, so long as the input linearity is not unduly degraded; this will not improve slewing, however.

Mr Olsson states that a tail-current of 10-20mA is impossible. This is certainly untrue, and I cannot fathom why he thinks so; the only bad consequences of increased tail-current would be an increased dc offset spread due to the greater base currents drawn by  $Tr_{1,2}$  through the circuit resistances. Mr Olsson's article omits his preferred tail-current value, and indeed just about every concrete quantity that might allow us to assess the success of his scheme. In particular, a real measured, before/after distortion plot is conspicuous by its absence; presumably we are expected to take this on trust, and I don't do that.

The only other question I would like to ask: how is stability attained if the  $\omega/l$  gain at all frequencies is increased by 12dB (I got this value from Olsson's Fig. 2) unless the amplifier was already over-compensated? Fig. 2 shows the second pole at 1MHz, and a stable 6dB/octave Nyquist intersection is only obtained because the demanded closed-loop gain is also falling by 6dB/octave from 300Hz. This is presumably due to the presence of phase-advance capacitor  $C_2$  across feedback resistor  $R_0$ ; if so this is a vital, if not the vital part of the scheme, and I would like to know more about it, not least because I have always found such a capacitor to worsens stability rather than improve it. This appears to be because  $Tr_2$  is liable to parasitic oscillation with  $C_2$  present, though of course the true explanation may be that I am hopeless at compensating amplifiers.

I read Mr Olsson's article with great interest, but found it raised many more awkward questions than it answered; I hope he will feel able to give a more comprehensive explanation of the thinking behind it.

## Self on J. Linsley-Hood

As someone who has always read John Linsley-Hood's material with interest, I was sorry to see that his article 'expert witness' in the August issue failed to provide valid information. I admire his determination in making some real tests, which other critics have apparently not felt to be necessary, and it is regrettable that I have to disown the results.

The bjt output stage used for these tests, is, I'm afraid completely

wrong. JLH's diagram shows to 100 $\Omega$  resistors inserted in the emitters of the bjt drivers. Why he put them there I cannot guess. They are most certainly not present in any bjt circuitry I have advocated, and they ruin the linearity. The effect of adding the 100 $\Omega$  on output gain is shown as the lower curve in the wingspread gain-diagram below. It can be seen that the drop-off in gain with load excursion is dramatic, and I am sorry to say that this renders his results fallacious.

I am also concerned that the amplifier used is nowhere near Blameless – the distortion to be expected with bjts is sub-0.001%, as I have shown many times in the past. Anything higher than this indicates that one of the eight distortion mechanisms has not been properly dealt with, and so the result is a complicated mixture of distortions that tell us nothing.

It is not my purpose to analyse the design decisions taken in the JLH small-signal stage. But I can see no provision for preventing the non-linear input impedance of the output stage from affecting the voltage amplification stage. The function of  $C_{12}$  in the input current-mirror is obscure, but it looks as if it does something drastic and unusual to the put  $g_m$ .

One major omission from the article is the amount of global negative feedback in use. It is clearly essential to know this before judging linearity.

It is regrettable that a better oscillator was not available, for 0003% oscillator distortion will completely swamp the actual amplifier non-linearities – in the bjt case at least. Oscillator distortion an order of magnitude lower is highly desirable. I would remind readers that my conclusions about fet linearity are not simply based on simulation, but also on practical measurements that were reported in the Distortion In Power Amplifiers series. (Oct 94-Feb 95).

If fets really were as linear as claimed, then there should be no difficulty in building an fet amplifier that equals or exceeds the performance of the Class-B amp I presented in *EW+WW* January 1994, while using the same amount on negative feedback. I shall wait with interest to see if any one can do it.

## Self on Duncan

Since Mr Duncan appears to have dispensed with the usual courtesies and conventions of public debate, I find I have better things to do than argue with him for example, the

grass needs cutting.

However, I feel should correct a couple of his mis-statements to prevent others from being misled. There is really no doubt of any kind that hf levels in music, etc are much below the lf levels, and I append below two references (reaching over no fewer than forty years) so that anyone who chooses can verify this.

I expect Roland will be less than pleased to have their magnificent *U110* described as a 'supermarket keyboard', when it is actually a professional Midi master keyboard that cost £1600 when I acquired mine. The rest of Duncan's letter is to a similar standard of accuracy.  
**Douglas Self**  
London

Langthorne-Smith, Radio designers handbook, Ch. 14.7, p. 622 4th edn, pub Iliffe 1953.  
Dibble, K., 'Hearing loss and music' p. 261, *JAES* Apr 1995.

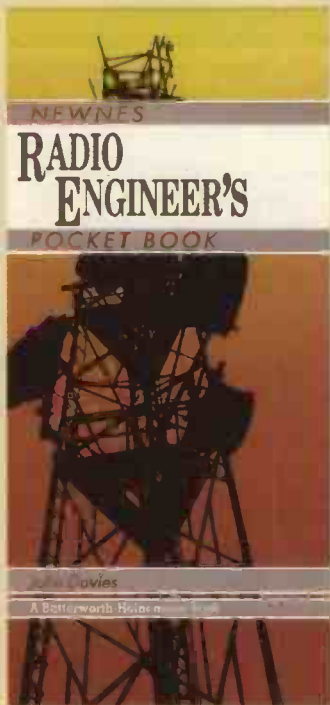
## Fair comment

Douglas Self, *Electronics World* Sept 95, refers to Bengt Olsson's amplifier article, *EW* Dec. 94, and presents an equal number of pages in antithesis, as if 'fairly' establishing that Bengt's design has problems and no benefits. Mr Self's Spice plots graphically show the non linearities we fully expect with reduced Class-A quiescent current and voltage drive directly to base-emitter or gate-source inputs. This Fig. 2 simulation circuit is quite different from other output stages and in analysing without high impedance drive (Bengt, Fig. 5) he has not compared like with like.

I surmise that his own Blameless Class-A, *EW+WW* Mar'94, would also behave 'fairly' non-linearly with a voltage source input connected directly to the base input of its second stage.

What is important is that both designs use similarly elegant 'p-to-n' voltage amplifying configurations, 'p' input differential to n common emitter or common source, and, while Bengt's simpler design does have less gain for negative feedback, when his Fig. 11 includes Self's  $Tr_{12}$ , it outputs a beneficial 100W into 4 $\Omega$  with 10kHz distortion measured at only 0.01% – A most acceptable non-linearity.

**Graham Maynard**  
Newtownabbey  
N Ireland



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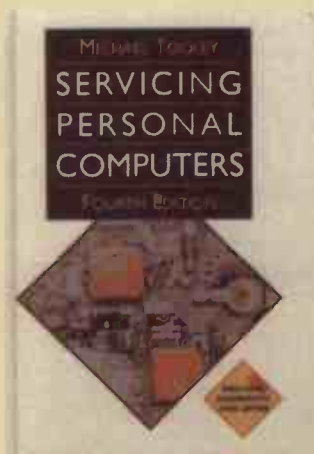
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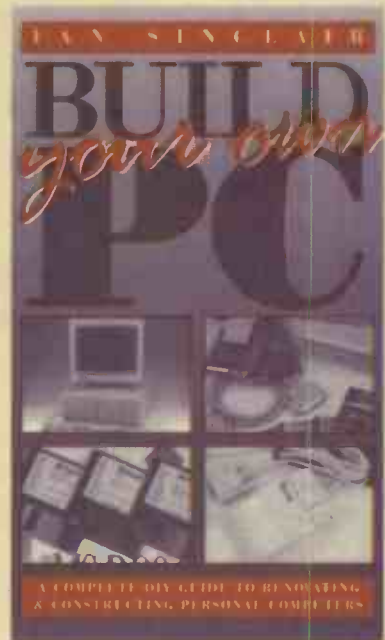
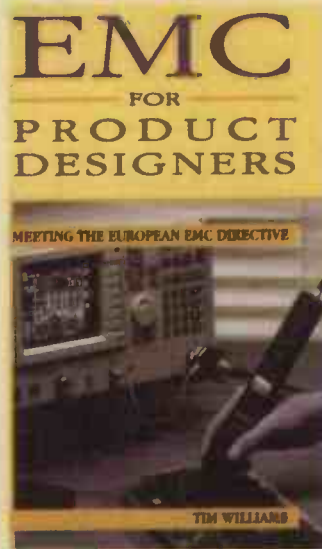
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# Analysing circuits via energy

**Andrew Gibson and Bernice Dillon** detail a new circuit analysis technique offering benefits over traditional network analysis. It covers dc, ac and transient sources – as well as reactive elements.

**E**lectrical circuit theory concerns the calculation of voltages and currents in electrical networks. The voltages and currents of a circuit describe the exchange of energy between power supplies and circuit elements.

Components such as capacitors and inductors store energy whereas resistors dissipate energy. In circuits with alternating current sources, these components exhibit an impedance,  $Z$ . This impedance value together with the element voltage or current can be used to define the energy stored or dissipated in individual components. For example the power consumption,  $P$ , of an impedance,  $Z$ , with terminal voltage,  $V$ , may be defined as,

$$P = \frac{V^2}{Z} \quad (1)$$

Developing on this definition, a novel circuit analysis technique can be introduced.

A power characteristic equation is formed by summing the individual power consumptions of each individual circuit impedance. A power curve or surface may be plotted using this characteristic equation. At the correct voltage and current values for the circuit, the curve or surface has a stationary turning point. This energy based approach has a number of advantages over conventional nodal and mesh circuit analysis techniques. It has the following features,

- easy implementation,
- one equation describes the entire circuit – no matter how many circuit components,
- the method avoids difficulties with voltage and current polarities,
- the solution can be illustrated graphically,
- it exercises simple calculus and the theory of maxima/minima,
- the method is equally applicable to non-linear components,
- the method can be extended to include magnetic circuits and electromagnetic field devices.

## Analysis techniques

Voltage and current distributions in electric

circuits comply with a stationary power condition. An alternative circuit analysis technique can be derived from this property.

The unknowns can be varied until power consumption of the circuit is calculated to be unchanging. At this point the voltages and currents are at their true solution. A formal procedure is summarised below,

- identify the unknown nodal voltages in the circuit,
- express power dissipated in each component as a function of the unknown nodal voltages,
- add all of the power terms together to form a power equation,
- find the stationary turning point of the power equation.

As a starting example consider the simple, two component circuit of Fig. 1. Two resistors in series are connected across a 10V dc source. In this case the impedance of each resistor is simply its resistance value in ohms.

An unknown voltage  $V_1$  exists at the junction of the two resistors. The terminal voltages for resistors  $R_1$  and  $R_2$  are,

$$(V_1 - 10), V_1 \quad (2)$$

respectively. Using this result together with equation 1, the total power dissipated in the circuit may be written in terms of the unknown voltage,  $V_1$ , as,

$$P(V_1) = \frac{(V_1 - 10)^2}{4} + \frac{V_1^2}{6} \quad (3)$$

This characteristic equation describes the power consumption  $P(V_1)$  of the circuit. As variable  $V_1$  is varied so will  $P(V_1)$ . The stationary turning point of  $P(V_1)$  can be determined graphically or analytically using some simple calculus.

Firstly, the graphical technique is illustrated in Fig. 2. You can see that the power is plotted as a function of  $V_1$ . It clearly has a turning point at the true solution of  $V_1 = 6V$ . This result can also be determined using differentiation techniques.

Differentiate the power equation with respect to the unknown voltage  $V_1$  and set the

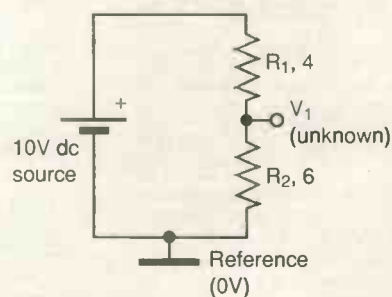


Fig. 1. Schematic diagram of a two resistor potential divider circuit with one unknown voltage  $V_1$ .

$P_S(V_1)$

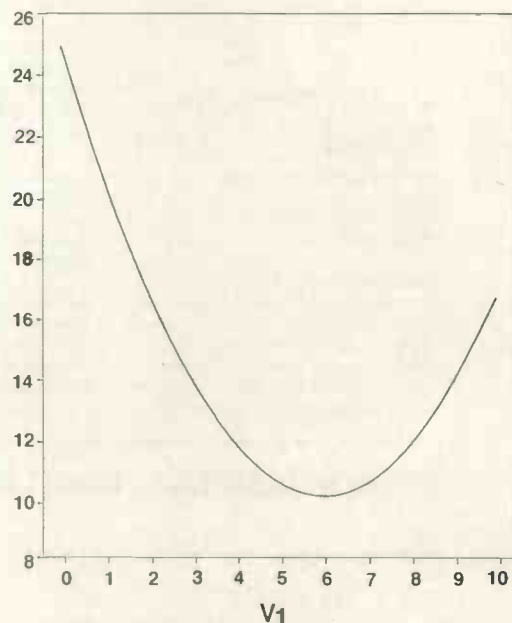


Fig. 2. Power curve  $P(V_1)$  plotted as a function of  $V_1$  for the circuit in Fig. 1.

result to zero as,

$$\frac{dP(V_1)}{dV_1} = 2 \frac{(V_1 - 10)}{4} + 2 \frac{V_1}{6} = 0 \quad (4)$$

This equation also has the correct solution of  $V_1 = 6V$ .



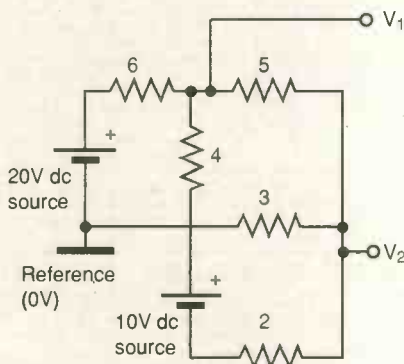


Fig. 3. Schematic of a resistor circuit with two unknown potentials  $V_1$  and  $V_2$ .

A circuit with two unknown voltages  $V_1$  and  $V_2$  is treated next, illustrated in Fig. 3. There are six branch resistances so there will be six power entries in the characteristic equation  $P(V_1, V_2)$ . It is constructed as,

$$P(V_1, V_2) = \frac{(V_1 - 20)^2}{6} + \frac{V_1^2}{4} + \frac{(V_1 - V_2)^2}{5} + \frac{V_2^2}{3} + \frac{(V_2 + 10)^2}{2} \quad (5)$$

Since there are two unknowns, two equations are required to make a solution. The first is found by partially differentiating  $P(V_1, V_2)$  with respect to  $V_1$  only, and by setting the result to zero as,

$$\frac{\partial P(V_1, V_2)}{\partial V_1} = 2 \frac{(V_1 - 20)}{6} + 2 \frac{V_1}{4} + 2 \frac{(V_1 - V_2)}{5} = 0 \quad (6)$$

The second equation is similarly derived by partially differentiating with respect to  $V_2$  only and by setting the result to zero as,

$$\frac{\partial P(V_1, V_2)}{\partial V_2} = -2 \frac{(V_1 - V_2)}{5} + 2 \frac{V_2}{3} + 2 \frac{(V_2 + 10)}{2} = 0 \quad (7)$$

The simultaneous solution of equations 6 and

7 provide the correct solution of  $V_1 = 4.096V$  and  $V_2 = -4.046V$ . The solution can also be illustrated graphically by plotting  $P(V_1, V_2)$  as a function of  $V_1$  and  $V_2$ . A minimum is exhibited in Fig. 4 at the correct solution.

AC analysis methods

The energy method is equally applicable to electrical circuits with ac sources and reactive elements. In this case impedance rather than resistance is used to determine individual power consumption in each component. The 'j' operator is used to introduce a 90° phase shift caused by capacitor and inductor impedances.

Consider the circuit in Fig. 5. There is one unknown nodal voltage  $V_1$  which is a complex quantity. Total power consumption of the circuit is the linear combination of the power in the inductive ( $j5\Omega$ ), capacitive ( $-j1\Omega$ ) and resistive ( $2\Omega$ ) impedances.

$$P(V_1) = \frac{(V_1 - 10)^2}{2} + \frac{V_1^2}{-j} + \frac{V_1^2}{j5} \quad (8)$$

As  $V_1$  is a complex quantity then it has real and imaginary parts which can be varied until a stationary point is deduced. This graphical technique is illustrated in Fig. 6.

Instead of a minimum power point the reactance circuit displays a stationary inflection point. This relates to the two types of energy in impedance circuits – stored and dissipated. In the usual way, the circuit solution can also be deduced analytically. Differentiate the power equation with respect to the unknown,  $V_1$ , and set the result to zero to find the stationary point as,

$$\frac{dP(V_1)}{dV_1} = 2 \frac{(V_1 - 10)}{2} + 2 \frac{V_1}{-j1} + 2 \frac{V_1}{j5} = 0 \quad (9)$$

This equation has a solution of  $V_1 = 2.8 - j4.5V$ .

Conclusion

Power circuit analysis techniques provide a new, easy to understand alternative to Kirchoff for network analysis. The same procedure applies to dc, ac or transient signal sources and to linear/non-linear electric or magnetic circuits.

It introduces the concept of a characteristic power equation to describe the behaviour of a circuit. By finding the stationary point of this equation the unknown circuit voltages can be defined. As power methods are already used for electromagnetic fields and in other areas it becomes possible to solve hybrid type structures in a unified way.

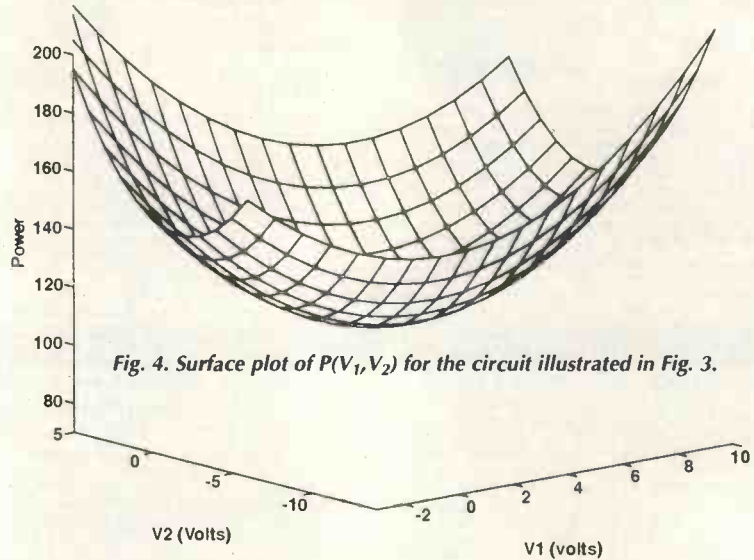


Fig. 4. Surface plot of  $P(V_1, V_2)$  for the circuit illustrated in Fig. 3.

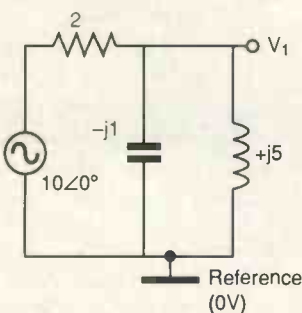


Fig. 5. Schematic diagram of a resistor, capacitor inductor circuit with an ac source and one unknown complex potential  $V_1$ .

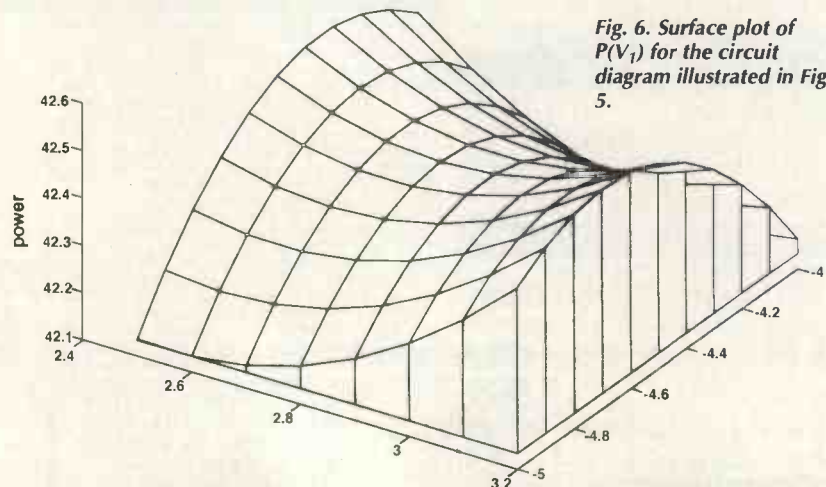
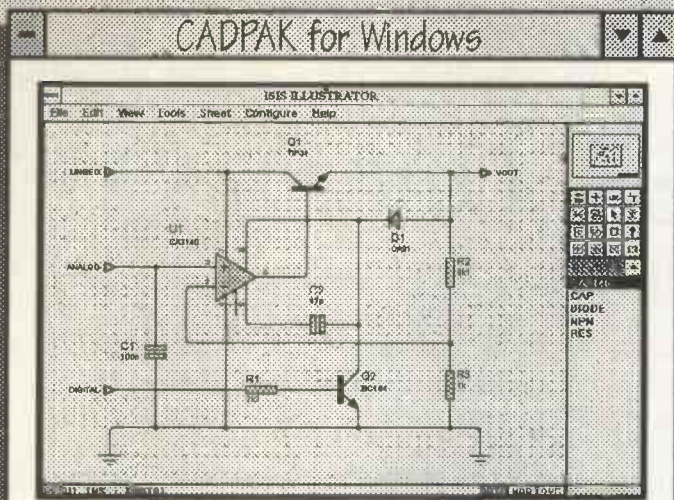
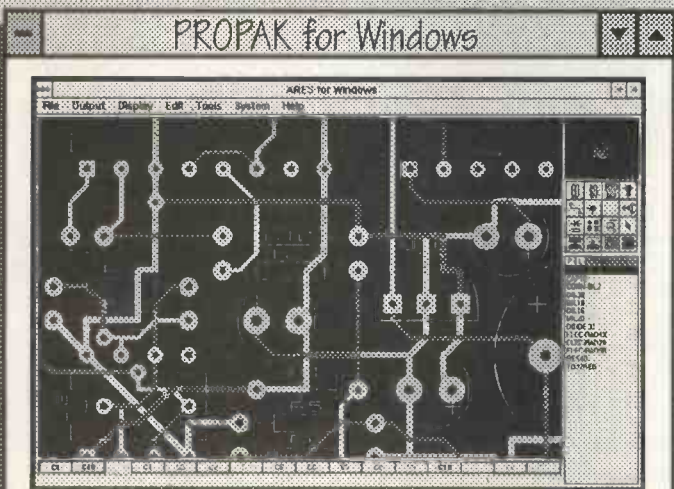


Fig. 6. Surface plot of  $P(V_1)$  for the circuit diagram illustrated in Fig. 5.



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resistance of 12 at 100mA, with a gate/source voltage of 3.5V. Rise and fall times are 8ns and 20ns at 250mA; input capacitance is 100pF. Zetex plc. Tel., 0161-627 5105; fax, 0161-627 5467.

### Memory chips

**4Mb sram. TC554161FTL**, a 256K by 16 static ram in 0.5 $\mu$ m silicon-gate cmos from Toshiba, provides a high-density, high-bandwidth memory with an access time of 70ns. Toshiba expects the device to cost the same as 4Mb of sram in four 1Mb srams by the end of the year. Toshiba Electronics UK Ltd. Tel., 01276 694600; fax, 01276 694800.

**16Mb nvram.** Benchmark announces the *bq4017* 16-megabit, non-volatile static ram which, organised as 2M by 8, has the largest memory of any such device. It acts as flash, eeprom, eprom, standard ram or silicon disk, offering the characteristics of an ordinary ram but with data retention. The device is effectively four 4Mb static rams with two lithium cells and power control. When the external 5V power supply is in tolerance, access time is 70ns and if it falls out of range, the memory is write-protected and power taken from the cells for up to five years. There are no limits to the number of writes that can be performed. Sequoia Technology Ltd. Tel., 01734 258000; fax, 01734 258020.

### Optical devices

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narrow-angle, high-intensity, clear lens type giving 7-200mcd at 20mA. Power needed is 5-30mA at a forward voltage of 1.7-3.2V. Lumek Opto/Components Inc. Tel., 001 708 359-2970; fax, 001 708 359-8904.

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**Wire chip coils.** 54-880nH *LQN1H* wire-wound chip coils by Murata have ferrite cores for use at hf and possess stability, high *Q* and low resistance at frequencies in the 30-150MHz range. At 3.2 by 1.6 by 1.8mm, parallel mounting on a 2.5mm pitch is possible. Murata Electronics (UK) Ltd. Tel., 01252 811666; fax, 01252 811777.

**Precision reference.** Zetex introduces the *ZR285 reference*, claimed to be the smallest and lowest power device presently obtainable in SOT23. Minimum operating current is 13 $\mu$ A; maximum is 20mA. Slope resistance at 1mA is typically 0.2 $\Omega$  and is maintained at a low value over the operating range - at 100 $\mu$ A it is 1 $\Omega$ . It will cope with surges to 200mA. Zetex plc. Tel., 0161-627 5105; fax, 0161-627 5467.

### Connectors and cabling

**Power inlets.** Rendar's new power inlets to IEC320 are rated at 10A, 250Vac, are available in hot or cold condition Class I and II and have either 6.3mm push connector or solder tags. The range of shuttered outlets includes a snap-in type with positive locking to prevent accidental withdrawal and fitting panels from 0.8mm to 3mm in thickness. Rendar Ltd. Tel., 01243 866741; fax, 01243 841486.

### Audio products

**Power amplifier.** Putting out 23W, the *TDA1561Q* dual audio power amplifier by Philips is meant for use in car radios, one aim being to reduce the size of heat sinks needed. It operates in an automatically switched dual mode: when high output power is needed for only a short time, as in music signal, an efficient, 23W single-ended connection is in use, but if longer periods of higher power are called for, it switches to the bridge-tied, 5W load connection in which, nevertheless, if temperature exceeds a safe level, the single-ended mode is re-established. A mode pin accepts various voltages to force single-ended working, mute and standby. As further safety precautions, the amplifier is protected against load dump transients, shorted loads and shorts to rails; an on-chip zero-crossing detector delays entry or exit to mute until the input passes zero to avoid clicks and mutes the amplifier if the supply drops below 7V during engine starting. Philips Semiconductors (Eindhoven). Tel., 00 31 40 722091; fax, 00 31 40 724825.

**Surface-mount coaxials.** Transradio has a wide range of s-m coaxial connectors, including those suitable for on board MMS and MC cards and for use with printed boards or as PCMCIA card edge connectors. All are said to offer extremely low vswr and insertion loss and models are available to work in the 0-8GHz range. The MC-card series is claimed to be the smallest true coax connector in the world. A range of accessories is offered. Transradio Ltd. Tel., 0181 997 8880; fax, 0181 997 0116.

### Displays

**10.4in colour lcds.** Two new colour liquid-crystal display modules are announced by Hitachi. They are both supertwist, 10.4in diagonal units offering a saving in power over earlier models at 2.2W for the VGA type and 2.5W for the SVGA. Display area is 211.2 by 158.4mm, the total size being 243 by 179 by 8mm, which is the same as the company's tft models. There is a single fluorescent backlight and contrast ratio is 30:1 for both models. Response time is 270ns. Hitachi Europe Ltd. Tel., 01628 585163; fax, 01628 585160

### Filters

**Emi filters.** Surface-mounted pi-section filters by Oxley are for use in



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filtered lead-through connection to shielded printed-board enclosures. Filters have capacitance values of 15-22nF and handle up to 10A at 200Vdc. Insertion loss of the *SM3* is up to 65dB at 10GHz. Oxley Developments Co. Ltd. Tel., 01229 582621; fax, 01229 585090.

**Low-pass filters.** Low-pass emi filters by Steatite are now available in solder-in, resin sealed bolt-in and high-current/voltage resin sealed types, approved to the MIL replacement Muahag listing. They are in multiple configurations of C, pi-type elements with tubular or disc

capacitors and are rated to 500Vdc. High-current versions take 100A continuously at up to 1250V dc. Steatite Insulations Ltd. Tel., 0121 6436888; fax, 0121 643 2011.

### Hardware

**Stainless steel enclosures.** Intek has a range of stainless steel computer enclosures to protect computers in a hostile world, holding at bay fluids, dust, hard knocks and tampering and foiling theft. Units hold a screen and either computer or terminal, the membrane keyboard being housed in a steel compartment. Other peripherals can go inside or be connected outside the enclosure. Frontal protection is to IP65 and a fan and filter are provided. Even water is excluded by baffles. Intek Electronics Ltd. Tel., 01352 810603; fax, 01352 810403.

**Industrial computing.** *500/700 Series* of industrial computer enclosures comprises an 8U monitor rack-mounting kit for up to 17in displays, an all-steel computer chassis meeting EIA-310G to take the Intel *Superfast MX Xpress* motherboards, 8-20 slot passive backplanes or a variety of motherboards and the Raid chassis holding up to eight removable SCSI hard disk drives with optional redundant power supplies and individual fans. There is also a 1U keyboard drawer. The company can supply a range of computers, backplanes and drives. Sight Systems Ltd. Tel., 01903 242001; fax, 01903 504494.

### Instrumentation

**200Msample/s dso.** Yokogawa's *DL1450* is the replacement for the earlier *DL1000* series, with sample rate and memory size doubled to 200Msample/s and 120Kword. Familiar shape is kept, so that the 7in screen instrument takes up less bench space than a copy of *EW+WW*.

It is a four-channel instrument with a 150MHz bandwidth; a GPIB interface is standard, as is a 3.5in floppy drive to allow data to be exported to a pc. The built-in printer can be used in real-time mode and there is a 'snapshot' facility to compare two signals on screen at one touch of a button. The history memory can recall up to 100 earlier displays and the rapid update of 60 displays/second emulates the display of an analogue oscilloscope. FFT analysis is provided as standard. Martron Instruments Ltd. Tel., 01494 459200; fax, 01494 535002.

### 'Smallest' 100MHz oscilloscope.

The *V-1565* 100MHz oscilloscope, said to be the smallest of its type available, has been redesigned and upgraded. It measures 275 by 130 by 360mm, weighs 6kg and uses 40W, while offering sweep-time autoranging, cursor readout, auto-trigger level, selectable signal output and a 100MHz frequency counter. Numerical screen data includes sweep speed, delay time and voltage sensitivity. Sensitivity is 2mV-5V/div and rise time 3.5ns. Trigger lock allows the display of a variety of irregular pulse trains without adjustment of trigger level. Hitachi Denshi (UK) Ltd. Tel., 0181 202 4311; fax, 0181 202 2451.



### Radar performance monitor.

Ranatec's *Model 9000* radar monitor may be installed permanently on site to measure continuously, in the 1.25-17.1GHz range, the output and reflected power of the transmitter and the noise figure of the receiver, showing alarm conditions on-screen. The instrument can be controlled remotely by means of an RS232 Interface, an optional GPIB or by modem. Rf heads are available to mount in the antenna transmission line to avoid interfering with normal radar operation. Anglia Microwaves Ltd. Tel., 01277 630000; fax, 01277 631111.

**Little dpm.** *DPM 3* by Lascar is an autopolarity, autozero digital panel meter that presents its readings in 11mm high characters, consumes 150 $\mu$ A and has a full-scale reading of 200mV. It snaps into place in the panel and is provided with a pcb socket strip. Lascar Electronics Ltd. Tel., 01794 884567; fax, 01794 884616.

### Hand-held, 5-digit multimeter.

In addition to its accuracy of within 0.025%, the 50,000-count *MX56* multimeter by Metrix has a combination of facilities including true rms, a 100kHz bandwidth, a timer-counter, audio power measurement and mains-disturbance indication. Resolutions are 10 $\mu$ V, 10nA, 10m $\Omega$  and 0.01dB. Metrix UK Ltd. Tel., 01256 311877; fax, 01256 23659.

**Seven-in-one.** The Australian McVan company announces the *BWD 604 Mini-Lab* which, in the one case, contains a 13MHz function generator with am and fm and a log/in sweep, a frequency counter, a power amplifier, a  $\pm 15\text{V}$  1A bi-polar psu, a  $+15\text{V}/-15\text{V}$  dual psu, a  $-5\text{V}$  3A psu and a 3.5-digit multimeter. All outputs are short-proof. Tandem Technology Ltd. Tel., 01243 576121; fax, 01243 576119.

### Interfaces

**Motion controller.** Compumotor has introduced the *OEM-AT6400* motion controller, which provides up to four axes of open-loop control for standard stepper and direction motor drives, controlled by pcs on the ISA bus; it occupies an expansion slot in the pc. It handles position data up to  $\pm 2,147,483,648$  steps, up to 1,600,000 steps/s and acceleration up to 30steps/s/s. Control programs are implemented in the Windows-based *Motion Architect* software, the board having 63Kbyte of battery-backed ram for program storage. Parker Hannifin

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**Coils for inductive joysticks.** Gardners is making the coils for a new design of inductive joystick by Penny and Giles. Compared with the carbon track type, the joysticks are more reliable. Gardners coils, now in large-scale production, consist of a central primary surrounded by four smaller secondaries, all wound to a very high degree of accuracy to ensure exact balance when the lever is centred. Gardners Ltd. Tel., 01202 482284; fax, 01202 470805.

plc, Digiplan Division. Tel., 01202 699000; fax, 01202 695750.

## Literature

**Cells and batteries.** In 64 pages, a technical catalogue from *Saft Nife* gives the necessary information on selecting the appropriate rechargeable cells and batteries for portable equipment, including descriptions of the chemistry involved, cell construction, charging methods and battery configurations, power source design and a glossary. There are fourteen ranges of nickel cadmium and nickel-metal-hydrate cells. Saft Nife Ltd. Tel., 0181 979 7755; fax, 0181 783 0494.

**Interfacing.** The IEEE has produced a new edition of the monograph *Interfacing Standards for Computers*; it is nine years since the first one appeared, so publication has clearly not been precipitate. If you have managed to hold out this long, £9.50, which includes postage, will bring you a copy. IEEE. Tel., 0171 836 3357; fax, 0171 497 9006.

**Shielding and insulation.** *Warth International's* new 84-page catalogue describes electromagnetic shielding and thermally-conductive insulation products, with technical notes. Warth International Ltd. Tel., 01342 315044; fax, 01342 312969.

**Motors, gearboxes.** *Bodine Electric* has an eight-page spreadsheet 'Motor

& Gearmotor Selection Guide', which details all the Bodine equipment. Determination of speed and torque are explained, as is the calculation of gear ratio. Bodine Electric Company. Tel., 01252 811800; fax, 01252 811801.

## Materials

**Shielding bags.** 3M's newest plastic static-shielding bags are in a 25µm polyester film with a thin metal coating near the outside of the film so that continuity testing can be done more easily. Inside, the bags are resistant to friction charge, since an anti-static material is compounded with the polyester before extrusion rather than being applied as a coating. 3M United Kingdom plc. Tel., 01344 858000; fax, 01344 858758.

## Power supplies

**Current-sharing psu.** Astec's *LP series* of power supplies now includes the *LP150*, a range of units offering 150W in single or quad output form with six voltage configurations, with single-wire current sharing and auto-ranging input and housed in a U channel with or without a cover. All have remote sensing and inhibiting and all quad output models and the 5V single have a 'power good' option on the 5V output. Two of the quad units have a floating output variable between ±5V and ±25V. Full protection is incorporated and the supplies are certified to all manner of standards. Astec Standard Power Europe. Tel., 01384 440044; fax, 01384 440777.

**Voltage regulator with watchdog.** *CS-8141* from Cherry Semiconductor is a voltage regulator and watchdog providing reset and enable to a microcomputer, all in the one ic. The regulator provides 5V±4% at 500mA. A pulse train from the computer feeds the watchdog, a reduction in frequency causing a reset to avoid erroneous operation; a reset is also emitted if the regulator output falls out by more than ±4% for longer than 2µs. Frequency of the watchdog input also controls status: if it falls below a threshold, the regulator powers down gracefully and sleeps. Cherry Semiconductor. Tel., 001 401 885-

3600; fax, 001 401 885-5786. Internet info@cherry-semi.com.

**Fast charge ic.** Benchmark's *bq2031* lead acid fast charge ic controls constant-current or constant-voltage charging, conforming to recommendations for both cyclic and float charging. It displays charge status and fault conditions, testing first for shorted, open or damaged cells and conditions the battery for fast charge, which is also controlled by user-selected temperature and voltage limits. Sequoia Technology Ltd. Tel., 01734 258000; fax, 01734 258020.

**10A voltage regulators.** Having a dropout at 1.3V maximum, the *EZ1082* from Semtech has output of 1.3-3.45V at full current to power the *Pentium P54C-VRE* or some of the *PowerPCs*. Output variation due to all causes is ±2% and the output is fixed or adjustable. Semtech Ltd. Tel., 01592 773520; fax, 01592 774781.

**Universal desktop power.** For low-power desktop equipment, Powerbox's *PUP553 range* of 55W universal external supplies consists of six models. There are five single-output units with outputs from 5V to 24V and a triple-output type providing 5V, 12V and -12V. A range of output connectors is available. EMI filtering is included and all outputs are voltage and current protected. Electrospeed. Tel., 01703 644555; fax, 01703 610282.

**Battery-management ic.** *LTC1325* from Linear is a software-programmed battery charger to cope with most types of battery. It is an 18-pin cmos device containing a programmed pwm constant-current switcher, a 10-bit a-to-d converter, programmable battery voltage divider, timer, fault sensor interfaces, current monitor, discharge controller and a serial interface to the system microcontroller. Other components needed to make a complete charger are the controller, which can be shared with other applications, battery sensors and components for the

switching power supply. Micro Call Ltd. Tel., 01844 261939; fax, 01844 261678.

## Radio communications products

**Telemetry transceiver.** Wood & Douglas's *SX450* miniature telemetry transceiver is now emc approved to ETS300 339, ETS 330 220, MPT1329 and ETS 300 086. Weighing a mere 70g, the unit measures 85 by 55 by 13mm and is for use in industrial applications such as bar-code reading, GPS and modem, being capable of tuning any of up to 256 channels in a 10MHz range in the 400-470MHz band. It puts out 500mW with a current consumption of 400mA at 7.2V on transmit and 50mA when receiving. Wood and Douglas Ltd. Tel., 01734 811444; fax, 01734 811567.

**1GHz transmitter IC.** From Maxim, the *MAX2402*, a transmitter IC containing a double-balanced mixer, buffered local oscillator, a variable-gain stage and 100mW power amplifier, for use in the 800-1000MHz band. It is compatible with direct-sequence and frequency-hopping

## Printers and controllers

**Thermal printers.** Sixteen models in a range of high-definition, static-head thermal printer mechanisms by Panasonic are intended for use in measuring instruments and data terminals. *EPL1100* printers are available to take paper widths of 60, 80 and 112mm at print speeds of 8 and 16lines/s. All models are capable of graphics and can be ordered with or without a paper-end sensor and with a straight paper path for labels. Able can supply its own serial interface boards for the whole range, but there are parallel boards for the higher-speed models. Able Systems Ltd. Tel., 01606 48621; fax, 01606 44903.



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spread-spectrum modes in the 902-928MHz ISM band. Limiting amplifier takes single-ended or differential signal at between -6dBm and +6dBm. Maxim Integrated Products UK Ltd. Tel., 01734 303388; fax, 01734 305511.

**Switches and relays**

**Pcb relays.** BLP's AZ942 series of low-cost, industry-standard miniature relays for pcb mounting have contacts rated at 5A, 30Vdc/125Vac in the medium-duty version and 6A,30Vdc and 10A,125Vac. Both types have dc coils taking 48Vdc that dissipate 230mW. Versions are available sealed in epoxy to withstand wave soldering at up to 270°C. BLP Components Ltd. Tel., 01638 665161; fax, 01638 660718.

**Sealed, 10A relays.** Three versions of LRE's F600 6-pole changeover balanced force relay are now available, all in a 1.5 cubic in case. F670 is for electrically powered vehicles and switches 1A resistive at 72Vdc, with a 1x10<sup>6</sup> operation life. With the same life expectancy, the FD670 is a 3-pole double-break type and switches 3A resistive at 72Vdc or 1A resistive at 110Vdc. F600 is a military version, lives for 100,000 operations and switches 10A at 28Vdc, coping with 1400A under 10A circuit breaker protection. Several varieties of terminal, socket and coil suppression are offered. LRE Relays + Electronics Ltd. Tel., 01962 734433; fax, 01962 734685.

**Quad spdt switch lcs.** Maxim's MAX394 contains four single-pole, double-throw analogue switches, operating from either ±2.7-8V or +2.7-15V. On resistance is under 35Ω

and matches to within 2Ω between channels, staying flat within 4Ω over the input range of 0.8-2.4V. Switching is break-before-make, turning off in 75ns and on in 130ns. Quiescent current is 1µA. Maxim Integrated Products UK Ltd. Tel., 01734 303388; fax, 01734 305511.

**Tough switch.** A 240Vac, 21A miniature snap switch in Cherry's D48 range is designed to cope with the kind of treatment domestic equipment often receives and for industrial and amusement machine use. Contact gap is under 3mm and contacts are silver. There are spst/no, nc or spdt types and a selection of actuators and terminals. Cherry Electrical Products Ltd. Tel., 01582 763100; fax, 01582 768883.

**Gray-code rotaries.** Grayhill has added a range of rotary switches giving a Gray code to the Series 26 binary-coded models. Stops can be supplied to give any number of 22.5° or 45° positions or with continuous rotation. The switches are rated at 200mA for make and break. EAO-Highland Electronics Ltd. Tel., 01444 236000; fax, 01444 236641.

**Transducers and sensors**

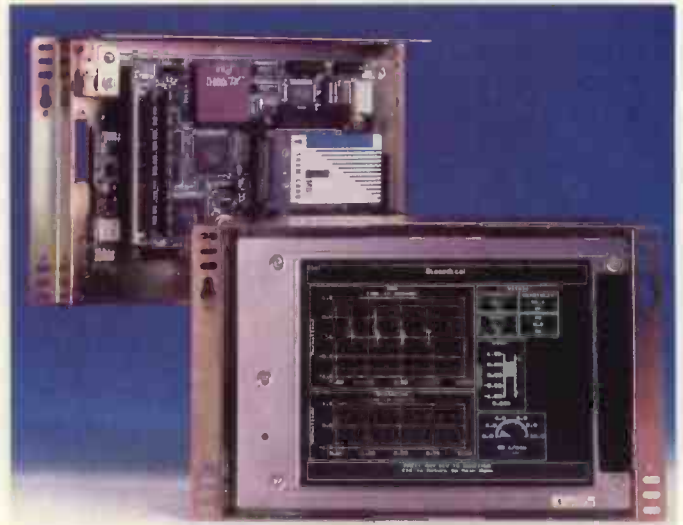
**Thermocouples.** Precious metal thermocouples in pairings and precision classes complying with IEC 584 are supplied by Heraeus in Germany. Types S, R and B are available to cover the 1400-1800°C range, the 0.05mm or 1mm diameter platinum and platinum-rhodium material being supplied in reels or rings in which the wires are wound side by side. Certification with each batch is traceable to the German Calibration Service. W C Heraeus GmbH. Tel., 0049 6181/35-5957; fax, 0049 6181/35-888.

**COMPUTER**

**Computer systems**

**Hand-held computer.** AMD offers the Pen\*Key 6100, a Windows-based hand-held computer using the Elan 386 processor chip. This is a 32-bit, PC AT-compatible chip, available in 25MHz and 33MHz versions, intended for use in telephones and pocket organisers, as well as computers, supporting all standard pc operating systems. Pen\*Key measures 7 by 4.5 by 1.2in, has a 240/320 pixel display and two PCMCIA slots. Advanced Micro Devices (UK) Ltd. Tel., 01483 740440; fax, 01483 756196.

**Master and slave PCs.** Blue Chip's MasterConsole reduces the cost of a multiple-pc network by controlling up to 16PCs from one station, which is the only one having keyboard, monitor and mouse. MasterConsole has intelligent processors at each port for keyboard and mouse emulation in booting and operation, 'plug-and-play' facilities allowing any mix of pc types to be used. Built-in commands eliminate the need for extra software



**Component PCs.** From Advanced Modular Computers comes the AMC-EP486 Embedded Panel PC, which effectively treats the computer as a component in a system. It consists of an open frame containing all the usual pc hardware, fronted by a flat-panel display, which can be specified as anything from a mono lcd to a bright active-matrix colour super-VGA tft.

The pc itself consists of a single cpu board, which is the ANC 4860, using a 486. Ethernet is on-board, as is a solid-state disk. Two serial ports are provided, a parallel port, IDE hard-disk drive controller for two disks, NE2000-compatible network interface, a floppy drive controller and a keyboard/PS/2 interface; a watchdog timer is included to reset the system in case of trouble. There is a three-slot bus expansion unit.

Since the computer is an operating PC, it will handle all the usual operating systems, software and development tools. Power needed is 3A at 5V. Advanced Modular Computers Ltd. Tel., 01753 580660; fax, 01753 580653.

for communication between master and slaves. Blue Chip Technology. Tel., 01244 520222; fax, 01244 531043.

**Data acquisition**

**60Msample/s digitiser.** Steatite offers the DA60 waveform digitiser board, which is a 12-bit type capturing 60 megasamples/second on a single channel or 30 on two channels simultaneously, which is claimed to make it the fastest of its type in existence. The board is ISA-bus-compatible with an analogue bandwidth of 30MHz on each channel and 512K samples of memory on board. Digitally controlled attenuators set the gain of each channel over a 34dB range in 0.2dB steps. A library of C functions and source code come with the board. Four input signals are two analogue inputs, an external trigger and external clock, the

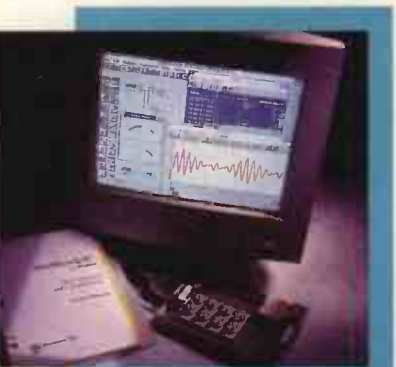
analogue inputs having full-scale voltage ranges of 100mVpk-pk to 5Vpk-pk at 50Ω. Triggers are single-shot and segmented, level being adjusted by the built-in d-to-a. Steatite Insulations Ltd. Tel., 0121 643 6888; fax, 0121 643 2011.

**Transient data capture.** Falcon from Endevo is modular transient data capture system that uses multiprocessing and Windows-based software for control, display and analysis. Data acquisition modules connect by way of a serial comms bus, each module comprising four simultaneously sampled 12-bit a-to-d converters with data storage of up to 1Msample/channel and each module has its own 32-bit processor. Using modules in this way eliminates loss in performance when more channels are needed in an ordinary multi-channel system; sampling rate stays the same, as does front-end performance. Channel specifications include ±2.5V single-ended or differential input, 1-50 gain, selected by resistor value and DDE Windows 3.1 compatibility. Endevo UK Ltd. Tel., 01763 261311; fax, 01763 261120.

**Boards with software.** Low-cost plug-in data-acquisition cards from Computer Instrumentation now come with free data logging, virtual oscilloscope and multimeter software for Windows and language drivers for programmers. The SoftLogger handles up to 24 channels at 10 scans/s, providing graphical display and data storage and direct digital exchange with other software. SoftScope is a two-channel dso with facilities including FFT and storage to disk, while the Virtual Multimeter simulates a hand-held instrument with digital or analogue display. Computer Instrumentation Ltd. Tel., 01903 765225; fax, 01903 765547.

**Development and evaluation**

**68HC05 emulation.** Pentica offers more 68HC05 device adaptors for the Mime-600 in-circuit emulator - now the X4, P6 and E6 variants of the Motorola processor are supported.



**Coder for LabWindows/CVI.** National Instruments has a new version of LabWindows/CVI visual data acquisition and instrument control development software for Windows PCs and Sun SPARCstations, using Ansi C. New development tools include CodeBuilder, which generates code automatically. There are also more custom components and new graphing and image display facilities. National Instruments UK. Tel., 01635 572400; fax, 01635 523154.

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The Pentica emulator for each chip recognises all the configuration registers and on-chip integrated support hardware, so that, in a system, it behaves in the same way as the processor hardware. Pentica Systems Ltd. Tel., 01734 792101; fax, 01734 774081.

**PIC development.** Microchip's *PICSTART-16B1* is a development system for the 18-pin and 28-pin *16C5X* and *16Cxx* microcontrollers that can be used on any host pc to read, program and verify PICs. The system comprises a simulator for symbolic debugging and i/o stimulus and an eprom programmer and macro assembler. There are also windowed parts for prototype use, sample software routines and documentation. Combined Precision Components plc. Tel., 01772 654455; fax, 01772 654466.

**Data logging**

**Sliced data logger.** Delta-T Devices announces the *DL3000* modular data logger, which comes in slices. It is based on Windows and is provided with *Acquire!* software to allow the definition of conditional and event-driven logging and to program inter-channel calculation. Resolution is typically 17 bits with 24-bit internal a-to-d and the inputs, which accept counter, digital, frequency, period,

voltage, resistance and current, are expandable from 24 to 384 channels. Physically, the logger is modular, each module being in an aluminium slice, so that the instrument may be up-graded in the field by adding more slices or fitting a *PCMCIA* memory card module. Since the characteristics of many sensors are held in a software sensor library or can be added by the user, many can be connected to 'plug-and-go'. Delta-T Devices Ltd. Tel., 01638 742922; fax, 01638 743155.

**Software**

**Workbench for Windows 2.0.** Version 2.0 of Strawberry Tree's *Workbench for Windows* has 5kHz real-time display rate, 100kHz streaming direct to disk and transient capture in burst mode at up to 1MHz. *Workbench* allows users to establish data acquisition, analysis and control systems on screen, data being collected in a matter of seconds, with no programming needed. Signal-conditioning, meters, chart recorders and all the other hardware are software items from a comprehensive library that can be selected on a point-and-click basis. Multiple displays can be used for one experiment to give different views of the data, without interrupting data capture, and historical trending for slow changes is carried out in real

time. Hardware required is *486 DX33* or better with 8Mb of ram and Dos 5.0, with Windows 3.1. Adept Scientific Micro Systems Ltd. Tel., 01462 480055; fax, 01462 480213.

**68000 debugger.** Intermetrics's *PassKey* high-level debugger is now available in a Pentica emulator version for the *68XXX* processor family. *PassKey* presents the most critical data updated each time the target halts, this among other extra features for point-and-click debugging. Other features include C expression evaluation, stack tracing, breakpoints with action lists, session record and playback and user-specified command lists after each line of source code. Pentica Systems Ltd. Tel., 01734 792101; fax, 01734 774081.

**Stock control.** *Stockit* is a stock control software package for smaller companies who have no need of the extensive, accounts-based type of program. It is, nevertheless, tailored for the job and is not simply a cut-down version of the more exotic types. It holds complete records of parts lists, stock levels and suppliers and will allocate stock when a batch is needed, being able to include cable lengths, consumables and labour in calculations and printing enquiry sheets for quotations. It is not necessary to convert from existing

systems immediately; the process can be gradual, while still realising the benefits of the package. Number One Systems Ltd. Tel., 01480 461778; fax, 01480 494042.

**Schematic/pcb design.** *Quickroute 3.5* has been completely restructured to incorporate a wide range of new features all compatible with Windows 3.1 and Windows 95. Features include 'design vectorisation' for enhanced Gerber generation, 'design quantisation', copper fill, better import/export support and an improved auto-router. The software comes with a new user interface - multiple button-bars giving instant access to editing and placement tools, a new 'parts bin' for storing components, track and pad styles etc. A free demonstration disk and brochure is available. Quickroute Systems Ltd, 14 Ley Lane, Marple Bridge, Stockport, SK6 5DD. Tel/fax 0161 449 7101.

**Factory layout.** Deneb Solutions offers *Quest* for Windows NT, an interactive, three-dimensional simulator for the design of batch processes that produces a textured, virtual-reality walk-through factory layout, complete with accurate representations of machinery. Intelligent System Solutions Ltd. Tel., 0161 745 7384; fax, 0161 7458264. ■

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EIP Type: 331 Microwave 25MHz-18GHz	£650
Racal 9904(D) 50MHz	£100
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Racal 9915(M) 10Hz-50MHz Fitted FX standard	£125
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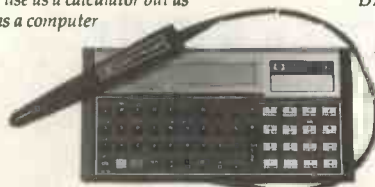
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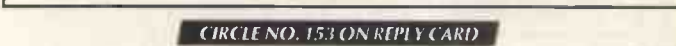
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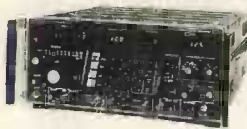
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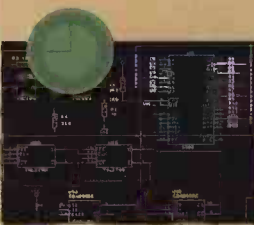


### Your move

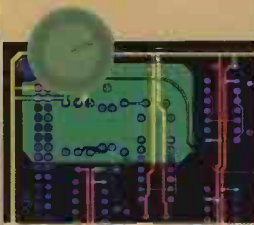
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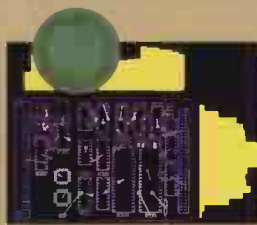
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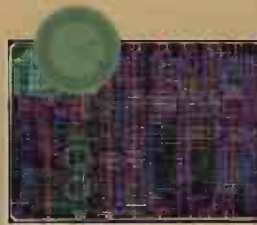
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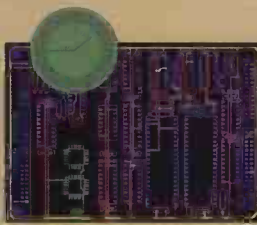
If you need partial ground planes, then with the Dos extended board systems you can automatically create copper polygons simply by drawing the outline. The polygon is then filled with copper of the desired net, all correct pins are connected to the polygon with thermal relief connections and user defined gaps are respected around all other pads and tracks.



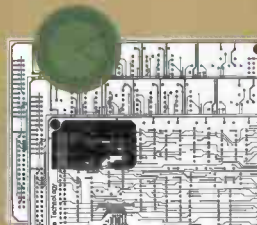
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