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DECEMBER 1985 95p



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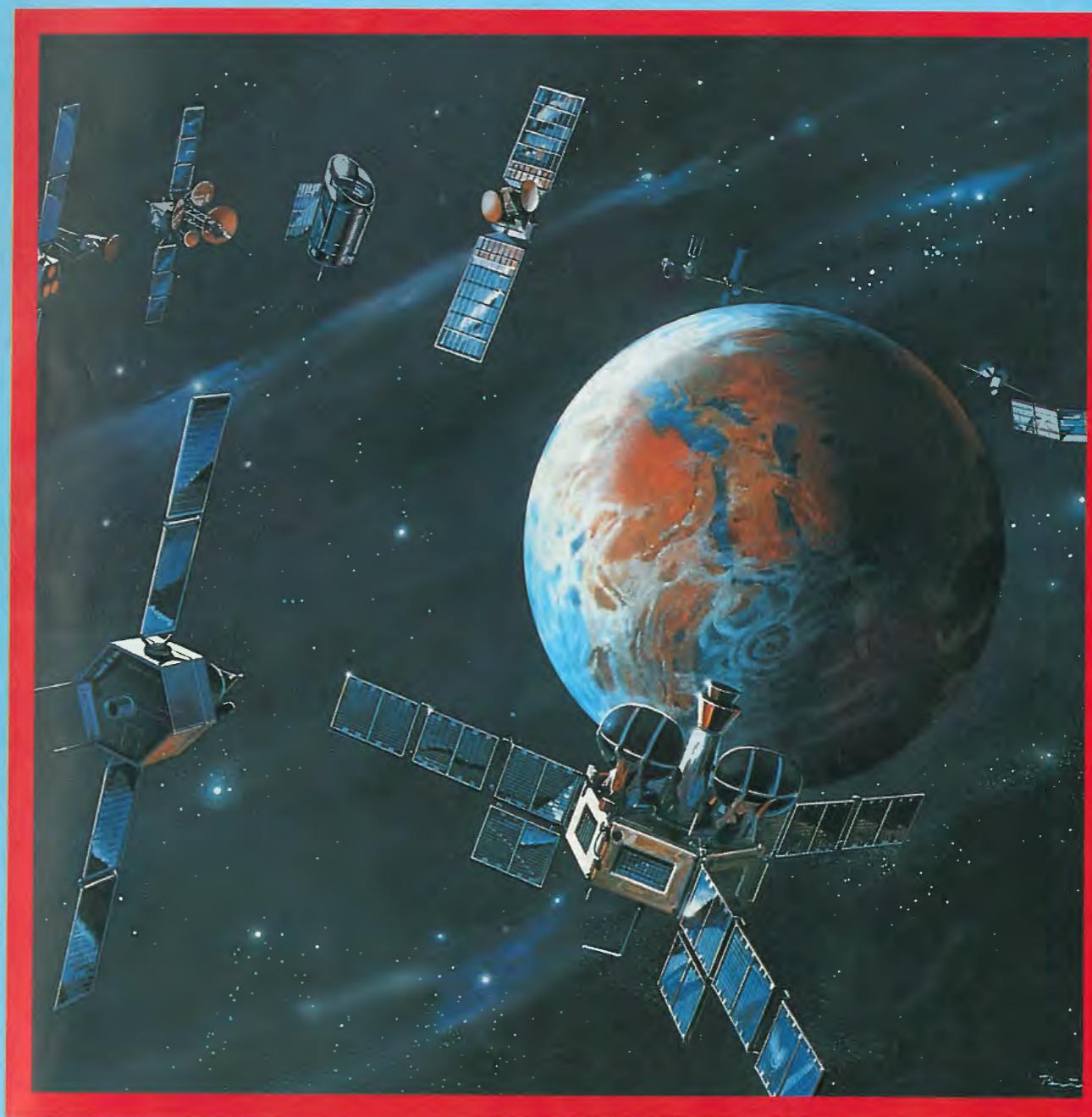


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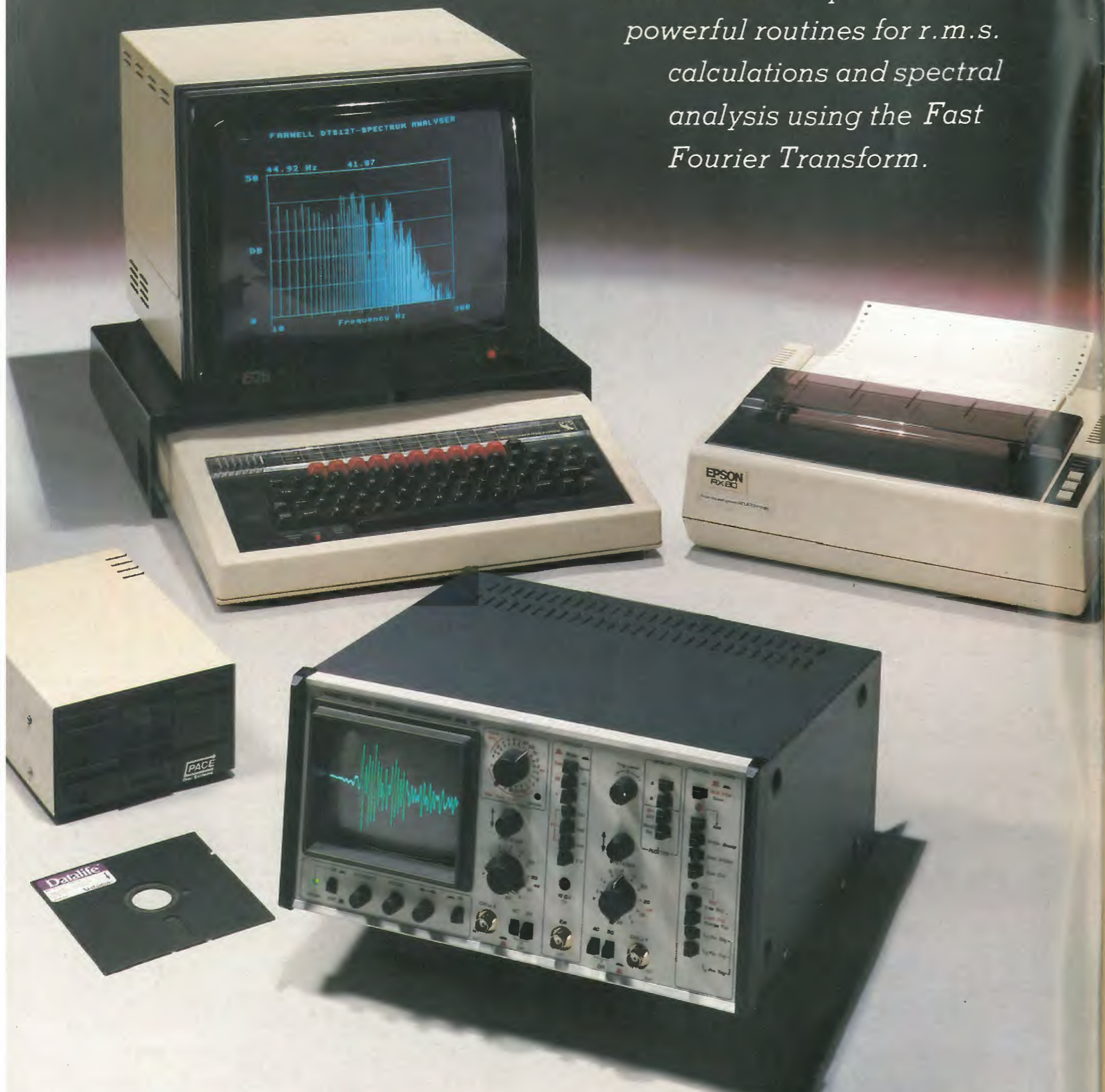
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The new DTS12T low cost Digital Storage Oscilloscope from Farnell provides 'listen' and 'talk' IEEE488 bus operation, will interface with the user port on a BBC model B microcomputer, and specially written software provides powerful routines for r.m.s. calculations and spectral analysis using the Fast Fourier Transform.



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Number 1598

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Who should market satellite TV

In October of 1979 a group of the first seven owners of home-constructed satellite earth stations got together and ran a seminar. Those who attended went home to their garage workshops and kitchen tables from where the satellite industry of the USA started. Many of the major multi-million pound companies started there in 1979. Now,

there are over a million home satellite stations, with sales estimated at four million units this year — a major market. The American experience shows that those still in the business five years on were the *technical* people that were in at the start. No doubt there were a few fast-buck merchants, but their names are well forgotten.

The UK should learn from its transatlantic cousins, says John Standen of North East Satellite Systems and author of the article starting on page 60, who feels that if satellite tv is marketed through the technically inexperienced the public will receive an inferior result which could set back this new technology by several years. "Sales of satellite tv

equipment should be made from a small network of qualified technical people with a genuine interest in the technology rather than a group of 'in it for the money' organisations who do not know a transistor from a London Bus." The market for the uninitiated must wait for d.b.s., he says, where the power level is so high that reception is just about possible from an open-ended waveguide pointed in roughly the right direction!

Transputer — a little late but fast enough

After a year's delay, Inmos Transputer evaluation boards are finally in the hands of distributors, but individual processors are not expected to be available until January.

Between £5 and £10 million has been invested in the Transputer project from a total Inmos investment of around £100 million. Although the processor technology was developed in the USA the device architecture is of UK origin. Manufacture is taking place in Newport, Gwent.

Despite this investment Inmos is not prepared to speculate about sales, using the argument that the Transputer is a new product and occupies a new market. 'We intend to leapfrog the competition' says Inmos leader Iann Barron.

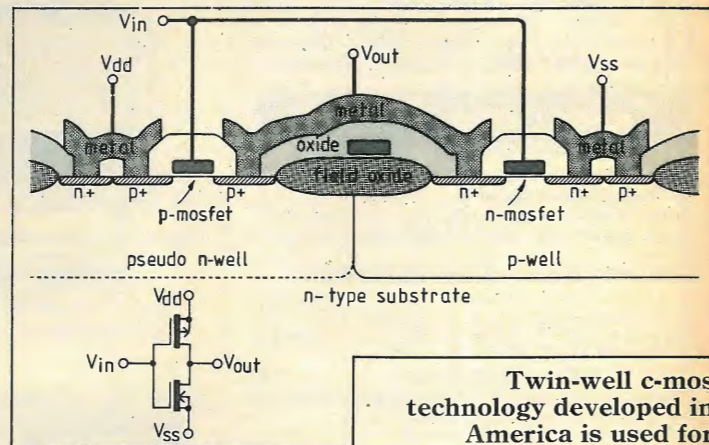
Transputer strategy, says Inmos, is to avoid direct competition with Intel and Motorola and 'be a DEC not an RCA'. There was no shortage of comparisons with the Motorola 68000 processor and its markets at the launch though — Iann Barron went as far as to say that Motorola and Intel are the company's main competitors. It was also said that the Transputer has not only the market of all of the other processors but others' new ones too.

Inmos speed and efficiency comparisons show that the Transputer outstrips the competition. But this completely new device operates in a different way to conventional processors which makes it difficult to judge

the real value of these comparisons. Two things seem clear though — it is easy to run many Transputers in parallel and performance increase is directly proportional to the number of processors used. This is not the case with conventional processors.

Motorola's European Product Marketing Manager Ray Burgess feels that the Transputer's market is certainly not the same as that of the 68000 and that it will never replace the general-purpose processor. "It's unlikely that you will find a Transputer at the heart of a large workstation" he says. "Of our high-end users, not one has considered using the Transputer instead of the 68000." But it is early days yet.

Inmos says that the lack of a second source is not a problem



Twin-well c-mos technology developed in America is used for Transputer. The chips, each holding 200 000 transistors, are made in Newport, Gwent.

but Burgess disagrees, asking "Which large manufacturer would take the risk?" A second source for the Transputer is not expected until the second quarter of next year. "Viability of the vendor is also an important factor to a large company when choosing a processor" he added.

Asked who would use the device, Burgess said that it would be used by universities and people who didn't have to

justify research costs. He says that the Transputer will compete more with new signal-processing devices rather than with general-purpose processors, finding applications in graphics terminals and voice and signature analysis.

Transputer technology

Unlike conventional computers which execute programs sequentially, the Transputer executes programs concurrently, which means that many steps of the program can be run at once. The device can be used on its own, but its forte is in multi-processor applications. The processor has special communication links for connecting to other processors of the same kind. Internally, the Transputer uses reduced-instruction-set computer architecture which is said to allow high performance

on a small silicon area. Simple operations like addition and subtraction take 50ns and more complex operations like scheduling take less than 1µs. Average operating speed is 10 million instructions per second. The instruction set is designed for executing complex programs and all instructions are one-byte long which means that programs are compact. The Transputer can be programmed in standard high-level languages like C, Pascal and Fortran, but to exploit its concurrent properties, a language

called Occam is used. This language has been available for more than two years (News, Feb. 1983, p.37). Programs running on a single Transputer system run twice as fast on a dual-processor system without software modification because of the nature of the software and architecture of the processor.

One particular area of interest is in so-called fifth-generation computers, say Inmos, where the Transputer is already being used in a number of experimental systems.

HDTV is coming

High definition television is definitely on the way. That was the message brought by Stuart Sansom of Sony Broadcast when he demonstrated his company's system in October to a meeting of the Royal Television Society and the BKSTS. The first broadcasts will come from NHK in Japan, who intend to begin a direct satellite service in 1988.

Enough equipment is available now for the Japanese 1125-line standard to make up a more-or-less complete broadcasting chain. In Sony's demonstration set-up were a video recorder, a camera, a mixer, a large-screen three-tube projection tv and c.r.t. monitors in sizes up to 28 inches. Equipment elsewhere includes standards converters and telecine. And tape-to-film transfers have become possible through an electron beam recording process developed by Sony. Mr Sansom also revealed that laboratory prototypes of an 1125-line domestic video recorder are already in existence.

The picture aspect ratio of Sony's system is 5.33 to 3 and the information content about five times that of a conventional 525/60 NTSC signal. The choice of format was guided by the need for compatibility with the cinema; the picture is much wider than the 4:3 aspect ratio of present-day tv broadcasts.

Sony's system is based on research findings* published in 1982 by NHK, the Japanese state broadcasting organization. The 1125-line standard was picked as representing a level of resolution beyond which improvements were unlikely to be visible under practical conditions.

Mr Sansom explained that the large screen was intended for fairly close viewing, at a distance of two to four times the picture height. Although only the central portion of the eye's retina had sufficient resolution to take full



On the set of *Onirico*, an experimental programme made by RAI Milan using Sony h.d.t.v. equipment

advantage of the high definition, it had been shown that a larger image was essential for creating an illusion of realism. The field rate of 60Hz, with 2:1 interlace, was found adequate to overcome the flicker associated with large high-brightness pictures.

Recordings shown by Mr Sansom included a demonstration tape from NHK featuring excerpts from the opening ceremony of the 1984 Los Angeles Olympic Games, moments from a selection of Japanese variety shows and some wildlife scenes. Although the recordings were third or fourth-generation analogue tapes, clarity and colour quality were very striking — every hair on the squirrel seen cracking nuts was sharply resolved. Line structure on the display could be seen only by peering closely at the screen.

Also shown was a short experimental production made by Radiotelevisione Italiana using equipment provided by Sony. The programme, a wordless melodrama entitled *Onirico*, was shot largely at night with the aim of showing any shortcomings of the system. High-contrast pictures showing car headlights in the dark street were realistically handled, though it was

noticeable that actors who moved in front of a light source became transparent for an instant. However, Mr Sansom indicated later that lag and other problems caused by electronic camera tubes could be expected to disappear as solid-state devices took over.

The audience also saw *Onirico* in a 35mm film

transfer, which made it plain that the 1125-line picture is potentially quite good enough for originating cinema material. The main defect was a characteristic shudder on movement. This, according to Mr Sansom, was a result of conversion from the 60Hz field rate of the original, and could be reduced by improvements in the process. He mentioned some Canadian research which had shown that the high resolution of which 35mm cine film was capable was never realised because of



The IEE Faraday lectures this year are to be given by British Telecom on the theme *Beyond the telephone: the intelligent network*. Bill Jones, BT's chief executive of technology, is the senior lecturer and is assisted by the anonymous 'digital man'. As in previous

years the lectures are a travelling road show intending to stimulate interest in the subject amongst young people, supported by the full works: Live demonstrations, audio-visual presentation, film and a number of special effects. Tickets: tel. 0462 5331

unsteadiness of the frame in the cinema projector: the real resolution never exceeded about 700 lines. One drawback of the Sony system is the enormous bandwidth needed for its transmission: 20MHz for luminance, 7MHz and 5.5MHz for chrominance. However, Sony has devised a compander system named *Muse*, which can squeeze the signal into a standard satellite channel and will be used for the forthcoming NHK service.

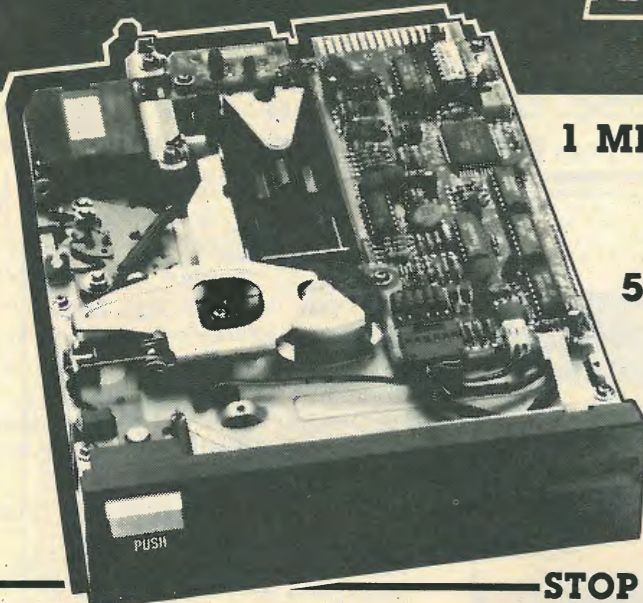
What about Europe? The CCIR has been meeting in Geneva to discuss a possible world standard. But Mr Sansom believed that agreement was unlikely to be reached quickly and that countries with existing 60Hz systems would probably go ahead with the Japanese standard.

Asked about the issue of compatibility with existing terrestrial tv standards, he told his audience that satellite interests would soon dominate the television medium and that the day of the terrestrial broadcaster was nearly over.

*Technical Monograph No. 32, Nippon Hoso Kyokai 1982.

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PRESET	0.12	4066	0.30	3764-20	2.50	C+K 8631 PUSH	0.25	47	7.5K	2M			47NF DISC	560 PF	4.7K	470K	10MF/16V	0.08			4066	0.30	4070	0.20	40 PIN	0.25	51	8.2K	2.2M			47NF DISC	1000 PF	4.7K	470K	10MF/25V	0.10			4066	0.30	4070	0.20	40 PIN	0.25	56	9.1K	2.4M			47NF DISC	1500 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	62	10K	3M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	68	11K	3.6M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	75	12K	4.3M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	82	15K	4.7M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	91	15K	5.1M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	100	16K	5.6M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	110	18K	6.2M			47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	120	20K	6.8M			47NF DISC	4700 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PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	390	68K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	430	75K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	470	82K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	510	100K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	560	110K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	620	120K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	680	130K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	750	150K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	820	160K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	910	180K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	910	180K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	150	24K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	150	24K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	160	27K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	160	27K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	200	36K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	220	39K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	240	43K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	270	47K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	300	51K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	330	56K				47NF DISC	4700 PF	4.7K	470K	10MF/16V	0.12			4066	0.30	4070	0.20	40 PIN	0.25	360	62K				47NF DISC	4700 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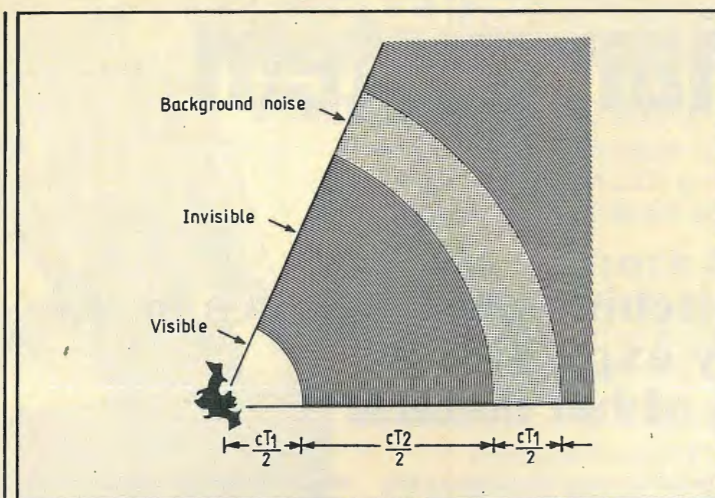


Fig. 3. Illustration of the 'sight' of bats using pulsed sounds. The range of detection is limited to within $cT_1/2$.

hypothesis that it is the mechanical switch of the ossicles controlled by the middle ear muscles which synchronizes with the transmitter (comprising the larynx and air cell) seems to explain why the detection range of bats is limited, as well as why bats can discriminate their own echoes in spite of the confusion of other's sound and an echo-cluttered environment. In a dark cave, hundreds of bats fly within the range of one another's sounds. Yet each bat can guide itself by the echoes of its own sounds.

Denoting T_1 as the receiving interval and T_2 the non-receiving interval, the rate of noise rejection is given by $T_2/(T_1+T_2)$, estimated to be 94% for case (a) 91% for case (b) and 86% for case (c). Further, the group of echoes returning from a prey in the range of

detection has the pitch frequency of $1/(T_1+T_2)$ which may discriminate echoes from click noise, viz., the long-path echoes and other's sounds which fall into the receiving interval. Besides, as generally expected in other animals, binaural hearing together with the super-directional ears of bats must be used to localize the echoes.

Fig. 3 illustrates the 'sight' of bats using pulsed sounds, where the 'visible' area is limited within the range of $cT_1/2$, c being the speed of sound. The 'invisible' area lies from $cT_1/2$ to $(cT_1/2) + (cT_2/2)$, from $cT_1 + (cT_2/2)$ to $cT_1 + cT_2$ and so on, and the area of background noise due to the long-path echoes is shown from $(cT_1/2) + (cT_2/2)$ to $cT_1 + (cT_2/2)$. The sonar of bats is suitable for the short-range target detection. The electroacoustic analogue of the sonar

is shown in Fig. 4, where the ossicles are illustrated as the switch that synchronizes with the pulse generator representing the larynx. It is interesting to recall that the middle ear muscles of bats are quite large compared with the case of other animals.⁴

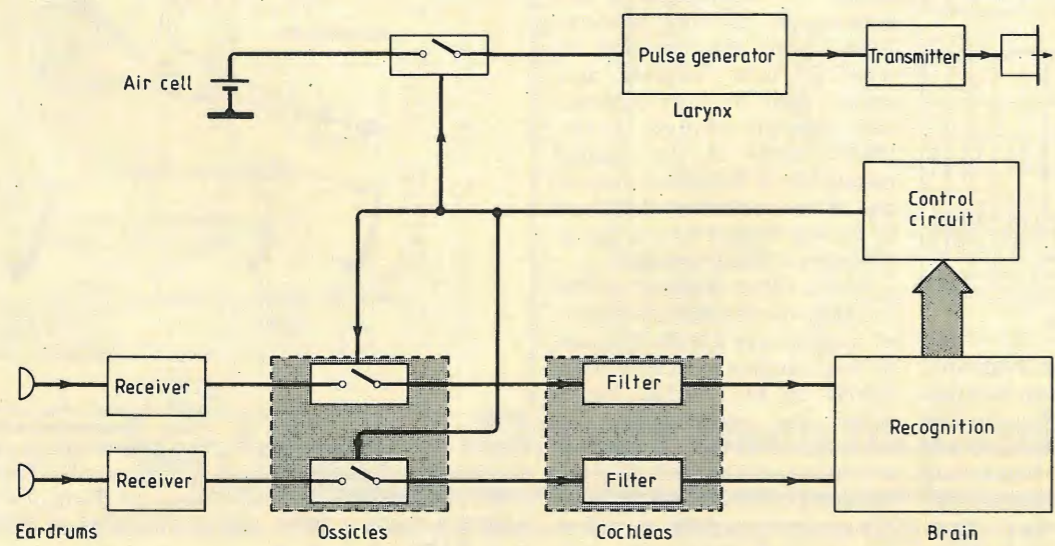
Although there is circumstantial evidence, some direct measurements are necessary and are expected to verify the above hypothesis. In addition, if the bats have refined their ears in the process of evolution, it is also necessary to answer the questions of why they chose the difficult way of training their ears and larynx instead of training their eyes in the dark, and how it was achieved.

However, ask, please, the domestic animals, and they will instruct you; Also the winged creatures of the heavens, and they will tell you. — Job 12:7

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Fig. 4. Electroacoustic analogue of bat sonar, where the ossicles are illustrated as the switch that synchronizes with the pulse generator representing the larynx of the transmitter.



THROWAWAY REMARKS

I wonder whether any of your readers are suffering the same concern as myself over the present trend towards maintenance-free items, or extended interval maintenance in particular, as applicable to modern cars.

It is reported that television servicing may become a thing of the past: "When a major fault occurs you simply replace the whole set" (*Television magazine*, October 1985).

In days, unfortunately, past, if my car developed an ignition fault, a process of elimination might trace it to worn contact-breaker points which were replaced at the cost of a few pounds.

Recently, an ignition fault in my C.t . . . n was quickly diagnosed, by myself, as the "transistorized module" — an encapsulated item manufactured by Ducellier, which cost £79.13 and which, according to "C.t . . . nian" magazine is stocked in quantity by dealers.

Perhaps a reader may be able to provide a replacement in Circuit Ideas.

A. Hardcastle
Wandsworth
London SW18

Wireless World (1926) 754, calls this quantity Magnification. Can any of your readers quote an earlier reference to Q in an American or British journal?
Bartha Jeffreys
Cambridge

LIGHT CONVERSATION

I read Mr Rollemas' article on the German speech over light equipment with considerable interest as I was in charge of the Signals Experimental Group MEF, the unit referred to in the text, and as a physicist in uniform sorted out the optics. These were most interesting but my memory is that the head amplifier and main amplifier were rather poorly designed and were quite noisy.

We had harbingers of the unit a long time before the complete article was taken, in that at intervals the photoelectric devices came into our hands. They were of course a piece of red glass with a speck of thallium sulphide, all mounted with contacts in a plastic case. The first ones were handed to me as probable Tourmaline plates of the frequency recorded on the case. It was thought that the enemy had run out of quartz! They were soon identified and the number on the case seemed to be a serial number.

Later in the war I was sent from Cairo to examine some u.h.f. equipment on the island of Rhodes, when the enemy gave up in those parts. Apart from this I found a number of the Lichtspricht 80 devices which, according to the Germans, had been used over short distances for secure communication between strong points on hills and capes of the island. They differed very little from the earlier models, except that some extra baffles had been inserted to cut down scattered light.

The u.h.f. equipment mentioned above was on a quite high frequency for those days, some 300MHz, I seem to remember, and was the end link of a line through the Greek Islands, Greece, Corfu, Italy and finally the terminals at Vienna and Berlin. Its Rhodes station was on one of the peaks of Monte Profeta. It carried a speech circuit and a number of cyphered teleprinter circuits. In going to visit the station, myself and another signals chap read the map wrongly and strayed into a mine field from which we retreated most carefully.

Whilst we were finding out how the equipment worked something occurred which is a tribute to the RAF and their aerial surveys. An RAF man appeared in a Jeep and said could we confirm that the site

was a radio station. We said, yes it was but how could he know as the aeriels were tucked away in a cistern with a plastic face. Consulting his records he said that there had been a mast on the site earlier in the year. The Feldwebel left in charge was puzzled at first and then remembered that when the Captain had been in hospital in Rhodes Town the troops had got fed up with their comforts receiver which, like most army issue, had a very short range, and that they had cut down a pine tree and erected it on the site with a clothesline aerial, better to hear the dear homeland. The Captain, on his return, caused its removal saying the RAF would see it. It seems that not only did they see it but were ready to land 60 tons of bombs on the area in a few weeks' time.

You can't be too careful in war!
E.C. Vast
(Ex-Major, Royal Signals)
Hastings
East Sussex

ELECTRO-MAGNETIC PARADOX

Is not the motion of Mr Aspden's isolated electron a contradiction in terms? If it is truly isolated there is not even a reference frame from which its motion may be recognized, let alone the effects of that motion upon its non-existent environment, thus it can not possibly moderate anything at all. (Yes, I know I'm awful.)

More seriously, displacement current may perhaps be best thought of as the movement of catastrophe into a storage cul-seac for later release: it is not a true current being devoid of holes and having no closed loop. Its release would be a current.

A limiting sub-mass (LSM) is capable of spatial spin and thus of having a linear axis of rotation, and thus poles which are dimensionless points. Consideration of and by the poles is essentially statics, while consideration of and by the equator is essentially dynamics because the thing moves. A "field" of LSMs may be random or polarized: the latter case demonstrates linear polar electrostatics and spatial equatorial dynamics in simultaneous existence at right-angles, but only the dynamic dimension is capable of harmonic relationships.

Because an LSM can spin, its spatial integration can have spin "energy" which I suggest is indeed "energy": its axis, an unborn denizen, can not. I suggest that e.m. radiation is the linear

transference of that energy with interaction between action and reaction, the universal precedent for evolution, interaction being faster than c. A photonic parcel then becomes a spinning LSM, and a collection of them can shunt in waves. It is not the waves which can be polarized but their constituents, by the refusal of constituents of the filter to spin randomly, so precluding the possibility of the transmitted LSMs demonstrating harmonic relationships, a declinatory matter lacking in spectaculr sparkle or highlights.

An LSM is a product of catastrophe, postulated following the consumption of much creamed rice: if their axes coil themselves around a tree they are no longer straight, and the facts become distorted by their wriggling which causes an intermixing of high and low-energy LSMs better known as the rough and tumble of chaos with a current flow which tends to zero. The problem then becomes the creation of order once more, calling for a cessation of argument and much learning so that the axes may be brought into line: but it starts in abstraction.

I would not expect a non-energetic displacement current to set up an energetic magnetic field any more than I would expect the cart to push the horse other than downhill, but I can see a non-energetic field co-existing with an energetic one which possesses dynamism, and I can see why artists paint pictures: the energy in a laboratory experiment remains only deduced, and it will always remain so even though it can be harnessed.
James A MacHarg
Wooler
Northumberland

VALVE DISC PREAMPLIFIER

I read with interest the article by Richard Brice on a valve disc preamplifier. I agree with his basic assumptions and design philosophy but he had made some errors of calculation.

The 10nF capacitor in conjunction with R_1 (1M Ω) will not produce the 7950 μ IEC bass roll-off. The Thevenin impedance seen by the 10nF capacitor is more of the order of 18M Ω due to the bootstrapping effect of the cathode follower. There is no need to change this — the IEC roll-off may be implemented elsewhere. His equalization is, unfortunately, incorrect. He has assumed that the equalization network sees only the 250k Ω resistors (where do you get 250k Ω resistors)? In fact, it sees the Thévenin equivalent circuit which is the 250k Ω resistor in

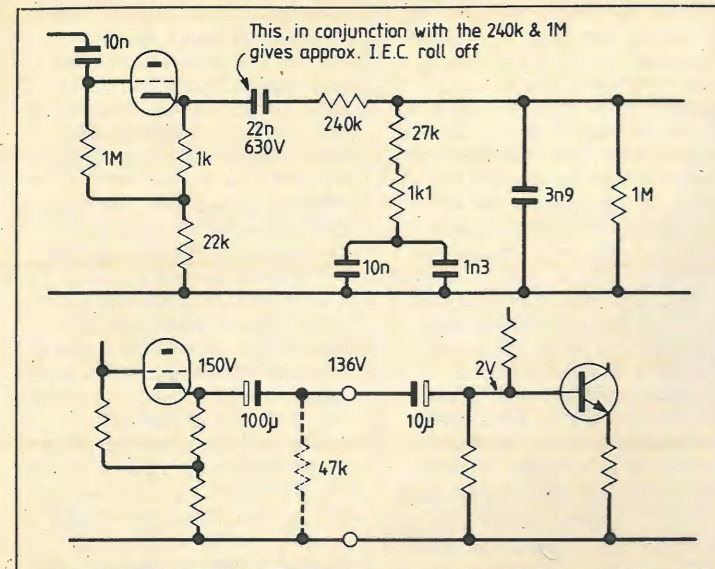
SWITCH CURRENTS

Spotted yet again: the common fault of placing a capacitor directly across a switch, such as the reset button on microprocessor designs. This leads to very large currents flowing when the contacts are made, and bang goes the switch. It is good practice to use a series resistor of a few ohms, which will prevent failure.

I believe that this was pointed out in a *WW* of 1958 or thereabouts.
H. B. Finney
Cambridge

A QUESTION ABOUT 'Q'

In the *Quarterly Journal of the Royal Astronomical Society* (1985) 26, 51-52, I traced the history of Q, but failed to find where it first appeared in the literature. I have now a reference to a footnote in the *Bell System Technical Journal* (1930) 507, a paper by C.E. Lane, Phase Distortion in Telephone Apparatus; however Sir Charles Oatley tells me that he heard of it from Mr Scroggie as the ratio of reactance to resistance of a tuned circuit before 1927. Butterworth,



parallel with the 1MΩ resistor. An easy mistake — I did the same for months in blissful ignorance.

If we re-calculate the values of components in the equalization network we can also add the IEC roll-off. This has the added advantage that we can replace the 1μF electrolytic capacitor with a better-quality component such as a polycarbonate. See diagram 1.

The only other changes I would make are of detail, the 1μF capacitor feeding the 250kΩ potentiometer could be replaced by a 0.22μF polycarbonate. The 1μF capacitor following could be replaced by a 10nF as used in the earlier stage.

The 100μF output coupling capacitor must surely be a bulky and suspect component; is it really necessary? The output stage should not be loaded by less than 10kΩ, so if we accept that as the minimum input impedance of the power amplifier, we find that 4.7μF would be an adequate value. It may be possible to use a non-polarized component here, but it would be expensive.

A word of warning, some transistor power amplifiers have coupling capacitors on their inputs with the laudable aim of preventing damage due to d.c. being applied at the input. Valve equipment always has an output capacitor to protect the following stage. Therefore connecting a valve pre-amplifier to a transistor amplifier should be safe. Occasionally this is not so; diagram 2 shows why.

The capacitors charge to equal values of charge, by applying $Q = CV$, we find that the small input capacitor of the amplifier has a large reverse voltage across it. It will fail quickly.

The solution is to add a resistor, shown dotted, or to remove one of the capacitors.
M. Jones
Shirley Warren
Southampton

SILICON DISC

The 'Silicon disc' design by J. Adams, published in the October issue, is certainly a good scheme for improving the response time of many floppy I/O bound programs and it also increases the amount of on-line storage available to the SC84 computer. However, with the extra memory configured as an extra logical disc (drive E), the system user must be especially careful to copy valuable files to 'real' disc before removing the system power. Also he is very prone to losing files through mains power failure. (In the rural area where I live, we lose the mains four or five times a year.)

An alternative approach, which uses exactly the same hardware, is to use the extra memory as a disc sector cache memory. With this system, when the disc driver routine is asked to read a sector, it first looks in the cache for the sector and returns the cache copy if found, else it must make an access to the physical disc to read the sector. Many algorithms for managing cache memories exist, and some are discussed in articles in *Byle* magazine, September, 1985.

The method I use is to hash all sector addresses to give a block number in the cache store. This gives a many-to-one mapping between all on-line disc sectors and the set of blocks in the cache. Upon disc writing, the sector data is also written to its cache block, overwriting previous data, and a directory field in the cache is updated indicating which physical sector is now held. Upon reading, the sector address is hashed and the appropriate cache directory entry is consulted. If the entry holds the desired sector addresses, which it (hopefully) generally does, then no physical access is required. In the same way as for physical writes, when physical

reads are indeed required, then the data is also copied to the cache. The code to perform all of this extends the device drivers by about sixty bytes.

Disadvantages are that the increased on-line storage is lost and that a method of clearing the cache each time a diskette is changed must be implemented.
David J Greaves
Romsey
Hampshire

NICAD BATTERY CHARGER

I write to enquire about the series of articles by Rod Cooper on Nicad batteries. Perhaps you could forward the following questions.

- What are the relay specifications?
- Are there suitable alternatives to the Ferranti transistors in the 2N series?
- I cannot see the point in two diode strings in the comparators — surely one is enough, as used in the low temperature cut off.

I have enjoyed reading *Wireless World* since the early fifties and endorse the comments in Feedback column for September by Mr Beud. I hope *Wireless World* keeps a balanced viewpoint. The American market is flooded with pulp: so called technical magazines that are no longer satisfying the do it yourself hobbyist!
W R Risk, F.R.C.S.
Pontiac
Michigan

The author replies:

The relay I used on my prototypes was a miniature enclosed type with silver contacts rated at 3A, single pole. The coil was 100 rated at 6V but closing at 4.8V. These inexpensive relays can be found in the catalogues of several UK component suppliers and I am sure there are similar relays available in the USA.

Ferranti transistors were specified not just because they are dependable and British, but because this firm is particularly helpful when it comes to supplying technical data and advice — other firms could take a leaf out of their book. For American constructors, ZTX108B = 2N3903 and ZTX213B = 2N3905, both are compatible with the ready-made p.c.b. supplied by Biles Eng.

Regarding the diode strings, it would be possible to use one string as suggested by Mr Risk if the ambient temperature were to be held exactly steady. It was not envisaged that the charger would be used in a temperature-controlled laboratory however! In typical everyday conditions the

ambient temperature fluctuations are very much larger than the 5°C rise produced by a fully charged NiCd cell at a charge rate of C/10, so this would produce false triggering of the comparator. In fact, single temperature-detection circuits are useless in such conditions.

What the second diode does is to keep track of the ambient temperature so that the comparator only triggers when there is a +5°C temperature difference between the two diode strings.

Even so, the tracking of the circuit is not perfect. With unmatched diodes, the circuit given is satisfactory for the climatic conditions in the UK, but may not cope with the extremes experienced in some parts of the USA, so it may then be necessary to match these diodes to get better tracking.

I must agree with Messrs Risk and Beud on the subject of computer projects. There is an unnecessary excess of these, not just in *EWW* but everywhere, and most display the sledgehammer-to-crack-a-nut syndrome.

FFT

The article on Fast Fourier transforms in the September issue* of *Electronics and Wireless World* ended with the hope that the article would be a source of inspiration for other readers. It certainly prompted me to resurrect some ideas I had for spectrum analysis which came about from trying to prove that there are always more ways than one of doing a job.

The programs I have seen for spectrum analysis have all been based on Fourier mathematics, so how could the same end result be achieved without recourse to such mathematics, or indeed knowledge of them? The following notes described one of my attempts using a BBC micro and simple basic.

An array of amplitude-sampled points store the waveform to be analysed, which may be input either through the a-d ports or, as in the case of the examples shown here, synthesized by software, and this array is then tested for a match with the cycles of range of reference frequencies, one frequency at a time.

The method used to determine the degree of match can be illustrated by considering a very simple case of testing for the match between one cycle of a sine wave held in the array and one cycle of reference frequency, where we know that we should obtain a maximum match.

The array is divided into two equal parts, the first part

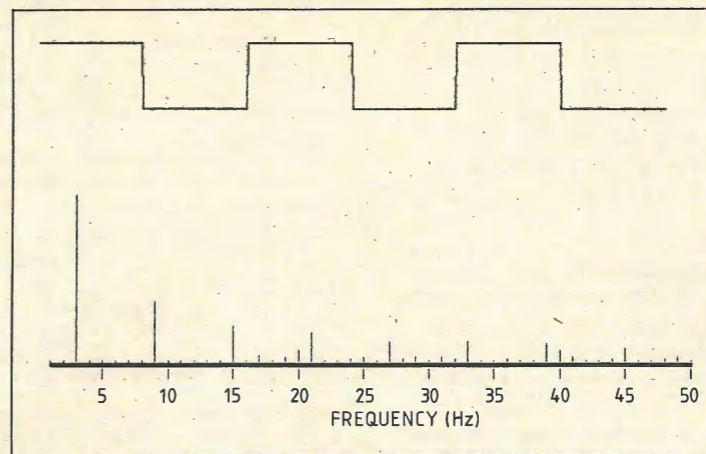
representing 0° to 180° (the half-cycle with positive values) and the second part 180° to 360°, the negative half-cycle. The elements of the 0° to 180° section are then summed and this result is added to the result of summing and inverting the sign of the 180° to 360° section. Thus we obtain a grand total of positive sign which represents a perfect match for that sine wave of that amplitude.

This, however, is true only for a sine wave. If the array held one cycle of a cosine wave, the above procedure would result in a total of zero. Therefore the procedure has to be repeated with the difference that the array is divided at the points equivalent to 90° and 270°, with the first summing taking place from 0° to 90° and 270° to 360°, and the second, with the sign inverted, between 90° and 270°. The grand total is then as for the sine wave. To take care of all phase angles the square root of the sum of the squares of the two grand results is taken.

Thus, in testing for a match with one cycle as the reference frequency, the array has been divided into four parts with sign changes at appropriate points. To test for a match with two cycles, the array is divided into eight parts, and so on for greater numbers of cycles.

At this point I considered the problem solved, but a few test runs showed an error which appeared for integer sub-harmonics of odd-order reference cycles, arising from asymmetry in the summing procedures. A few extra lines of software corrects for the error, although it does dictate that the analysis must run from the higher frequencies to the lower to save duplicating some of the calculating.

The first example given shows the analysis of a waveform synthesized from equal amplitudes of 1, 5, 12, 17, 33 and 46 cycles, all of which have been resolved satisfactorily by the program. The second example is for three cycles of square wave, where the expected harmonic pattern is shown by the analysis. There are some small spurious results, which



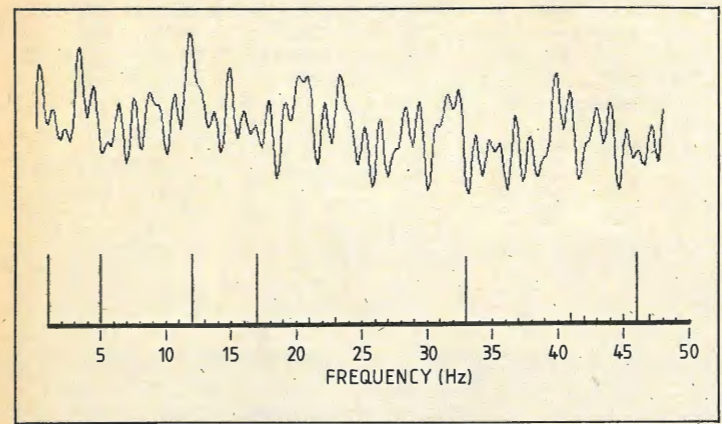
I believe arise from rounding problems in the program.

The listing shows the simplicity of the programs. In line 20 the number of array points required is chosen for D%, 500 in this case. Likewise, the maximum number of cycles for the reference frequency is chosen for F% and is 50 here. The only limits for these figures are available memory, run time for the program and resolution or aliasing problems. Lines 60 to 110 synthesize and draw the waveform. The remainder perform the calculations and draw the result as the program runs. Line

```

10 REM "FREQUENCY ANALYSIS" R.B.HALE
20 DI=500:FX=50
30 DIM AX(DI),RX(FX),SX(FX)
40 CLS:MODE
50 PRINT "    Sample Waveform"
60 S=2*PI/DI
70 MOVE INT(1200/DI),900
80 FOR YX=1 TO DI:T=S*YX
90 AX(YX)=SIN(T)*400+SIN(T*5)*400+COS(T*12)*400
  *SIN(T*17)*400+SIN(T*33)*400+SIN(T*46)*400
100 DRAW INT(YX*1200/DI),AX(YX)/10+900
110 NEXT
120
130 VDUS
140 MOVE 100,30:PRINT"Frequency Analysis"
150 FORV=FX TO 1 STEP-1
160 H=DZ/(VX*4):NX=1:JZ=0:KZ=0
170 REPEAT
180 LZ=0:ZZ=NZ*M
190 IF NXMOD2=0 KZ=KZ ELSE JZ=-JZ
200 FOR YX=(NX-1)*M+1 TO ZZ
210 LZ=LZ+AX(YX)
220 NEXT
230 JZ=JZ+LZ:KZ=KZ+LZ:NZ=NZ+1
240 UNTIL YX=DI-1
250 Z=SOR(JZ*JZ+KZ*KZ)*.625/DZ
260 RX(VX)=Z:C=0
270 FOR AX=3 TO FX-1 STEP 2
280 IF VX=AX:FX LZ=0 ELSE LZ=AX
290 C=C+SZ*(VX*LZ)/AX
300 NEXT
310 SZ(VX)=Z-C:P=VX*(VX/1250/FX)
320
330 MOVE P,120
340 DRAW P,C-120
350 T=INT(VX*.01)*10/10
360 IF F MOD 5=0 MOVE P,100:DRAW P,80:
  MOVE P-145,70:PRINT INT(VX*.01)
370 NEXT
380 VDUD

```



160 determines the division factor for the array and line 190 organizes the required sign changes. The summing occurs in the loop at 200, and the error correction in lines 270 to 310. I am sure that considerable polishing can be done to the program and perhaps some kind reader can turn it into machine code!
R B Hale
St Albans
Hertfordshire

FUNDAMENTALS OF ENERGY TRANSFER

May I offer a few thoughts on that part of Chris Parton's letter (*E&WW*, Dec, 1984, p.66) relating to the NPL definition of the ampere involving infinitely long parallel conductors? It seems to me that electric current is defined simply by the differential equation.

$$i = dq/dt \tag{1}$$

When the unit of charge is the coulomb (a physical quantity) and the unit of time is the second (a physical quantity) we obtain the unit of current called the ampere (a mathematical concept).

The NPL definition of the ampere seems at first sight to involve forethoughts that the ampere has to be subsequently realised by a physical system because of its practical utility and that the definition is "superior" to the c.g.s. "circle of wire" definition. When we try to define it apart from Eq (1) we always run into difficulties over concepts such as "conductors of infinite length" and so on which are not allowed in real life physical systems. The c.g.s. "circle of wire" definition also involves difficulties: does a unit pole exist? How does the current enter and leave the coil? What is meant by force? etc.

What the NPL is doing is defining the ampere in an

"absolute type" of way. The term "absolute" was introduced by Gauss, meaning independent of the size of any particular instrument, or the value of gravity at any particular instrument, or the value of gravity at any particular place, or of any other arbitrary quantities than the three standards of length, mass and time. But with concepts such as electric current I do not see how the difficulties I have mentioned can be avoided. Mechanical quantities can be defined in Gauss's absolute way but it is better, I think, to refer to "the NPL definition of the ampere" and to realise that it is an "absolute type" of concept from which any practicality has been removed. When the NPL has to realise the ampere practically then, of course, all thoughts of infinite conductors, unit poles, etc. vanish from their minds and they turn to the realities of forces and torques on mutually magnetically coupled coils, masses and levers.

May I draw readers' attention to part of a comment by Dr A. T. Starr (whom I knew at Callender's Research Laboratories in 1939 and for whom I have the utmost respect)? "The development of electromagnetism adopted in this chapter is regarded with disfavour by many teachers of the subject and international committees. A few words in defence of the method will therefore be given... we are told that the idea of a magnetic pole is very deluding and that we are likely to find ourselves 'floundering in a world of make-believe'. This need not alarm us, as anything abstract is 'make-believe' and may be indulged in, provided it is self-consistent and useful... the fact that a North Pole is associated with an equal South Pole is not sufficient to make the idea of a single pole useless or misleading, just as the fact that every reaction does not prevent the idea of force from being useful." The whole of this reference is very worthwhile reading.

In conclusion, I feel that the 'wisdom of our forefathers' was in the c.g.s. definition and that the NPL has erred in adopting the infinitely long wire definition. Chris Parton is to be congratulated on explaining the absurdity of introducing infinity into any 'absolute type' of definition. May I gently remind him, however, that parallel infinite wires never occurred originally in any c.g.s. definition?
Philip John Drake
Whickham
Newcastle-on-Tyne

Reference
1. A.T. Starr, *Electric circuits and wave filters*. Pitman, 2nd ed., 1944, pp.63-65.

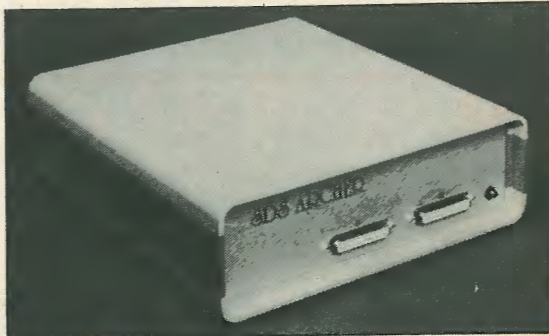
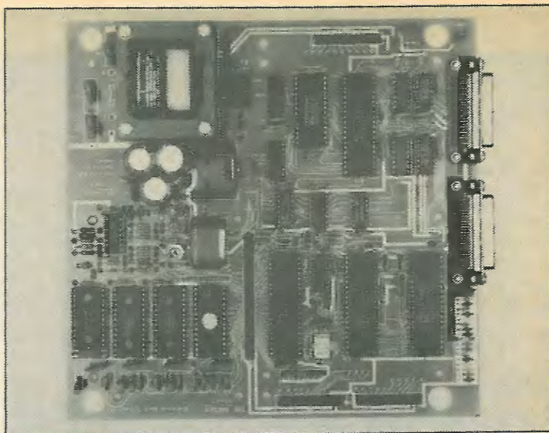
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CIRCLE 38 FOR FURTHER DETAILS.

Digital filters explained

by H.J. Hutchings, B.A.



H.J. Hutchings is currently lecturer in electronics and control in the School of Engineering, Humberside College of Higher Education. A graduate of the Open University, he worked as an instrumentation technician with Hawker Siddeley (Aviation) Ltd before entering technical education in 1973 as an assistant lecturer at the Grimsby College of Technology. He is also a part-time tutor with the Open University.

The daunting mathematics commonly associated with descriptions of sampled data systems has been largely avoided in this geometrical interpretation of a first-order low-pass filter.

The rapid growth of microelectronics and home computers has meant that the characteristics of low-pass, band-pass and high-pass filters can be readily implemented in software form. This article describes the behaviour of a simple low-pass digital filter in terms of convolution, impulse response, amplitude and phase response, using only elementary mathematics.

Digital filters have numerous attractions. Weight, size, cost and flexibility favour the digital filter. The response of a digital filter is independent of component tolerances, does not require alignment and is immune to temperature variations. Simply modifying the program changes filter characteristics. And it is possible to achieve results that are impractical using the analogue counterpart.

The implementation

The diagram of Fig. 1 shows how an analogue-to-digital converter, computer and digital-to-analogue converter may be connected to achieve the necessary signal processing for digital filtering. The filter is implemented by an algorithm within the computer which convolutes the sampled sequence $x(n)$ with the impulse response $h(n)$ to provide the output sequence $y(n)$. Convolution is the name given to the ordered combination of multiplication and summation. The mathematical shorthand is deceptively simple:

$$y(n) = h(n) * x(n) \quad (1)$$

where $*$ indicates the operation of convolution.

This terse symbolism represents a powerful mathematical technique applicable to both analogue and digital systems. Fortunately the

sampled data version of convolution requires little prior knowledge and an adequate toolkit is the ability to draw and understand a graph, to multiply in an ordered manner and finally to add the products. I now show a little of the physical reality which lies behind the abstraction of equation 1.

The analogue signal presented to the a-d converter will be converted into a series of discrete samples; how many samples or snapshots must be taken to describe the signal completely? To avoid aliasing it is necessary to sample at a rate which is at least twice the highest frequency present — the Nyquist frequency. The Fourier spectrum of the signal, together with the sampling frequency illustrates the point — Fig. 2.

The recipe

Suppose the sampled signal can be represented by the sequence of pulses x_0, x_1, x_2, x_3, x_4 , each of which is separated from its neighbour by the fixed interval T , the signal processing time. Each sample pulse in turn will stimulate the filter, which responds with a train of weighted output pulses. If the memory of the filter is long enough, there will still be a vestige of the previous history of weighted outputs, even as the current pulse is being processed, which as you might expect complicates matters. An input sequence together with a possible impulse response is shown in Fig. 3. Both series of pulses are assumed to start at time $t=0$.

To evaluate y_n , the output of the system after n conversions, a systematic approach is required. In the Table the rows represent the response to the input sequence x_0, x_1, x_2, x_3, x_4 respectively.

t=0	t=T	t=2T	t=3T	t=4T
x_0h_0	x_0h_1	x_0h_2	x_0h_3	x_0h_4
	x_1h_0	x_1h_1	x_1h_2	x_1h_3
		x_2h_0	x_2h_1	x_2h_2
			x_3h_0	x_3h_1
				x_4h_0

The columns show the terms present at times $t=0, t=T, t=2T$ etc. The response y_n is simply the sum of the terms in the n th column. The sums of the five columns are

$$\begin{aligned} Y_0 &= x_0h_0 \\ Y_1 &= x_0h_1 + x_1h_0 \\ Y_2 &= x_0h_2 + x_1h_1 + x_2h_0 \\ Y_3 &= x_0h_3 + x_1h_2 + x_2h_1 + x_3h_0 \\ Y_4 &= x_0h_4 + x_1h_3 + x_2h_2 + x_3h_1 + x_4h_0 \end{aligned}$$

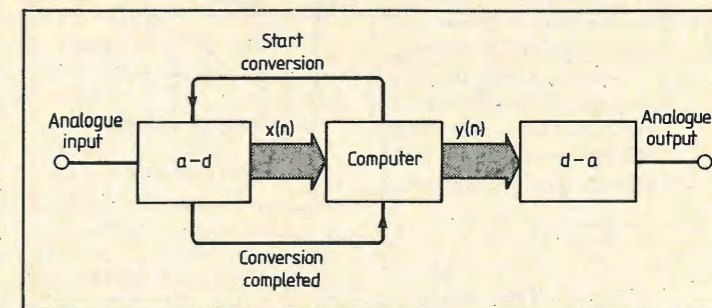


Fig. 1. The a-d converter is synchronized with the program by control signals to enable the complete signal processing time (T) to be fixed.

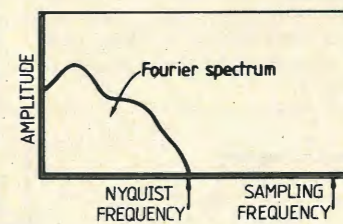


Fig. 2. Shannon's sampling theorem in action. To avoid signal corruption due to aliasing the sampling frequency $f = 1/T$ should be set at least twice the Nyquist frequency.

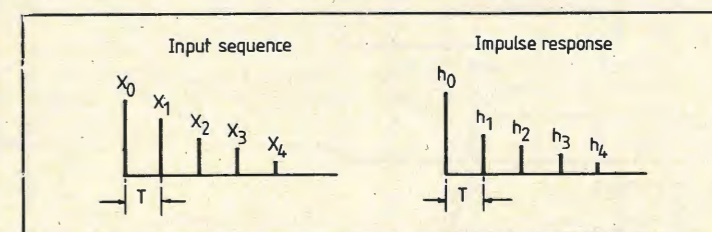


Fig. 3. Sampled input signal is represented as a sequence of pulses each separated by the fixed interval T. The behaviour of the linear processor is characterised by the impulse response sequence.

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50	12+12	8.40
50	15+15	8.40
50	15+15	8.40
90	15+15	8.40
90	25+25	8.40
90	12+12	9.60
90	20+20	9.60
90	25+25	9.60
90	30+30	9.60
130	18+18	10.20
130	22+22	10.20
130	30+30	10.20
130	35+35	10.20

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30	9+9	7.25
30	12+12	7.25
30	15+15	7.25
30	20+20	7.25
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63	12+12	8.50
63	15+15	8.50
63	18+18	8.50
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63	30+30	8.50

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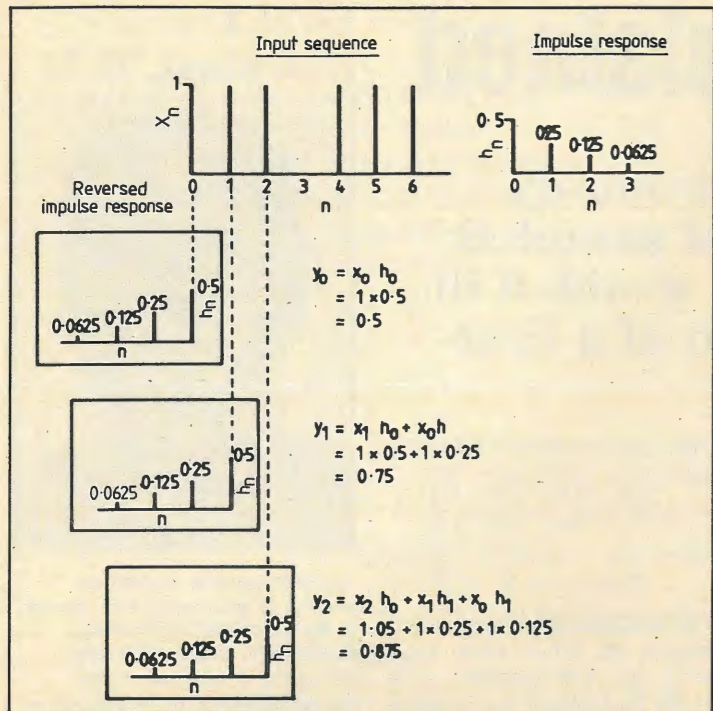


Fig. 4. Graphical convolution of the input signal and impulse response is achieved by reversing the impulse response under the sample of current interest. Sum of the coincident cross products is the convolution of

Fig. 5. Effect of signal processing has been to remove certain high-order harmonics and to produce some time delay. Remaining harmonics sum at the output to produce the familiar exponential response.

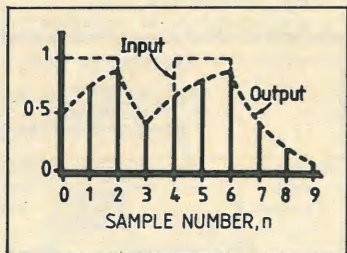


Fig. 6. This simple first-order low-pass analogue filter has time constant $\tau = CR$ seconds. Magnitude and phase response show that low frequencies are processed with negligible amplitude and phase modification.

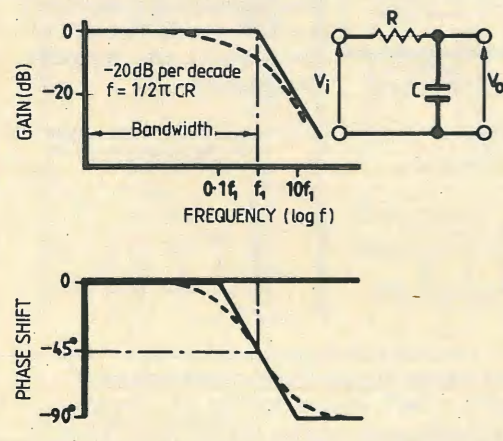


Table 1

Sample n	Current input x_n	Previous output y_{n-1}	Current output y_n
0	1	0	$\frac{1}{4}$
1	0	$\frac{1}{4}$	$\frac{2}{4}$
2	0	$\frac{2}{4}$	$\frac{3}{4}$
3	0	$\frac{3}{4}$	$\frac{1}{4}$
4	0	1/16	1/32
5	0	1/32	1/64
etc.			

To find the rest of the terms in the sequence in Fig. 4, continue moving the reversed impulse response to the right until none of the samples of the two sequences overlap. My results,

$y_3 = 0.4375$ $y_4 = 0.6875$
 $y_5 = 0.8125$ $y_6 = 0.875$
 $y_7 = 0.4375$ $y_8 = 0.1875$
 $y_9 = 0.0625$

are plotted in Fig. 5. The exponential nature of the output waveform suggests the digital signal processing is similar to that provided by a simple low-pass analogue filter. Fig. 6 shows the type of circuit together with the expected frequency and phase response.

If you have time, investigate the effect of the digital filter on the sampled sequences shown in Fig. 7.

I hope you found the effect of processing the unit sample is simply the impulse response of the filter h_n . The impulse response characterizes the behaviour of the filter. Convolution in the time domain corresponds to multiplication in the frequency domain, which is equivalent to multiplying the signal frequency spectrum by the system frequency response, the product corresponding to a filtering operation.

The program
 To implement this particular filter in software form, consider

the recurrence relationship

$$y_n = 0.5x_n + 0.5y_{n-1} \quad (2)$$

where x_n is the current input sample, y_n the current output and y_{n-1} the previous output. Let's apply unit input sample $x_0=1$ and see what happens. Assume that the previous output y_{n-1} is zero prior to this event; intuitively, this is reasonable since the effect cannot precede the cause.

Again a systematic approach is advisable, the notation used in Table 1 where the current output becomes the previous output one sample later is a useful way of keeping track of the calculation.

This type of filter is said to have an infinite impulse response; compare with the impulse response shown in Fig. 4, in which the response was

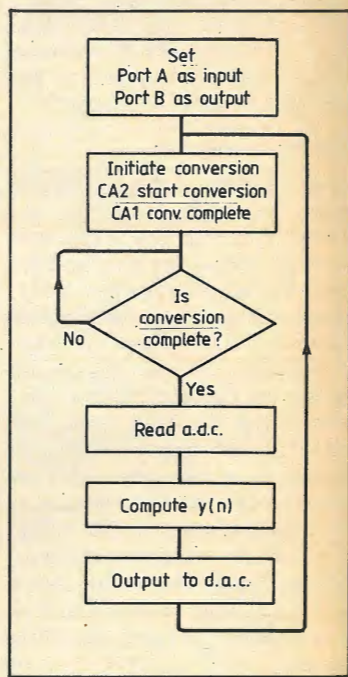


Fig. 8. Demonstration program samples audio input, computes $y(n) = 0.5x(n) + 0.5y(n-1)$ and outputs binary value.

Fig. 7, left. Example waveforms, see text.

deliberately truncated after the first four terms for ease of calculation.

A computer performs calculations such as these with ease, see Fig. 8. If the speed of operation is not critical a Basic program would be suitable but for real-time programming of audio signals Basic is far too slow and machine language is the best choice.

Assembler 6502

```

LDA #00
STA A003
LDA #FF
STA A002
LDA #0E
STA A 00C
LDA #0C
STA A00C
LDA #0E
STA A00C
LDA #00
AND #02
BEQ LABEL
LDA A00D
LDA A001
LSR A
ADC TEMP
STA TEMP
STA A000
LSR A
STA TEMP
JUMP START
  
```

AIM Basic

```

10 POKE 40963, 0
20 POKE 40962, 255
30 POKE 40972, 14
40 POKE 40972, 12
50 POKE 40972, 14
60 P=PEEK(40972)
70 IF P AND 2 THEN 90
80 GOTO 60
90 X=PEEK(40961)
100 X=X*0.5
110 Y=X+Z
120 POKE 40950, Y
130 Z=Y*0.5
140 GOTO 30
  
```

Frequency response and the operator z

Anyone who has persevered so far must be wondering when we are going to explore the familiar themes of cut-off frequency and bandwidth in relation to a digital filter. As the Nyquist frequency is half the sampling frequency any gain-frequency response plot must be expressed in terms of this. The precise shape of the filter response can be readily deduced from the positions of the poles and zeros plotted in the z-plane. Unfortunately I cannot justify this statement without exposing you to a panorama of some relatively advanced mathematical concepts, which is not my intention. However, if you are prepared to accept this in good faith, then what follows will provide a reasonable insight into the frequency

response of digital filters together with a pictorial explanation of aliasing.

The recurrence relationship obtained for y_n involved taking a sample, weighting it, and then adding it to the previous weighted output, delayed by one sampling period. In an effort to make the recurrence relationship a little more attractive I use the notation

$$y(n) = \frac{x(n)}{2} + \frac{y(n-1)}{2} \quad (3)$$

which means exactly the same as equation 2. This can be transformed into an expression in z by writing

$$y(n) = y(n), \\ x(n) = x(z) \text{ and } \\ y(n-1) = z^{-1}y(z).$$

A unit time delay transforms into a multiplication by z^{-1} in the z-domain. Conversely, a unit time advance

$$y(n+1) = zy(z)$$

becomes a multiplication by z in the z-domain. Using the z transform on equation 3:

$$y(z) = \frac{x(z)}{2} + \frac{z^{-1}y(z)}{2}$$

When rearranged and expressed as the ratio of $y(z)/x(z)$ the expression is the transfer function. Calling the transfer function $H(z)$, we write

$$H(z) = \frac{z}{2z - 1}$$

The zeros are the terms in the numerator that make $H(z)=0$, and the poles are the terms in the denominator that make $H(z)$ non-analytic, i.e. make $G(z)$ tend to infinity, hence the name pole (imagine a vaulting pole standing on its end). Poles and zeros are critical frequencies at which something happens to the transfer function; as a result of poles and zeros the function changes as z varies. No real function can have more zeros than poles. For reason of stability the circuit designer must ensure the poles are placed within the unit circle.

These results can be plotted in the z-plane, where angular frequency is represented as an angle, a rotation of $360^\circ C$ corresponding to the sampling frequency. A numerical example may help: suppose the sampling frequency is 1000Hz then the Nyquist frequency of 500Hz is located at the point p_1 on the unit circle.

To deduce the amplitude of the frequency response without recourse to Fourier transforms simply requires that we calculate $|H(j\omega)|$, or in plain English the magnitude of the zero vector divided by the magnitude of the pole vector, for values of frequency from $f=0$ to $f_s/2$ (half the sampling frequency). The net phase response is given by the argument of the zero vector minus the argument of the pole vector.

Using this rule we can produce a reasonable amplitude and phase response relatively painlessly by employing simple geometry, Fig. 10.

Remember that . . .

Remember that the magnitude and phase response of the filter was obtained by geometrical measurement. Greater precision could be obtained by calculation but the aim here is to keep the mathematics to a minimum.

To become proficient at designing in the z-plane try varying the position of the pole and then the zero. How does the frequency response vary?

I hope you find that moving the pole along the negative real axis modified the filter behaviour, transforming it into a higher-pass response. Cascading a low and high-pass filter would produce a band-pass characteristic, although a greater selectivity could be obtained by making the poles complex. Second-order digital filters have no place in this introductory account: if you wish to pursue this topic the article by J.T.R. Sylvester Bradley provides a particularly succinct exposition.

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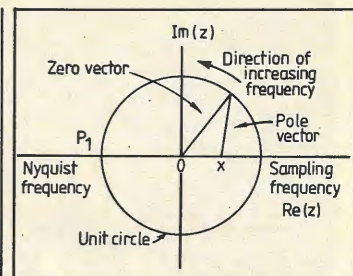


Fig. 9. z-plane diagram of the digital equivalent of a low-pass filter.

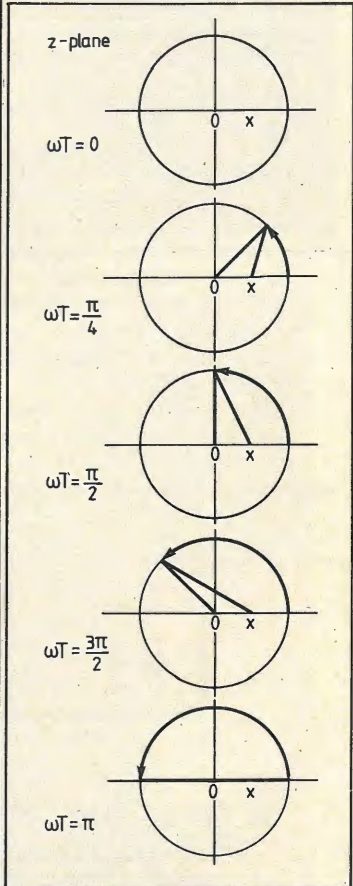


Fig. 10. As the pole and zero vectors rotate about the unit circle, so the magnitude and phase of the frequency response may be calculated for values of $\omega T = 0, \pi/4, \pi/2$ etc.

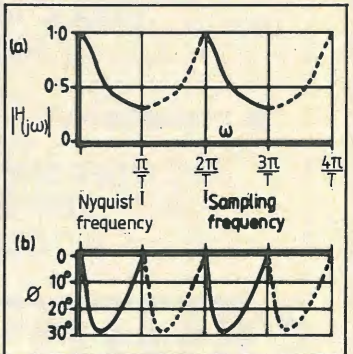


Fig. 11. Spectrum of digital filter is a repeated version of the low-pass response from $\omega = 0$ to $2\pi/T$.



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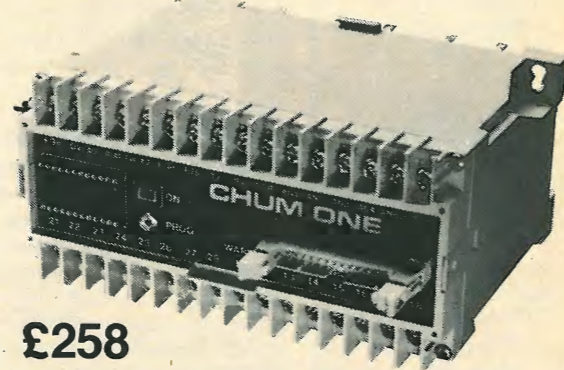
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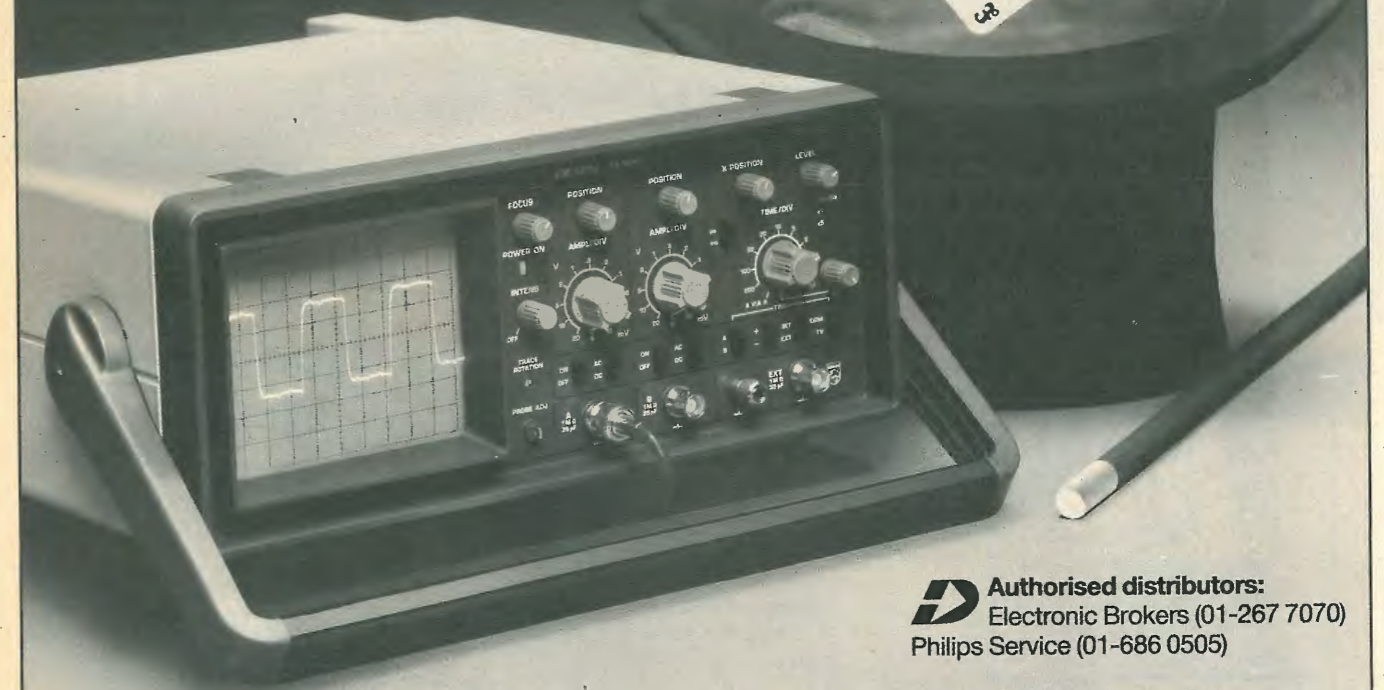
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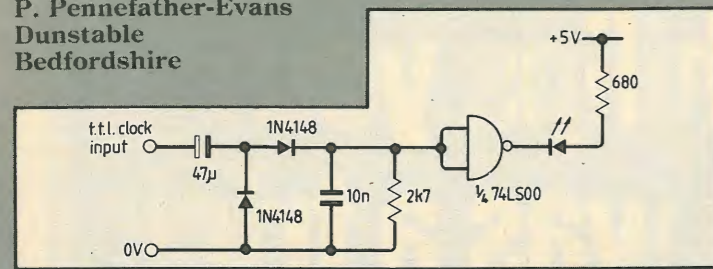
PHILIPS

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ELECTRONICS & WIRELESS WORLD DECEMBER 1985

Activity indicator

P. Pennefather-Evans
Dunstable
Bedfordshire



Motion detector

Motion is usually detected by taking Doppler shift measurements and require two transducers, one for transmitting and one for receiving. This circuit only needs one transducer.

The transducer, forming part of an oscillator, determines oscillating frequency. It also forms part of a bridge circuit. Oscillator feedback voltage is determined by the voltage ratio of R to impedance of the transducer at series resonance. As shown, oscillator frequency is between 38 and 42kHz.

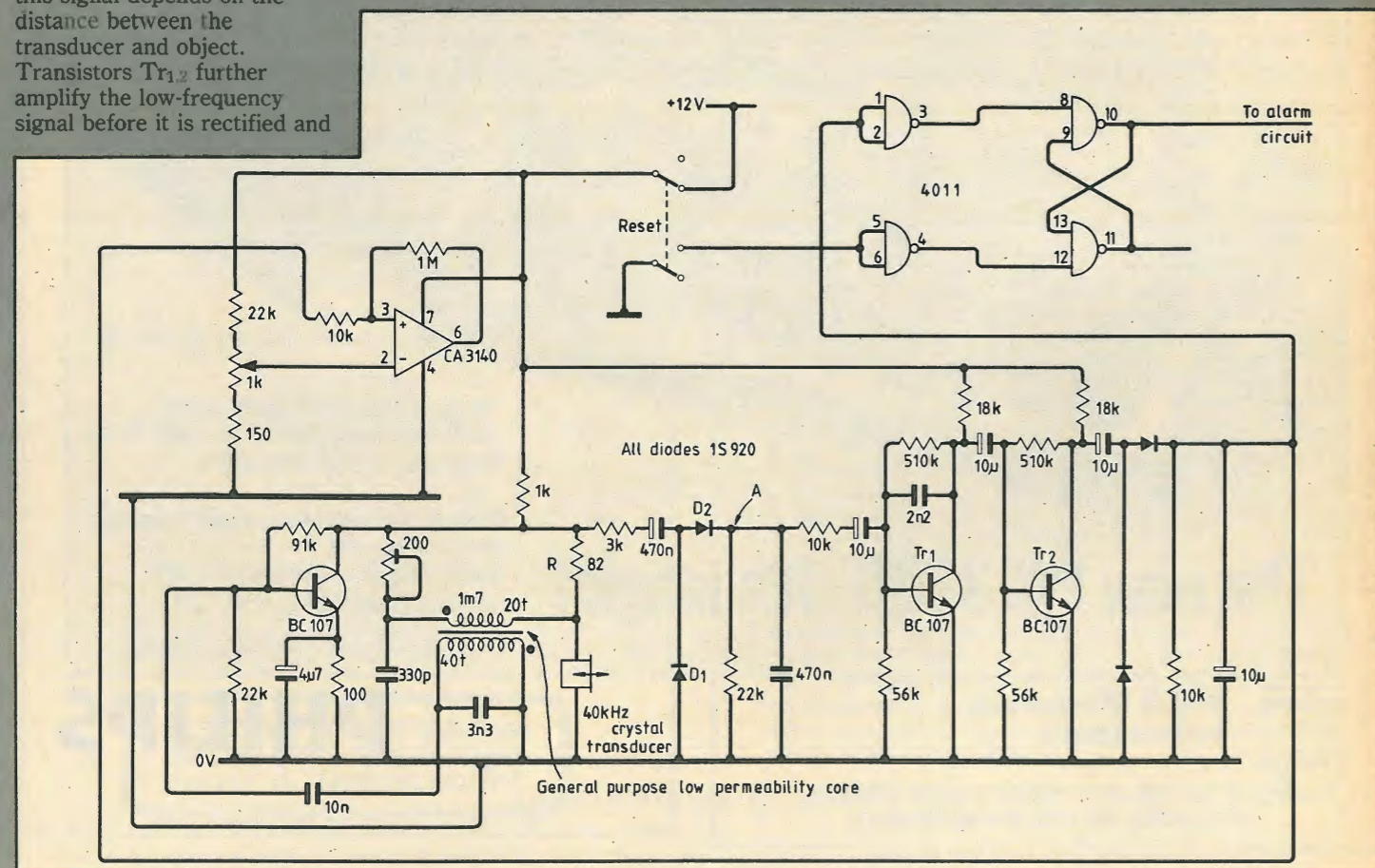
Output from the oscillator is rectified by D_{1,2} to give a low-frequency signal. Amplitude of this signal depends on the distance between the transducer and object. Transistors Tr_{1,2} further amplify the low-frequency signal before it is rectified and

filtered to give a direct voltage.

Reference voltage feeding an op-amp comparator is set to cause switching when the object is at a given distance from the transducer. With the prototype, it is possible to detect a person crossing the detector path at 3m.

Alignment is easy; using a digital voltmeter, set point A to about 1V with the bridge potentiometer. Ask someone to move around at the required detection distance and adjust the comparator-reference potentiometer accordingly. The set/reset bistable circuit latches output.

R. Punter
Cleethorpes
Lincolnshire



Engine speed indication

Many medium and high capacity motorcycles now have electrical systems fed by a permanent-magnet type alternator which produces a.c. If the alternator is driven directly by the crank shaft, frequency of the alternating signal relates directly to engine speed.

Using alternator output can be the simplest way of obtaining pulses to drive an engine speed indicator. A number of modern motorcycles are fitted with integral electronic ignition systems which prohibits use of the points as a signal source and tapping a signal from the h.t. leads is also problematic.

Alternator construction varies between manufacturers but the basic design remains the same. Six coils wired in series and wound alternately in different directions form the stator. The rotor is a star-shaped magnet with alternate north-south lobes.

There are often two sets of coils on the stator, the second

set being wound only on two opposing limbs and over the existing set. Battery charging is carried out continuously by the two smaller coils while the lights are powered by the main coil set under manual control.

Rectification is most often by means of a silicon or selenium-diode bridge circuit. After rectification, regulation may be performed by a large zener diode (Triumph/Lucas) instead of a switched coil; sometimes a small zener and large thyristor (Stanley) are found. Larger cycles with three-phase systems may use a thyristor bridge regulator but here, the large generator is often away from the crankshaft, driven by gears or a chain.

I carried out tests on a number of vehicles with help from my local dealer. Frequency of the a.c. output was found to be the number of crank-shaft revolutions per second multiplied by the number of alternator pole pairs, or 50Hz for 1000rev/min with a six-pole rotor. After full-wave rectification, the frequency doubles.

Using this frequency to drive a counter directly would be unacceptable due to the long delay produced by gating so I used a phase-locked loop circuit to multiply the input frequency. Having worked out how to process and display the pulses, all that remained was to find a way of sampling them that did not involve extensive modifications to the vehicle.

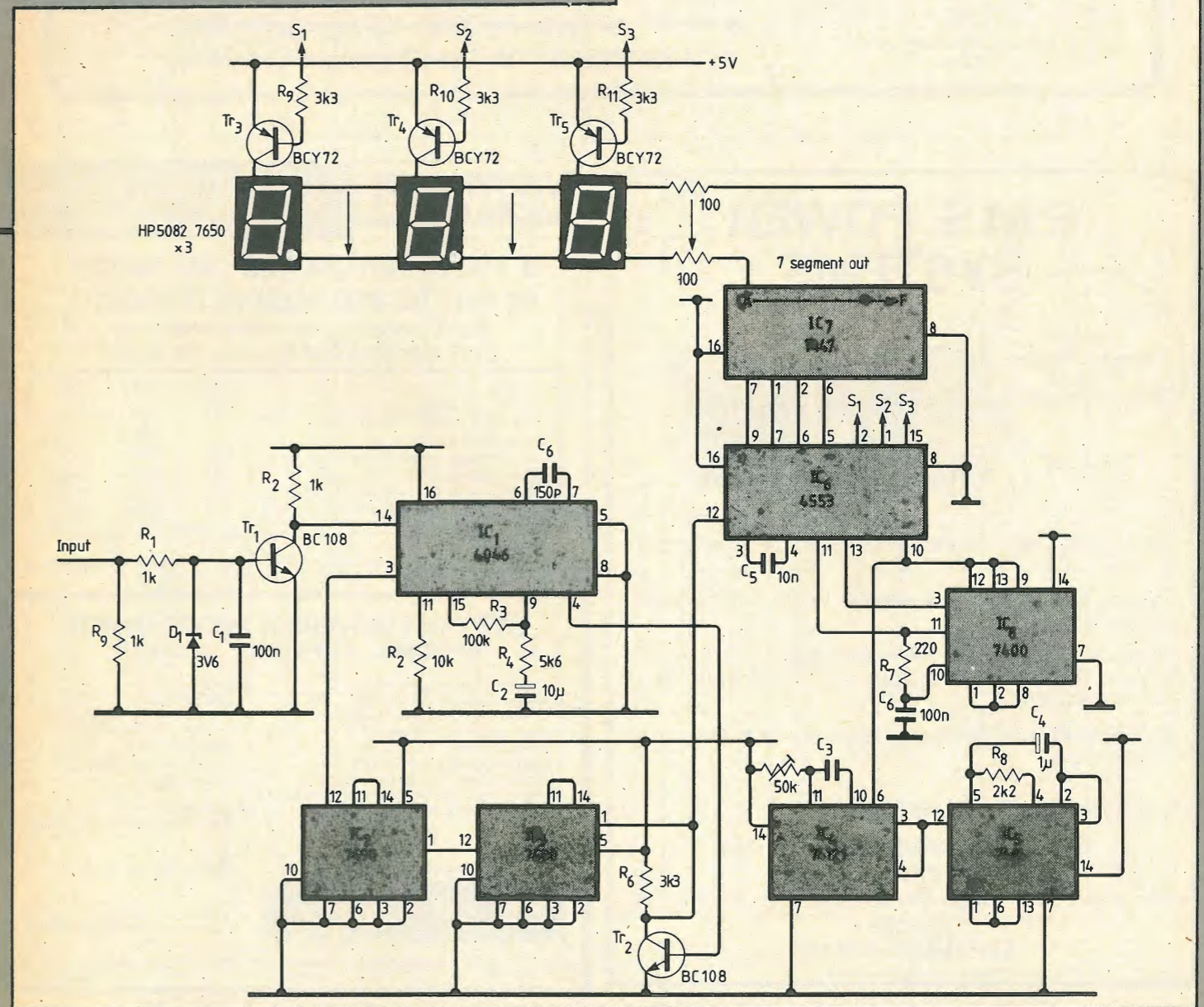
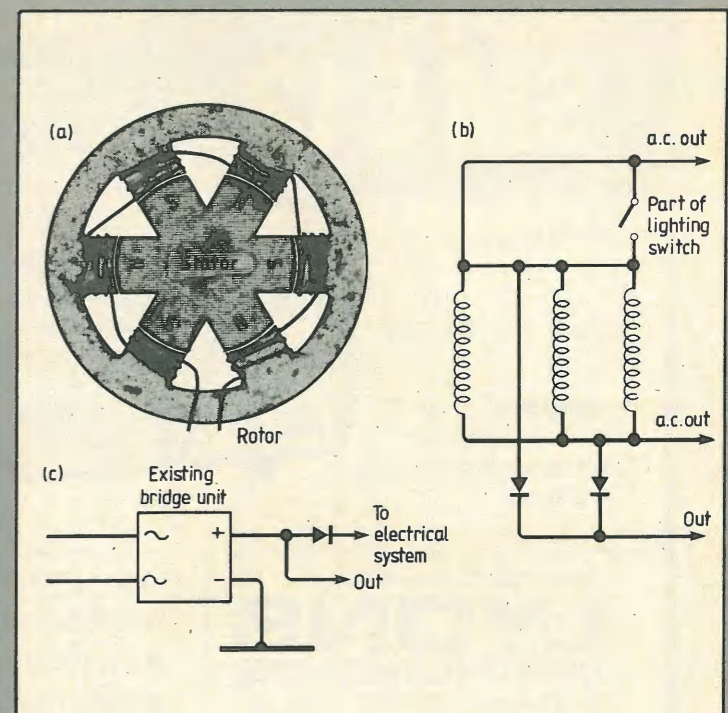
I tried two methods of extracting alternator pulses; both are shown. The first method involves placing a large diode in series with output from the existing bridge rectifier. This method was not so suitable due to the counter being affected by pulses from the light switch. The second method is far more effective, making use of the full set of coils, and

requires only small diodes.

Rectified alternator supply is smoothed by the battery, so the signal for the pulse counter must be taken before these rectifiers, as shown. The same principle applies to star-configuration alternators using six diodes.

The potentiometer on the circuit, which should be a ten-turn type, is used to calibrate the counter. If the cycle produces 50Hz for 1000rev/min, calibration is simple using mains-derived pulses. If not, a pulse generator and frequency counter may be necessary. Accuracy to within 10rev/min was easy to obtain with the prototype. Supply-rail stabilization is needed.

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ELECTRONICS & WIRELESS WORLD DECEMBER 1985

The deeper hidden message in Maxwell's equations

by Ivor Catt

Why have Maxwell's Equations survived for so long?

In November's article¹ I investigated Maxwell's Equations, generally regarded as the greatest mathematical achievement in science.*

In the 1830s, Faraday discovered electromagnetic induction, thus closing the loop between electricity and magnetism. This discovery paved the way toward the rapid growth of electricity-based industrialization and the high technology which shapes today's world.

By making the key discoveries of their era, uneducated technicians like Michael Faraday and James Watt threatened the scholastic myth, that all progress, including scientific progress, needs must use the rigour and discipline controlled and celebrated by academics in places like Cambridge University. The ultimate in scientific rigour (*rigor mortis?*) was held to be mathematics. Biography and History of Science writings spawned in academia present the thesis that, lacking mathematics, Faraday could not and did not really effect his discovery of electromagnetic induction. Rather, he stumbled into it, but it could only be properly exploited decades later, after Professor Maxwell had placed a mathematical structure upon Faraday's fumbling, unscholarly ideas. Thus, according to the Platonic interpretation of history, Professor Maxwell, not Faraday the technician, paved the way for massive exploitation of electromagnet-

ism in transformers, motors and generators. The deeper hidden message in Maxwell's Equations is that, do what they will, the local yokels will not replace mathematical academia as the fount of knowledge and progress.

In a previous article¹ I posed two questions:

Do Maxwell's Equations contain any information about the nature of electromagnetism?

Why do academics and practitioners generally think that Maxwell's Equations are useful?

I am sure you will have found my answers unsatisfactory. The reason is that they were based on certain assumptions, and failed to dig deeply enough into the underlying motivation, psychoses and myopia within contemporary science.

The underlying battle for the soul of science is between the practical engineer on the one hand and the Platonic pure mathematician on the other.² For his part, the mathematician sees this battle as more important than search after truth or technology-fuelled search after new sources of wealth. For him, the important thing is Form; the purity and beauty of his world, and his ability to control and manipulate it intellectually. (The profane aspect of this idea is the desire to impose a structure onto any 'discipline' such that it is easy to teach and, more importantly, easy to set exam questions on). One FRS told me that physical reality was composed of sine waves, and this encapsulates the

mathematician's attitude to our world.

A good example of an academic with the mathematician's attitude is Sir James Jeans. He was highly regarded in the 1930s both as a Cambridge academic and as a populist, much like Sir Fred Hoyle in the 1950s. In his book "The Mysterious Universe",³ Jeans gives a clear view of the

well's Equations for such a long time.

Jeans then goes on helpfully to point out the flaw in his argument:

"This [last] statement can hardly hope to escape challenge on the ground that we are merely moulding nature to our pre-conceived ideas. The musician, it will be said, may be so engrossed in music that he

"... I was struck by the lack of any significant link between the Higher TEC syllabuses that I taught and the real subject, electronic design..."

attitude of the Platonic mathematician discussed in the last paragraph.

"By 'pure mathematics' is meant those departments of mathematics which are creations of pure thought, of reason operating solely within her own sphere, as contrasted with 'applied mathematics' which reasons about the external world, after first asking some supposed property of the external world as its raw material"

On the next page, Jeans goes on to write,

"... the universe appears to have been designed by a pure mathematician."

The important thing is not to ponder over the possible contradiction between these two statements, but to grasp the mentality underlying them. This mentality, usually in a better camouflaged and less grotesque form, is what made possible the survival of mathematical absurdities like Max-

would contrive to interpret every piece of mechanism as a musical instrument; the habit of thinking of all intervals as musical intervals may be so ingrained in him that if he fell downstairs and bumped on stairs numbered 1, 5, 8 and 13 he would see music in his fall. In the same way, a cubist painter can see nothing but cubes in the indescribable richness of nature — and the unreality of his pictures shews how far he is from understanding nature; his cubist spectacles are mere blinkers which prevent his seeing more than a minute fraction of the great world around him. So, it may be suggested, the mathematician only sees nature through the mathematical blinkers he has fashioned for himself. We may be reminded that Kant, discussing the various modes of perception by which the human mind apprehends nature, concluded that it is specially prone to see nature through mathematical spectacles. Just as a man wearing blue spectacles would see only a blue world, so Kant thought

*And if mathematics is the highest flowering of science, then Maxwell's Equations become the greatest achievement in all science.

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that, with our mental bias, we tend to see only a mathematical world. Does our argument merely exemplify this old pitfall, if such it is?

"A moment's reflection will show that this can hardly be the whole story. The new mathematical interpretation of nature cannot all be in our spectacles - in our subjective way of regarding the external world - since *if it were we should have seen it long ago* [my italics]. The human mind was the same in quality and mode of action a century ago as now; the recent great change in scientific outlook has resulted from a vast advance in scientific knowledge and of from any change in the human mind; we have found something new and hitherto unknown in the objective universe outside ourselves. Our remote ancestors tried to interpret nature in terms of anthropomorphic concepts of their own creation and failed. The efforts of our nearer ancestors to interpret nature on engineering lines proved equally inadequate. Nature refused to accommodate herself to either of these man-made moulds. On the other hand, our efforts to interpret nature in terms of the concepts of pure mathematics have, so far, proved brilliantly successful. It would now seem beyond dispute that in some way nature is more closely allied to the concepts of pure mathematics than to those of biology or of engineering, and even if the mathematical interpretation is only a third man-made mould, it at least fits objective nature incomparably better than the two previously tried."

Professor Einstein argued similarly in 1949⁴:

"... the approach to a more profound knowledge of the basic principles of physics is tied up with the most intricate of mathematical methods."⁵

I have put the weak point in Jeans' argument above in italics. The mathematicization of science developed with a vengeance as a result of the professionalization of education. Dr Ivor Grattan-Guinness once pointed out to me that the decline, or ossification, of science into 'maturity' was a necessary result of the in-

⁴In passing, it is worth noting from page 62 of the same book, where Einstein writes: "The special theory of relativity owes its origin to Maxwell's equations of the electromagnetic field." In the literature we repeatedly come across assertions that Maxwell's Equations play a pivotal role in science.

roduction of universal education in the mid-19th century, because it caused the growth of a powerful group with a vested interest in knowledge, the professional teachers.

Basil Bernstein⁵ says that a body of knowledge is property, with its own market value and trading arrangements, to be protected by the social group which administers that body of knowledge.

If only those who lived off a body of knowledge could make that knowledge more secure, their careers and pensions would be protected. Two stratagems were open to them^{6,7,8}:

- to freeze the knowledge base so that it would not be a prey to the ebbs and flows of the real world, and
- to develop the thesis that any change in, or extension of, the knowledge base could only be properly effected by the professional 'knowledge magicians', 'knowledge doctors' or 'knowledge brokers', with their special, skilled, occult ways of pushing forward the boundaries of knowledge.'

"One FRS told me that physical reality was composed of sine waves, and this encapsulates the mathematician's attitude to our world."

It would of course be less effective for the professional group of knowledge brokers merely to bless or condemn influxes of new knowledge. (Admittedly they *do* do that. All my attempts to publish work on electromagnetic theory and on computer architecture (US patents 3913072 and 4333161) were blocked for more than ten years by learned journal referees, who are by definition knowledge brokers). The knowledge brokers' power would be greater if they required that new knowledge arise in their own prescribed style, preferably devised by one of their members, a knowledge professional. An early example of this in my own publications is that under threat of firing by my boss, who was also a Fellow of the IEEE, I was compelled⁹ to include a ghastly, recondite, mathematical last section, written by someone else, in my 1967 IEEE paper¹⁰.

We have reached the following point in the argument. Under cover of claiming to maintain standards of scholarship, or to maintain rigour, knowledge brokers (1) block the ingress of new knowledge, particularly revolutionary knowledge in the Kuhnian sense¹¹, and also (2) they make a last-ditch, bitter defence of old, discredited knowledge, like Maxwell's Equations.

"Unfortunately, however, when the body of knowledge is bigger and the rate of inflow of new knowledge is smaller, more and more of the activity within the knowledge [base] becomes 'celebration', more and more ceremonial rather than exercise in depth. As a result, a different calibre of person is attracted to that large knowledge, lacking the ability to understand and defend a body of knowledge with many levels of meaning. They are 'maintenance men' rather than 'builders'. The central body of knowledge ossifies, becomes brittle and then disintegrates."⁶

We need to realise that the cardinals who suppressed Galileo did not need to be competent theologians or scientists;

they only needed a much narrower competence, the ability to distinguish between the orthodox and the heretical, in both content and in style¹². As to style, it is worth pointing out that possibly the ability to publish radically new, revolutionary¹¹ knowledge in the old accepted style would prove that after all the new knowledge was not truly revolutionary. So arguments about style, which are regularly lodged against my writing, including my last article,¹ create a beautiful Catch 22 situation where no new knowledge can be published.

In this penultimate paragraph I mention in passing The Lateral Arabesque, 'Arabesque' having the meaning ascribed to it by Dr Peter¹³ rather than its dictionary meaning. In the engineering sense, the supposed situation where academia controlling a discipline - electromagnetic theory for example - maps onto the real subject, is

unstable. If at any moment the professors administering a discipline happen to be weak in one branch of it, they will tend not to examine their students in it, and so will tend to select those up and coming students who have that sub-discipline as their strength. Positive feedback down the generations of students will further the retreat from that particular sub-discipline. (Sir James Jeans and Einstein could be said to be telling us very wordily that academia have selected out budding scientists who showed a grasp of the physics, rather than the maths, of their subject.) Similarly, the whole of academia will move deeper and deeper into any misconception or aberration, and there is no corrective force. In my view, 'The Lateral Arabesque' makes it possible for an academic subject's content to end up with no overlap *at all* onto the real subject from whence that branch of academia sprang. I have just completed four years as Principal Lecturer in a College of Further Education, where I was struck by the lack of any significant link between the Higher TEC syllabuses that I taught and the real subject, electronic design, in which I had been earning my living in industry for the previous 20 years. As a minor example, academia evolved the myth that dissatisfaction among logic designers with the indeterminate state of an R-S bistable if driven on both its inputs at the same time led to the development of the J-K bistable; then that the instability of the J-K led to the development of the Master-Slave J-K, regarded in academia as the Rolls-Royce of bistables. A nice idea, but with no historical foundation.

A larger example would be academia's fixation on Quine-McCluskey, something not even heard of, let alone used, by engineers in the real world of logic design. Although I was in the best position possible to introduce or alter syllabuses, being on the County Committee, during my four years as a P.L. I failed to change one word of one syllabus. I struggled very hard to do so.

To sum up. Professionalization of knowledge leads to a vested interest in knowledge,

Continued on page 75.

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by R.F. Coates

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Kaycomp's 68000 peripheral interface/timer option adds three eight-bit ports for input and output.

Kits mentioned in the first 68000-board article now include plated-through-hole p.c.b.s instead of ordinary double-sided ones for the same price of £99 inclusive. A line-by line assembler is included for a further £14. See October and November issues for details.

Kaycomp accommodates two 68000 peripheral i.c.s — an MC68681 dual asynchronous receiver/transmitter described last month and an optional MC68230 peripheral interface/timer. The 68230 provides digital i/o interfacing and a programmable timer, similar to the older 6821 p.i.a. or 6522 v.i.a. but much more powerful. The processor interface is similar to that of the duart, but there are no IRQ and IACK pins, and the clock for the timer is an external input which in Kaycomp is driven from the processor clock. There is also an extra register-select line RS₅ driven by address line A₅.

On the peripheral side 28 i/o pins are arranged in three ports of eight lines, PA₀₋₇, PB₀₋₇, PC₀₋₇ and four handshake lines, H₁₋₄. All of these lines and the 5V supply are brought out to a 34-way connector, plug five, and are available to the user.

Some port C pins serve either as normal i/o lines or as timer/interrupt control pins, the function being selected under software control. Four of these function as the interrupt request output and acknowledge input for the timer and peripheral i/o sections of the device. Line PC₃ doubles as timer-output signal TOUT which can be used to generate an interrupt and PC₇ timer interrupt-acknowledge input TIACK. If interrupts are to be used on the i/o side, PC₅ is the peripheral interrupt request output PIRQ and PC₆ is the peripheral interrupt acknowledge signal PIACK. These four pins are not connected on the board to any interrupt pin or the IACK signal. If they are to be used, links can be made on P₆ as required.

There are certain points to be noted if using interrupts on the p.i.t. First, the IRQ outputs are not open drain but normal mos

outputs and so cannot be wire-or'd with other interrupt sources, such as the duart. This means that the two p.i.t. interrupt sources TOUT and PIRQ cannot be used together on the same interrupt line. Also the p.i.t. requires that IACK only goes to the input that has caused the interrupt and not the other one, so only the one being used should be connected.

For the majority of Kaycomp applications these restrictions are not a problem. It is of course possible to use one or both of interrupt lines IPL₀ and IPL₂ if they are not being used by the G64 bus. Connections between NMI/IRQ and IPL_{2,0} of the processor are wire links instead of p.c.b. tracks to allow these links to be made across the board.

These restrictions on p.i.t. interrupts could not be tolerated on larger systems where it may be essential to have many devices connected to one interrupt level. This means extra arbitration logic, for instance open-collector buffers on the outputs so that they may be wire-or'd together, and gating to direct the IACK signal only to the interrupting source.

Rather than increase the complexity and cost of Kaycomp plug six, as well as being a patching area, has all relevant interrupt signals and the 5V supply brought to it. This location can be fitted with a 10-way header plug for connecting to a small separate board which implements the required arbitration logic to give full interrupt facilities. All the necessary signals are also available on plug five for convenience.

The duart IRQ output is directly connected to IPL₁ but there is a small link between two diagonally running parallel tracks just above plug five which if broken makes the

duart IRQ and IPL₁ signals available separately at plug six.

Power

Requirements for the fully populated Kaycomp board are +5V at 0.7A, and positive and negative 12V rails at 30mA each. If the serial interfaces are not being used, the 12V supplies can be omitted.

Power is supplied through the G64-bus connector from the bus backplane. If the external bus is not being used then the connector could be omitted and supplies from an external power source wired directly to the board.

Circuit and component options

In its basic form, Kaycomp can be used with just the processor, ram, eprom and the serial ports, the processor clock being derived from the duart clock. A wire link is fed from the pad next to pin 32 of IC₁₆ to pin 10 of IC₁₄ for the clock signal. Components R₂, C₉, C₁₀ and XL₂ are not fitted.

Having a processor clock of about 3.6MHz means that the slowest 68000 processor and 450ns-access or faster memories may be used. Gate IC₁₄ may be either an l.s.t.t.l. type or a t.t.l.-compatible high-speed c-mos device such as the 74HCT00. If a separate processor crystal is used or anticipated, IC₁₄ must be a high-speed c-mos type.

I recommend that you use sockets for the larger i.c.s like the processor, peripheral i.c.s and memories. Remaining i.c.s may be soldered in or socketed.

If your p.c.b. has plated-through holes then any type of good-quality socket may be used. If not there are a number of turned-pin socket types available that allow soldering on both sides of the board using a fine soldering iron tip. The easiest types to use are socket

terminal-carriers which consist of individual sockets with no insulator mounted on an aluminium 'dummy' i.c. After soldering, the dummy i.c. is removed, leaving the individual pins in place to form an i.c. socket.

If crystal XL₂ is to be used to take full advantage of the speed of the board then IC₁₄ must be a 74HCT00 and C₉, C₁₀ and R₂ must be fitted. Crystal frequency is chosen to suit your needs and the speed selection of the processor and memories used. At present 68000 microprocessors are available with 4, 8, 10, 12.5 and 16MHz maximum clock rates. The board was designed to be used with an 8MHz crystal, but the clock oscillator will work down to 2MHz. In practice the boards have been found to work up to 10MHz satisfactorily.

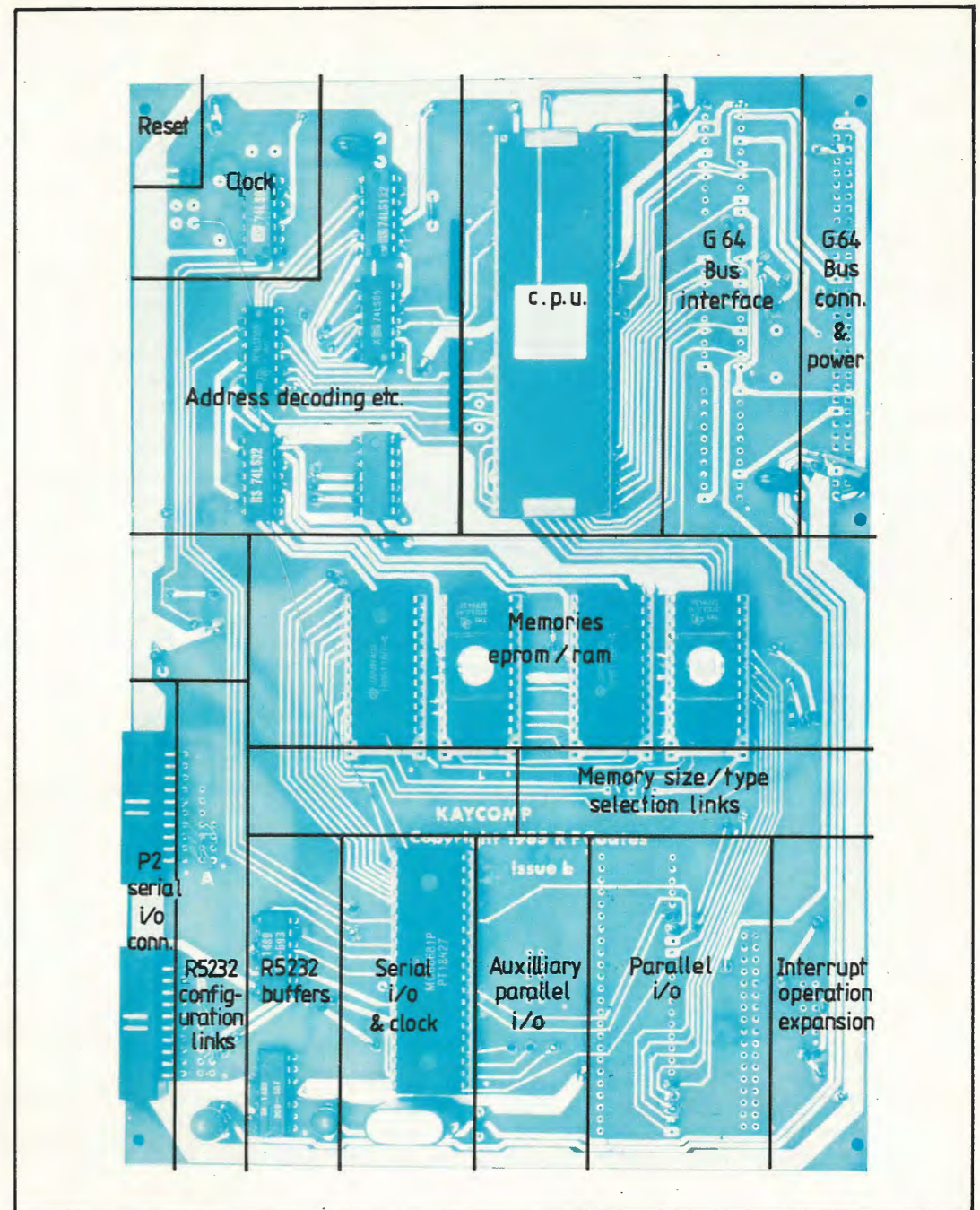
The 68000 peripheral devices will work with any clock speed, as the asynchronous bus transfer technique means that access to these chips will be slowed down to suit them, but the memories must be capable of operating at the appropriate speed. Maximum access times of devices that may be used at various crystal frequencies in nanoseconds are 660 for 4MHz, 290 for 8MHz and 230 for 10MHz.

The 'Kaybug' monitor fits into two 2732 eproms, but these are most commonly available with 450ns access times. This though is the worst case and in practice, 450ns devices tried in prototype boards have always worked satisfactorily at 8MHz although this cannot be guaranteed, of course.

If peripheral i/o is required then IC₁₅, the MC68230, must be fitted. A socket should be used here, but 48-pin sockets are not very common so it may have to be built up using socket strips or by using two 24-pin sockets mounted end-to-end. A connector for the i/o lines will also be required, this being a 0.1in 34-way straight p.c. mounting plug.

Three i.c.s, IC₁₁₋₁₃, resistor R₁ and the bus connector need fitting if use is to be made of the G64 bus. It is also worth noting that if the G64 bus interface is fitted it is possible to use the board without the two on-board serial ports, allowing IC₁₆₋₁₈ and X₁ to be omitted. A special version of the Kaybug monitor

is available on request which instead of using the on-board ports for terminal and host communication uses a G64 dual serial interface card instead, the Thomson EFS-SI01.



is available on request which instead of using the on-board ports for terminal and host communication uses a G64 dual serial interface card instead, the Thomson EFS-SI01.

All resistor and resistor network values quoted are typical and are not critical, as are all capacitors with the exception of C₇, C₉ and C₁₀. Holes are provided at each corner for mounting the board if required, or of course the board will fit a standard 6U-high Eurocard rack. In this case a Vero KM6 card front panel may be fitted using the two front mounting holes.

Construction is straightforward provided that the printed circuit

board designed for the project is used. Using strip board or wire-wrap could prove a false economy and it may not be possible to achieve correct operation due to the high speeds involved on the board.

These construction details assume that you are building the fully populated board. After inspecting the bare p.c.b., there are three insulated cross-board links which may need to be fitted. If G64 NMI and IRQ lines are to be applied to IPL₂ and IPL₀, then fit wire links A-B and C-D respectively.

These letter references are on the p.c.b. component position diagram supplied with the kit. Components IC₁ and IC₁₂ fit

over the top of the links to hold the wire in place. If a separate processor clock crystal is not needed, a wire link should connect point E next to IC₁₆ to the holes for C₁₀ nearest to IC₁₄.

Next, optional connectors PL₁₋₃ and the i.c. sockets are fitted. The memory sockets should be fitted in the order IC₃, IC₅, IC₂ and IC₄. Sockets for IC_{1,16} and optional IC₁₅ are fitted in the same manner.

Now fit connectors P₅₋₇, as required. If you do not intend to change the memory types or the serial port configuration then wire jumpers can be used on links one to three. If changes are anticipated, fit wire-wrap pins instead. Resistors,

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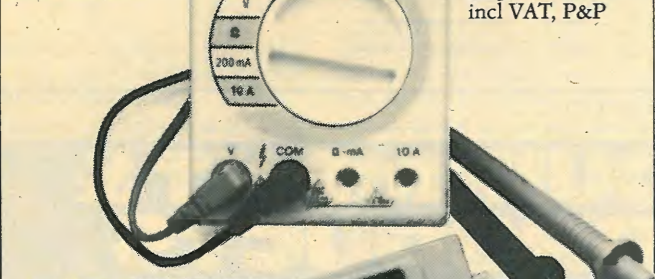
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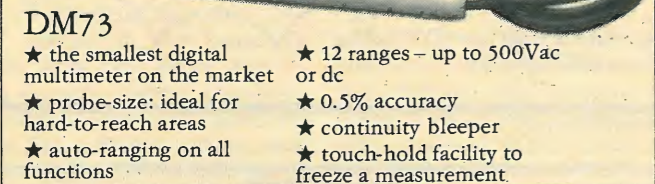
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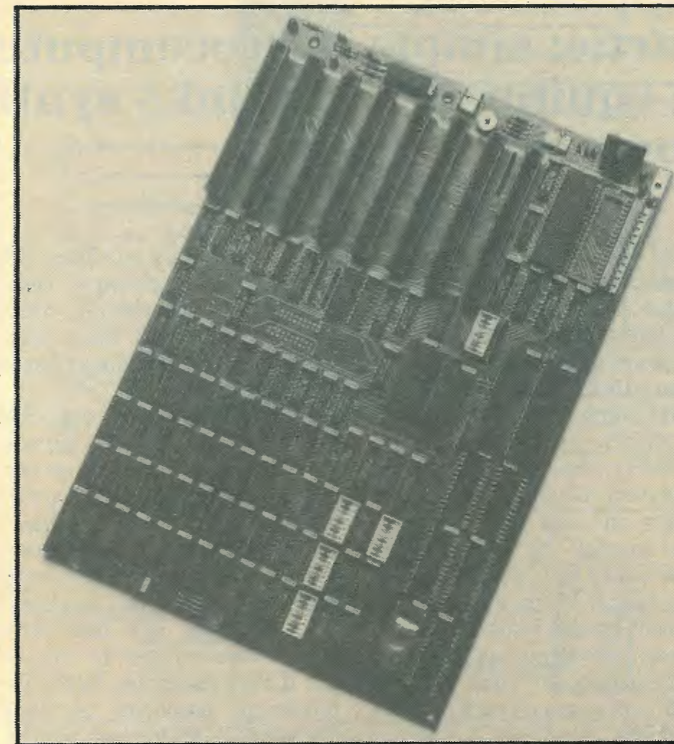
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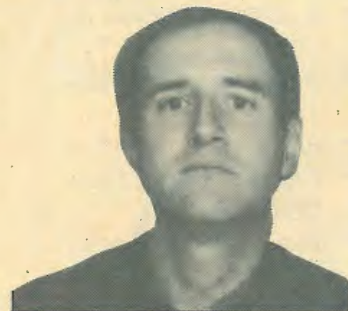
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ELECTRONICS & WIRELESS WORLD DECEMBER 1985

Computer-controlled radio receiver system

Introducing Smartie: simple microcomputer and radio tuning interface equipment that links synthesized receiver with home computer.

by D.W. Harris
B.Sc., F.I.E.E.



After gaining his degree in Electronics from Southampton University in 1970, Dave Harris, who is 36, worked for the BBC until he joined Radio Botswana in 1984. Appointed Chief Engineer two years later, he became Botswana's Deputy Director of Information and Broadcasting in 1980 working on the expansion of domestic broadcasting until his return to the UK at the end of last year. He hopes it is only the current recession in the broadcasting industry that has so far prevented him from obtaining full-time employment in the UK and professes to be torn between engineering consultancy and community radio. His hobbies include musical composition, drama, playing jazz, computing and amateur radio. This article is based on a paper he wrote "somewhat tongue-in-cheek" for the 1984 conference of the Commonwealth Broadcasting Association.

References

1. Poel, W. Sony's ICF2001. *Radio & Electronics World* Nov. 82, p. 46.
2. Coll, J. The BBC Microcomputer User Guide. BBC.
3. Ward, B. BBC Micro System User Guide. BBC.

Sony's ICF2001 is typical of a modern generation of domestic radio receivers designed around synthesized oscillators. It covers the m.f. and h.f. bands from 150kHz to 30MHz in 1kHz steps, as well as band II in 100kHz channels from 76 to 108MHz. Its internal tuning, keyboard handling, display circuitry and any other features are handled by a variety of specialized l.s.i. chips resulting in a design which, despite some selectivity and front-end limitations, represents an amazing achievement in such a small and relatively inexpensive package.

The Sony receiver has been reviewed on various occasions - ref. 1 reproduces the circuit diagram and so this article does not dwell on the circuitry. There are 28 keyboard buttons which allow direct entry of frequency, selection of preset channels, scanning limits, fast and slow tuning, and so on. External control of the functions of these buttons allows a multitude of applications, enhanced by access to the receiver's a.g.c. line.

The BBC microcomputer is also well-known and has rapidly established itself as a versatile controller thanks to its excellent input-output facilities. Of particular interest to the present application are the eight-bit user port, the internal timer and the analogue-to-digital converters. Simple interfacing allows operation of the receiver under program control.

The ICF2001 keys are scanned through the conventional multiplexing arrangement found in calculators and computers. Eleven individual key lines are switched to one or

other of two main bus lines, K3 and K4, Fig. 1, giving 22 combinations. The remaining functions are provided via a third bus-line, K2, or as direct hardware switches.

Analysis shows that only one of the eleven K3/K4 switches may be validly pressed at any one time; it may be necessary to simultaneously press the 'fast' or 'enter' keys for such functions as storing a frequency in memory or rapid scanning. This suggests the use of a 4-to-16 line decoder to control the K3/K4 functions. In the interests of simplicity no other decoding is performed for the remaining bits of the port.

Interface details

Complementary mos4016 quad switches simulate the operation of the press buttons. These are controlled either directly from the 6522 port in the BBC micro

or via 7404 hex inverters from a 74154 4-to-16 line decoder which is itself operated from the lower four bits of the user port.

An intermediate bus-line, not directly accessible from the computer, allows either K3 or K4 to be connected to any one of the associated 11 lines. The 4016 associated with the K2 switches ('fast' and 'enter') are directly controlled by bits 6 and 7 of the v.i.a. whilst bits 4 and 5 are used for K3 and K4. See Fig. 2 for the circuit diagram.

The receiver's a.g.c. line is brought out separately and in the prototype connected to channel 1 of the computer's analogue-in socket. Threading this line a few times through a small toroid helps reduce r.f. 'hash' picked up by the radio from the computer. Incidentally, it is practically impossible to eliminate such interference;

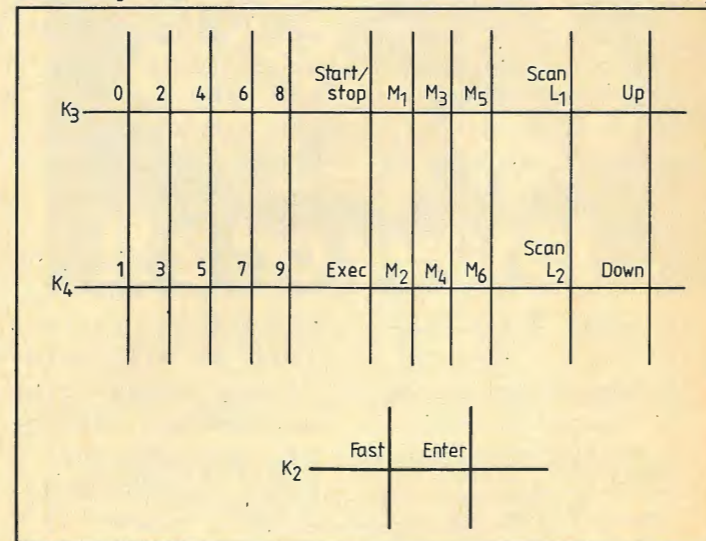


Fig. 1. The keyboard-scanning matrix for the Sony ICF2001 receiver is similar to that found on calculators and computers. Lines K2, K3 and K4 are bus lines from the receiver's microcomputer.

pickup on the ribbon cable to the keyboard and directly by the aerial gives a permanent background buzz on most channels, which fortunately is easily over-ridden by any reasonable signal picked up on a short external aerial. Experimentation with earthing arrangements can significantly improve matters.

Power for the interface is derived from the computer +5V line; the Sony can be used with its own mains power supply but there is some advantage in constructing a small (600mA) regulated 5-volt supply to eliminate the effects of mains voltage changes over long periods. The prototype interface was constructed using wire-wrap techniques and housed in a suitable die-cast box.

Software

BBC Basic features named procedures and these can be used extensively to give an easily documented control program. For example simulating the pressing of key 9 requires:

```
DEF PROCkey9
?&FE62=&FF: REM all v.i.a.
lines configured as outputs
?&FE60=&27: REM put a
short across K4 and key 8/9
FOR delay=1 TO 100: NEXT:
REM short delay to allow
Sony to respond
?&FE60=&00: REM release
the button
FOR delay=1 TO 100: NEXT:
REM delay as above
ENDPROC
```

It is then a simple matter to make up any sequence of key presses, such as

```
PROCkey7: PROCkey2:
PROCkey5: PROCkey5:
PROC- execute:
REM 7255kHz
```

which happens to be the frequency for Radio Botswana's 41 metre domestic short-wave transmissions!

Direct manipulation of the v.i.a. registers as above is not 'legal' across Acorn's Tube interface; if necessary the appropriate osbyte call (&97) should be used (see page 468 of ref. 2).

Having set up a frequency on the Sony, adval readings may be taken as frequently as the computer can handle the conversions - i.e. every 10 ms if necessary and if *FX16 is set appropriately - but take care to allow the a.g.c. line to settle down after a change of frequen-

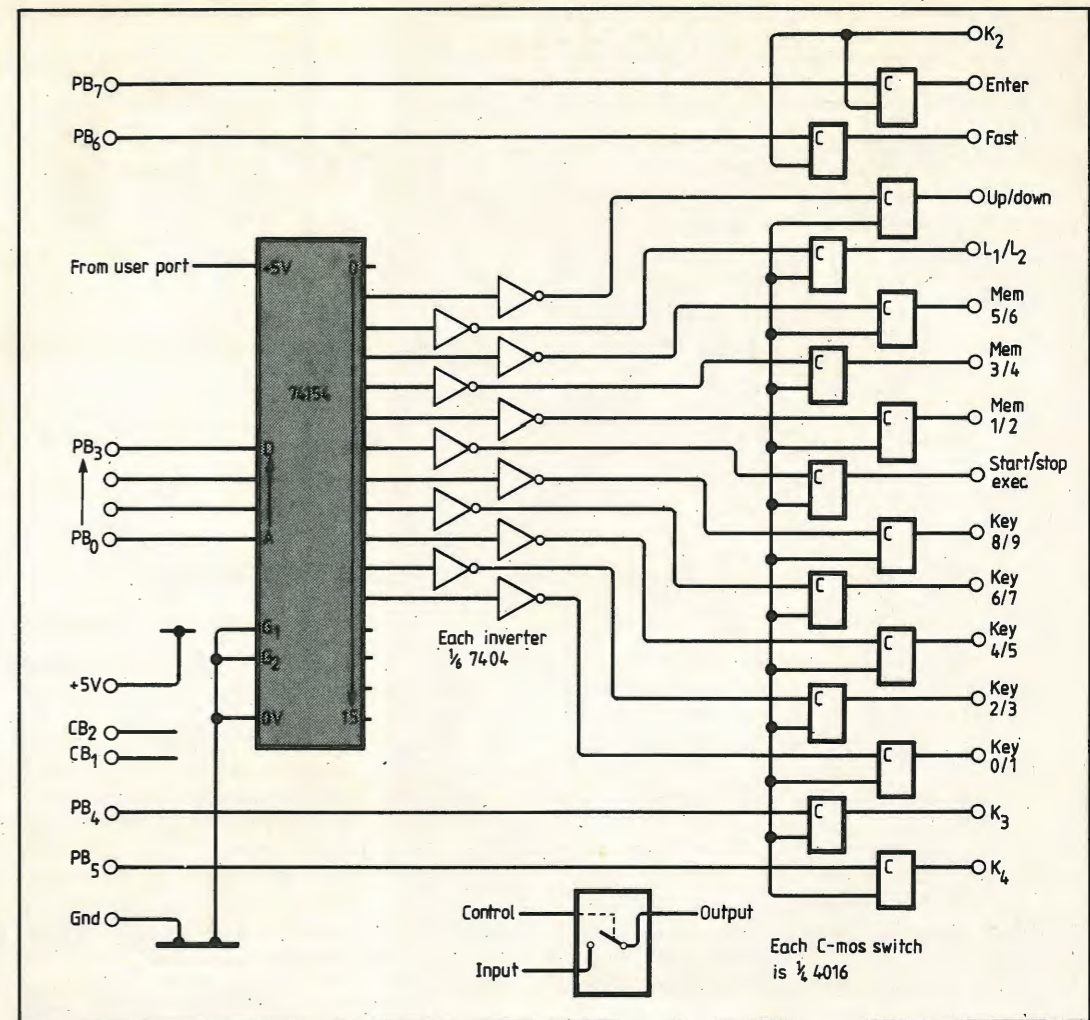


Fig. 2. Circuit diagram of the Smartie interface between the BBC micro user port and the keypad on the ICF2001. Type 4016 c-mos switches make the connections and simulate operation of the push buttons.

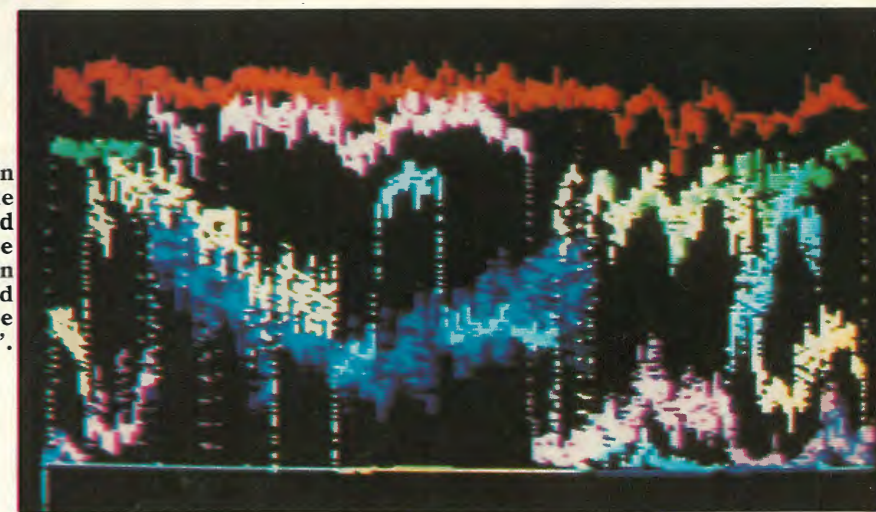
cy e.g. when sequencing around the memory buttons. A settling time of about one second has been found appropriate in such cases.

Applications

Probably the easiest application is the use of the apparatus for simple chart recording of field strength, which can be performed at whatever sample interval is felt appropriate. In this case although the computer has

very little work to do during the recording process, it can be used for subsequent analysis of the data. For example, raw samples could be taken several times per second to allow detailed investigation of fading, but equally useful information could be derived from the same data by smoothing it using a running average (such as is done with the sunspot number). This would be difficult to achieve with conventional

Program listings in Basic for the applications discussed are obtainable from the editorial office, in return for a stamped and addressed envelope marked 'Smartie'.



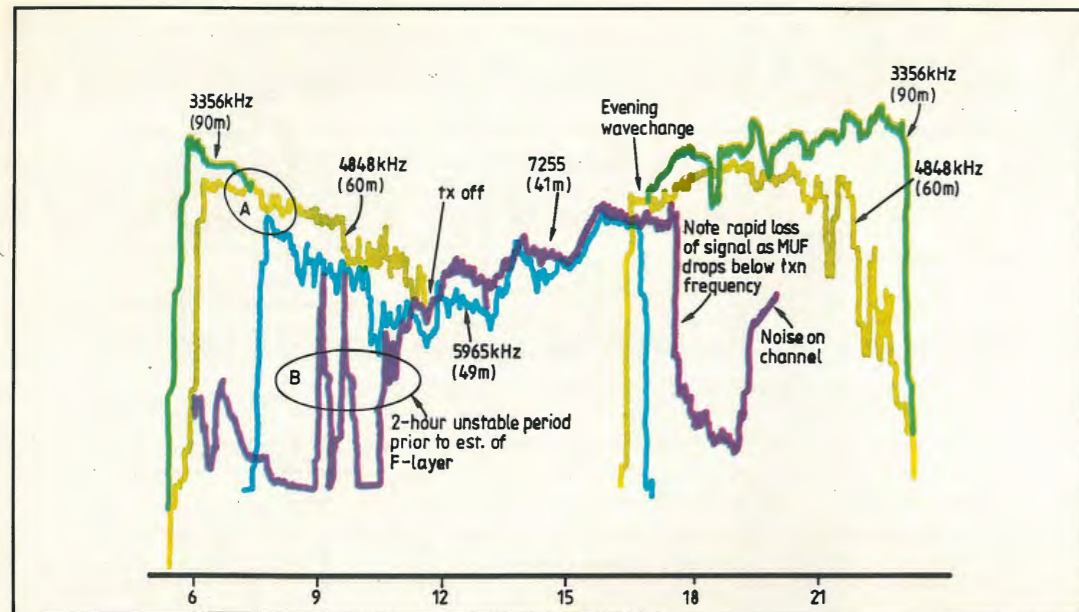
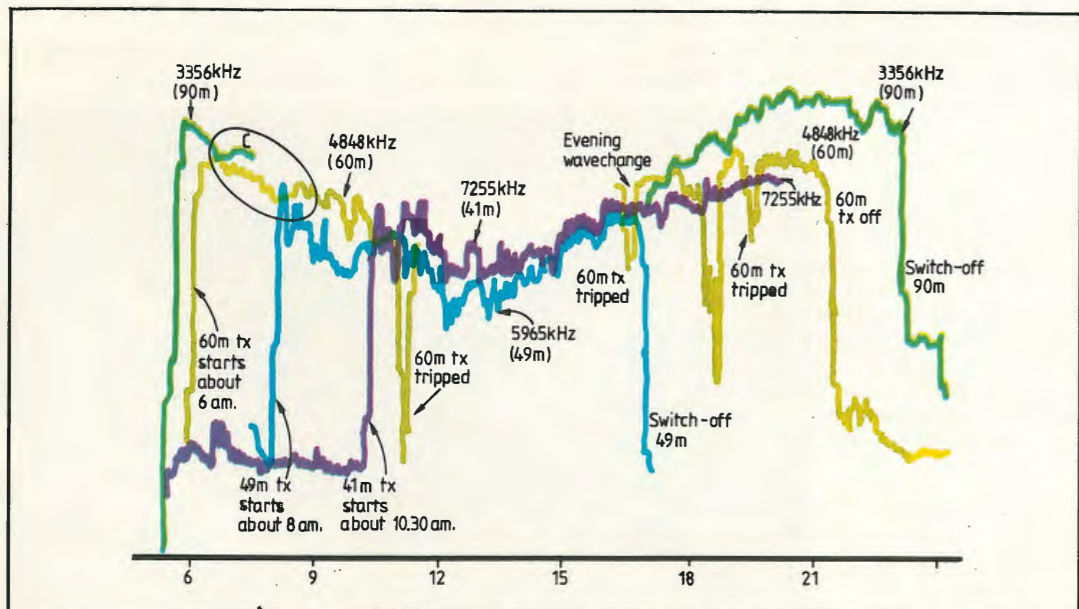


Fig. 3. Signals monitored at Serowe (250km from transmitters) on 27 October 1984 show propagation characteristics of the different frequencies, which can easily be compared with each other and the theoretical predictions. Normally such monitoring would require four separate receivers and chart recorders and superposition would be difficult.

Fig. 4. As Fig. 3, but the following day. Note the differences, notably in propagation of 7255kHz (41m).



paper chart roll and would in any case be impractical with a short sampling interval because of the length of chart likely to be generated. Calibration accuracy is of course limited by the receiver and no-one would seriously consider this as a replacement for a conventional field strength meter.

Such a task, however, hardly begins to utilise the power of the Sony/BBC micro combination. A simple spectrum analyser is achieved by pulsing the up/down buttons and keeping track of the frequency at the same time with the computer. This idea can be extended to generate the band occupancy charts beloved of h.f. monitoring stations and may of course be used to find clear frequencies for forthcoming schedules. A completely different ap-

plication is that of sequential monitoring. In this case the memory preset buttons are preloaded with the various frequencies in use by the station and the computer simply steps around them in an endless loop. Audio output from the Sony may be taken to a monitor loudspeaker or a data logging tape machine (a Racal Autostore is used by Radio Botswana with excellent results), or a.g.c. output used to detect carrier failure. The cost of a purpose-built sequential monitor is of the same order of magnitude as the Sony/BBC combination!

Program 1 illustrates an even more interesting use*. This is a combination of the previous two: the preset memory buttons

*See note on page 43.

are loaded with frequencies to be monitored and then a continuous sequential scan is made of these memorized frequencies whilst taking a.g.c. readings. The readings, with time markers inserted every minute or so, are stored sequentially (256 at a time by the BBC disc filing system) on a floppy disc. When replayed with a suitable analysis program (e.g. program 2) the various frequencies can be de-multiplexed then and separated from time markers to simulate a multichannel chart recorder.

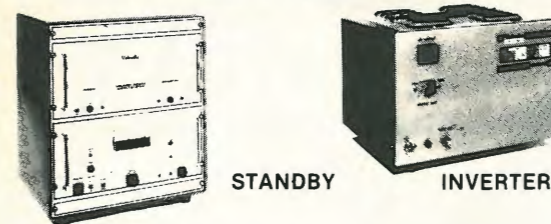
Typical output from this program is shown in Fig. 3 and 4 in the form of screen dumps taken from a colour monitor. In the original recordings seven transmissions were monitored over two successive days; for clarity the analysis program has selected for display only the short-wave frequencies transmitted to the area at any given time. It is possible to see at a glance the propagation efficiency of all the transmissions over a complete day - four in this case but it could have been more - essential information for planning times of frequency changes and comparing actual reception with predictions. In particular the time at which propagation to a given area fails as the operating frequency drops below the m.u.f. (or to be more accurate, the h.p.f.) can be seen very clearly and this information may then be extrapolated for other distances in accordance with the well-known secant law. Such observations, if taken carefully, are a valid substitute for ionosonde data which may be otherwise unobtainable.

The vertical axis of Fig. 3 and 4 is arbitrary for three reasons. Firstly, the Sony a.g.c. voltage is dependent on incoming supply voltage. When the unregulated Sony accessory a.c. adaptor is used this can be easily seen in the correlated movement of two or three channels, particularly the lower frequency ones where computer buzz is measured in the absence of genuine transmissions. Secondly, the inherent sensitivity of the Sony varies across its extensive frequency range, and even if this were negligible, corrections would be needed for the antenna in use.

continued on page 87.

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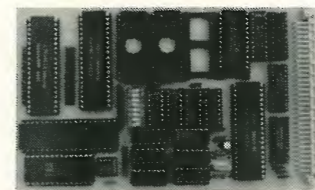
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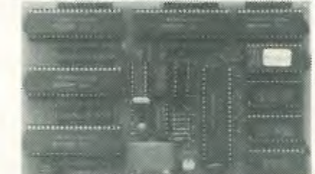


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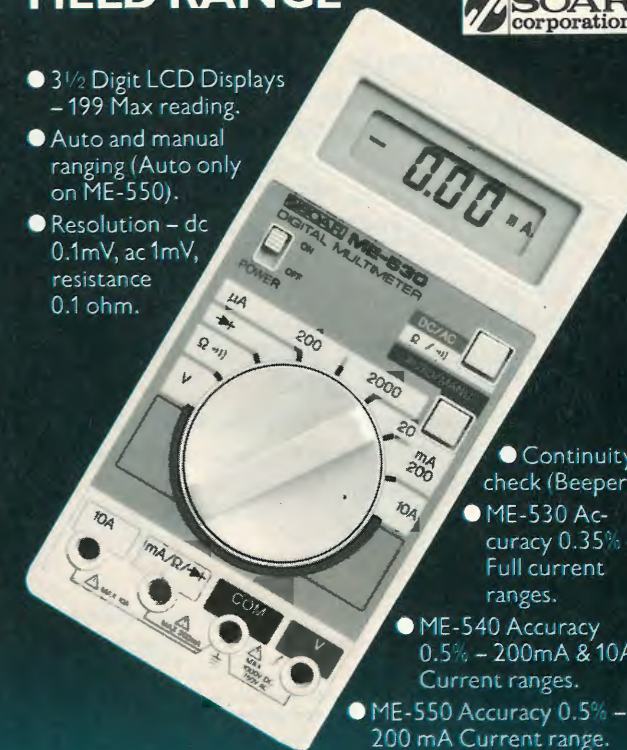
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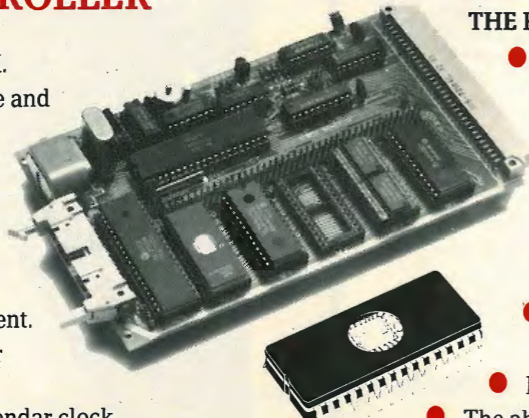
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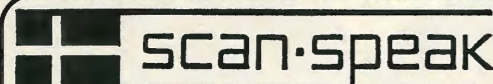
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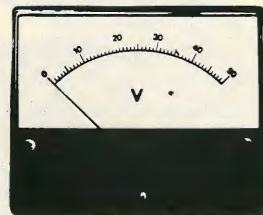


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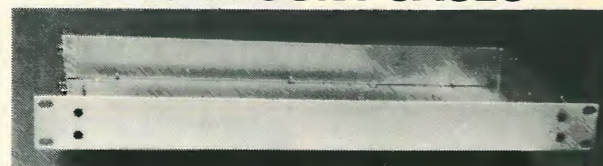


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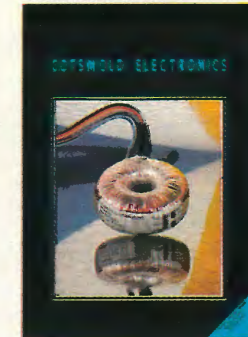
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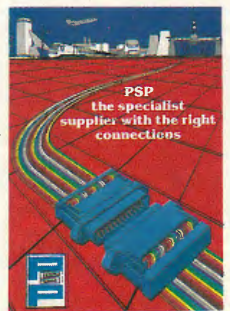
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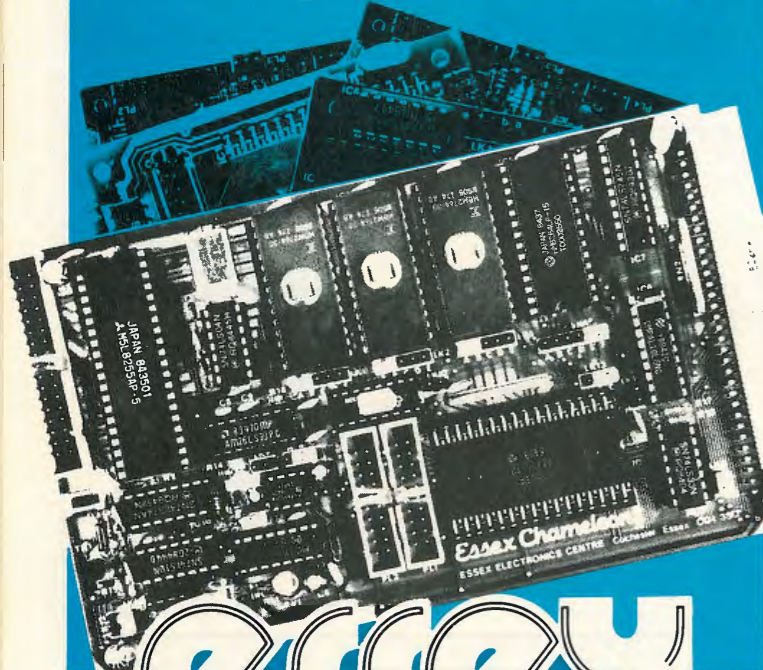
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Naiad training robot

Designed by Dick Becker and Peter Wells of Cybernetic Applications this desk-top micro robot gives low-cost experience in robotics, and features hydraulic, pneumatic and electric drives.

Unlike stepper motors, a servo motor will attempt to rotate continuously when there is current passed through it and the torque generated depends on that current. Servo control of an axis can be implemented with no more than a position-sensing potentiometer and an error amplifier which derives a d.c. servo motor in that direction and causes the error signal to reduce, Fig. 9. The error signal is the voltage difference between the potentiometer output and the voltage which defines the desired position at which point the error signal is zero, meaning that there is then no more current from the error amplifier to drive the motor.

Although there have recently been some high torque 'Megatorque' d.c. motors recently introduced, the torque from most motors is too small to operate an axis directly and some reduction gearing is necessary. This gearing presents a few problems. Inevitably gear teeth do not mesh perfectly and sloppiness called backlash occurs. If the position sensor is fitted to the motor the backlash becomes uncorrected error. If the sensor is fitted to the axis, and the servo system is sensing movement in the backlash, it has to cope with achieving stability whilst there is a varying torque on entering and leaving that region. Harmonic drive gearboxes, having only three moving parts, greatly reduce the backlash and are becoming widely used, but having relatively small teeth overload and slipping must be avoided. Also, harmonic drives are still single-sourced and expensive.

To fully appreciate and understand the capabilities of a machine there is nothing to beat 'hands-on' experience. However, most industrial robots are in the price range

£15k to £50k and have power and accuracy far in excess of that required for training purposes. Like any other machine tool, industrial robots can be dangerous, particularly in inexperienced hands. Training, at least initially, should be carried out using small, inexpensive machines that perform the same functions as their larger counterparts, albeit at a lower specification, and be sufficiently forgiving of mistakes to be suitable for experimentation.

The Naiad was designed specifically for the purpose of safely acquiring 'hands-on' experience at low cost. Of the

various configurations possible the articulated arm was selected, being the most versatile. The complexity of movement provides plenty of programming challenges and it has the most interesting mechanism. Hydraulics, used as energy source for the most powerful industrial machines, was chosen as the main source of energy. To increase its educational value each axis operates in a different manner. There are both single-acting cylinder and double-acting cylinders, a double-ended cylinder, rubber seals, p.t.f.e. seals, and a rolling diaphragm seal. Linear motion

By R.H. Becker

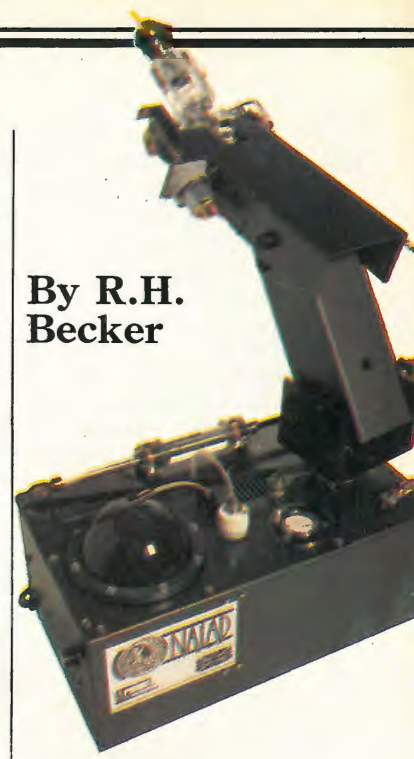


Fig. 9. This very basic control system has a d.c. motor driving the output. The mechanical output is directly coupled to the position feedback potentiometer. Signal from this potentiometer is a direct voltage level dependant on its position. The input to the system, or position demand signal, can be a similar potentiometer, digital-to-analogue converter or anything that provides a d.c. level proportional to position.

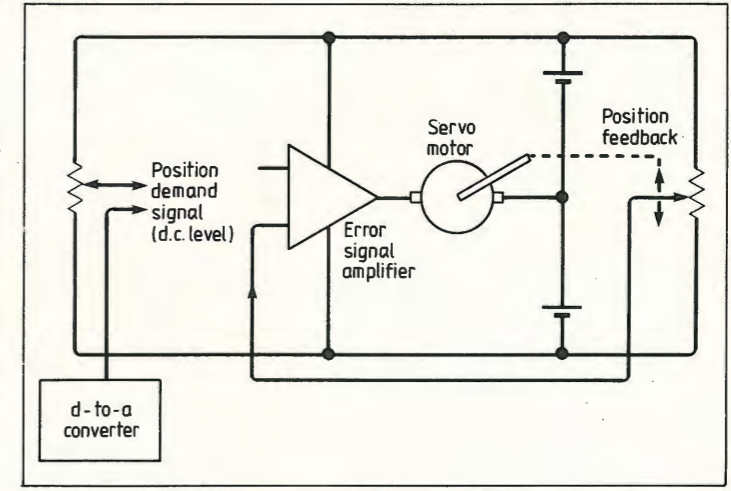
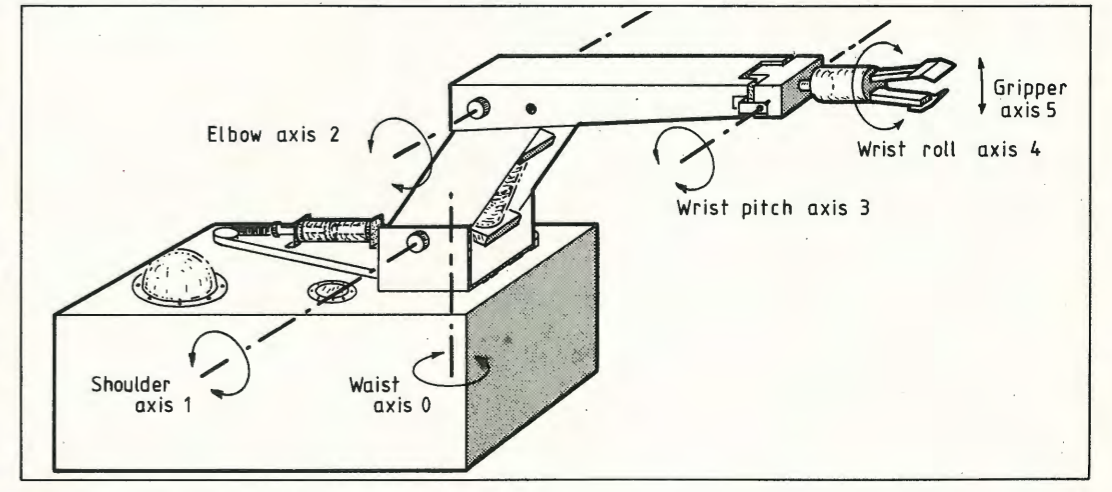


Fig. 10. Schematic of Naiad Robot showing the arrangement of the various axes. There are four hydraulic cylinders, one d.c. servo at the wrist pitch axis and the gripper can be pneumatically or hydraulically operated.



ELECTRONICS & WIRELESS WORLD DECEMBER 1985

Fig. 11. Axis 0, the shoulder-rotation cylinder is a double acting actuator set between two idler wheels. The connecting rods, which protrude from each end of the cylinder through p.t.f.e. seals, are joined together by a toothed belt. The belt passes over the idler wheels and around a third, larger toothed wheel forming part of the axis 0 rotation assembly.

is converted to rotary motion by crank, by rack and pinion and by toothed belt with pulley. Hydraulics and pneumatics are alternative operating modes for the gripper and a d.c. servo motor with software-adjustable servo amplifier is also included. Control is by microcomputer and a simulator is included for one of its learning modes.

Experience in working with oil hydraulic systems has convinced me that for use in laboratories and education establishments a non-messy

working fluid is essential and Naiad therefore uses plain tap water. To help understand the operation of a hydraulic system, many components are manufactured from Perspex, enabling workings to be clearly visible. The use of water assists in visibility and also means that the robot can be cleanly stripped down for deeper investigations or re-assembly by a different group of students.

Naiad has six axes of movement, including the gripper, with each axis built in a dif-

ferent way, Fig 10. Axis 0, which is the waist, uses a double-acting cylinder, Fig 11. There is a water connection at each end and a three-way valve for each end. Whilst water is pumped in at one end water from the other side of the piston is returned to the sump. On the return stroke the opposite end of the cylinder has water pumped into it whilst water from the first end is returned to sump. To each end of the piston rod is fitted one end of a toothed belt that passes round two

idler pulleys and round a similarly toothed larger pulley which rotates the arm at the waist. The linear motion of the piston rod has thus been converted to the rotary motion of the arm.

Axis 1, the shoulder, uses a single-acting cylinder, Fig. 12. The weight of the arm means that gravity provides the force for the return stroke, though there is a small spring at the top of the cylinder to provide extra return force when the arm is near vertical, at which point the turning moment from gravity approaches zero. Water is controlled by two on/off valves, one connected to the sump and the other to the pump. On the upstroke the valve connected to the pump is turned on whilst the other is off. On the return stroke the opposite occurs. When both valves are off the axis is stationary.

The cylinder, which uses a rolling diaphragm seal, is in two halves, clamped together to secure the diaphragm. This cylinder has a bore diameter of 32mm which with a pressure of 7 bar gives a force of about 550 newton to lift the arm, converting linear to rotary motion in crank-like manner. This operation can best be visualized by considering the arm to be stationary and the trunnion rotating. The trunnion acts as a crank of about 30mm length which then rotates through 90°, about the limit for this type of system, though it does have the desirable feature of simplicity and is widely used on robots, excavators and other hydraulic machinery.

Axis 2, the elbow, also has a single-acting cylinder, Fig. 13. Here there is less weight for returning it than on axis 1, so a longer and more powerful spring is used. The cylinder uses a conventional piston with p.t.f.e. seal. This has considerably more friction than a rolling diaphragm and the system is designed for the effects of friction to be observed and measured. There is an adjustable bypass valve and pressure gauge on the top plate of the robot to enable the pressure of the hydraulics to be finely set to facilitate the measurements on the axes.

Axis 3 is the wrist elevation and is powered by d.c. servo motor, Fig. 14. To give the required torque and angular

velocity, gearing of over 1000:1 reduction is necessary. The motor comes with an integral gearbox, but would have insufficient strength to cope with the torque if its gears gave all of this reduction. External gearing in the form of a 15:1 reduction worm and wheel is therefore used together with 75:1 reduction from the motor gearbox. Worm gears mount tangentially on the worm wheel thereby turning the direction of rotation through 90°, enabling the motor/gearbox to be fitted to the side of the forearm.

Axis 4 is the wrist rotation, and here a double-ended cylinder with a rack and pinion in the centre converts the linear motion of the pistons into rotary motion, Fig. 15. The

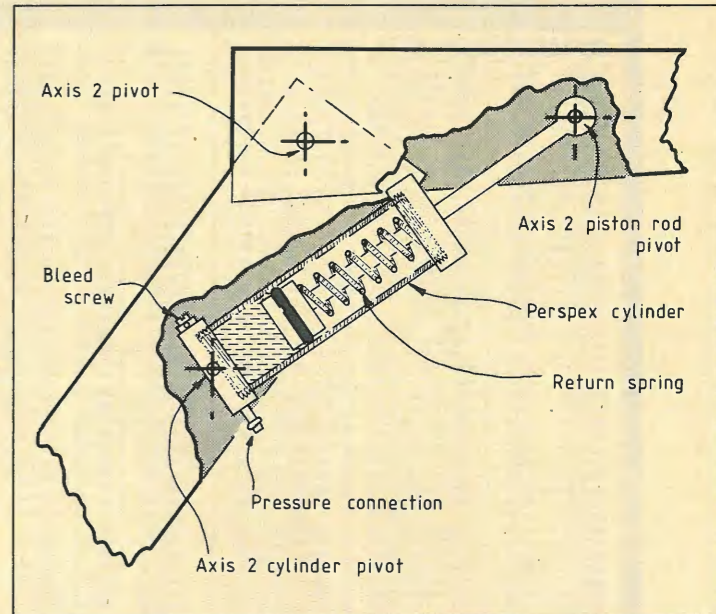


Fig. 13. Axis 2 actuator is a single-acting piston and cylinder assembly of conventional design. Water is retained within the cylinder by a p.t.f.e. band and O-ring seal around the piston.

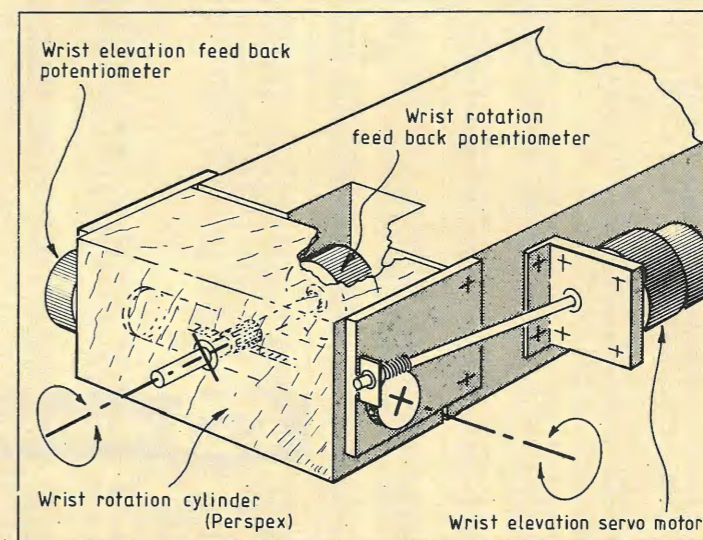


Fig. 14. Wrist elevation is achieved using a d.c. servo motor with integral 75:1 gearbox. On its own, this produces insufficient torque to raise the gripper carrying its rated load, and additional gearing of 15:1 is incorporated together with a 90° direction change.

bore of this cylinder (16mm dia.) is too small for p.t.f.e. seals which require space under them for a rubber O-ring and consequently rubber piston seals are used. Rubber O-rings under the p.t.f.e. seals are necessary as the seals have little natural resilience to hold them against the cylinder wall.

Rubber seals cause more friction than p.t.f.e. ones but they do seal a bit better. Like an oil hydraulic system there is a small loss of fluid past all seals except the rolling diaphragm, but, unlike oil, water soon disappears by evaporation and the robot is clean in use.

To be continued

Fig. 15. Wrist rotation cylinder is a double-acting assembly formed by two pistons joined back-to-back by a length of rack in mesh with a pinion whose central shaft protrudes from both sides of the cylinder.

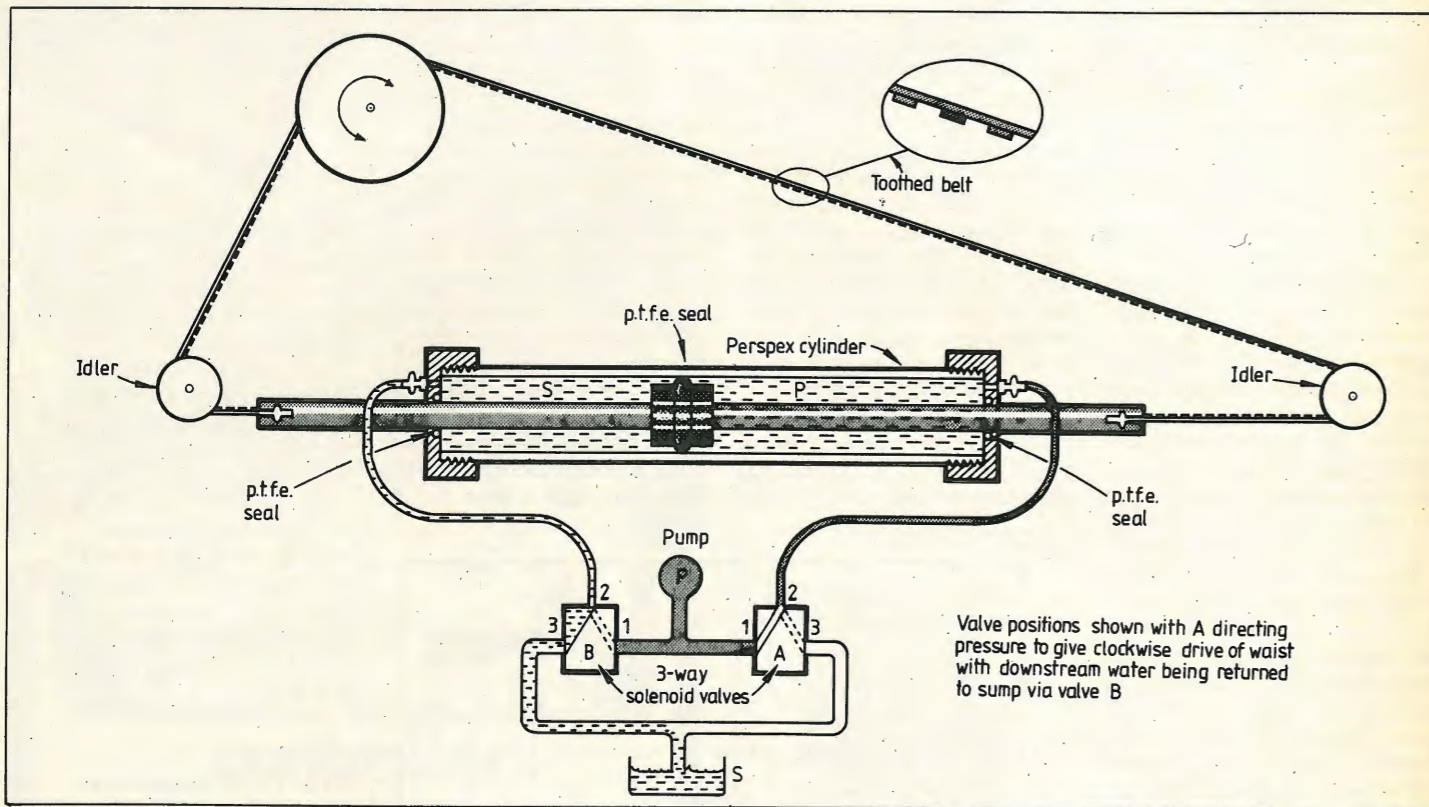
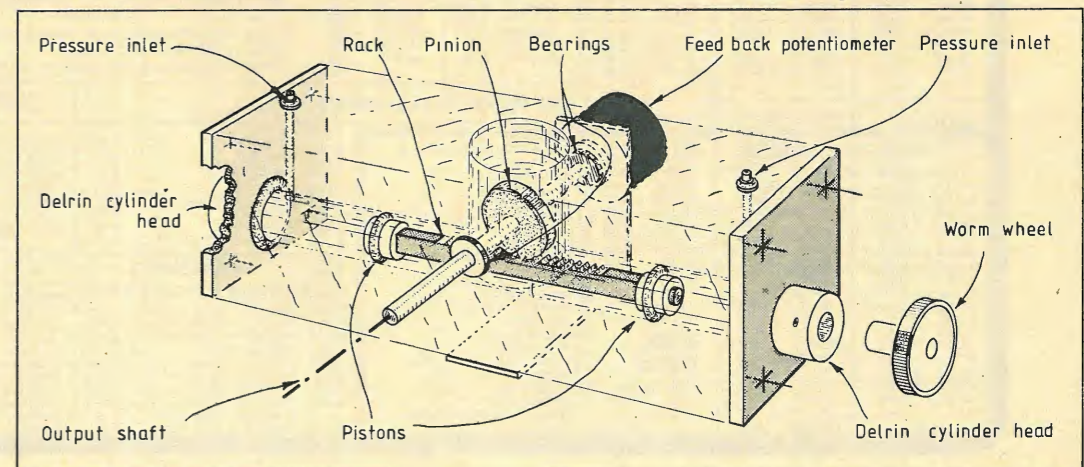
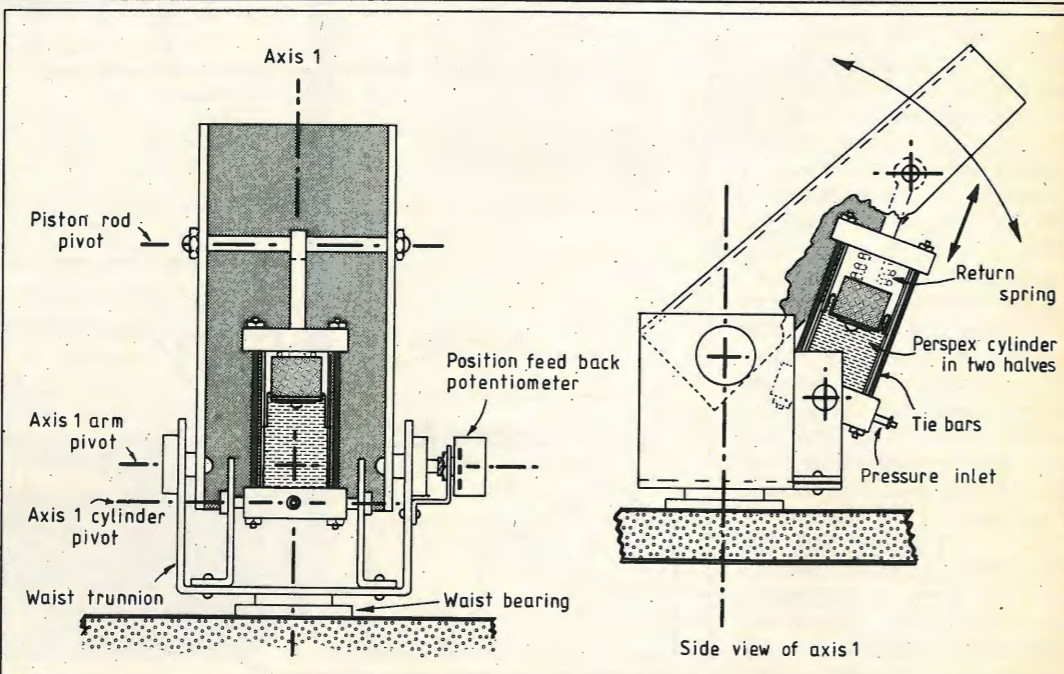


Fig. 12. Axis 1 is driven by a single acting, rolling diaphragm actuator. Pivot point at the lower end of the cylinder is actually part of the trunnion assembly. The connecting rod is linked to an axle toward the top end of axis 1 channel section. Lower end of channel section is secured to the trunnion by a delrin bush on each side; these are fixed to the channel and are free to rotate in the trunnion. The feedback potentiometer is fixed to a bracket on the trunnion and the shaft is locked into the delrin bush. Potentiometer gives position feedback of the axis 1 channel section.



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CIRCLE 92 FOR FURTHER DETAILS.

Satellite television in the band 10.9-11.7GHz has been called 'Europe's best kept secret'. There is very little published on the subject, so this article aims to answer some of the many questions asked and provide an insight into this new and fast-expanding field.

Currently, transmissions are available from four satellites, ECS1, ECS2, Gorizont and Intelsat 5 (27.5°W). Between them they provide at least 18 different stations. ECS3 would have provided several more, but due to the failure of the launch we will have to wait before the replacement satellite is launched. It is quite legal to receive these transmissions and a licence is available on application to the DTI, the fee is £10 for an indefinite period.

11GHz band

The 10.9 to 11.7GHz band is designated as a Broadcast Service Satellite band though the transmissions are at a much lower power level than that intended for direct broadcast satellites. The original purpose of the transmissions was to provide feeds to the cable head-end of networks such as Swindon Cable. Signals are received at the main antenna site by large (4 to 5m) dishes and after demodulation distributed via cable to houses connected to the monthly subscription service.

Until recently it was illegal to receive these transmissions without a Test & Development licence, only granted to com-



Not everyone is clear about satellite tv. Is it only for the future? What is receivable now? How much does it cost? And is it legal?

panies involved in research or antenna manufacture. At the time that the power levels of transmissions in the 11GHz band were decided it was considered financially impossible for the public to receive these broadcasts, as the level of technology at the time required a very large dish at considerable cost. But technology does not stand still and with the low-noise amplifiers of today the dish size has been reduced to a

point where a small dish in a suburban garden will give very high quality reception.

The size of dish required for above-threshold reception depends on the receiving location. Reference to footprint charts, which give the signal strength contours, a signal strength of -118dBW/m² can be achieved in the centre of the spotbeam. The beam patterns for ECS1 and Intelsat indicate that reception is adequate with a 1.6m dish, and signals 3dB down on the spot centre will require a 2.4m dish, and 6dB down a 3.2m dish. Reception becomes more expensive the further from the UK one gets: in southern Spain a 3m dish is required for ECS.

Antennas

In the USA, which has had satellite tv for some time, the wire mesh type of parabolic dish is quite common. This is suitable where reception takes place at C-band (3.7-4.2GHz) as in the USA, but this type of dish will not function on the 11GHz band as mesh size must be no larger than 1/10th wavelength.

In the UK a solid aluminium parabolic dish is required to be accurate to ±1.5mm. Three types of mount are used: fixed, 'az/el' and polar. The fixed mount is usually of A-frame construction and is intended for fixed use onto a single satellite, or mounted on a trailer for demonstration purposes, when it can be quickly set to any satellite. The az/el mount allows the dish to be moved in both azimuth and elevation, and each time a different satellite is required the dish must be realigned. The polar mount is the most difficult to set initially but the easiest to use. Once set, it will always track the geostationary arc; a manual-only version requires a tracking handle to be turned to scan the correct arc. This type of dish can be motorized with a single motor having a control unit alongside the tv receiver. For UK reception a dish with a gain of 42dB minimum at 11GHz is required.

Low-noise block downconverter and feed horn

The microwave electronics is situated at the focal point of the

dish in most domestic applications. With dishes of F/D ratio between 0.35 & 0.42 the feed is commonly of the scalar horn type, consisting of concentric rings with a circular waveguide matched to the WR75/WG17 waveguide normally used in these low-noise blocks (l.n.bs). Energy focused onto the horn is fed by circular waveguide to a probe feeding the first GaAs f.e.t. amplifier. A two-stage r.f. amplifier is normally used prior to a mixer with a dielectric resonator oscillator. The i.f. output of 950 to 1750MHz is amplified within the l.n.b. giving an overall gain of around 55dB.

Amplifier noise figures have been steadily reducing recently and this is the main reason why a smaller dish than first anticipated can be used. A 2.9dB noise figure is easily achieved and 2.5dB is common; higher grade l.n.bs can be obtained with figures as low as 1.9dB. Power to the unit is by coaxial cable which should be of very low loss in the range of 1 to 2GHz.

The demodulator

Coaxial cable from the low-noise block is taken to an indoor unit normally situated on top of

the television receiver.

Many different demodulators are available for this purpose spanning a wide price range, but the principle of operation is mainly the same. The i.f. of 950 to 1750MHz is mixed down to a second or third i.f. and being an f.m. signal is demodulated by either phase-locked loop or quadrature demodulator. After amplification, de-emphasis and clamping, the video signal is available as a 1V pk-pk signal and in some cases is applied to a modulator to give a u.h.f. channel 36 signal for connection to the tv set. Subcarriers are separately demodulated and as satellite broadcasts use various audio subcarriers (4 to 8MHz) it is necessary to be able to tune these from the front panel.

The nature of the satellite transmissions has made the life of the designer somewhat difficult. Intended for cable networks where a 'dedicated' demodulator is assigned to each channel, the variety of audio and video formats is of little tance. But for domestic use all channels will need to be receivable by the same unit. To produce a multi-standard demodulator would be too expensive for most people so some sort of compromise is usually inevitable.



Typical scalar horn feed with polarization boss that allows l.n.b. to rotate polarization by 90° enabling reception of both x and y polarized transmissions.

Market potential

To achieve a large-scale market sales must be made to the public, but before that happens people need to be made aware of the entertainment possibilities available. This will be done by television retailers moving into this new field with a demonstration unit in their shops. Having just spent a week at a recent exhibition on behalf of Connexions Satellite Systems with a complete satellite system on the stand, it appears that a great many

computer dealers are keen to move into a new product and can see the potential offered by satellite television. And public awareness will be greatly increased as hotels and apartment blocks establish master antenna systems. Universities are finding it an excellent medium for language studies.

Although dish size and cost increases outside the UK there are many ex-patriat residents in France, Spain and Portugal for whom reception of English language programmes is worth the high purchase price — around £3000 duty paid.

Although current satellites offer a variety of programme material, for those less well off the future looks bright. Apart from French and German satellite broadcasting it is likely that ITV will run its Superchannel Europe through the French high power d.b.s. satellite TDF1 next year. Reception in Spain should be possible on a 1m dish with a basic system at around £500.

Programme material

On a satellite by satellite basis it may be of interest to readers to see what is available from a home system. Intelsat 5 (27.5°W) currently has four operational transponders. From 0800 to 1500h the Children's Channel transmits programmes for 4 to 11-year-olds (cartoons, stories, things to make and do). After 1500h this transponder becomes Premier, a Thorn EMI channel, and runs films about every two hours till the

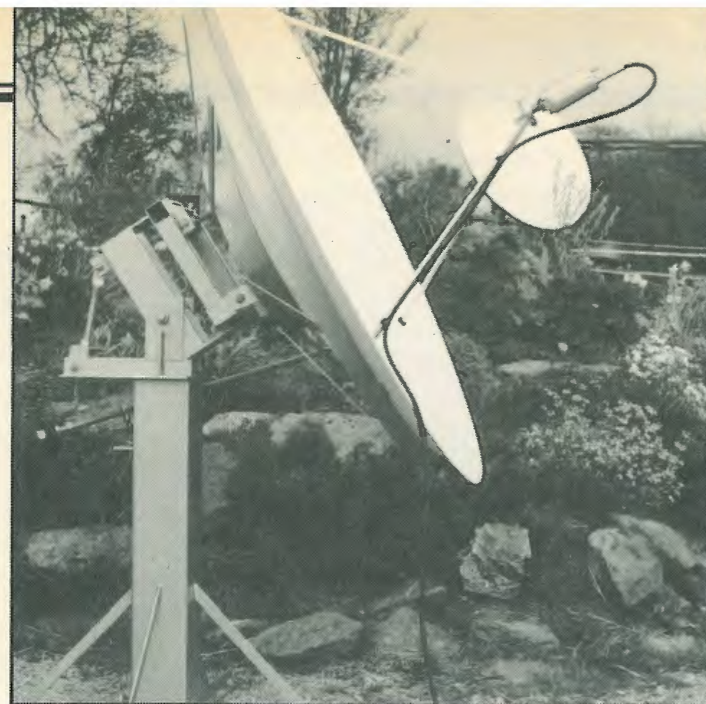
SATELLITE TRANSPONDER ALLOCATIONS

Satellite	Station	Freq. (GHz)	Audio (MHz)	Language	Material	Standard Bandwidth Beam		
ECS1	RAI	10.991	6.6	Italian	National 1 programme	PAL	36	West
	SAT 3	11.056	SIS	German	Films & news	PAL	36	East
	EUROPA	11.175	6.6	Eng/Dutch	General interest	PAL	30	West
	TV5	11.472	6.65	French	Best of French TV	SECAM	36	West
	CATALAN NEWS	11.472	6.65	Catalan	News feed daily	PAL	36	West
	EBNET	11.512	6.65	Various	Religious channel	PAL	36	West
	WORLDNET	11.512	6.65	English	US Information Agency	PAL	36	West
	SKY	11.650	6.65	English	General & also stereo	PAL	27	West
	TELECLUB	10.987	6.5	German	Films	PAL	36	West
	FILMNET	11.140	6.65	Eng/Dutch	Films	PAL	30	West
	WPN	11.140	6.65	Eng/French	Documentary	PAL	30	West
	SAT 1	11.507	SIS	German	Films, News	PAL	36	West
	VOA	11.507	7.00	English	Pop music & news (audio)	NONE	-	West
MUSIC BOX	11.674	6.65, 7.02, 7.2	English	Pop Video Channel	PAL	36	West	
INTELSAT F10 (27.5°W)	PREMIERE	11.015	6.6	English	Films	PAL	30	West
	CHILDREN'S CH	11.015	6.6	English	Children's programmes	PAL	30	West
	SCREEN SPORT	11.135	6.6	English	Many different sports	PAL	30	West
	MIRRORVISION	11.175	6.6	English	Films	PAL	30	West
CNN	11.155	6.6	English	USA 24h NEWS	PAL	36	East	
INTELSAT VA (18°W)	BBC	11.150	6.65	English	Very rare. News feeds	PAL		West
GORIZONT (14°W)	USSR	11.512	7.0	Russian	Rare. Prog 1 tests	SECAM		
ECS 2	NORWAY	11.676	None	Norse	C-MAC system	C-MAC		West
	EBU	11.142	SIS	Various	News feeds & sport	PAL		West
	EBU	11.176	SIS	Various	News feeds & sport	PAL		West

SIS — Sound in sync, PCM2 system. EBU — European Broadcasting Union transmitting material in the language of the originating member. NOTE: It is not possible to receive either audio or clear video from SIS or C-Mac Transmissions with a normal PAL Rx.

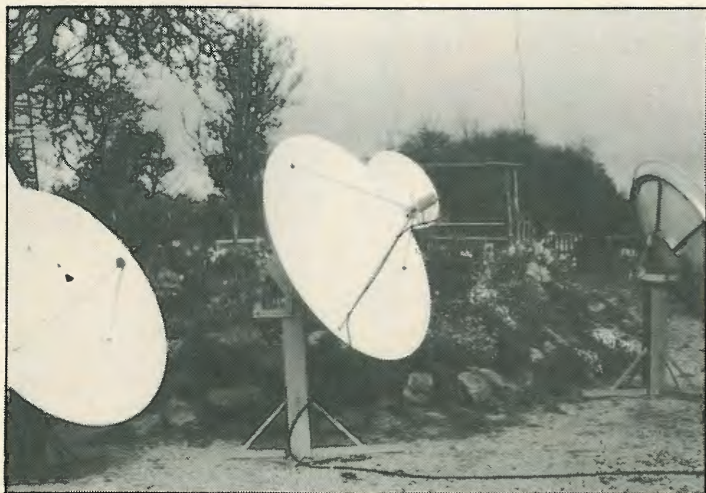


Polar mount rear support structure can be fitted with motor and remotely controlled from the tv set or simply hand operated with lead-screw to reduce cost.



North East Satellite Systems spun aluminium dish antenna with low-noise block measures 1.6m diameter — the smallest dish size that can be used on Intelsat transponders with current power levels for film and sports channels up to the Scottish border.

John Standen owns North East Satellite Systems of Cropton, North Yorkshire, a company dedicated to making microwave technology affordable. He is also technical director of Connexions Satellite Systems Ltd.



this is a film channel which should not be watched without the permission of the programme provider.

Filmnet is uplinked from Belgium mainly in English with Dutch subtitles and is another film channel. The same channel is used by World Public News for its morning schedule dealing with current affairs in English and French. There is also a feed to Israel in English.

Olympus is a Pan-European tv station run by the European Broadcasting Union and contains material from its members. Materials is of a similar format to conventional tv with European news, current affairs, travel, music, films, etc. It is hoped that material broadcast in the language of the providing country will be available in teletext subtitles to other language groups.

Worldnet is provided by the US Information Agency based on the previous night's US news broadcast, and intended for US embassies around Europe.

So far everything has been in PAL colour; now a couple of odd men out. TV5 in SECAM transmits the best of French-language tv from France, Belgium and Switzerland, with about four hours of programmes per night of a general mix.

The New World Channel (EBNET) is uplinked from Norway and is a multi-lingual religious transmission now in PAL.

Sat 1 is a German channel which although PAL has sound-in-sync and the short sync pulse distorts the picture. Without the correct decoder no sound is possible, however on this and other sound-in-sync channels an audio subcarrier has been found at around 3.5MHz but very distorted. This may be an analogue component of the digital waveform and will require further investigation. Sat1 also carries a VOA pop music and news programme in English on an audio subcarrier.

Sky Channel, the first satellite tv operator that predates ECS1, was operational on the Orbital Test Satellite (OTS 2). The audio, which includes both mono and stereo subcarriers, is clear but the video is inverted. Music Box is an 18 hour pop-video channel, available with both a mono and stereo sub-carrier; the stereo is com-

panded and requires expansion.

On the Eastern spot is Luxembourg in German and Sat 3 (sound-in-sync) but the signal is too weak from a 1.6m dish for reception in the UK and at least a 3m dish is needed.

The terms of the licence are that the material must be for ultimate public reception, and a considerable amount of material on ECS 2 does not fall into this bracket. For example, feeds to the USA are run where conversations at set up time are transmitted when the viewer would be watching a 'commercial' and language can be Anglo-Saxon in the extreme! The Eurovision song contest goes through this satellite, as do many of the sports exchanges. Material is mostly sound-in-sync. Sweden has two transponders operational but with a non-public reception system. Norway has a C-MAC transponder, which requires a special decoder.

Test transmissions from the Soviet Union have recently been received from the 14°W Gorizont on 11.640GHz. The transmission is by circular polarization which requires a simple and cheap modification to the feed horn if a 3dB loss is not to be incurred.

Had ECS 3 not had to be destroyed there would have been another 12 transponders available very shortly, but now we will have to wait till the ground spare can be fitted into the launch schedule*.

Other 11GHz satellites carry the point-to-point telephone network between the UK and USA and it is an offence to attempt to receive these transmissions. A news feed from the BBC has recently been seen on the 18°W Intelsat.

For those with teletext receivers, many of the transponders carry a text service which gives details of future programme material, as well as other interesting data.

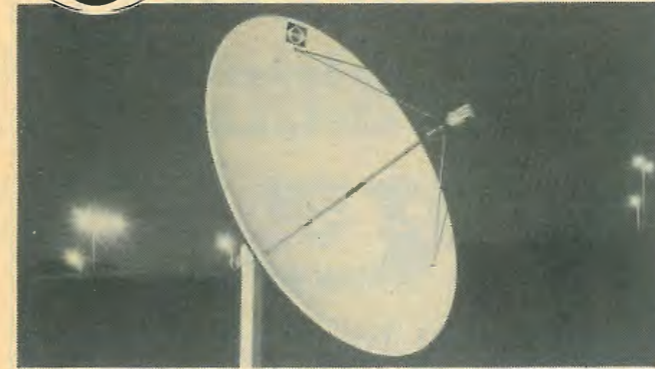
System costs

Being a microwave system, initial prices followed very closely to the high-cost military market and two years ago £10,000 would not have been a high price to pay. Today the retail price of a system starts at £995 + VAT up to £1,790 for a remotely-controlled dish plus electronics.

*Now expected in April.



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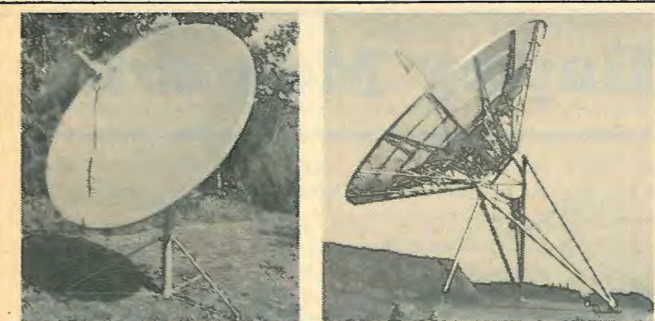
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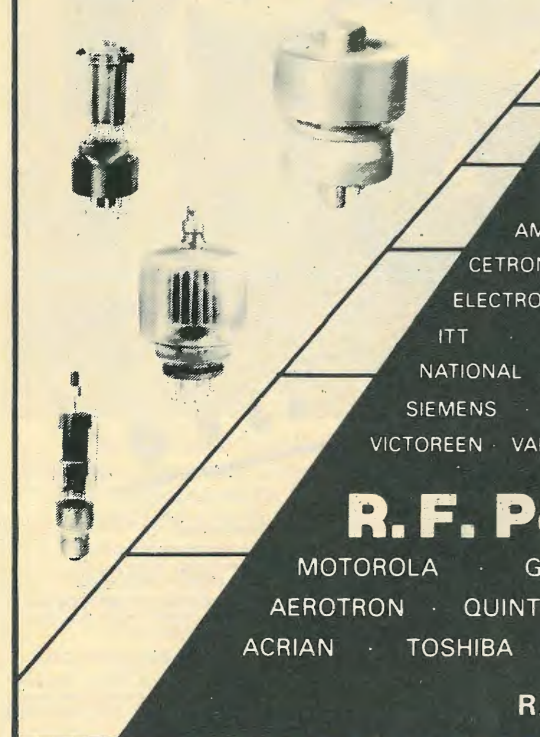
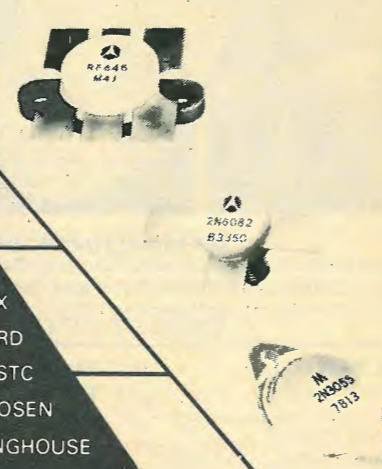
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CIRCLE 43 FOR FURTHER DETAILS.

Equitable access to satellite communication

A conference of the ITU has decided to change the basis of regulating the use of the radio spectrum for satellite communication so that every country will have a guaranteed option of setting up a national system.

Developing countries complain that the developed countries are seizing most of the available positions in the geostationary satellite orbit for their own satellites. Countries that may want to set up their first national satellite system in 10 or 20 years' time fear that there will be no room left for them. Changes are being demanded in the international regulation of the use of the orbit and the radio spectrum, in particular for satellites that interconnect fixed earth stations (the fixed-satellite service or FSS), to guarantee access for all countries, no matter how late they start.

A guarantee of equitable access to orbit and spectrum for the FSS presents real problems. The ITU is an international body, not a supranational one, and the policies which could be followed to achieve equitable use of the g.s.o. for the FSS are only those which all member governments can agree to implement. All countries support justice in principle, but there will also be insistence on protection of existing systems and current growth and a limit on the near-term economic impact of changes designed to provide for uncertain needs in the distant future.

Delegates of over a hundred countries attended a conference in Geneva, 8 August to 15 September 1985 under the aegis of the International Telecommunication Union (ITU) with the chief item of finding ways of guaranteeing equitable access to the Clarke Orbit. There is to be a second session of the conference in 1988, whose full title is "the World Administrative Radio Conference on the use of the geostationary satellite orbit and the planning of space services utilizing it" or WARC-85 for short.

Every national government reserves to itself the right to assign frequencies to radio stations within its jurisdiction. The basic method that is used to keep order in the radio spectrum involves international registration through the ITU of national frequency assignments that have been shown not to cause unacceptable interference to one another; a new assignment is added to the register if it raises no interference problems with assignments which are already registered. Governments undertake not to allow their radio stations to interfere with stations using internationally registered assignments. This principle has been elaborated into a frequency coordination procedure for space radio services. It has been called "first come, first served" by its critics, with truth. It works fairly well but it is not equitable.

The only other method that has been found to work, and which is used for some services and in some frequency bands, is radically different. This consists of drawing up a plan for the use of the band, based not on present or planned use but on the stated national requirements. So-called *a-priori* frequency assignment plans have been used, for example, for some terrestrial services and for satellite broadcasting in the 12GHz band. This method provides guaranteed access to a late-comer country. It also provides stability of frequency usage and this is of great value for some radio services and especially for broadcasting. The much greater versatility

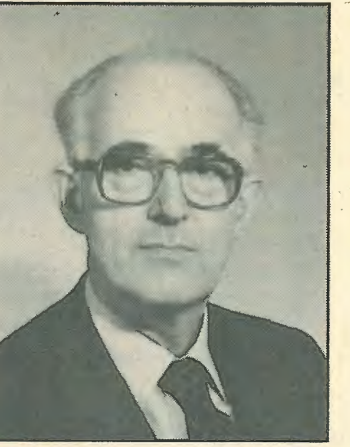
and changeability of the FSS makes the *a-priori* method particularly unsuitable for that service.

The most serious drawback of *a-priori* planning, however, is that the ITU has never found a way of sharing the medium between countries in proportion to their actual needs; it has only been able to share it in accordance with the stated requirements of each country, and in practice this has usually involved assigning equal shares to all countries, large and small. It is like sharing a cake. Sharing a cake between about 160 claimants can lead to small portions. It is possible to make the cake bigger by requiring all systems to meet high technical standards, but at a price. If the portions are to be big enough for the bigger users, the price is likely to be very high, and it is money wasted if many assignments are not used, as is likely with *a-priori* plan.

Coordination is clearly more effective and economic for the FSS than *a-priori* planning, but can it meet the needs of late-comer countries? Growth in the FSS is buoyant. Indeed there is little doubt that the rate of growth of national satellite networks for developing countries would be determined mainly by the availability of capital to finance them, given that orbital slots could be found for all the new satellites. The demand might eventually top-out as the scale of traffic on major domestic telephone routes rises to the point where terrestrial transmission media such as radio relay and optical fibre cables compete economically

with satellite networks but this will take a long time. Thus, much depends on the feasibility of finding enough orbital slots to meet the demand. So far it has always been possible to find a slot for a satellite by using the coordination procedure, although it is getting difficult in the more crowded arcs. Finding enough room for future growth is primarily a technical matter. A great deal has already been done, mainly through the ITU, to reduce the minimum acceptable separation between satellites and so to increase the number of slots. By 1987 all newly launched geostationary satellites will have to be maintained very precisely on station. The level of inter-network interference which all networks are recommended to accept has been increased by stages over the years and further increases are impending. Considerable progress has been made in reducing the gain of earth station antennas in the direction, not of their own satellite but toward other satellites nearby in orbit. There is much more that could be done. In particular:

by D. Withers, F.I.E.E.



David Withers, who retired as chief engineer of BT International in 1985, is a consultant in satellite communication. For the last ten years he has chaired a CCIR working party studying ways of achieving efficient use of the g.s.o. by the fixed-satellite service.

with satellite networks but this will take a long time. Thus, much depends on the feasibility of finding enough orbital slots to meet the demand. So far it has always been possible to find a slot for a satellite by using the coordination procedure, although it is getting difficult in the more crowded arcs. Finding enough room for future growth is primarily a technical matter. A great deal has already been done, mainly through the ITU, to reduce the minimum acceptable separation between satellites and so to increase the number of slots. By 1987 all newly launched geostationary satellites will have to be maintained very precisely on station. The level of inter-network interference which all networks are recommended to accept has been increased by stages over the years and further increases are impending. Considerable progress has been made in reducing the gain of earth station antennas in the direction, not of their own satellite but toward other satellites nearby in orbit. There is much more that could be done. In particular:

- The geographical area covered by satellite antenna beams could be tailored much more closely to the required service area, with a

- The process of coordinating satellites which are neighbours in orbit should be made much more effective and could benefit from more cooperative attitudes on the part of negotiators.
- Some specific types of radio signal are particularly susceptible to interference from other specific types of signal. Segmentation of the spectrum to ensure that these signal types are not assigned the same frequency channel in different satellites could yield considerable benefits.

Given all of these further improvements, the number of satellites that could operate in the FSS would be very great.

The number will grow from decade to decade as better hardware becomes available at affordable prices. For example, much depends on the smallness of satellite antenna beams and therefore on the largeness of the antenna reflectors that can be launched as a piece or unfurled or erected in space. However studies made in CCIR, assuming the use of only those frequency bands which are already in general application and no new launcher facilities, have come up with an estimate of 1000 satellites.

Progress is being made in all of these areas in the ITU and in particular in the CCIR. Agreement comes only slowly because these changes of technology and practice tend to

be more costly than they have already been adopted. The price presents itself in a variety of ways; in the capital cost of satellites, in lost business opportunities, in operational difficulties in working networks and in increased planning costs. Ways will be found for reducing these costs and finding the best combination of measures, from decade to decade, that will provide for the growth in global capacity that is required by growing demands at a cost that can be borne. Despite this prospect of improvement, it cannot be said that coordination offers a bankable guarantee of equitable access.

The agreement at WARC-85

After prolonged discussions WARC-85 agreed to a compromise between efficiency and reassurance in the form of a package of proposals for the FSS. In future, three regulatory methods will be applied to the FSS in its various frequency bands.

One group of frequency bands will be regulated by *a-priori* planning. This will not be the very rigid form of frequency assignment planning used, for example, for satellite broadcasting. A new and more flexible process called frequency allotment planning, much better suited to the multifarious and changeable FSS, is to be developed. These plans will provide a guaranteed allotment for every country, available for use whenever the need arises.

A second group of frequency bands will continue to be regulated by coordination methods, but these methods will be improved in order that these bands might better carry the heavy traffic load that will develop in the future. No doubt international systems like Intelsat will be substantial users of these bands in the future, but there will be no bar to their use for national systems, and they will undoubtedly be so used, for example when the capacity provided by the allotment plan is insufficient to meet national needs.

The remaining frequency bands allocated to the FSS would be regulated basically as now, but the procedures are to be reviewed and amended if ways can be found to facilitate their application.

The *a-priori* frequency allotment plans are to be drawn up for two sets of frequency bands,

- one, with a transmission bandwidth of 300MHz, with earth to space links around 6.7GHz and space to earth links around 4.6GHz
- and the other with a transmission bandwidth of 500MHz, with earth to space links around 10.8 and 11.4GHz and space to earth links around 13GHz.

These bands were first allocated to the FSS in 1979 and have not been taken into use yet to any considerable extent.

The form which the allotment plan would take has not been worked out in detail yet, but it is likely to be broadly as follows.

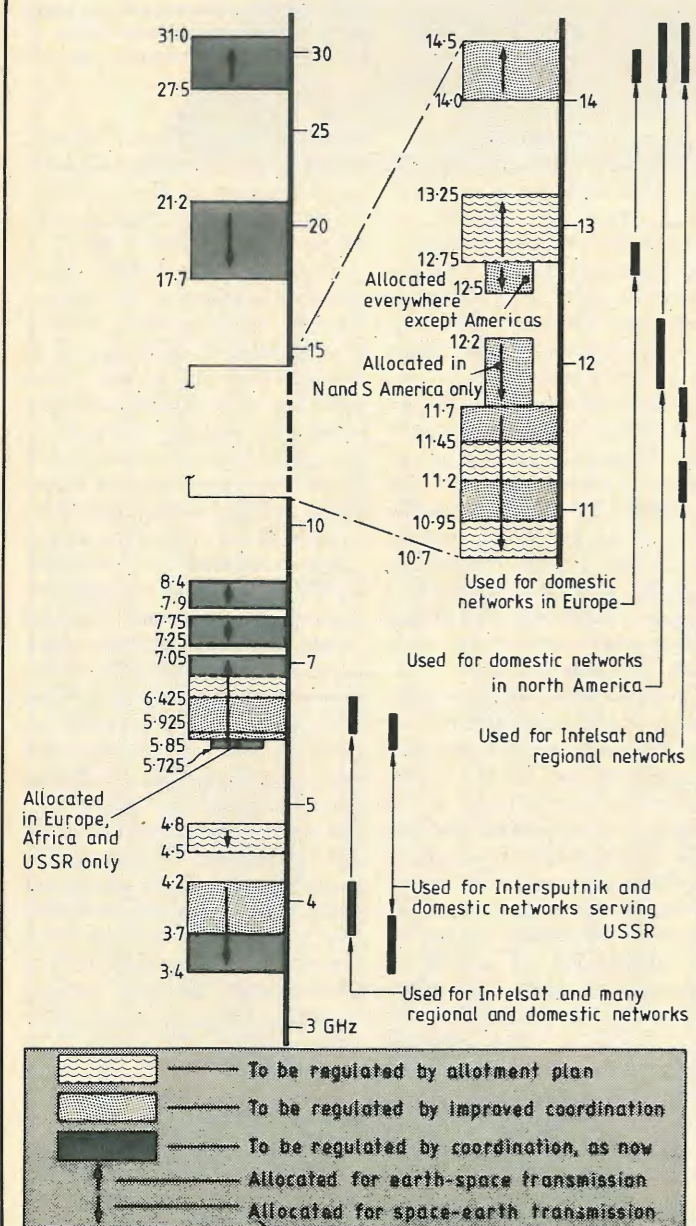
- The g.s.o. would be divided into short orbital arcs, each allotted for the satellites of a short list of countries.
- Some of the basic technical parameters would be defined within which all networks must operate, such as satellite antenna and earth station antenna out-of-beam gain, spectral power radiation density and interference sensitivity.
- A bandwidth would be allotted to each country within the agreed frequency bands and the permitted service area would be defined. The whole bandwidth available in both sets of bands, namely 800MHz, will probably be allotted for the use of each country, unless this would demand unacceptably stringent technical parameters.

Each country's guarantee of access arises from its inclusion in the allotment plan. The plan would be flexible, within limits, allowing a range of network parameters to be used and levelling some latitude for countries to group together, if they wish, to set up a satellite jointly. When a country or a group of countries is ready to take up an allotment and launch a satellite, it would discuss its proposals with other countries with working satellites or allotments nearby in the geostationary orbit register the assignments after successful coordination.

Improved procedures based on the present coordination pro-

continued on page 75

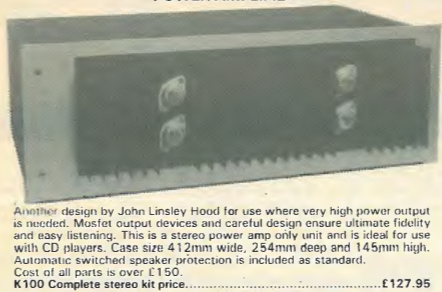
Fixed-satellite service frequency allocations



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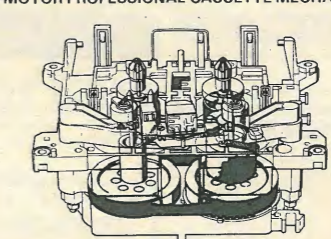
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HS61 Metal Tape Erase Head. Full double gap..... £4.90

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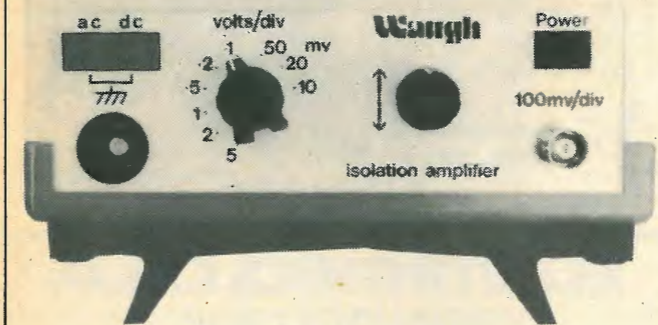
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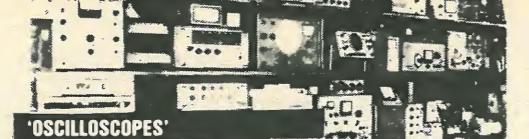
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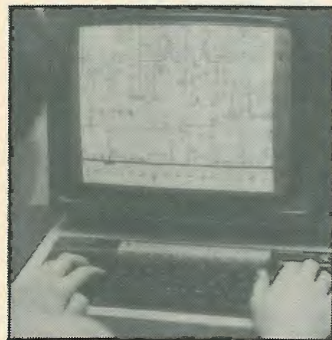
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CIRCLE 72 FOR FURTHER DETAILS.

ELECTRONICS & WIRELESS WORLD DECEMBER 1985

Polyphonic keyboard

4

Audio and interface circuits of David Greaves' digital musical instrument

Push-to-make switches are used on the front panel. These are wired in a rectangular array of three rows by eight columns. Since no two buttons in the same row need be pressed at once, multiplexing diodes are not necessary.

The columns connect directly to the Q-bus. This bus is terminated with 2.2kΩ pull-up resistors which reduce line ringing and provide a logical one if a switch is not pressed. The rows are fed from open collector buffers which pull the row line low when the 8088 wants to read the switches. If a switch is pressed, a line in the Q-bus will be pulled low when the row line is pulled low. De-bouncing of the switches is performed in software.

The front panel leds are controlled by writing to a single eight-bit latch consisting of IC_{48,49}. For economy, a single 74LS374 could be substituted for these two integrated circuits. Ten sets of single leds or pairs of leds need to be addressed so decoder IC₆₃ is used to extend the eight available bits.

Analogue electronics

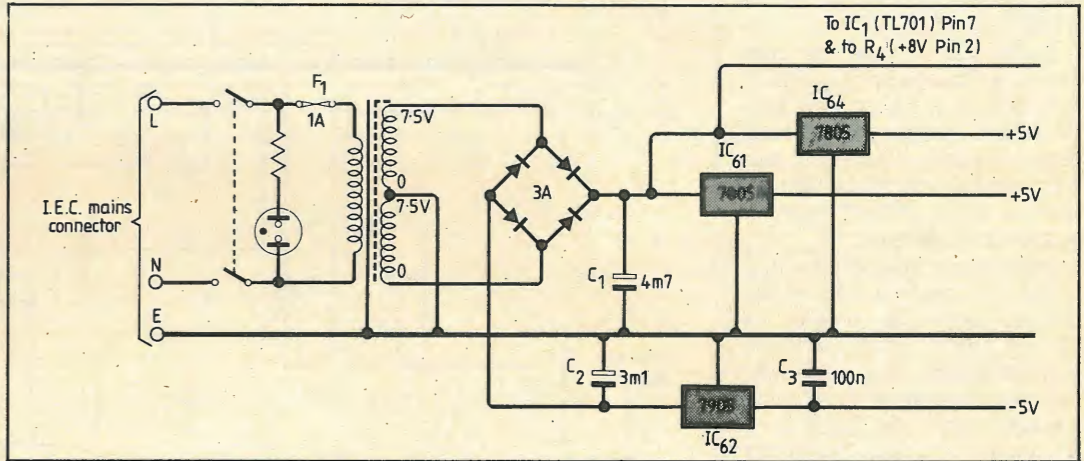
Component IC₃₂ is the control digital-to-analogue converter. It is an eight bit device and produces a voltage on pin two proportional to the number that the 8088 has put into IC₃₁.

Control-converter voltage is fed to the comparator, IC₁ where it is compared with the front panel knob voltage to give a single above/below bit. This bit is gated onto the Q-bus by IC₂₃ when the 8088 reads the status port. The control converter also feeds IC₃₇, a CD4051 eight way bilateral switch.

When the 8088 writes to its influence register, one of the switches in IC₃₇ is enabled and

this feeds the control voltage to one of five holding capacitors. These are the five sample-and-hold circuits described earlier. Holding capacitor C₁₅ is buffered by IC₄₄. This capacitor

sets the tremolo and vibrato oscillator frequency. The oscillator is a standard phase-shift ring type based around half of IC₄₆. Its output feeds IC_{40, 45}, both CA3080 variable



Polyphonic keyboard i.c. list

1	TL701	KNOB comparator	32	DAC0801	Control d-to-a converter
2	74LS161	Microcode address upper 4 bits	33	74LS00	Carry-bit logic
3	6349-1	Microcode prom	34	74LS74	Carry-bit reg.
4	74LS734	Current op-code reg.	35	74LS10	Converter address detector
5	74LS154	Op-code decoder	36	74LS00	
6	TMM2016P	2K x 8 100ns ram	37	CD4051	Converter influence reg.
7	74LS181	Lower four a.l.u. bits	38	741	Audio output filter
8	74LS181	Upper four a.l.u. bits	39	TL081	Fine pitch control
9	74LS74	Host command 2-bit shift reg.	40	TL081	Vibrato depth control
10	74LS161	Microcode address lower four bits	41	74LS13	Master clock Schmitt trigger
11	74LS126	Addressing mode steering	42	CA3080	Tremolo depth amplifier
12	74LS157	Host-command address switch	43	TL062	Tremolo-depth/timebase-speed buffer
13	74LS244	V-reg. buffer	44	TL081	Tremolo and vibrato-speed buffer
14	74LS373	Host data reg.	45	CA3080	Vibrato depth amplifier
15	74LS374	Audio d-to-a converter reg.	46	SFC2458	Tremolo and vibrato osc.
16	74LS245	A-bus to D-bus buffer	47	7407	Front panel o.c. buffer
17	74LS374	Accumulator	48	74LS161	Upper led latch
18	74LS245	Indexed addressing buffer	49	74LS161	Lower led latch
19	74LS04		50	8284	8088 clock generator
20	74LS02		51	8088	processor
21	74LS08		52	HMM6116LP-3	2K x 8 c-mos memory
22	74LS161	V-reg.	53	2764	8K x 8 n-mos eprom
23	74LS367	Status port buffers	54	74LS10	8088 address decoding
24	74LS00		55	74LS373	8088 lower address bus latch
25	74LS373	Data for host index reg.	56	74LS245	8088 i/o transceiver
26	74LS04		57	7805	8088 5V regulator
27	DAC0801	Audio output converter	58	74LS04	8088 logic
28	74LS373	Data destination reg. & Midi out	59	74LS00	8088 logic
30	74LS154	Host reg. address decode	60	74LS10	8088 i/o decoder
31	74LS373	Control converter reg. & key scan address	61	7805	5V regulator
			62	7905	-5V regulator
			63	74LS138	Front-panel led controller
			64	7805	Microcode logic regulator
			65	74LS04	Midi data buffer

by D. J. Greaves
B.A.

ELECTRONICS & WIRELESS WORLD DECEMBER 1985

transconductance amplifiers. Their transconductance is set by a current into pin five.

Similar circuits are used for both 3080 devices consisting of transistor constant-current generators whose current is set by one of the influence register sample and hold circuits.

Amplifier IC₄₅ modulates reference current in the audio-output converter IC₂₇ to provide a tremolo effect. Because the converter zero output is actually half way through its range, the tremolo modulates the d.c. level of the output. This effect can be nulled by adjusting the tremolo null potentiometer to add an opposite d.c. modulation directly to the output. The potentiometer is the only preset requirement in the whole instrument.

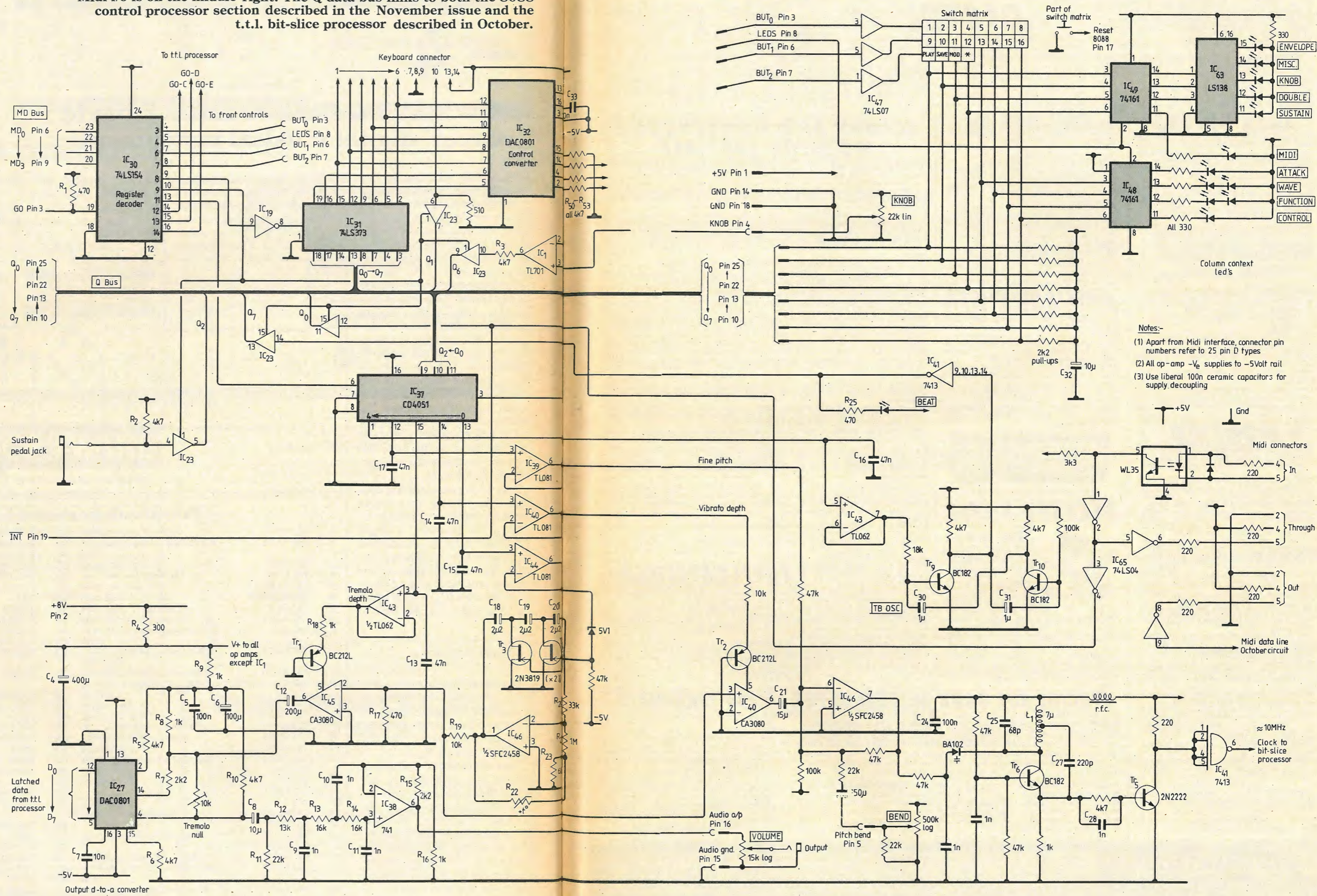
Audio output from the converter is filtered by IC₃₈ which is wired as a Sallen and Key three-pole low-pass filter with pass-band gain of three and cut-off frequency of 5kHz. Output level is about 200mV when a single key is pressed.

Component IC₄₀ feeds IC₄₆ which controls frequency of the master clock to provide a vibrato effect. Along with the vibrato signal, IC₄₆ sums the fine pitch setting from IC₃₉ and voltage from the pitch-bend front-panel control. Output of IC₄₆ biases a voltage-controlled capacitor in the tank circuit of the clock. For a swing from +5 to -10V in bias, clock frequency varies by about 1.5MHz in 10MHz. However this range is not all needed and only a positive and negative swing of about 2V is produced by IC₄₆. A swing of 600kHz in the master clock changes the instrument pitch by one semitone.

Control-converter register IC₃₁ is also used to read the keyboard. The prototype keyboard has a built-in digital encoder provided with a 14-pin di header connector. A five octave C-to-C keyboard has 61 notes which therefore need a six bit address.

The six least-significant bits of IC₃₁ are fed to the keyboard and a single bit is returned. Numbering the lowest C as zero, if the key addressed by the six-bit number is pressed, then a logical one is returned, otherwise a zero. The bit is buffered by IC₂₃ onto the Q-bus so that it appears as bit one in the 8088 status port.

Digital-to-analogue conversion, interfacing and audio processing are main functions of this final section of the digital polyphonic keyboard. Midi i/o is on the middle right. The Q data bus links to both the 8088 control processor section described in the November issue and the t.t.l. bit-slice processor described in October.



- Notes:-
- (1) Apart from Midi interface, connector pin numbers refer to 25 pin D types
 - (2) All op-amp -V_e supplies to -5V rail
 - (3) Use liberal 100n ceramic capacitors for supply decoupling

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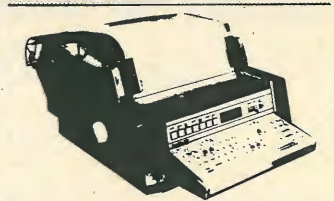
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Fast Fourier transform

September's program, by Dyvik and Larsen, needs a correctly-formatted random access data file to work on

Some readers have vainly tried to run the program listed in September's issue*, according to one 'even shifting the program down to &E00'. I must admit that this thought never struck me in writing the text, or else I should have explicitly warned against the waste of effort! True, the program appears deceptively short, being some 3563 bytes long in published form. However, it is extremely greedy on memory for its syn-

*Fast Fourier transforms using a microcomputer, by G.K. Dyvik & T. Larsen, pages 80-2.

damic variables, due to all the arrays involved. After compacting, it occupies some 2030 bytes and even so the PAGE must be lowered to &1300, which is as low as it can be set catering for the necessary random access files to be used.

The compacted version of the program represents a nearly illegible piece of listing, which is why we didn't use it instead of the full-blown listing in the first place. In contrast to some other methods of compacting — which translate Basic keywords into their internal equivalents

— the present compacted version is still completely Basic-compatible on the highest level: the compacting has been effected only by removing unnecessary spaces, REMs, line numbers, using shorter names for variables, functions and procedures (implying that the version will tolerate a certain amount of tampering even after compacting. But please note that the few open spaces left in the program lines proper are crucial).

The FFT listing needs a correctly formatted random access data file to work on, containing successively the number of samples to be treated (256), and the actual numbers representing the (256) data to be input. We assumed that such a file could be generated by most readers with a little knowledge of random access files, but in case of difficulty I have prepared a tiny data-generating Basic-program for this purpose, see FUNCTN listing.

A disc containing the compacted version is available*. In addition to FFTCOMP, it contains the data files referred to in the article (five files: F.EXAMPLE, and F.EXIDATA to F.EX5DATA) and also a screen dump routine (PRINTER) for Epson MX80/FX80 and compatible printers. To provide enough memory for running the program, PAGE should be lowered to &1300 before loading or chaining FFTCOMP (the penalty for which is that the program becomes corrupted during the execution, and must be reloaded afresh from disc for a new run).

As mentioned in the article, only slight modifications are needed in the program to demonstrate a digital filtering effect as well. I have included a version on the disc that will do this (FFTFILT). Once more, the "PAGE=&1300" bit is

*Please send a formatted disc together with stamped and addressed envelope to the editorial office, envelope marked FFT.

LISTING OF "FFTCOMP"

```

10DIMAA 20:PROCAA:MODEO:PROCCBA:TIME=0:PROCCA:PROCDA:FORIX=1TOLZ:QZ=0:RZ=JZ:FO
RKZ=1TOPZ:XZ=QZ/JZ:SZ=USR(AA) DIV&FFF AND&FF:Z1=R(SZ):Z2=S(SZ):FORMZ=QZTORZ-1:A
1=D(MZ):A2=E(MZ):UZ=MZ+JZ:B1=Z1*D(UZ)-Z2*E(UZ):B2=Z2*D(UZ)+Z1*E(UZ)
240D(MZ)=A1+B1:E(MZ)=A2+B2:D(UZ)=A1-B1:E(UZ)=A2-B2:NEXT:QZ=QZ+2*JZ:RZ=RZ+2*JZ:
NEXT:PRINTAB(42+IZ,18) "-:JZ=JZDIV2:PZ=2*PZ:NEXT:BZ=0:FORXZ=0TONZDIV2:YZ=USR(AA)
)DIV&FFFAND&FF:Q(XZ)=SQR(D(YZ)*D(YZ)+E(YZ)*E(YZ)):IFQ(XZ)>BZTHENBZ=Q(XZ)
350NEXT:PRINTTAB(15,18)STRING$(40," "):TZ=60:VDU5:OZ=2^(11-LZ):K=100/BZ:FORZ=
0TONZDIV2:HZ=INT(Q(ZZ)*K+.5):MOVETZ,160:PLOTT,0,4.2*HZ:IFHZ>EATHENPLOT, -20,40:P
RINTHZ
420TZ=TZ+OZ:NEXT:PROCCA:END
480DEFFPROCCA:LX=8:NZ=256:PZ=1:JZ=NZ/2:FZ=NZ-1:HZ=FZDIV2:GZ=HZDIV2+1:DIMD(FZ),E
(FZ),Q(JZ),R(HZ),S(HZ):VDU23,248,24,24,24,24,24,126,60,24
500INPUTAB(10,10)" Which data file to be used? (RTN) gives F.EXAMPLE "N:C
LS:IFN$=""THENNS="F.EXAMPLE"
550INPUTAB(10,10)" Lower limit for per centage? (RTN) gives 10 "EA:CLS:I
FEA=0THENA=9
570VDU23:8202,0,0,0:ENDPROC
600DEFFPROCCA:PZ=AA:[OPTZ:CLC:TXA:STA&70:LXD#8:.FA:LDA&70:ROLA:STA&70:TYA:RORA:
TAY:DEX:BNEFA:RTS:]:ENDPROC
810DEFFPROCCA:A=OPENINN$:INPUT#A,f:M=0:FORZ%=0TOFZ:INPUT#A,D(Z%):M=M+D(Z%):NEXT
:CLOSE#A:GA=M/NZ:FORZ%=0TOFZ:D(Z%)=D(Z%)-GA:NEXT:FORI%=0TOGZ:R(I%)=COS(I%*PI/128
):NEXT
850FORI%=0TOGZ:S(I%)=R(GZ-I%):NEXT:FORI%=GZTOHZ:R(I%)=-S(I%-GZ):S(I%)=R(I%-GZ)
: NEXT:ENDPROC
930DEFFPROCCA:@%=&020207:F=f/NZ:PRINTTAB(21,2)*** FAST.FOURIER TRANSFORM ***
":PRINTTAB(21,3)" (BBC BASIC/HYBRID VERSION)":MOVE50,150:DRAW1100,150
980MOVE50,150:DRAW50,650:PROCCA(0,0,1279,1023):PROCCA(255,703,1024,831):PRINT
AB(69,27)"FREQ."
1020PRINTTAB(1,11)"AMPL. Freq. at "CHR$(248)":":PRINTTAB(17,18)"COM
PUTATIONS IN PROGRESS":PRINTTAB(43,18)STRING$(LX+1,"*")
1050VDU5:MOVE50,142:PRINT"FREQUENCY INTERVAL:":F:"Hz":VDU4:MOVE256,768:FORXZ=0
TOFZ:DRAWXZ*3+255,768+D(XZ):NEXT:@%=&0302:ENDPROC
1130DEFFPROCCA(rZ,sZ,tZ,uZ):MOVErZ,sZ:DRAWrZ,uZ:DRAWtZ,uZ:DRAWtZ,sZ:DRAWrZ,sZ:EN
DPROC
1190DEFFPROCCA:VDU4:PRINTTAB(25,30)"PUSH <ESC> FOR PRINTER COPY!":TAB(53,11)"Tim
e:":TIME/100"s":VDU5:XZ=200:YZ=200:*FX4,1
1230*FX11,5
1240*FX 12,1
1250*FX229,1
1260MOVEXZ,YZ:GCOL3,7:VDU248:REPEAT:KZ=GET:VDU8:VDU248:IFKZ=27THENGCOL0,1:VDU8:
VDU248:PROCCA:ENDPROC
1290IFKZ=136THENXZ=XZ-2ELSEIFKZ=137THENXZ=XZ+2
1300IFKZ=138THENYZ=YZ-2ELSEIFKZ=139THENYZ=YZ+2
1310IFXZ<54THENXZ=54ELSEIFXZ>1078THENXZ=1078
1320IFYZ<200THENYZ=200ELSEIFYZ>630THENYZ=630
1330MOVEXZ,YZ:VDU248:VDU4:PRINTTAB(30,11)INT((XZ-54)/8*F+.5)" Hz":VDU5:UNTILFA
SE:ENDPROC
1380DEFFPROCCA:VDU2
1410*PRINTER
1420VDU3:ENDPROC
    
```

LISTING OF "FUNCTN"

```

10 REM: THIS PROGRAM DEMONSTRATES HOW DATA FOR TESTING THE
20 REM: "FFTCOMP" PROGRAM CAN BE GENERATED. ANY CONVENIENT
30 REM: MATHEMATICAL EXPRESSION FOR A PERIODIC FUNCTION COULD
31 REM: BE USED, PREFERABLY WITH AN INTEGER NUMBER OF PERIODS
32 REM: FOR THE SAMPLING INTERVAL (TO AVOID "WINDOW" EFFECTS!)
33 REM: THE EXAMPLE SUGGESTED WILL GIVE 5 PERIODS OF A 5 Hz
40 REM: SINE-WAVE WITH SUITABLE AMPLITUDE, ASSUMING THAT THE
41 REM: TOTAL SAMPLING TIME REPRESENTS 1 SECOND.
50
60 REM: ANSWERING "45*SIN(5*X)+15*SIN(15*X)+9*SIN(25*X)" INSTEAD,
70 REM: WHEN ASKED FOR A FUNCTION, WOULD GIVE A COMPOSITE WAVE
80 REM: TRAIN, RESULTING IN FREQUENCY LINES AT 5Hz, 15Hz, 25Hz
90 REM: WITH AMPLITUDES IN THE RATIOS CORRESPONDING TO THE THREE
91 REM: FIRST FOURIER TERMS OF A 5 Hz SQUARE WAVE. ETC,ETC.
100
110 REM: EXAMPLES OF SUITABLE, PIECE-WISE CONTINUOUS FUNCTIONS
120 REM: (LIKE PERFECT SQUARE WAVE, SAW-TOOTH, ETC.) CAN BE
130 REM: GENERATED FROM SMALL BASIC PROGRAMS, TAILORED FOR THE
140 REM: PURPOSE: BY SUBDIVIDING THE ABSICISSA VALUES INTO GROUPS
150 REM: THAT CORRESPOND TO THE CONTINUOUS PORTIONS - WHERE THE
160 REM: VALUES OF THE FUNCTION CAN BE COMPUTED BY SIMPLE MATHE-
170 REM: MATICAL EXPRESSIONS ----
180
190 MODE 0
200 INPUTTAB(10,10)"GIVE YOUR FILE A NAME,PLEASE!" "B$
210 X=0
220 DIM Y(255)
230 PRINT:PRINT
240 INPUTTAB(0,10)"WRITE THE FUNCTION HERE (example: ""50*SIN(5*X)"" ) "A$
250 MOVE 0,400
260 FOR I=0 TO 255
270 Y(I)=EVAL(A$)
280 DRAW Y*200 ,Y(I)+400
290 X=X+PI/128
300 NEXT
310 R=OPENOUT B$
320 PRINT #R,256
330 FOR I=0 TO 255
340 PRINT #R,Y(I)
350 NEXT
360 CLOSE #R
    
```

necessary before running the program. As memory really is at a premium, this program is very skimpy on the text side, but it should be fairly self-explanatory.

While I have been exclusively working with Basic 2 for a long time, it struck me that some readers might still have Basic 1 in their machines. I have

therefore carried out tests to check the compatibility of the program with Basic 1, and it turns out that it needs to have the word OPENIN inserted near the start of line 810. After that, it will run on both Basic versions. (The disc programs would also need the addition of the word OPENIN in line 810 to work with Basic 1, of course.)

The deeper hidden message

from page 34

which leads to the disintegration of competence among the knowledge professionals as well as the prevention of the ingress of new knowledge. Something like this syndrome is needed to explain the survival of Maxwell's Equations for so long.

Moving graphics help to illustrate the subtleties of electromagnetic theory. For information on the availability of videotapes, please write to Ivor Catt, 15 King Harry Lane, St Albans AL3 4AS.

References

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Equitable access to satellite communications

continued from page 66

cedures would be applied in the frequency bands where most FSS networks operate now, namely a bandwidth of 500MHz at 6 and 4GHz and another 500MHz bandwidth at 14 and 11GHz. There was discussion at WARC-ORB as to what changes should be made to the methods used in these bands. No decisions were taken, but the improvements are likely to be selected from among the following:

- (a) The present procedure, based on advance publication of an outline of the technical details of a planned system, followed by multiple bilateral coordination negotiations, is becoming cumbersome to operate. It may be replaced by multi-lateral negotiations which could take the form of a conference every couple of years attended by representatives of every country with an FSS satellite in orbit or with plans for launching within say five years.
- (b) The concept of spectrum segmentation may be developed for reducing the incompatibilities between different networks having satellites which are neighbours in orbit.
- (c) The technical principles of coordination may be simplified in order to make the process of harmonization more amenable to computation in a multi-lateral environment.
- (d) A stimulus might be given to cooperation between countries sharing an arc by the formulation of principles of burden-sharing.
- (e) A more systematic approach may be made to the periodical up-grading of technical standards.

The allotment plan is to be prepared and the new coordination procedures are to be agreed at the second session of the conference, in 1988. Extensive preparatory work will be necessary meanwhile.

Broadcasting

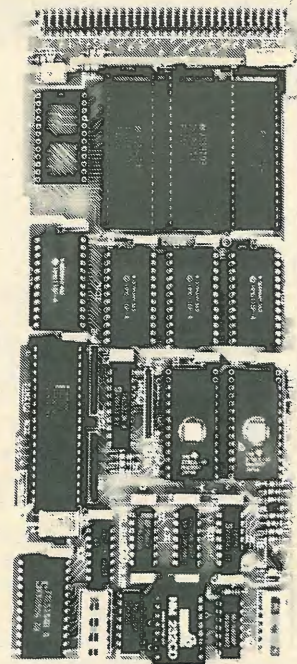
While guaranteeing equitable access to the g.s.o. for all countries was the chief task of this Conference, its other activities, all related to satellite broadcasting, are also interesting. The frequency assignment plan for satellite broadcasting at 12GHz in North and South America was given formal approval. parameters for the links which will be used to feed programme material to satellites broadcasting in other parts of the world were discussed and agreed; a plan for these feeder links is to be drawn up at the 1988 conference. Proposals for sound broadcasting by satellite, suitable for reception on portable or car receivers, were considered; there is a major problem here in that it has not been possible yet to find a suitable frequency band that could be allocated for this purpose but the search continues and the 1988 conference will return to the matter.

Finally, WARC-85 discussed ways of providing for high definition television broadcasting by satellite. In two of the three Regions into which the ITU divides the world there is a frequency band for satellite broadcasting at 22GHz, but this does not include Europe. Now that the 12GHz direct broadcast band is planned world-wide for 625 and 525-line tv, there is a lot of interest, particularly in Japan and USA, in using the 22GHz band for high definition systems needing much more channel bandwidth. This enthusiasm is spreading to Europe and the feasibility of allocating a band at 22GHz world-wide will be discussed again in 1988 after further study of the problems posed by existing services already operating in the band.

David Withers is reporting WARC-85 in greater detail at OnLine's Satellite Communication 85 Conference, at the Tara Hotel, London, Dec 3/4. Proceedings are available from OnLine at 01-868 4466.

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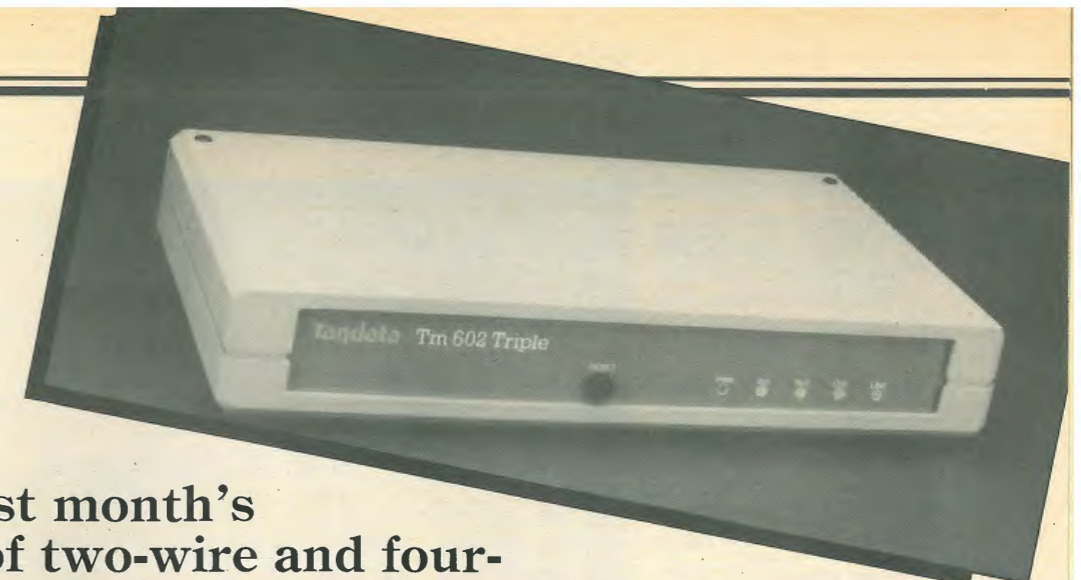
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CIRCLE 80 FOR FURTHER DETAILS.

Modems

To supplement last month's article, a survey of two-wire and four-wire modems for dial-up or leased lines



In this survey we have concentrated on general-purpose modems suitable for use with a wide range of computer systems. For individual computers there are in addition many special-purpose modems: plug-in cards for the IBM p.c. and its rivals, cartridges for the CBM64, firmware-controlled modems for the Sinclair QL, the Amstrad, the BBC Micro and so on.

The modems listed in the table are designed for use on leased lines or over the public telephone network.

Most models listed are CCITT-standard, mains-powered, table-top units for direct connection to the telephone line.

The table shows their basic

specifications, though many of the more expensive modems have advanced features which cannot be described in detail here. A few of the commonest are the following:

- Bell modes, for communication with computer systems in the Americas
- Software control of modem configuration, test routines etc.
- Automatic dialling on the public telephone network (in leased-line modems, for back-up circuits)
- Automatic answering of calls
- Hayes protocol compatibility
- Error detection and correction to protect transmitted data
- Buffering of data to permit use with computers which cannot send and receive at different rates

• Data security: encryption of data in the transmission link

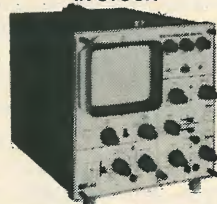
• Rack-mounting options for larger installations.

Another class of modem not included here is the short-haul modem or line-driver, which suits applications where the whole course of the line runs on private property. In such installations, the complexities of a BAPT-specification modem are not needed and the traffic can be handled by a simpler and potentially cheaper device.

Short-haul modems are obtainable for speeds of up to 9600 bit/s and more, and for distances up to a few tens of kilometres.

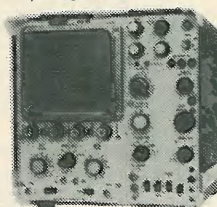
Tandata's new Tm602 modem combines V.21, V.22 and V.23 modes. It has a variety of advanced features such as auto-dial, auto-answer, data buffering and a self-test routine. Software control is by a command string which can include telephone number, data rate and passwords.

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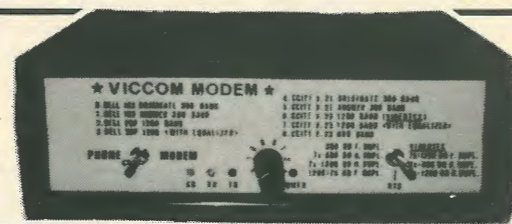
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Communication Receivers. Racal 500KC/S to 30MC/S in 30 bands 1MC/SWIDE — RA17 MK11 £125. RA17L £150. RA17E £200. New Metal Louvred Cases for above £25. All receivers are air tested and calibrated in our workshop — supplied with dust cover — operation instructions — circled — in fair used condition. Racal Synthesizers (Decade frequency generators) MA350B Solid State for use with — MA79 — RA217 — RA1218

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DCE-Interlekt Ltd, 24 Portman Road, Reading RG3 1LU.

Epson (UK) Ltd, Dorland House, 388 High Road, Wembley, Middlesex HA9 6UH.

Ferranti Computer Systems Ltd, Simonsway, Wythenshawe, Manchester M22 5LA.

Gandalf Digital Communications Ltd, 19 Kingsland Grange, Wolston, Warrington WA1 4RW.

GEC Telecommunications Ltd, P.O. Box 53, Coventry CV3 1HJ.
IAL Data Communications, Jays Close, Viabes, Basingstoke, Hampshire RG22 4BY.

IBM United Kingdom Ltd, 389 Chiswick High Road, London W4 4AL.

Jaguar Communications Ltd, 32-34 Upper Marlborough Road, St Albans AL1 3UU.

Kirk Automation, Bridge Works, St White's Road, Cinderford, Gloucestershire GL1 4HV.

Master Systems (Data Products) Ltd, Network House, Stanhope Road, Camberley, Surrey GU15 3BW.

Micom-Borer Ltd, 15 Cradock Road, Reading RG2 0JT.
Micro Scope Videotex Systems, Mill Lane, Taplow, Maidenhead, Berkshire SL6 0AA.

Miracle Technology (UK) Ltd, St Peter's Street, Ipswich IP1 1XB.
Modem House (D.T.M.C. Ltd), 70 Longbrook Street, Exeter EX4 6AP.
Modular Technology Ltd, Zygal House, Telford Road, Bicester, Oxfordshire OX6 0XB.
Pace Micro Technology, 92 New Cross Street, Bradford BD5 8BS.
Racal-Milgo Ltd, Landata House, Station Road, Hook, Basingstoke, Hampshire RG27 9PE.
RS Components Ltd, PO Box 99, Corby, Northamptonshire NN17 9RS.
Steebek Systems, 3 The Paddock, Hambridge Road, Newbury, Berkshire RG14 5TQ.
Tandata Marketing Ltd, Albert Road North, Malvern, Worcestershire WR14 2TL.
Telindus Ltd, 2-4 Oxford Road, Newbury, Berkshire RG13 1PA.
Trend Datalink Ltd, Knaves Beech Estate, Loudwater, High Wycombe, Buckinghamshire HP10 9QX.
Transam Microsystems Ltd, 59-61 Theobald's Road, London WC1X 8SF.

British Standards

Below is listed a selection of British Standard specifications dealing with data communications. Copies of these publications can be bought by post from the Sales Department, BSI, Linford Wood, Milton Keynes MK14 6LE.
BS 5397: High level data link control procedures.
BS6301: 1982. Specification for safety requirements for apparatus for connection to British Telecommunications networks.
BS 6305. Specification for general requirements for apparatus for connection to BT networks.
BS6320: 1982. Specification for modems for connection to the BT public switched telephone network.
BS 6328. Apparatus for connection to BT private circuits.

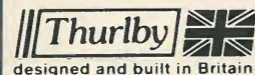
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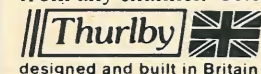
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CIRCLE 35 FOR FURTHER DETAILS.

Digital storage oscilloscopes

from Hitachi
more speed, more memory, more features

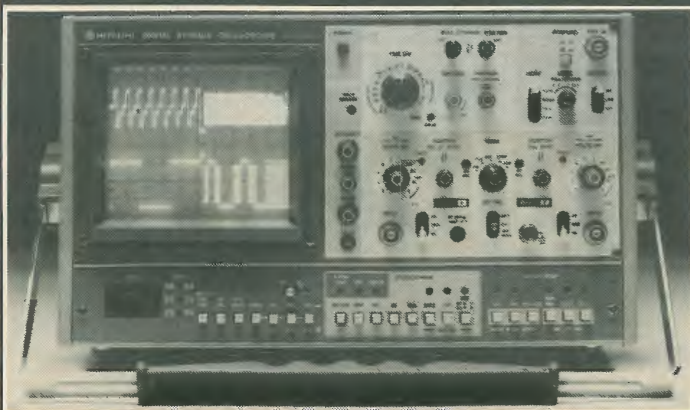
The new Hitachi VC-6041 combines a 40MHz A-D converter with 8K words of acquisition memory to provide ultra high resolution capture of high speed transient events.

A wealth of state-of-the-art features includes signal averaging, roll mode, variable pre-trigger, post storage expansion to X100, digital voltage and time readout, pen recorder output, and a GPIB data option.

If your application is a little less demanding choose the VC-6015, it's the easiest to use digital storage 'scope around. It has dual channel storage at 1MHz clock rate, 2K word memory, variable pre-trigger, X10 post storage magnification and a full pen recorder output, yet it costs only £1,350.

We hold the complete Hitachi 'scope range in stock for immediate availability. Ring us now to get full specifications and prices or to arrange a demonstration on (0480) 63570.

Thurlby-Reltech Instruments, 46 High Street, Solihull, W.Midlands, B91 3TB



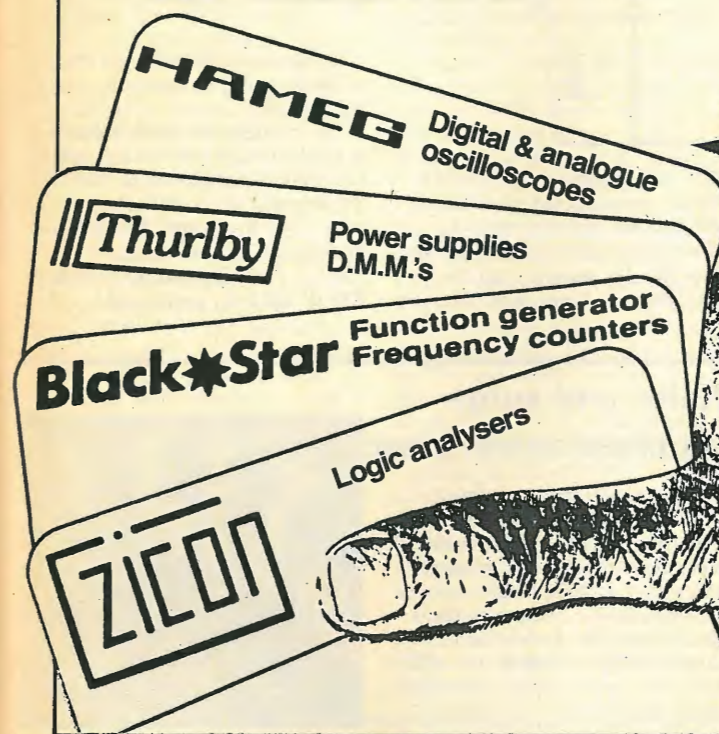
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ELECTRONICS & WIRELESS WORLD DECEMBER 1985

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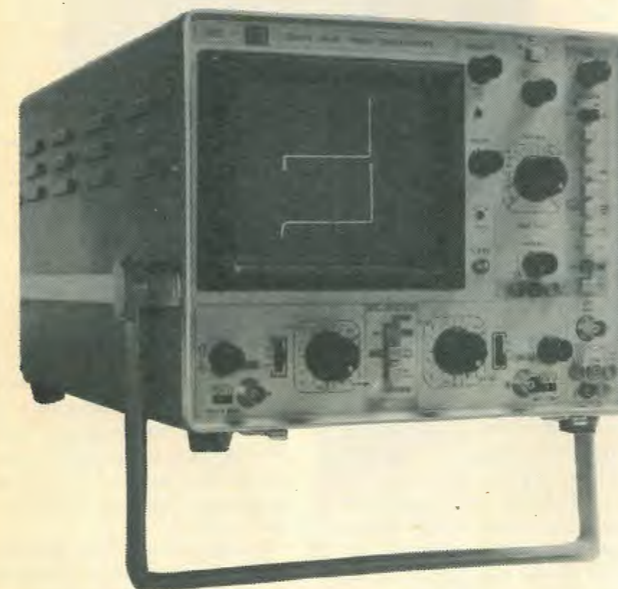
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CIRCLE 24 FOR FURTHER DETAILS.

ELECTRONICS & WIRELESS WORLD DECEMBER 1985

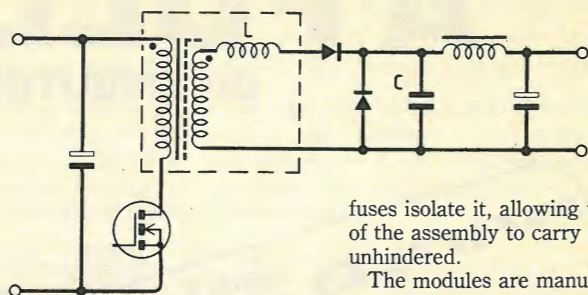
Chocolate-bar s.m.p.s.

If you increase the chopping frequency of a switch-mode power supply, you can make it much smaller by slimming down bulky inductors. The miniature power supply is an appealing thought, but until it has been frustrated by the inevitable increase in switching losses, a major cause of which is leakage inductance in the transformer.

Yet in a novel range of d.c. converters from Vicor Corporation, this undesirable parasitic element (L in the diagram) has been turned into a virtue by resonating it with a

capacitor, C. The result is a forward converter which generates not square waves but half-cycles of sine wave. As the current now rises more slowly at the instant of switching, power loss is greatly reduced. The mosfet switching element of the Vicor module chops at around 1MHz, making it possible to pack the entire assembly, p.c.b. and all, into a module the size of a bar of chocolate. Even at this frequency, an efficiency of 80% is quoted.

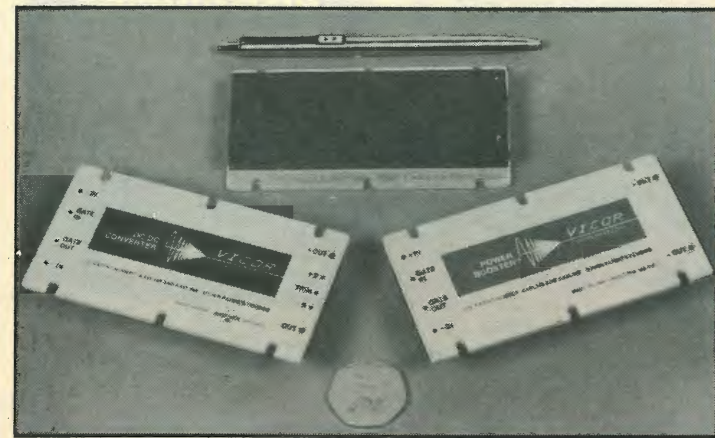
Modules are offered in 50, 75 and 100W versions with nominal input voltages ranging from 12 to



270V and outputs from 5 to 48V. For a.c. points, a simple bridge rectifier and capacitor must be added. For higher powers, 200W slave booster modules can be paralleled up. If one fails, internal

fuses isolate it, allowing the rest of the assembly to carry on unhindered.

The modules are manufactured in the United States but are due to be second-sourced shortly from the Republic of Ireland. One-off price is currently less than \$200. Powerline Electronics Ltd, 5 Nimrod Way, Reading RG2 0ED. EWW 221 on reply card.

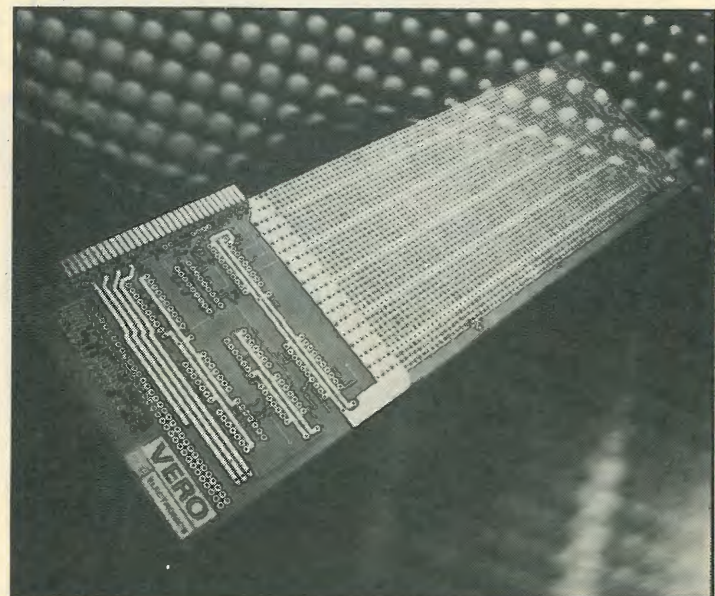


Prototyping board for PCs

Although the number of add-on boards for the IBM PC is increasing almost daily, it may be that you need to develop one for a specific task which is not already made. BICC-Vero have made this a lot easier by providing two prototyping boards which will plug into the PC's expansion slots. One provides normal solder pads for hand wiring, the other provides

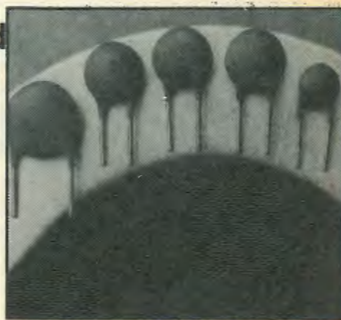
low-profile terminals for use with Vero's Speedwire pen. The use of normal wire-wrap terminals is not possible as there is insufficient space between the expansion slots on the IBM computers. Both boards have plated-through holes.

BICC-Vero Electronics, Unit 5, Industrial Estate, Hedge End, Southampton, Hants SO3 3LG. EWW 213 on reply card.

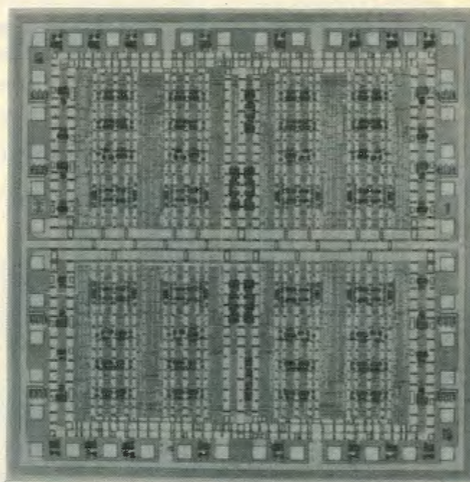


Noise and surge suppression combined

Disc ceramic capacitors are available which incorporate varistors, and can thus provide simultaneous noise and surge protection within a single two-terminal device. Known as the BCV range, the devices have direct voltage ranges of 9 to 25V with the corresponding varistor range of 18 to 45V. Capacitance values are from 2200pF to 0.47µF and the BCV operates between -25 and +85°C at ±20% capacitance tolerance. The smallest in the range has a 4mm diameter while the largest is



10mm wide, all 3mm thick. Murata Erie Electronics (UK) Ltd, 100 Albert Street, Fleet, Hants GU13 9RN. EWW 217 on reply card.



GaAs cell array

Semi-custom circuits of medium-scale integration in gallium arsenide are now possible with the use of a Harris cell array. The HMD-11100 array has the equivalent of 300 gates and can achieve switching rates up to 3GHz, said to be 50% faster than other GaAs cell arrays and five times faster than similar circuits with silicon e.c.l. gates.

The device is a fixed-location array, with 48 AND gates, eight NOR gates, eight D-type flip-flops, six clock drivers/differential

amplifiers, along with 16 input buffers and eight output buffers, all with fixed positions. They are interconnected by two metallic layers which are configured to provide the required functions. Harris claim to be able to provide completed circuits 16 weeks after a customer's layout is received and audited.

Further details from Harris-MHS Semiconductor, Eskdale Road, Winnersh, Wokingham, Berks RG11 5TR. EWW 206 on reply card.

68000 computer with new bus

A superfast British computer runs a Motorola 68000 16/32-bit processor at 12MHz. Gemini Computers, who developed the 80-bus system for Z80-based computers, have continued their philosophy of concentrating on bus-based systems and in Challenger 20 they have devised a new bus called 68K-bus. (VME, Gemini say, offers overkill and is expensive to implement; other buses commit the user to various combinations and sizes of Eurocards that are difficult to house vertically.) The system is based around the c.p.u. backplane design which combines several functions, containing c.p.u., ram, disc control, i/o and of course the backplane for connecting further cards. The bus and cards have been designed so that future 32-bit developments may be incorporated.

Using GM3001 c.p.u. backplane, Challenger 20 also incorporates a floppy disc drive, a 20Mbyte hard-disc drive with controller and power supply. There are four slots for expansion cards, and the bus is

carried through a socket to the outside of the case so that a further backplane may be added or development cards can be plugged in directly. The computer incorporates 512K of dynamic ram, two RS232 serial ports, a Centronics parallel port and a battery-backed real-time clock.

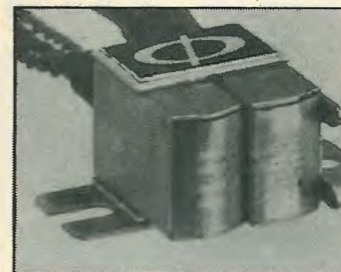
To complement the new computer and promote the 68K-bus, Gemini have a number of additional boards: four graphics circuits based around the Hitachi 63484 c.r.t. controller; a modem, 1 or 2Mbyte ram boards and serial interface boards. Full details of the bus are available in the hope that other manufacturers will adopt it and produce compatible peripheral equipment. The computer is offered with a choice of operating systems including CP/M68K, BOS and the p-system. Gemdos is to be added and a further c.p.u. card is being developed to run Unix.

Gemini Computer System Ltd, Springfield Road, Chesham, Bucks HP5 1PU. EWW 220 on reply card.



Four-channel magnetic tape head

Claimed to be the first four-channel magnetic tape head, the CR44R44P1 audio cassette head is designed and built in Britain by Phi Magnetronics. The tape head allows tape monitoring on each of four independent tracks at the same time, particularly useful in sound mixing systems and communications monitoring equipment. Dimensions of the head conform to EIAJ standards and it can be interchanged with existing heads on the majority of cassette tape transports. Record and replay sections are completely independent, each with a screened



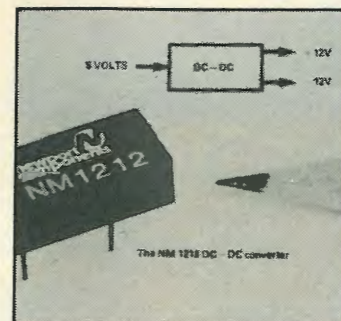
case with head pole gaps optimized for their specific function.

Available from Monolith Electronics Ltd, 5 Church Street, Crewkerne, Somerset TA18 7HR. EWW 216 on reply card.

Miniature d.c. converters

Eliminating the need for multiple power supply systems and complex power bus lines on p.c.b.s, the NM1212 and NM1515 are 5V to ±12V and ±15V converters that provide localised on-board power. Application at the point of load also improves overall performance by reducing noise and decoupling problems.

The package occupies 206mm² of board space and is only 7mm high yet delivers up to 300mW of power. The device operates between 0 and 70°C with no derating of the output current and with 70% efficiency. Newport



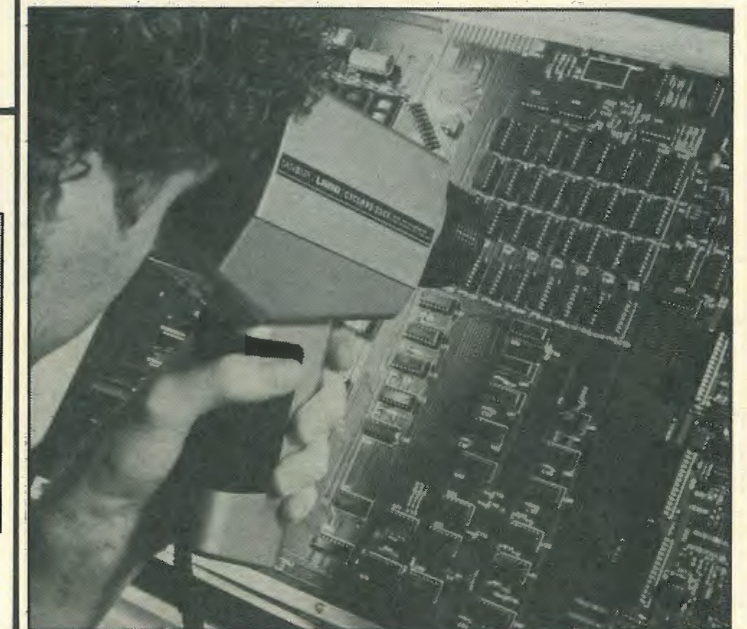
Components Ltd, 134 Tanners Drive, Blakelands North, Milton Keynes MK14 5BP. EWW 215 on reply card.

Infra-red spot thermometer

Cyclops, a range of infrared thermometers from Land Infrared, now includes the Minolta/Land Cyclops 33CF, which is designed to make spot measurements. It uses a fixed-focus Minolta lens of 170mm, and diameter of the measuring circle with the lens in focus is 2mm.

One views the target through camera-type reflex optics, which defines the target area and presents the digital temperature

reading when the trigger is pressed. Range is -50 to +600°C. Readings are continuous, peak temperature hold or valley hold and an internal calculator is able to show mean, maximum or minimum of a series of readings, which are also presented at an output for connection to a data logger, printer or computer. Land Infrared Ltd, Dronfield, Sheffield S18 6DT. EWW 219 on reply card.



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PC-XT EXPANSION CARDS — STORAGE DEVICES

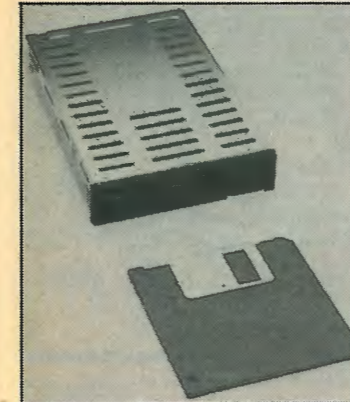
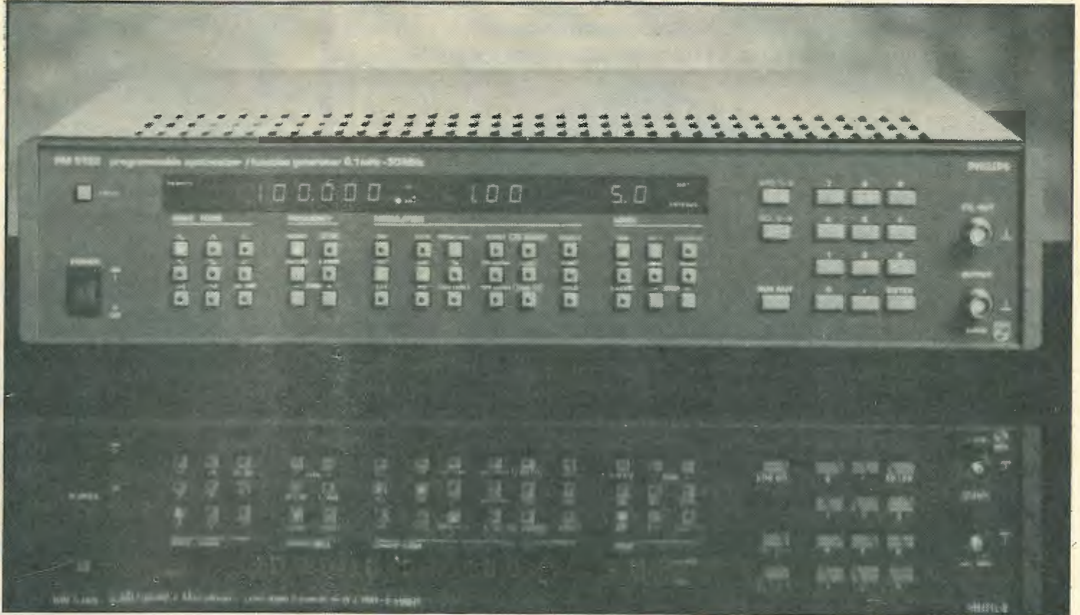
<p>AST: SIX PACK PLUS with 64K & s/ware from £199 MEGA PLUS II with 64K & s/ware from £279 I/O PLUS II & software from £139 MINI I/O software £139 MP MINI (to 384K) with 64K £215 ADVANTAGE (128K to 384K) with 128K £439 PREVIEW (PC/XT/AT) mono & s/ware £269 MONOGRAPH PLUS (PC/XT/AT) & s/ware £365 GRAPH PAK with 64K & s/ware £365 AST-3760 £269 AST-SNA PC £669 AST-BSC PC £519 AST-PC OX £639 AST 5251 £585 CC-223 £239</p> <p>HERCULES: HERCULES graphics card £299 HERCULES colour card £165 INTELLIGENCE UK £599 PC EXPRESS 128K £599 PC EXPRESS 256K £599</p> <p>LAB-MASTER: 12 BIT DATA ACQUISITION £445 Above with 40kHz and prog. gain £1374 8 BIT DATA ACQUISITION £499 64 CHANNEL data acquisition £1799 DATA Acquisition with 80kHz £1889 ORCHID TECHNOLOGY £POA</p> <p>QUADRAM: QUADLINK (emulates Apple II) £479 QUADBOARD II with 64K £274 EXPANDED QUADBOARD 0K £299 QUADCOLOUR £199</p> <p>SATURIN & TITAN TECH: ACCELERATOR PC board £689 ACCELERATOR PC Aux. board £135</p> <p>TECMAR: TECMAR RAM BOARDS—100 numerous please CALL 20020 FIRST MATE with 64K £319 21044 CAPTAIN with 64K £329 20005 SPEECH MASTER voice synthesizer £339 21005 AUXILIARY VOCABULARY for above £39 20015 PROTOZOA prototyping board £65 20017 Extender board for PC and compat £89 20033 AMOEBA prototyping for baseboard £159</p> <p>MEGA Mainboard PC/XT £229 SUPER Mainboard PC/AT £289 512K RAM EXPAND (2 DIP SWITCH) OK £89 Parallel printer card £39 Parallel card with 84k buffer (OK) £109 Monochrome (text) display card £119 PC Express/Intelligent Research 512K £798 Titan Accelerator 128K £329 Titan Accelerator 512K £729</p>	<p>COLOUR/GRAPHICS Card (2 layer) £149 COMPOSITE COLOUR/RGB monitors £149 SUPER COLOUR/GRAPHICS Card (4 layer) £399</p> <p>PC/XT PCAT COMPATIBLE: MONOCHROME GRAPHIC CARD VERSION II £136 single parallel port standard MULTI I/O CARD — 5 WAY III £169 Dual floppy controller interface Asynchronous RS232C serial comms port Parallel printer port, games adaptor Clock/Cal with battery backup £169 EPROM WRITER CARD up to 128K £149 MODEM CARD V21/V23 CCHT AA/AD £169 FLOPPY DRIVE CONTROLLER (4 DRIVES) £75 TEAC FD-55B half ht. 320K floppy drive £119 RS-232 SERIAL (I/O) 1port 50-9600 £49 SERIAL Async RS-232C 2port 50-9600 £69</p> <p>GAMES ADAPTOR £39 AD/DA 12 BIT 16ch-A/D 16ch-D/A £139 8K Cherry Style KEYBOARD £99 TRANS-NET NETWORKING BOARD £399 NetMAIL Software £499 NetSPOOL Software £229 NetDISK Disk Server Software £139 NetDMS Data Management Software £169 NET BOOT ROM for floppyless ops £39 NET STARTER KIT £975 (NOTE: We can supply most of the above as UNPOPULATED boards for OEMs.)</p>	<p>384K MULTIFUNCTION CARD—SIX WAY!!! *Parallel Printer Port *64K to 384K RAM Memory *RS-232C Serial Port *Real Time Clock/Calendar with Battery Backup *RAMDISK & PSPOOL Software *games port Built & Tested £195.00</p> <p>4-LAYER PC/XT MAINBOARD *64K to 1MB ON BOARD *8 Fully Compatible Slots Built & Tested £295.00</p> <p>PC/XT CASE *8-Slot *Hinged lid *Includes hardware £85.00</p> <p>IRWIN TAPE DRIVES—IBM COMP 110 - 10MB INTERNAL £415 125 - 20MB INTERNAL £499 145 - 40MB INTERNAL CALL 310 - 10MB EXTERNAL £599 325 - IBM AT 20MB EXTERNAL £699 IRWIN TAPE DRIVES APRICOT 210 - 10MB INTERNAL £469 225 - 20MB INTERNAL £519</p> <p>PLUS 5 - IBM/OLIVETTI/ERICSSON/APRICOT + SIRIUS FIXED DISK SUBSYSTEMS 10MB £995 20MB £1129 40MB £1899 65MB £2590 FIXED + REMOVABLE SUBSYSTEMS 10MB + 5MB £1895 20MB + 5MB £1995 40MB + 5MB £2845 20MB + 20MB £5445 PLUS NET HOST ADAPTOR KIT £325 (available for IBM-PC/XT, IBM-AT, APRICOT, SIRIUS, EPSON QX10, ERICSSON, OLIVETTI M24, SANYO and most compatibles.)</p>
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Unit M, Charwoods Business Centre,
Charwoods Rd, East Grinstead, W. Sussex RH192HH
Tel (0342) 24631 Telex: 957418

CIRCLE 77 FOR FURTHER DETAILS.

Slimline disc drive

Claiming to be the world's slimmest is the Epson SMD-280 drive for 3.5in floppy discs. At only 25mm high it is suitable for portable computers and with a power requirement of only 1.6W at 5V may be used with smaller, and simpler power supplies. It offers an unformatted capacity of 1Mbyte with a track-to-track access time of 3ms. It has a chucking hub for centering the disc and a drive pin structure (patent pending) to ensure accurate index pulse timing. Epson (UK) Ltd, 388 High Street, Wembley, Middlesex HA9 6UH. EWW 207 on reply card.



Programmable frequency synthesizer/function generator

A programmable or self-contained 50MHz frequency-synthesized signal generator, type PM5193 from Philips, provides eight waveforms in the range from 0.1 to 50MHz. With frequencies referred to a crystal oscillator and settable to within 1 in 10⁶, the instrument can be set to sweep between key-selections. Waveforms provided are sine, positive or negative pulse, square,

triangle, haversine, positive and negative sawtooth. Modulation is internal or external a.m., f.m. or non-phase-related gating, and internal frequency sweep and phase-related burst. Numerical displays indicate frequency setting, modulation, output level and status, and all front-panel settings can be committed to the controller's

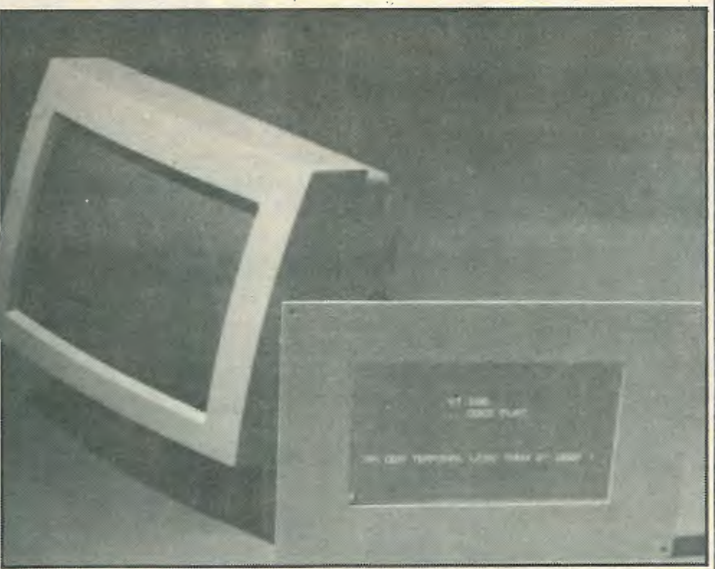
memory when in the IEEE bus mode and recalled by sending a simplified string corresponding to the settings. Up to ten front panel settings can be stored internally in a non-volatile memory for later recall. PM5193 costs £2995, plus vat, and is handled in the UK by Pye Unicam Ltd, York Street, Cambridge CB1 2PX. EWW 218 on reply card.

C-mos 68000 processor runs at 1/10 power

Motorola and Hitachi are jointly manufacturing an advanced c-mos version of the 68000 microprocessor which requires less than 40mA supply current operating with no load at 12MHz. This power reduction means that it is now possible to add the feature of portability to computers.

In consuming less power, the c-mos 68000 is more reliable than its n-mos counterpart. High-density c-mos devices are also less susceptible to damage from static electricity than i.c.s made using earlier c-mos processes, and noise immunity is of c-mos i.c.s is good. Cooler devices however allow moisture seepage more readily than higher-powered ones but this problem is greatly reduced by recent advances in plastics i.e. package design. Lower operating temperatures mean higher integration and smaller end products.

Samples of ceramic dil and pin-grid-array c-mos 68000 processors are available and mass production is expected to start in the first quarter of 1986. By the second quarter, plastic dil and chip-carrier types will be available. Initially prices will be about twice those of equivalent n-mos parts, eventually falling to about 20% higher. EWW 223 on reply card.



Slimline terminal

A flat-screen display that is 51mm thick offers full emulation of the VT100 terminal. The high-resolution d.c. plasma panel has 640 by 400 picture elements with a high-contrast orange display, completely free from flicker. All the usual terminal facilities are available including double-height and/or double-width characters on a display format of 80 columns by

24 + 1 lines. The screen may be split and it is possible to give a smooth continuous scroll or jump from page to page. The controller has built-in decoding for both a qwerty keyboard and a touch panel. Power consumption is 25W. From Perdux Components Ltd, Unit 4, Airport Trading Estate, Biggin Hill, Kent TN16 3BW. EWW 208 on reply card.

Transputer evaluation systems

Evaluation boards, holding a 32-bit Transputer, memory and two RS232 ports, are available from Hawke Electronics and Rapid Recall and range in price from £1700 to £2100 (see News for further details). The Transputer, designed to run in multiprocessor applications, is capable of executing ten million instructions per second and each instruction is only one byte long, which makes programs compact. A language called Occam specially designed to exploit the Transputer's concurrent processing capabilities has been in use since 1983.

Link adaptors consisting of a single c-mos i.c. for linking the Transputer to standard eight-bit peripherals cost between £30 and £42 and development software for Stride 440 or Vax computers is £3000. Further boards for connecting to the IBM PC at £2900 with 1Mbyte memory or £3300 with 2Mbyte memory are expected to be available in November as is development software for the PC costing £3000. EWW 222 on reply card.

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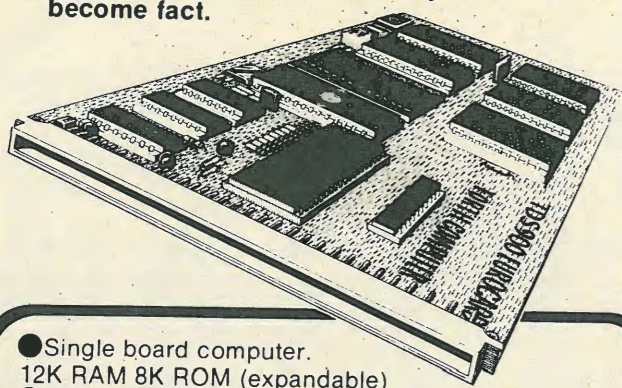
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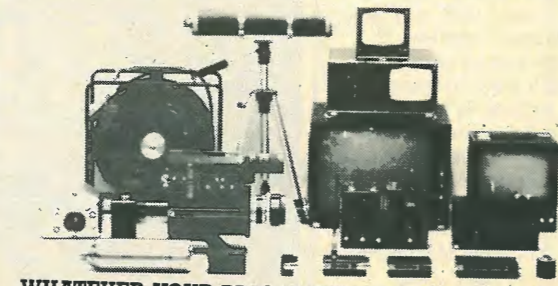
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BOOKS

Computer-controlled radio

Continued from page 44

Thirdly, and potentially the most serious of all, the manual aerial tuning potentiometer - which controls varicap diodes in the front end - requires different settings for different frequencies and different supply voltages. The only answer to this would seem to be either direct control of the varicap voltage by the computer, perhaps from a d-to-a converter with a software feedback loop to find the optimum tuning point, or by switching a number of preset potentiometers across the existing control. The second option is likely to give better results although it has not been tried.

For these reasons no attempt has been made to calibrate the apparatus. It would of course be possible to provide, for example, a crystal oscillator rich in harmonic output and previously

calibrated against an r.f. voltmeter or spectrum analyser. Harmonics close to the frequencies of interest could be used as a reference for arithmetic correction of the measured readings prior to data storage. However, whilst this would undoubtedly make the equipment more useful, it seems to be moving away from the original cheap and simple concept. In many cases it is sufficient to detect the presence of a transmission without worrying about its absolute strength.

Because data is stored on disc, it is a relatively simple matter to dump it via a modem down an ordinary telephone circuit. To do this, the remote computer must be re-configured to "auto-boot" a simple RS423 terminal program on power-up (or after the inevitable power cut!). The strapping of a link on the keyboard

achieves this and is well documented on page 80 of the disc system user guide (ref. 3). Proprietary software such as COMMSTAR may be used at both ends with a short interrogation and control program at the local computer to use error-correction protocols. Taking readings at two-second intervals about 40K of data is generated in 18 hours, which dumps down in as many minutes at the end of the day. Typically, such files are received without detectable errors at 300 baud.

Such a system has been experimentally tested at Radio Botswana with great success. It is possible to send direct commands and even modifications to the data logging program to the remote computer (example programs are available on request). Thus the program can be run and stopped at will and manual intervention is not needed at all at the remote end.

BBC designs

Continued from page 6

through major projects by the transmission group alone are likely to exceed £3M against a cost of just £1.85M. Furthermore, these figures do not take account of BBC Engineering's involvement in the BBC Microcomputer project, which has brought the corporation more than £12M in royalties.

A BBC idea newly launched commercially is Datacast, an information system which makes use of spare capacity in the teletext lines. Datacast could be useful to organizations such as financial institutions or bookmakers who need rapid nationwide distribution of data. The extra information stream is fitted into packets 29 and 31 of the teletext signal, where it is inaccessible to ordinary teletext sets. But the BBC's demonstration receiver, an Acorn teletext adapter with special software, showed the speed (1500 characters per second) and the very low error-rate possible with the system.

If Datacast turns out to meet a need, transmissions could be greatly expanded by the introduction of full-field teletext during the night.

On the microelectronics front, Designs Department has adopted the TMS 32010 microprocessor for digital audio signal processing. This device is fast enough to perform useful tasks in real-time. Especially attractive to the audio designer is its built-in hardware multiplier, which can multiply two 16-bit numbers in 200ns, making the chip suitable for a variety of uses in mixers and filters.

Other developments in digital audio have continued with further work on the BBC's Nicam-3 encoding equipment. It has turned out that the l.s.i. chip produced for encoding digital stereo sound-in-synchs is fast enough to code six channels, with help from a Z80. So savings will be possible in the cost of expanding the p.c.m. radio distribution network and of replacing the original 13-channel linear p.c.m. equipment.

Books

The BBC Microcomputer in Control: Interfacing Projects for Beginners, by Paul Beverley, Nigel Eames and Geoff Osborne. Prentice/Hall International, 150 pages, soft covers, £7.95. The BBC Micro is still the best computer for the experimenter; and with plans for a variety of simple projects, the Beeb buffs of Norwich City College show you how to get started. Their selection includes relay drivers and power switches, a sound-to-light system, a logic probe and a slot-car timer, all with relevant software in BBC Basic. An interesting section on communications explains how to modify a mains-borne intercom system to make it carry computer data. There is a chapter on construction techniques for those new to the soldering iron. Irresistible Christmas present for computer kids.

Practical repair and maintenance of communications equipment by Albert D. Helfrick. Prentice/Hall International, 308 pages, soft covers, £15.40. Trouble-shooting methods for radio communications hardware of all kinds, professional and amateur. With the help of many illustrations, the author describes the circuit elements found in modern equipment, the faults to which they are prone and the instruments you can use to examine and repair them.

Radio Database International - Part 1: International Broadcasting Edition, edited by Lawrence Magne and Tony Jones. International Broadcasting Services, 240 pages, soft covers, ISBN 0-914941-01-1.

A remarkable achievement: a channel-by-channel, hour-by-hour plan of the high-frequency broadcasting bands. Some 200 pages of neat computer-generated charts list the occupants of each channel and the source, target area, language and power of each transmission. The information is compiled from actual monitoring observations by a worldwide team of contributors and includes details not to be found in published schedules, such as clandestine stations and jamming. At the back of the book is a review section describing a dozen top-of-the-range h.f. receivers.

For the enthusiastic listener, the charts make a valuable adjunct to the well-established World Radio TV Handbook. But they do not replace it, since they offer no direct way of tracing the individual broadcaster's schedules.

Part 2, of 59 pages (ISBN 0-914941-02-X), covers the tropical bands from 2247kHz to 5720kHz. The parts are published separately but can be obtained together at \$12.95 (plus \$1.95 for surface postage outside North America or \$8.95 for air mail): International Broadcasting Services Ltd, P.O. Box 300, Penn's Park, Pennsylvania 18943. There are plans to revise the guides annually.

The Advanced Disk User Guide for the BBC Micro by Colin Pharo. Cambridge Microcomputer Centre, 446 pages, ring bound, £14.95. Uniform in style with the same publisher's Advanced User Guide, this new volume covers the Acorn disc filing system with the same authority and attention to detail.

Early chapters cover the Acorn disc format, the 8271 floppy disc controller and the command list and memory usage of Acorn's DFS 0.90. Forming the core of the book is a lengthy reference section which describes the many DFS system calls one by one and illustrates most of them with example programs - information which will be of enormous value to the machine-code programmer.

A section on file-handling techniques explains the possibilities of index files and hashing algorithms and provides a useful introduction to the theory and practice of database construction.

Also included are listings of several disc utility programs, accompanied by full explanations of their workings: an overlay program, a special formatter to produce combined 40/80-track discs, a program to recover deleted files and a sector-editor.

The original Acorn DFS, which forms the bulk of the book's subject matter, is undoubtedly one of the weak points of the BBC machine, possibly the weakest. Despite this, the author says hardly anything about disc filing systems from competing suppliers.

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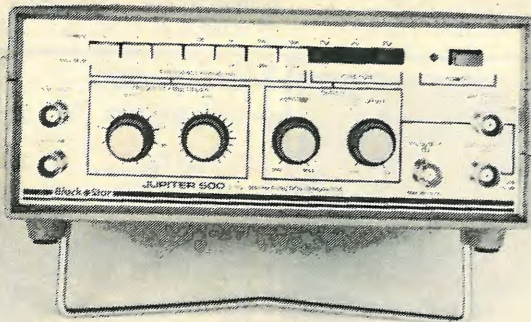
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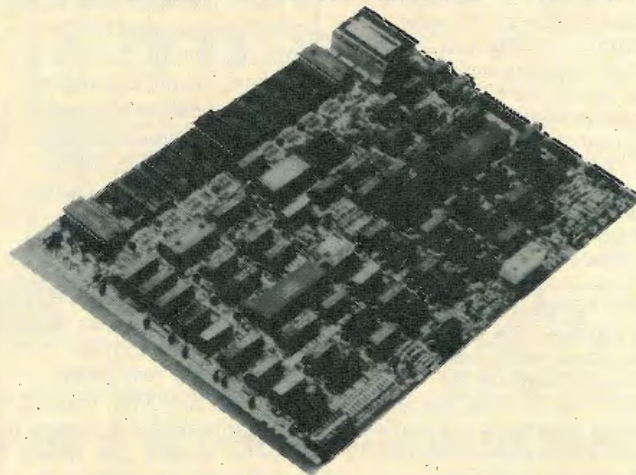
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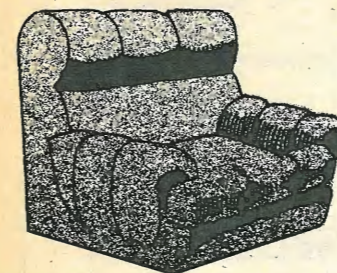
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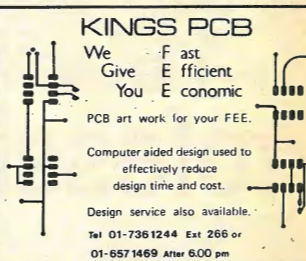
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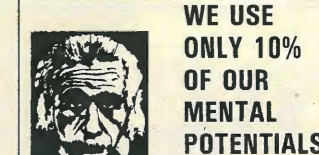
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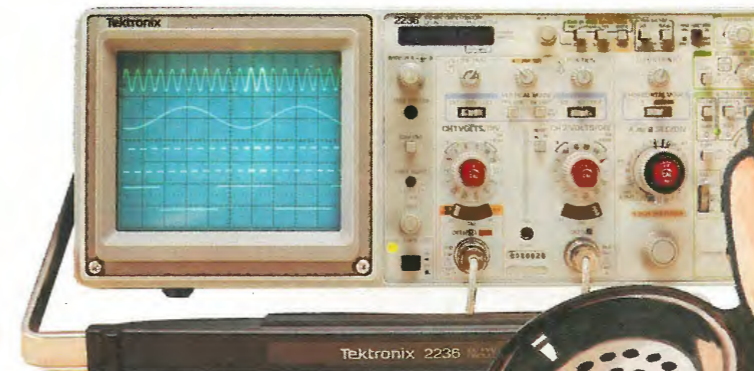
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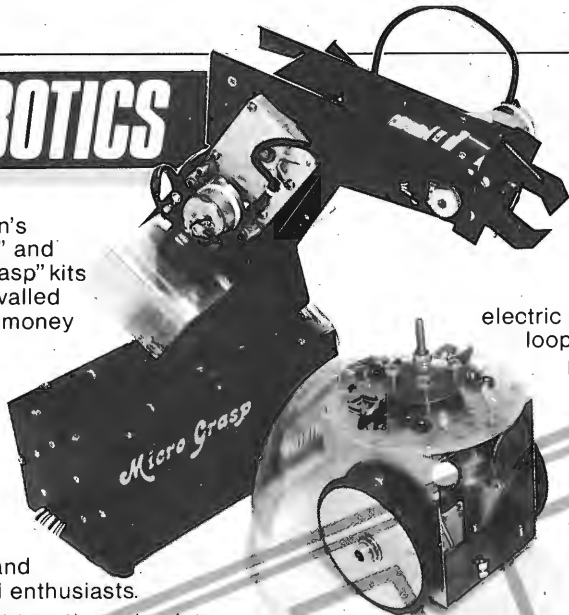
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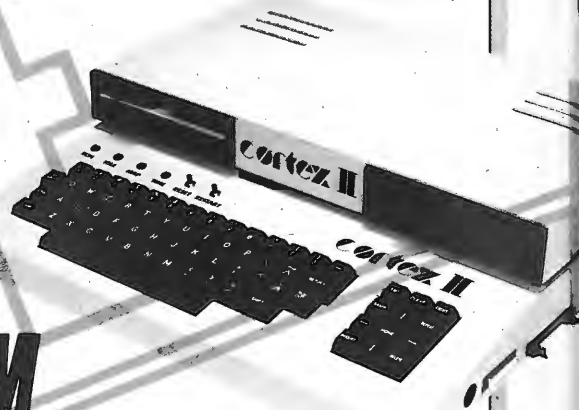
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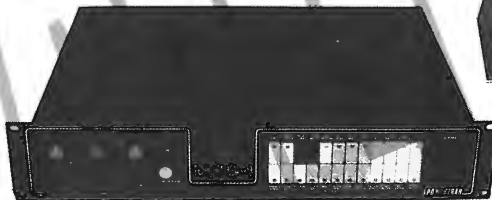
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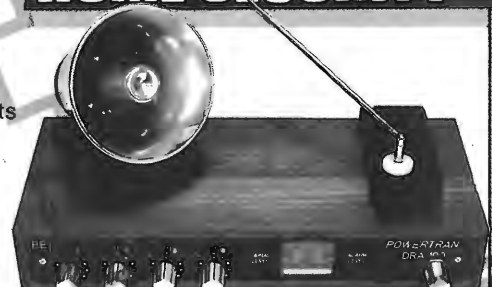
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