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BYTE

APRIL 1989

A McGRAW-HILL PUBLICATION

REVIEWS

Wells American CompuStar 286

2 Color Scanners

Extend

Mac Presentation Software

PRODUCT FOCUS

BYTE Lab Tests 12 UPSes

33 MHz

SIA's new 386/32

33-MHz rated, 32.5-MHz clock, special cooling, fast caching

IN DEPTH

CASE

PLUS

Discus Rewritable

TRON

Sharp Wizard

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0360-5280

Laser System 150, 15 pages per minute: $5,995.
Laser System 80, 8 pages per minute: $3,295.
Laser System 60, 6 pages per minute: $2,195.

All Dell laser printers come with 1.5 MB RAM, full-page 300 DPI graphics, and have 31 standard fonts (7 resident and 24 downloadable from diskette). Dell laser printers also provide Hewlett-Packard LaserJet Plus,* Epson/FX,* IBM Proprinter* and Diablo 630* emulations.

THE DELL SYSTEM 310 20 MHz 386.

The best combination of performance and value available.

STANDARD FEATURES:

- Intel 80386 microprocessor running at 20 MHz.
- Choice of 1 MB or 4 MB of RAM* expandable to 16 MB using a dedicated high speed 32-bit memory slot.
- Advanced Intel 82385 Cache Memory Controller with 32 KB of high speed static RAM cache.
- Page mode interleaved memory architecture.
- VGA systems include a high performance 16-bit video adapter.
- Socket for 20 MHz Intel 80387 or 20 MHz WEITEK 3167 math coprocessor.
- 5.25" 1.2 MB or 3.5" 1.44 MB diskette drive.
- Dual diskette and hard disk drive controller.
- Enhanced 101-key keyboard.
- 1 parallel and 2 serial ports.
- 200-watt power supply.
- 8 industry standard expansion slots.

OPTIONS:

- 20 MHz Intel 80387 math coprocessor.
- 20 MHz WEITEK 3167 math coprocessor.
- 1 MB or 4 MB RAM upgrade kit.
- 2 MB or 8 MB memory expansion board kit.

***Lease for as low as $141/Month.*

System 310	With Monitor & Adapter			
Hard Disk Drives	VGA Mono		VGA Color Plus	
	1 MB RAM	4 MB RAM	1 MB RAM	4 MB RAM
40 MB 28 ms	$3,899	$5,199	$4,199	$5,499
90 MB 18 ms ESDI	$4,699	$5,999	$4,999	$6,299
150 MB 18 ms ESDI	$5,199	$6,499	$5,499	$6,799
322 MB 18 ms ESDI	$7,199	$8,499	$7,499	$8,799

THE DELL SYSTEM 220 20 MHz 286.

It's fast as most 386 computers. But at less than half the price. The footprint is small, too.

STANDARD FEATURES:

- 80286 microprocessor running at 20 MHz.
- 1MB of RAM* expandable to 16MB (8 MB on system board).
- Page mode interleaved memory architecture.
- LIM 4.0 support for memory over 1 MB.
- Integrated diskette and VGA video controller on system board.
- Socket for Intel 80287 math coprocessor.
- One 3.5" 1.44 MB diskette drive.
- Integrated high performance hard disk interface on system board.
- Enhanced 101-key keyboard.
- 1 parallel and 2 serial ports (integrated on system board).
- 3 full-sized industry standard expansion slots available.

OPTIONS:

- External 5.25" 1.2 MB diskette drive.
- 3.5" 1.44 MB diskette drive.
- Intel 80287 math coprocessor.
- 1 MB or 4 MB RAM upgrade kit.

***Lease for as low as $86/Month*

System 220	With Monitor	
Hard Disk Drives	VGA Mono	VGA Color Plus
One Diskette Drive	$2,299	$2,599
40 MB 29 ms Hard Disk	$2,999	$3,299
100 MB 29 ms Hard Disk	$3,799	$4,099

THE DELL SYSTEM 200 12.5 MHz 286.

This full-featured 286 computer runs at 12.5 MHz, and is completely Microsoft MS-DOS and MS OS/2 compatible.

STANDARD FEATURES:

- 80286 microprocessor running at 12.5 MHz.
- 640 KB of RAM expandable to 16 MB (4.6 MB on system board).
- Socket for Intel 80287 math coprocessor.
- 5.25" 1.2 MB or 3.5" 1.44 MB diskette drive.
- Dual diskette and hard disk drive controller.
- Enhanced 101-key keyboard.
- 1 parallel and 2 serial ports.
- 200-watt power supply.
- 6 industry standard expansion slots.

OPTIONS:

- Intel 80287 math coprocessor.
- 512 KB RAM upgrade kit.
- 2 MB RAM upgrade kit.

***Lease for as low as $93/Month*

System 200	With Monitor & Adapter	
Hard Disk Drive	VGA Mono	VGA Color Plus
40 MB 40 ms	$2,499	$2,799
40 MB 28 ms	$2,699	$2,999
90 MB 18 ms ESDI	$3,499	$3,799
150 MB 18 ms ESDI	$3,999	$4,299
322 MB 18 ms ESDI	$5,999	$6,299

Performance enhancements (Systems 325, 310 and 220): Within the first megabyte of memory, 384 KB of memory is reserved for use by the system to enhance performance.

APPLICATION SOFTWARE.

We offer a complete line of software. Everything from complex CAD/CAM applications to fun flight simulator programs. All at extremely competitive prices.

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Dell Enhanced Microsoft MS-DOS 4.0: $119.95
(Both MS-DOS versions with disk cache and other utilities.)
Dell Enhanced MS* OS/2 Standard Edition 1.0: $324.95
Dell UNIX* System V/386, Release 3.2:
Available 3/31/89. Call for details.

Technically speaking, the System 325 is the most advanced 386™ computer we've ever built. And, according to PC Magazine, it's one of the most advanced 386 computers they've ever tested.

In benchmark after benchmark, the Dell System 325 25 MHz ran circles around a field

A PERSONAL COMPUTER THAT'S REALLY PERSONAL.

Of the more than 150,000 personal computers we've sold to date, each one's been individually configured to fit the needs of its owner.

The System 325 takes that idea to its logical extreme.

For example, it runs either MS-DOS,® OS/2, or our own Dell UNIX® System V. Which is compatible with AT&T's System V Interface Definition. And the world of XENIX® applications.

If speed is of the essence, we can include an optional Intel®

THE DELL 386 SYSTEM 325 HAS A 25 MHz CLOCK RATE, CACHE MEMORY CONTROLLER, 90 MB 18ms ESDI DRIVE, PAGE MODE INTERLEAVED MEMORY, AND 100% COMPATIBILITY WITH MS-DOS, OS/2 AND UNIX SYSTEM V.

of 386-based systems. A field that included the Compaq® 386/25.

A show of prowess that earned the System 325 PC Magazine's Editor's Choice award.

It was a goal we set for ourselves from the very beginning. And an objective anyone with a penchant for power and performance can appreciate.

80387 or WEITEK 3167 math coprocessor. And since nothing about this system is lightweight, the standard mass storage is a 90 MB ESDI disk drive. Or we can configure it with a 150 or 322 MB unit.

As you might expect, the output is just as intense. You can choose between VGA mono with paperwhite screen, or VGA Color

DOT MATRIX PRINTERS.

Printer System 800: $699.95
Our highest resolution text and graphics, 24-pin dot matrix printer. Draft quality at 200 cps. Letter quality at 66 cps. Parallel and serial interfaces. Wide carriage.

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LASER PRINTERS.

SO HOW COME YOU NEVER CALL?

THE DELL SYSTEM 325 25 MHz 386

PC MAGAZINE EDITOR'S CHOICE

"The new top-of-the-line Dell System 325 is a flagship worth putting out in front of the fleet."
February 14, 1989

STANDARD FEATURES:

- Intel 80386 microprocessor running at 25 MHz.
- Choice of 1 MB or 4 MB of RAM* expandable to 16 MB using a dedicated high speed 32-bit memory slot.
- Advanced Intel 82385 Cache Memory Controller with 32 KB of high speed static RAM cache.
- Page mode interleaved memory architecture.
- VGA systems include a high performance 16-bit video adapter.
- Socket for 25 MHz Intel 80387 or 25 MHz WEITEK 3167 math coprocessor.
- 5.25" 1.2 MB or 3.5" 1.44 MB diskette drive.
- Dual diskette and hard disk drive controller.
- Enhanced 101-key keyboard.
- 1 parallel and 2 serial ports.
- 200-watt power supply.
- 8 industry standard expansion slots.

OPTIONS:

- 25 MHz Intel 80387 math coprocessor.
- 25 MHz WEITEK 3167 math coprocessor.
- 1 MB or 4 MB RAM upgrade kit.
- 2 MB or 8 MB memory expansion board kit.

***Lease for as low as $228/Month.*

System 325	With Monitor & Adapter			
	VGA Mono		VGA Color Plus	
Hard Disk Drives	1MB RAM	4MB RAM	1MB RAM	4MB RAM
90 MB-18 ms ESDI	$6,299	$ 7,599	$6,599	$ 7,899
150 MB-18 ms ESDI	$6,799	$ 8,099	$7,099	$ 8,399
322 MB-18 ms ESDI	$8,799	$10,099	$9,099	$10,399

Disclaimer: All systems are photographed with optional extras, which some computer retailers won't even recognize.

BEST OF ALL, YOU WON'T HAVE TO EXPLAIN TO A COMPUTER RETAILER WHAT ALL THAT MEANS.

Plus, for high resolution colors displayed on a larger screen.

Even though the 325 gives you all this performance, it still leaves you six open slots for whatever else you might want to add.

And once you've told us what you want, we'll make sure what you want works—by burning-in the entire system unit.

COMPUTER RETAILERS ARE NO KNOWS.

In all probability, the average computer retailer won't have any understanding what makes the System 325 go.

He will, however, be quite aware of the fact that he could add a 35% markup if he could sell it in his store.

Which he can't.

Because we sell direct.

Meaning you now have the unique opportunity to talk directly with a computer expert. And ask things like, "What's the difference between IDE and ESDI?" Or, "How much SIMM RAM should I add?"

In other words, the kinds of questions you should be able to ask a retailer, but usually can't.

So as you might suspect, dealing direct not only saves you the 35% markup, but 100% of the aggravation.

WE COME WHEN WE'RE CALLED.

One of the things that very clearly sets a Dell system apart from other computers is not just how they're sold, but how they're supported.

Overkill was one description used in a recent PC Week article.

Perhaps.

But then, we think you'll agree, when something goes wrong, you want as much help as possible, right?

Which is why every Dell system comes with a toll-free technical support line and self-diagnostic software. We're able to solve 90% of all problems right over the phone. The other 10% receive next-day, deskside service.

And you get all this help for a full year—whenever you need it—at no extra charge.

As you've probably guessed, one of the things that drives us most is customer satisfaction.

So we'd like to give you the ultimate guarantee: Try a System 325 in your office for a month. Run your toughest applications. Put it through its paces, at your pace. If you're not completely satisfied, send it back anytime within 30 days. And we'll refund your money.

No questions asked.

MAYBE YOU SHOULDN'T BUY ONE AFTER ALL.

No matter how many reasons we give you to buy a Dell system, sometimes it makes more sense to lease one instead.

Whether you need a single computer, or an entire office full, our leasing plan is just like 100% financing. So you don't tie up working capital. Or credit lines.

Of course, there can be tax advantages as well.

And just as we can custom configure your computers, we can fit a lease plan to the exact needs of your business. A fact that has not gone unnoticed. Especially by the Fortune 500. Over half of whom now own or lease Dell systems.

We welcome your business, too. Just call us, toll-free. And don't be afraid to ask us the tough questions.

That's the part we like best.

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BYTE

APRIL 1989 VOL. 14/NO. 4

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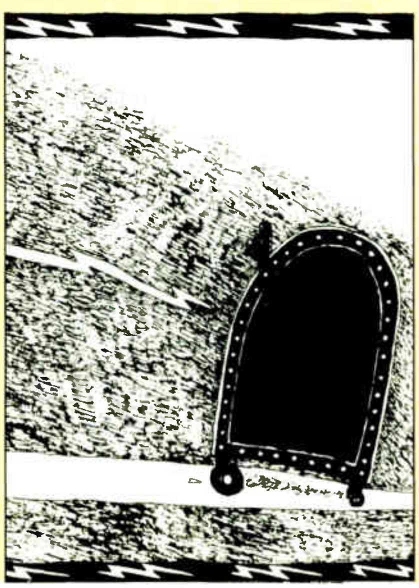

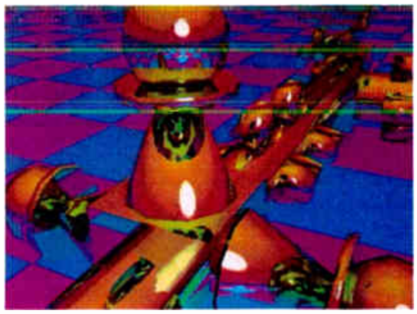

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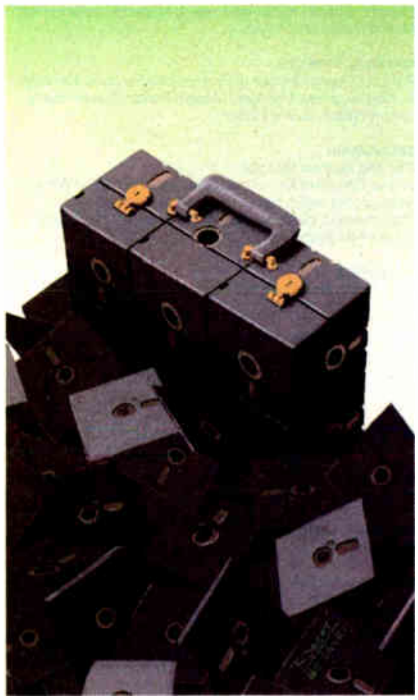
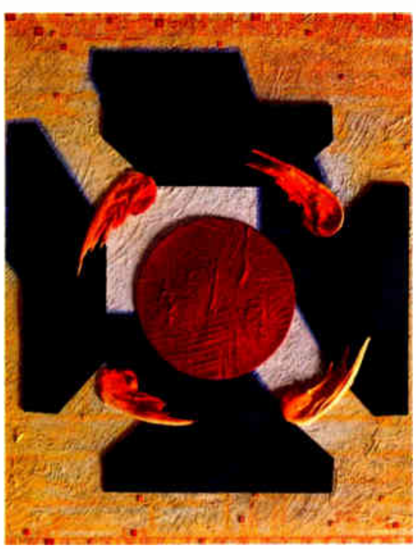
IN DEPTH

FEATURES



CASE/206



TRON/292

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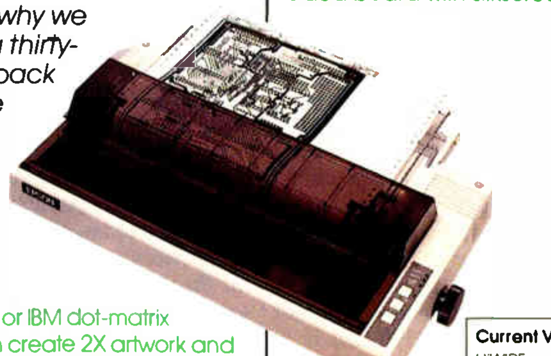

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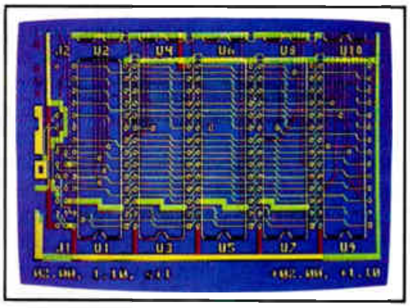
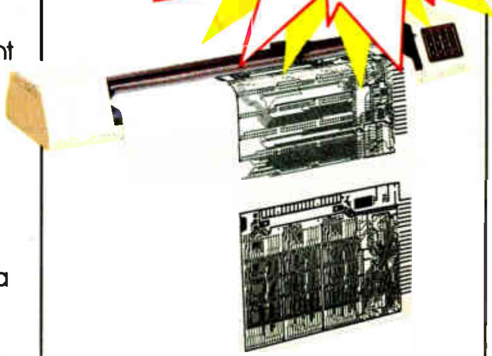


2" by 4" section of a 10" by 16" double-sided board with silkscreen layer

Current Versions

HiWIRE	V1.1r1
smARTWORK	V1.3r5



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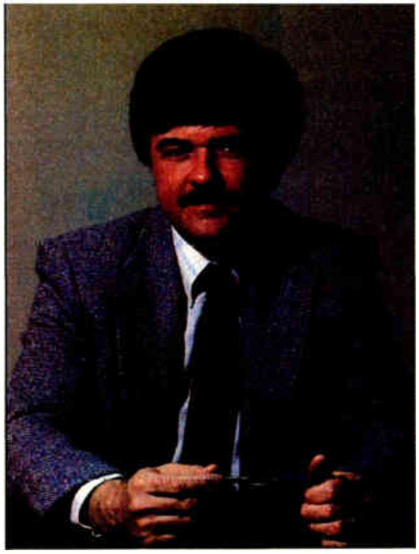


OPEN EVERYTHING

GUIs, grails, embedded processors, and one-station radios

We frequently get visitors here at BYTE. People come to demonstrate new products, to find out more about our editorial content and policies, and sometimes just to meet the folks who put out the magazine. It's always a pleasure to talk to the visitors, and it's always good to exchange information and gain insights on what's happening in the industry.

Sometimes, these insights arrive in unexpected ways.

For example, after a tour of our Lab and editorial facilities, one recent visitor was puzzled at the large number of different computing environments he'd seen: SunView, Windows, the Macintosh's Desktop, OS/2's Presentation Manager, Open Look, Next Step, and Open Software Foundation's Motif all were either up and running, or had been running and were now visible as slides and screen shots lying around various offices.

"They all look the same," the visitor said. "It's getting hard to tell them apart."

Indeed. Oh, there are real, substantive differences among these environments. The internal variances are enough to turn a systems programmer prematurely gray. And as for on-screen differences, well, these three interfaces have three-dimensional highlighting of the user-selectable buttons, this one has tear-off menus, that one has virtual pushpins. . . .

But focusing on these differences is equivalent to "looking at the trees," while our visitor was commenting on a forest of remarkably uniform vegetation. From an end user's perspective, the differences among the top-of-the-line graphical user interfaces (GUIs) are overshadowed by the many similarities.

In some very real ways, today's best GUIs have attained the long-sought grail: They are alike enough to be generally familiar, even at first sight. Most people (certainly most BYTE readers) can sit down and start poking their way around one of these advanced GUIs without a lot of trouble. No, these GUIs aren't perfect, but they've gone a long way toward knocking down the walls separating different machines and operating systems.

This breaking down of walls has profound implications for the hardware and software we'll be using in the future. Even so, it's only part of an even larger trend toward openness. To continue the analogy, just as the walls are breaking down, so are the ceilings.

The old distinctions between workstations and microcomputers have totally vanished. You can't tell the difference anymore between a high-end microcomputer and a low-end workstation because there is no difference, as our 50-page supplement on personal workstations last February indicated.

And, just as the walls and ceilings are breaking down, so are the floors: A computer is no longer just the box on your desk. Computers are also powerful processors invisibly embedded in numerous products, from laser printers to network controllers and beyond. Our fundamental definition of "what is a computer" will soon have to change.

I'm not talking about the glorified digital clock mounted in a Mr. Coffee: Intel says that as recently as last year, 30 percent of all top-of-the-line 80386 chips were installed in products not traditionally thought of as "computers." (Motorola tells a similar story.) The demand was so strong that Intel designed a variant of the 80386 just for embedded applications; it's called the 80376 and is a close cousin to the 80386SX.

So, the walls, the ceilings, and the floors of computing are all vanishing. What's left?

A new openness. In fact, an astonishing openness. Some of it is so blatant that it hits you over the head: Open Token, Open Look, Open View, Open Link, Open Software Foundation, and Open Systems Interconnect; Open this and Open that.

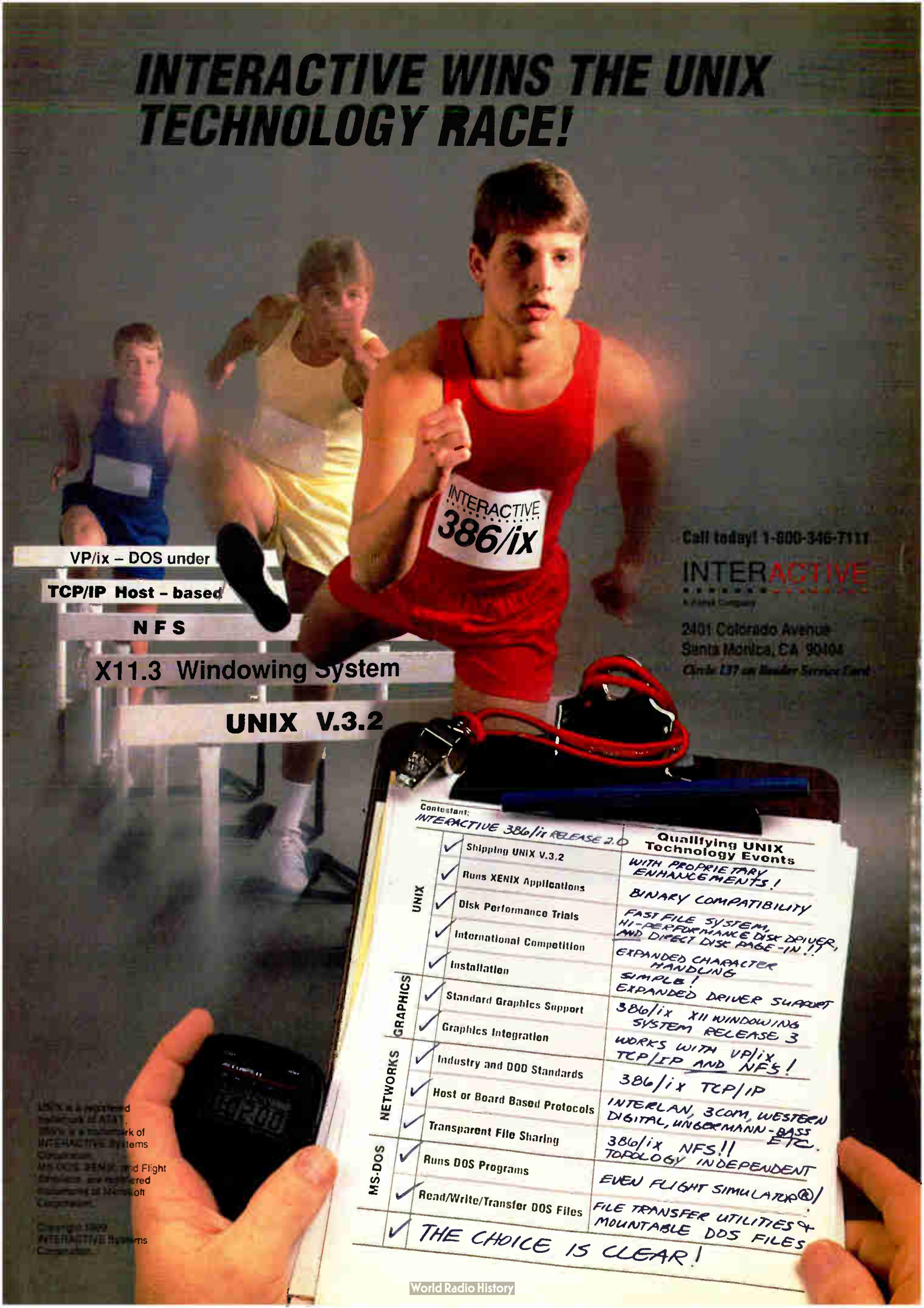
A bit more subtly, there are hardware developments like EISA (Extended Industry Standard Architecture), which will try to keep at least a piece of the 32-bit bus market open; and software developments like the wide acceptance of XWindows, SQL (Structured Query Language), RenderMan, and PostScript and its clones—which, while not fully open, serve somewhat the same purpose by being *lingua francas* of the microcomputing software world.

The trend to extreme openness in official and de facto standards is happening faster and spreading far more widely than most people predicted. And there's no slowdown in sight—standards themselves are starting to merge.

For example, XWindows is gaining three-dimensional graphics by merging with PHIGS—the Programmer's Hierarchical Interactive Graphics System, itself an internationally accepted standard for three-dimensional graphics. This hybrid of the two separate standards will be bundled, along with other extensions to XWindows, in a package called PEX.

So, the trend is crystal clear: Nowadays, the old, easy, one-brand or one-standard solutions to microcomputing problems make about as much sense as one-station radios: You may get that one station very well, but you'll be missing a world of other, possibly better, options. To master today's mixed microcomputing environment, you need information on the full spectrum of possibilities—and that's what BYTE is all about. There's no room for closed minds in today's open environments.

—Fred Langa
Editor in Chief
(BIX name "flanga")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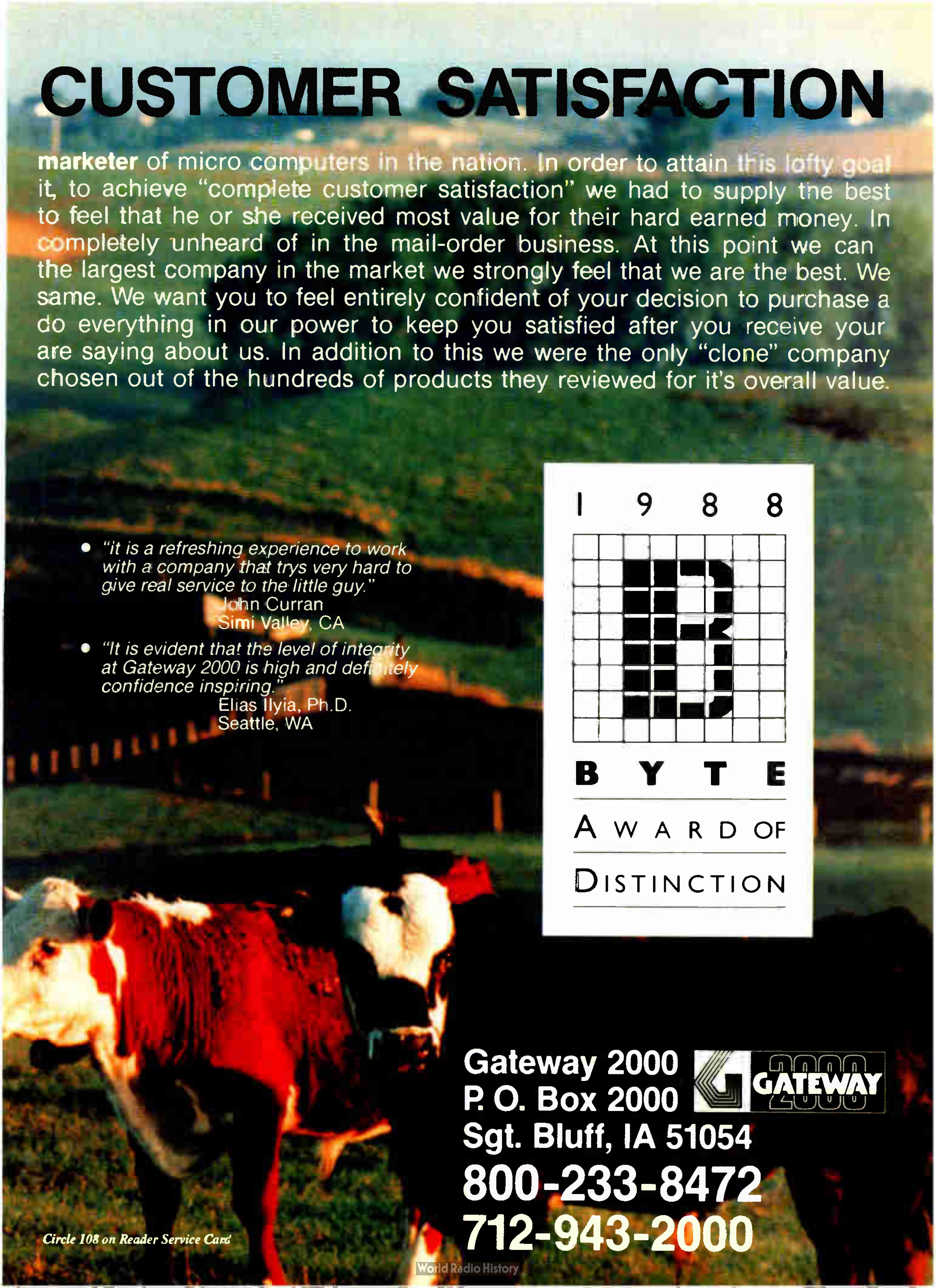
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MICROBYTES

Staff-written highlights of developments in technology and the microcomputer industry, compiled from Microbytes Daily and BYTEweek *reports*

Intel's New 80860 CPU Aims to Be a Cray on a Chip

Amid all the speculation over Intel's forthcoming 80486 chip were rumors that it was powerful enough to become the heart of a minicomputer or even a future mainframe. Now it looks as though the subject of those rumors wasn't the 80486 after all, but another new Intel CPU—one that's designed to be a microprocessor version of a Cray supercomputer.

The new 80860 (or i860) came out from under wraps at the end of February, but engineers at the IEEE International Solid-State Circuits Conference in New York got a sneak preview two weeks before the official debut. Code-named "N-10" while under development, the 80860 is a 64-bit RISC CPU containing one million transistors and running at up to 50 MHz.

Most current RISC processors use several chips; SPARC CPUs, for example, use five different chips, and Motorola's 88000 consists of a three-chip set, including instruction and data caches. But the 80860 has them all on a single chip. One-third of the CPU consists of the data and instruction caches; another third handles integer instructions; and a final third comprises the floating-point adder and multiplier units. Intel says the on-chip caches allow a data transfer rate of nearly 1 gigabyte per second, far faster than is possible with external cache chips. And the three separate FPUs can work in parallel, meaning as many as 120 million operations per second at 40 MHz.

Still more interesting, according to members of the 80860 design team, is the fact that the FPUs are modeled after Cray's pipelined vestor architecture. When a series of identical floating-point operations is performed on a collection of data, the chip doesn't have to wait for the first calculation to finish before beginning work on the second calculation. The FPUs are designed to work in stages, so they perform each part of a lengthy floating-point operation separately and then send the result down the pipeline to the next stage. (The separate adder and multiplier can even be configured to automatically send the results of one FPU to the other for more processing.) That translates into extraordinarily fast vector processing. Intel claims that the 80860 runs at better than 85,000 Dhrystones per second, which would be about twice as fast as the next quickest RISC chip available (one company's 33-MHz version of Sun's SPARC chip has been clocked at 40,000 Dhrystones per second).

The chip also includes three-dimensional graphics hardware for Gouraud and Phong shading. It's as a graphics coprocessor that Intel will initially market the chip, and the company expects to see it appearing by late this year on add-in boards for graphics workstations.

Fabrication on the 80860 is 1-micron static CMOS, which means you can stop the clock without losing anything. Intel already has FORTRAN-77 and C compilers for the chip, and Unix version 4 is supposed to be ready by the end of the year. When we spoke with Intel right before press time, the company was quoting quantity prices of $750 for end-of-the-year delivery.

Does the introduction of the 80860 mean that Intel is abandoning the world of 80x86 processors? Hardly. The 80486 is scheduled to be out by the end of this year, and Intel officials believe that the IBM PC–compatible line still has plenty of life in it. The 80860's future may be as part of a superminicomputer, with multiple 80860 CPUs running Unix; such a machine could show up sometime next year. But that doesn't mean the new chip couldn't show up in personal computers, as well; an 80860 and 8 megabytes of RAM would easily fit on an IBM PC AT-style plug-in card. Cray-in-a-box, anyone?

New "Mainstream" Mac II: Three NuBus Slots, Less Filling

Last month, Apple Computer introduced yet another member of its growing family of Macintosh II systems, a model that CEO John Sculley described as the "mainstream" machine of the Macintosh II "modular product line." This one, called the Macintosh IIcx (at least that's how Sculley referred to it when Apple gave us an early look), features three NuBus expansion slots

continued

NANOBYTES

• Mach Three: Unix software house **Mt Xinu** (Berkeley, CA) plans to develop versions of Carnegie-Mellon University's **Mach multiprocessing operating system** for three workstation environments: **Sun-3, DEC VAX, and IBM RT PC**. Mt Xinu says its versions will be binary-compatible with Berkeley Unix 4.3, will adhere to "standard interfaces such as Posix, and will also incorporate Carnegie-Mellon's Andrew software and MIT's X Window system." Releases are slated to start early next year, the company says.

Mach acquired some commercial glamour last October when Steve Jobs introduced the NeXT computer, which incorporates the Mach kernel. (For more on Mach, see the November 1988 BYTE cover story on the NeXT system.)

• The jaded ones who said they saw nothing new at the recent **MacWorld Expo** in San Francisco must have missed the **WristMac**, a Seiko-built digital watch that **connects directly to a Mac serial port via a special cable** for uploading or downloading information.

The watch holds up to 80 two-line displays of 12 characters each, which you can edit on either the Mac or the watch. Push a button, and the WristMac will cycle through up to a dozen files you've

continued

NANOBYTES

created. Each file can contain up to 79 entries. Files can be timed alarms that display a message, names or phone numbers, or any other text the user enters or downloads. It's $225 for the standard black WristMac. A $295 "executive version" comes with a water-resistant metal case, gold buttons, and a one-piece metal wristband; it's glitzy, but no one will mistake it for a Rolex. From Ex Machina, of New York City.

• **Hitachi Ltd.** of Japan has developed a **semiconductor laser** that acts as a light source for "very **high-speed optical** communications systems." The laser has a proprietary new structure that comprises an "organic insulating film embedded in the semiconductor element," a spokesperson said in an interview with Microbytes Daily.

The semiconductor laser operates on a single line spectrum that allows it to transmit 16 gigabits of information per second over long distances "without waveform distortion," the spokesperson said. The laser is seen as a key component in the development of a high-speed optical transmission system to link geographically distributed computers. The new semiconductor also has its place in the ongoing development of video phones and high-definition TV (HDTV) image transmission systems.

• **Microtec Research** (Santa Clara, CA) has a new **high-level debugger for the Zilog Z80 processor**. The

continued

instead of the six slots available in the Mac II and Mac IIx. The Mac IICx has the same 16-MHz Motorola 68030 processor as the Mac IIx; and we found no difference in performance between it and the Mac IIx. However, the new system comes in a smaller box—the footprint is about 12 by 15 inches. Although it's more compact, the Mac IICx's logic board has the same circuitry as the Mac IIx, including the 256K-byte single in-line memory module ROM and the Floppy Disk High Density internal floppy disk drive controller, which can read and write MS-DOS, OS/2, and Apple II 3½-inch floppy disks.

The newest Mac features a modular design reminiscent of the IBM PS/2 series. The cover, power supply, and drive housings have snap fittings made of high-strength plastic, so you can disassemble the entire machine in a few minutes. The machine has a quiet 90-watt power supply (in contrast to the 220-W power supply of the Mac II and IIx) and two 3½-inch bays for a floppy disk drive and a hard disk drive (the Mac II and IIx have 5¼-inch bays that can also be used for 3½-inch drives). Other changes include an external DB-19 serial port for connecting an external floppy disk drive and an auto-restart capability so that the machine automatically reboots in the event of a power outage.

At press time, Apple had not yet specified prices or configuration options for the Mac IICx. However, Sculley said the Mac IICx would cost roughly the same as the Mac IIx (basic unit, $6969; with an 80-megabyte hard disk drive, $7869). Sculley said it's roughly an equal trade-off to swap three slots and a small box for the six slots and larger footprint of the Mac IIx.

Apple also brought out a new 15-inch portrait-oriented gray-scale monitor with 640- by 870-pixel resolution, and a 21-inch gray-scale monitor offering 1152- by 870-pixel resolution. Both come with a Mac II video card that can be configured with either 2-bit or 4-bit pixel depth (allowing display of 4 or 16 shades of gray, respectively).

Apple also announced a 160-megabyte 5¼-inch hard disk drive (Model 160SC) for the Mac II and IIx. The 160SC does not fit into the new Mac IICx's hard disk bay.

We'll have more details and photographs of the newest Mac in the May issue of BYTE.

"Location Transparency" Next Hurdle for Database Technology

They don't care where it is; they just want to get there. Database users say their biggest concern is access to data—data scattered across several computers that don't talk to each other. A few computer companies are firing up their efforts to make access an easy, invisible process. Most of these companies are focusing on the mainframe level, but many of the techniques they develop will wind up on microcomputers within the next few years. IBM, for example, is working on the concept of "location transparency" for its DB2 relational database system, says Jnan Dash, manager of Data Systems Strategy at IBM.

Location transparency would mean that an end user could access data without knowing where the data resides on the database network, which could include machines running AIX, OS/2, and IBM's mainframe VM operating system. An engineer at an airplane manufacturer working on an AIX-based CAD workstation, for example, would be able to transparently call up some data from an IBM 370-based mainframe running VM.

The basis for location transparency is the idea of the distributed database, which is finally starting to become a real technology. The client/server model is "old hat," according to DEC product architecture manager Barry Rubinson. Rubinson says "interoperation" in a "mixed vendor shop" will become the standard environment in the next few years and that database vendors will have to provide the tools that allow their products to transparently access data stored under other vendors' database products. The key, he says, will be the standardization of Structured Query Language. The current chaos in SQL standards will change in the next few years, he says. "Right now, SQL usually means you have a SELECT statement in your database language." However, Rubinson predicts that a standard for SQL will emerge by 1991.

Performance is the least concern of database software buyers, according to vendors, corporate planners, and consultants at the recent DB/Expo in San Francisco. What buyers are most concerned with is the ability to work transparently with diverse and remote systems and to have a data typing scheme that is fluid enough to handle complex objects like airplane diagrams, digitized photos, and recorded speech.

"Mixed environments

continued

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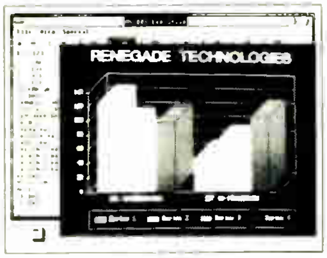

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NANOBYTES

XRAYZ80 package includes a compiler, an assembler, a software simulator, and a debugger. In addition to the vendor's own simulator package, the debugger supports in-circuit emulators from Hewlett-Packard, Applied Microsystems, and ZAX. The IBM PC version starts at $3500; the solo debugger starts at $1750.

- **NEC** (Tokyo) plans to start offering samples of a **33-MHz μPD70632 MPU** this fall and a 40-MHz model next year. A 45-MHz version has been tested, and the company hopes to develop 50- and 60-MHz versions next.
- Guides to Words and Street Corners: **Highlighted Data** (Washington, DC) has put out two of the more **helpful CD-ROM products** we've seen yet. The first is a **version of Webster's Ninth New Collegiate Dictionary**, which not only includes all the text and diagrams in the hard-copy version, but **also talks**. The disk contains recorded pronunciations of each word in the dictionary. The second CD-ROM can generate almost any street map of the U.S., the vendor says. The company claims that the **Electronic Map Cabinet** disk includes every street corner in the country. Both disks cost $200.
- **Farallon Computing** (Berkeley, CA) plans to extend its reach into high-speed Ethernet systems, company officials say. Due for a midyear release, Farallon's **EtherTalk** is an implementation of Ethernet protocols for AppleTalk networks. Like Local-

continued

are the real environments," says Martin Sprinzer of Relational Technology. "We must get to data no matter where it resides," and this process should be transparent to the user. For corporate database users, that means transparent no matter what the location, data format, operating system, or user interface.

Distributed databases and gateways are seen as part of the answer. But as database guru C. J. Date puts it, there are some very difficult problems associated with distributed systems. To maintain data integrity in a distributed system, for instance, various data replication schemes have evolved. All of them require a tradeoff, said Date, between data protection (during retrievals or updates) and performance. An emerging technique called *snapshots* essentially takes a "picture of the database" that can be used in read-only mode while other users can continue using the database. Snapshots might be promising, Date says, because they offer "some of the functionality of replication but without some of the headaches."

Another important advance we heard forecast at DB/Expo involves extensions to the relational database model, which will allow for object-oriented data, such as a document data type or a drawing or image data type. Experts at the conference also said I/O performance of transaction processing is speeding up considerably, approaching 2.5 I/O cycles per transaction (the rate has traditionally been 10 to 20 I/O cycles per transaction, according to Rubinson). Relational Technology redid the architecture on its DBMS this past year, says Sprinzer, and the company will be releasing products over the next two years that will let users "write an application once on VMS and then access it transparently on Unix." Oracle's Ken Jacobs said his company wants to "make GUIs [graphical user interfaces] invisible. We're working very hard to make a product look like Presentation Manager, the Mac interface, or Open Look." Expect announcements "very soon," he said, that point toward the "integration of data dictionaries and computer-aided software engineering tools into one thing."

Most vendors and consultants agree that the database environment isn't all that mixed at the top. IBM has the mainframe world sewn up with DB2. "Anybody who'd compete against DB2 is nuts," said one panelist at DB/Expo.

Superconductivity Still a Cool Proposition

Superconductor research in the past year has advanced incrementally, but researchers have made no major breakthroughs, according to the evidence at the second annual International Superconductor Applications Convention, held recently in San Francisco.

Although one young company called Magnetic Power, Inc. (Sebastopol, CA), claimed to have developed a bismuth thin film with superconducting properties at room temperature, the claim has not been verified or reproduced in other laboratories. Another researcher, Dr. Brian Ahern of the Rome Air Development Center at Hanscom Air Force Base in Massachusetts, claimed to be close to verifying room-temperature superconductivity using titanium boride. However, the evidence was still preliminary and has not been reproduced.

Conference keynote speaker Dr. Simon Foner, chief scientist at the Francis Bitter National Magnet Laboratory, warned against the "overhype" of the media regarding superconductivity breakthroughs and called for more "truth in advertising." Foner said that high-temperature (77° Kelvin) superconductive materials still are not practical for most commercial applications. But Foner also cautioned against the pessimistic fears that the U.S. "will lose to Japan" in the race to produce commercial superconductivity applications.

Most of the research presented at the conference is still focused on a transition temperature range of 4° to 120° Kelvin. The transition temperature is the maximum at which the material exhibits the superconducting properties of virtually zero resistance and high magnetic shielding. Both U.S. and Japanese researchers are working on developing new materials and production methods for superconductivity applications.

A major problem involves increasing the thermal and structural stability of very thin films used in superconductivity applications. Because of the sensitivity to temperature and the large energy density required in superconductive materials, thermal stability and high yield strength are crucial properties. Some of the research going on at companies like General Electric and IBM involves developing high-strength substrates, such as graphite, on which the superconducting film is mounted.

According to some sources, financiers are keeping a cool eye on the super-

continued

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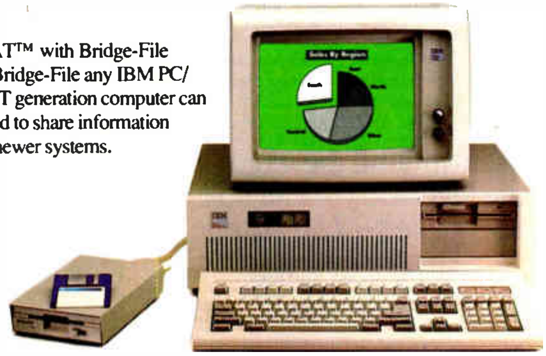

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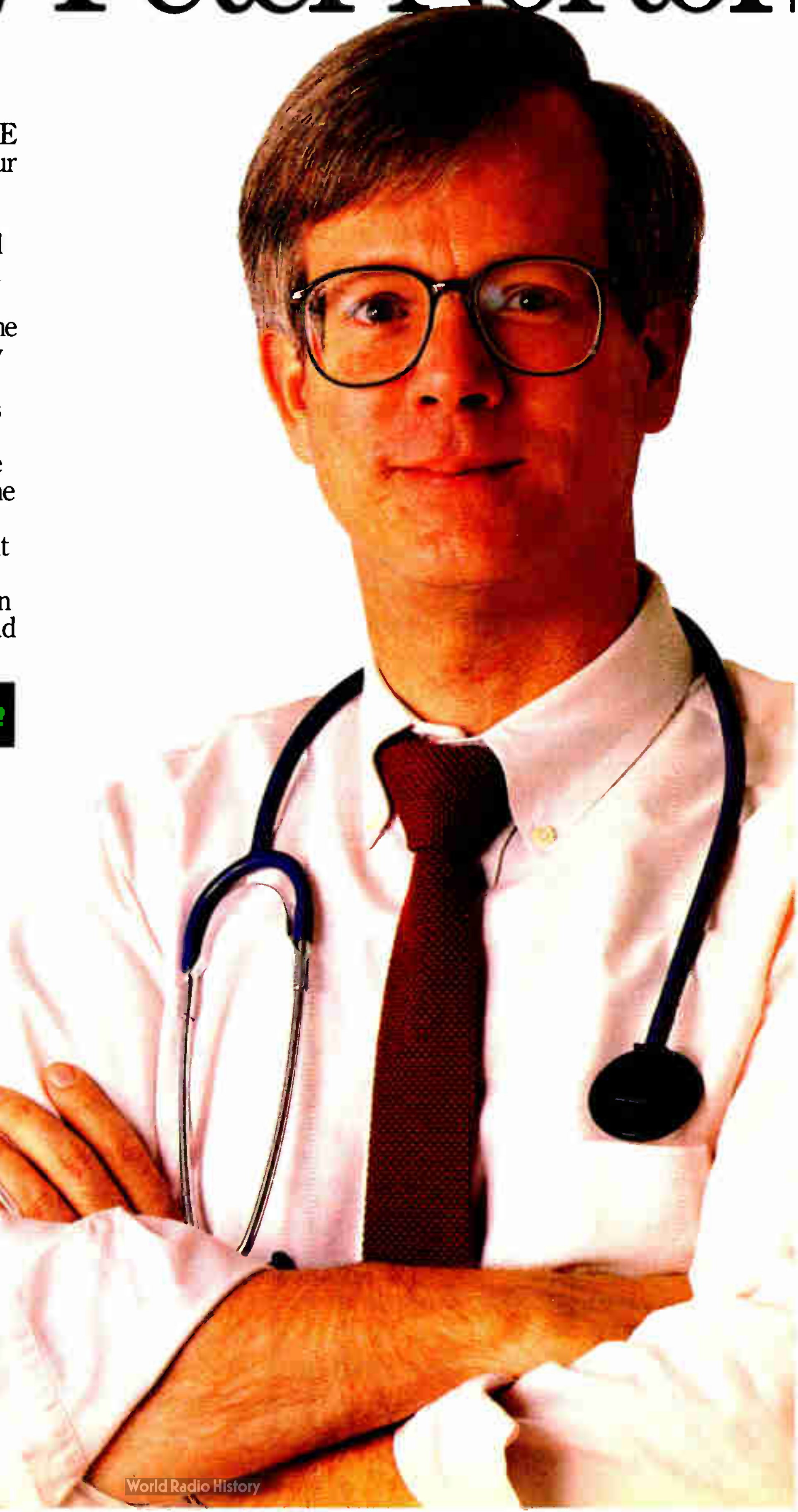

If you've ever seen the words "GENERAL FAILURE ERROR" on the screen of your PC, you know you don't need to be ill to feel sick.

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determine the exact nature of the problem, report it and, in most cases, fix it for you.

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In which case, you'll need to refer to *The Norton Troubleshooter,* a 158-page guide to finding and fixing most anything that could go wrong.

Don't worry, you don't have to go to the bookstore or the library to refer to it, because the Troubleshooter is included in the Advanced Edition.

Frankly, the Norton Disk Doctor and the Norton Troubleshooter are worth the price of the new Advanced Edition all by themselves.

But, of course, they aren't by themselves.

They're accompanied by a wish list of features, functions and enhancements sufficient to satisfy the yearnings of all those people who've been politely writing and calling to request them.

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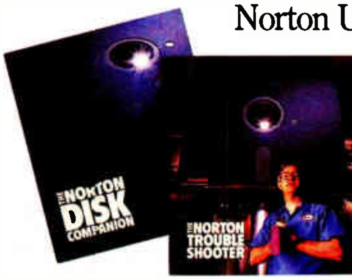

The Norton Disk Companion is included, free, with both the Standard and Advanced Editions. The Norton Troubleshooter is yours with the Advanced Edition.

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Our user interface, which *InfoWorld* said made the Utilities "as easy to use as possible," now comes with pop-up windows and dialog boxes.

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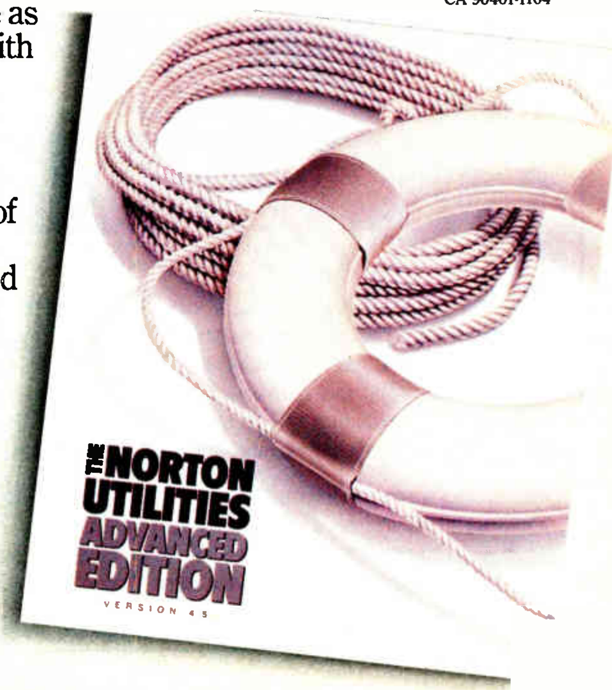

NANOBYTES

Talk, EtherTalk will use the twisted-pair wiring of telephone lines. Farallon plans to provide all the "in-building plumbing" for EtherTalk, said company president Reese Jones, including connectors that attach to a standard Ethernet AUI port ("all IEEE 802.3 devices") and a StarController to build and control topology. PhoneNET with EtherTalk will run about $1000 per machine, Jones said.

• **Publishing with Iris:** Full Color Computing (Danbury, CT) now has versions of its **Full Color Publisher** desktop-publishing-and-more program for Silicon Graphics's three-dimensional

continued

conductor industry. Although the government and some venture capitalists are willing to take the risk of funding something so abstract, many would-be backers think commercial development is too far away. In a survey by Coopers & Lybrand's High Technology Industries Group in Boston, 60 percent of the capitalists questioned said an adequate return on investment would not be possible in the 10-year span of a limited partnership; 29 percent said the technological risk is too high.

CEBus Chip Provides Brains for Jetsonesque Smarthome

You're on the couch, watching television. The turkey is done roasting, so, using your TV remote control, you shut off the oven. Then you realize how cold it is inside the house, so, using the same remote control, you close the curtains and turn up the thermostat. Then it's back to Big Time Wrestling.

That's part of the promise of "home automation" as promoted by the Electronic Industries Association. The EIA wants the backbone of Jetsonesque abodes to be the Consumer Electronic Bus (CEBus), proposed as a standard by which home appliances can communicate with each other. Some heavyweights of home electronics demonstrated this LAN for dishwashers, VCRs, microwave ovens, and entertainment systems at the Winter Consumer Electronics Show in Las Vegas.

The CEBus "Smarthome" exhibit at CES featured 17 appliances, including a Sony television, an RCA VCR, and a Johnson Controls thermostat (those companies are all EIA members). At the heart, or brain, of the demonstration was AISI Research's "Spirit" technology, the first implementation of the CEBus architecture in a silicon chip.

CEBus is a set of specifications for encoding and transmitting information over almost any medium, including twisted-pair wires, coaxial cable, infrared signals, radio waves, and fiber optics. But the focus of AISI and the EIA is to use a house's existing AC power

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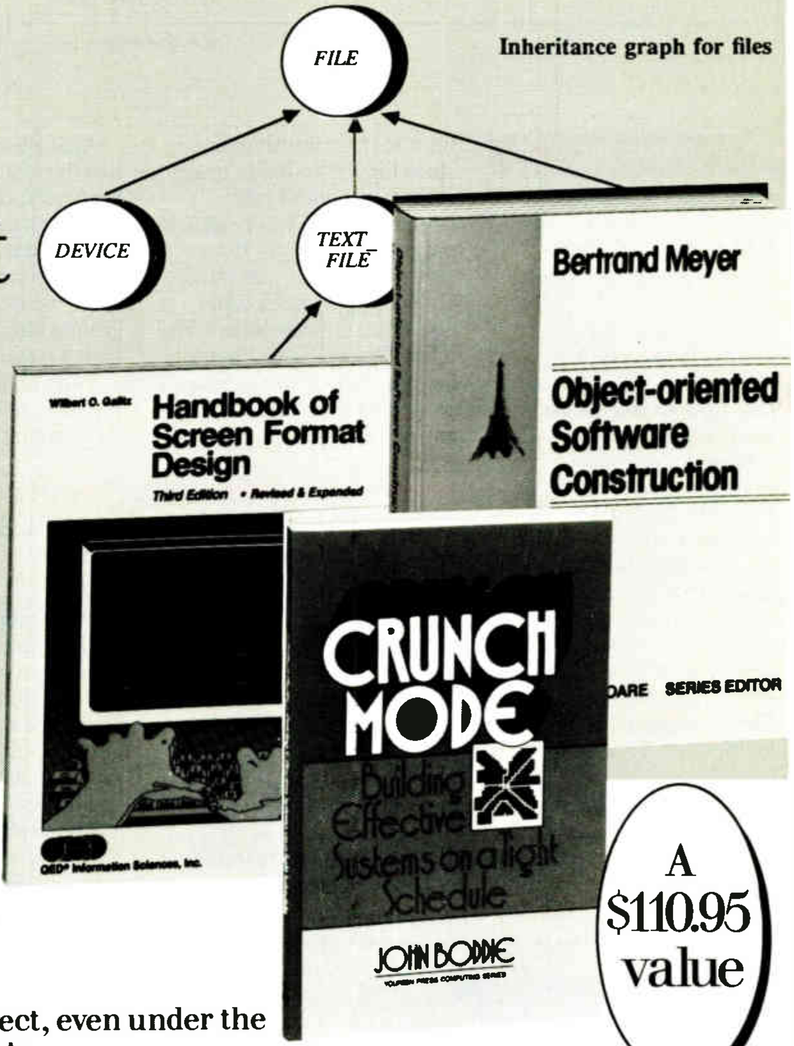

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NANOBYTES

workstations, including the mighty Personal Iris. Full Color Publisher goes beyond page layout; it also has tools for painting, retouching photos, and manipulating images. The package that runs on SG's Personal Iris sells for $8995.

• **How do you pronounce "loquitur"?** The Franklin Language Master-4000 can tell you. It's a **hand-held electronic talking dictionary** capable of speaking 83,000 words. You simply type in the word you'd like pronounced. The product, by **Berkeley Speech Technology** (Berkeley, CA) and **Franklin Computer** (Mt. Holly, NJ), uses

continued

lines for transmitting data, since they're found in practically every house today. Not only would you be able to program appliances from, say, your cellular car phone, but your appliances could also communicate with each other. With CEBus, manufacturers could create a remote control that handles any number of appliances.

AISI Research admits that the technology hasn't been perfected. Most home power wiring isn't designed to carry data, so there's a tendency for home systems to have "white noise"—background interference caused by the system.

The Spirit chip evolved from the AISI "brick," a device containing a microprocessor coupled with analog circuitry to provide full implementation of CEBus. The Spirit chip incorporates all the functions of the much larger brick, while improving performance and signal quality, according to AISI Research. The company, based in Vancouver, BC, is marketing the chips for about $5.

Insignia Sees Unix Market as Biggest Opportunity for DOS Emulator

Although Insignia Solutions introduced a new version of its SoftPC MS-DOS emulation program for the Mac II at January's MacWorld Expo, the company's bigger target market for SoftPC is the Unix environment. Because of the virtual memory capabilities of Unix-based machines, the emulator can access a much larger memory space than it can on the Macintosh, and therefore it can perform better and run MS-DOS programs using EGA and VGA graphics standards, says Insignia CEO Nick Samuel.

The whole trick to the SoftPC emulation technology is translating the MS-DOS Intel-based instruction to a set of instructions executable by the host machine's processor, which can be a Motorola, as in the case of the Mac, or other RISC processors supported on various Unix platforms. However, the translation is not one-for-one. On the Macin-

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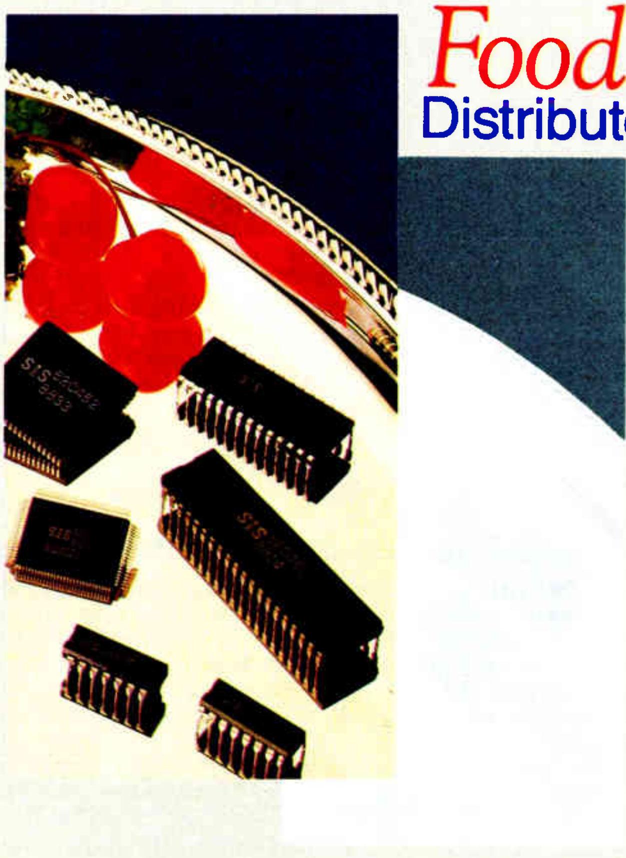

MAPPLICATIONS ■ Sean O'Sullivan

Understanding Desktop Mapping

Find Addresses *MapInfo Corp.'s street maps come with every address already in place, accurate to the correct block and side of street. Simply type an address and MapInfo locates it for you. Or, automatically plot existing data on the map for "what-if" analyses.*

Recent developments by MapInfo in linking graphics with traditional database information results in new ways to visualize data

Rarely has a new software technology burst into the marketplace with the excitement and high potential of desktop mapping. More than 100 specific applications for desktop mapping technology have already emerged, and current estimates suggest that over 80 percent of PC users could make immediate use of this new tool.

Desktop mapping has little to do with the *making* of maps — the job of cartographers. Rather, it involves the *use* of maps to get more meaning and knowledge out of databases — particularly if they have some type of locational field, such as a street name and number, city, ZIP code, county or state.

A lot of software products out there are putting themselves (or are being put) into the desktop mapping category, which runs the spectrum of functionality, ease of use, and price.

One product—MapInfo, from MapInfo Corp.—has emerged in the past year, offering the performance and capabilities of high end packages at a price and ease of use that competes with the low end.

Database Information Merged With Maps *MapInfo's desktop mapping package is the only software designed to directly tie dBASE files with maps. The parcels shown here were automatically shaded based on the data about each. A window is displayed that reveals the actual data behind a particular parcel.*

On the low end, there are products that sell for $350 to $1000 and which are just specialized graphics programs. These programs allow you to take a map, such as one of the United States, decide what colors each state should be, and then overlay text for titles, legends, and captions. However, they cannot interact directly with your database, and do not have the intelligence to find specific addresses at the street map level.

On the high end, there are expensive software products that are typically called Geographic Information Systems (GIS). These products will let you combine your databases with maps in an interactive fashion. Even when converted for use on a PC, however, they are still expensive, starting at $5,000 for the software alone.

But for $750 you can buy the powerful MapInfo engine, which reads your dBASE files and lets them interact with an incredible range of computer maps. The maps, including street maps for most metropolitan areas, county maps, state maps, and so forth, are available separately from MapInfo Corp.

Already, thousands of people across the nation are using MapInfo to gain more knowledge from their databases. Synthes Corp. is using MapInfo to do new market research in Paoli, PA. The Syracuse, NY police department is using MapInfo to visually compile crime statistics. And, in San Francisco, MapInfo is being used to compare locations of AIDS cases with educational outreach efforts.

> ***"One product—MapInfo, from MapInfo Corp.—has emerged in the past year, offering the performance and capabilities of high end packages at a price and ease of use that competes with the low end."***

MapInfo Corp. has established a special toll free hot line to answer your questions about this exciting new technology. For more information, call 1-800-FASTMAP or 518-274-8673, or write to them at 200 Broadway, Troy, NY 12180.

NANOBYTES

Berkeley's Bestspeech text-to-speech (T-T-S) conversion technology, which previously has been available only at high prices for personal computers and mainframes. Speech quality and pronunciation accuracy are on par with Berkeley Speech Technology's most expensive speech-to-text systems, the company claims. The Franklin Language Master-4000 retails for about $300. Berkeley Speech Technology has also licensed Bestspeech T-T-S to Hewlett-Packard and Personal Data Systems. Possible future applications, besides talking dictionaries, include talking translators, word processors, and talking toys with mega-vocabularies.

• ROMmed DOS: **Award Software** (Los Gatos, CA) has started shipping a **ROMmed version of Digital Research's DR DOS**, an MS-DOS 3.3-compatible operating system. The **Award ROS** is available in a "hard" version on four 64K-byte chips (on a $250 plug-in board) or as modules of object code that can be licensed to system developers. Developers can pick from modules that perform parts of DOS and then burn those modules into ROM chips to be installed on the system baseboard. For diskless workstations and smart terminals, Award claims ROS will allow faster, more intelligent, and more flexible designs. "With key parts of the OS at the workstation, you don't have to go back to the server to get every command," said Award president Rene Vishney.

tosh, for example, SoftPC executes from 8 to 10 native Motorola 68000 instructions for every DOS instruction. This ratio determines the performance of the DOS emulation. On a 68020, DOS programs running under SoftPC perform approximately equivalently to an Intel 8088-based personal computer. On a 33-MHz 68030-based machine, Samuel said, SoftPC runs like a 6-MHz IBM PC AT.

At MacWorld Expo, Insignia had SoftPC with EGA support running on Intergraph workstations with the Fairchild Clipper processor. A version without EGA support is available on HP 68000-based workstations. Insignia plans to have this high-performance version running soon on other Unix systems and on DEC VAX minicomputers.

Samuel says Insignia has discussed with NeXT a possible port of SoftPC to the NeXT computer. But he said it is "premature" to talk about these discussions. It requires a major investment to port SoftPC to a new hardware platform—in the "high six figures," he said.

Insignia Solutions also has a SoftPC version ready for Apple's A/UX version of Unix, but, according to Samuel, there has been very little demand for it.

Contact: Insignia Solutions, 787 Lucerne Dr., Sunnyvale, CA 94086, (408) 522-7600.

C&T's New Cache Controllers Compete with Intel's

Chips & Technologies' new set of cache controller chips suitable for 20- and 25-MHz 80386-based systems is entering a market currently dominated by the Intel 82385 cache controller, which is used in the IBM PS/2 Models 70 and 80 and in the Compaq Deskpro 386. The company hopes to compete with the Intel device by offering a unique design that integrates memory and cache control on the same chip. This integrated chip eliminates the need for a discrete DRAM controller, which is required with the Intel chip, according to Chips & Technologies product manager Nelson Chan.

Chan said the company's integrated design also minimizes cache "misses" and thus makes possible better performance than the Intel 82385. Chips & Technologies claims that its cache controllers (called the 82C307 and the 82C327) will result in OEM system cost savings of up to $94 in comparison to the cost of implementing the Intel cache controller design.

Possible customers for the Chips cache controllers include Zenith, Tandy, Texas Instruments, and Dell Computer, though none of these companies have officially confirmed that they will use the controllers.

Graphics Hardware Designers Like Mac SE/30's Slot

Although the Mac SE/30's 030 Direct Slot is incompatible with the NuBus and the standard Macintosh SE expansion slot, designers of graphics equipment are very pleased with it. Why? The 030 slot offers faster video performance than the NuBus, since there's no need to worry about bus arbitration or synchronizing with the 10-MHz clock speed of the NuBus. In interviews with Microbytes Daily at MacWorld Expo, designers agreed that the 030 Direct Slot is a nice piece of engineering.

SuperMac quickly introduced its Spectrum/8 Series II card right after the official debut of the SE/30. This 8-bit color display card supports displays of up to 1024 by 896 pixels with 256 colors. Its Virtual Desktop feature lets you work with a larger desktop than the actual size of the monitor. (The actual size depends on the graphics mode; in black and white, the Virtual Desktop is 4096 by 1792 pixels.)

E-Machines has introduced a 21-inch monochrome display for the Mac SE/30. The Big Picture Z21 SE/30 has a two-million-pixel video memory and can support screen sizes up to 2048 by 1024 pixels.

RasterOps doesn't have anything for the Mac SE/30 yet, but vice president David Smith says it will offer all its color boards and a version of its new accelerator board on the Mac SE/30.

TECHNOLOGY NEWS WANTED. *The news staff at BYTE is interested in hearing about new technological and scientific developments that might have an impact on microcomputers and the people who use them. If you know of advances or projects relevant to microcomputing, please contact the Microbytes staff at (603) 924-9281, send mail on BIX to Microbytes, or write to us at One Phoenix Mill Lane, Peterborough, NH 03458. An electronic version of Microbytes, which offers a wider variety of computer-related news on a daily basis, is available on BIX.*

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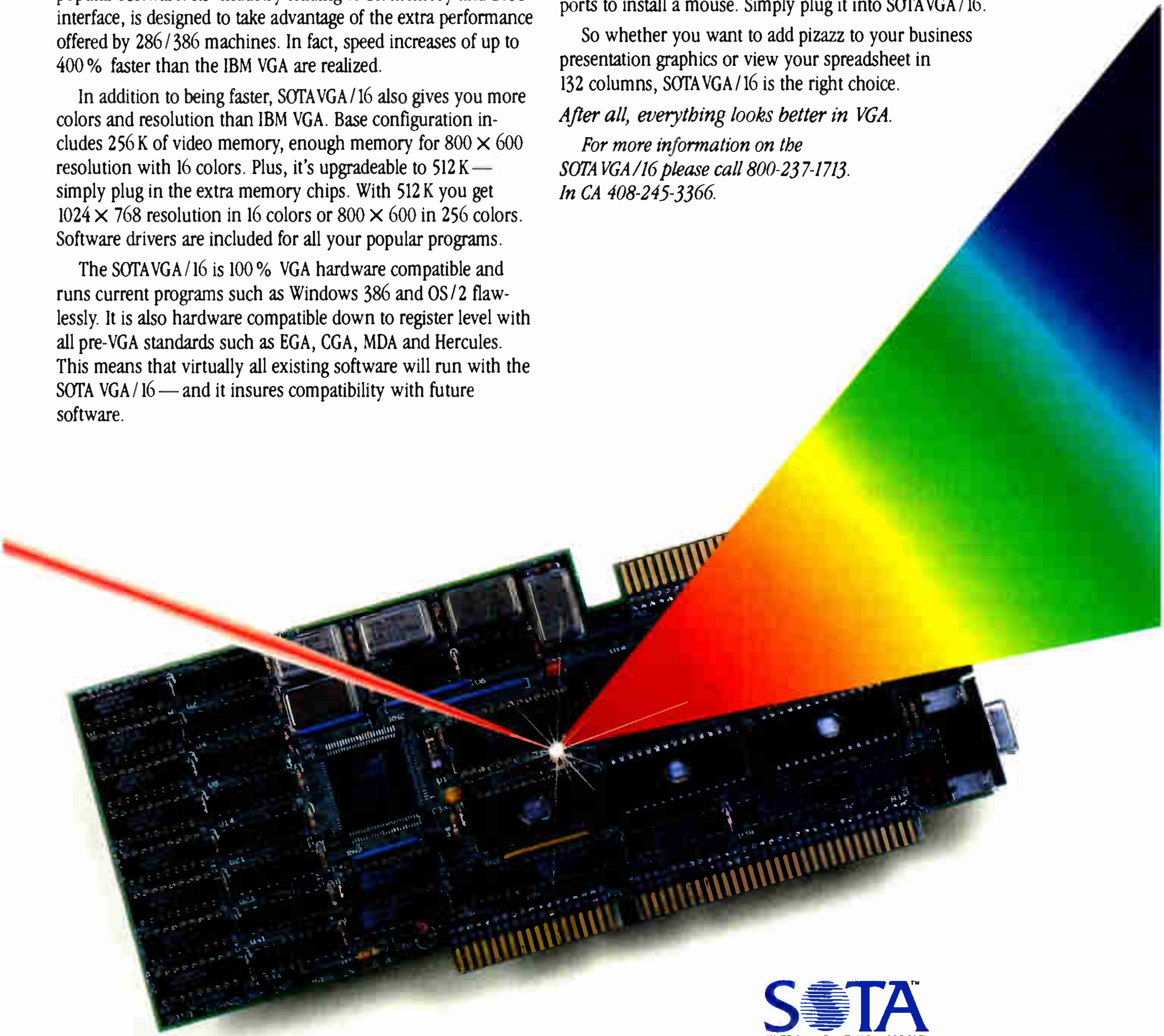
The SOTAVGA/16 is 100% VGA hardware compatible and runs current programs such as Windows 386 and OS/2 flawlessly. It is also hardware compatible down to register level with all pre-VGA standards such as EGA, CGA, MDA and Hercules. This means that virtually all existing software will run with the SOTA VGA/16 — and it insures compatibility with future software.

SOTAVGA/16 has two output connectors for full analog and digital monitor support — not just analog, as some manufacturers offer. And, since many of today's programs require a mouse, SOTA built this option right on the board. No longer do you have to compromise an extra slot or sacrifice one of your COM ports to install a mouse. Simply plug it into SOTAVGA/16.

So whether you want to add pizazz to your business presentation graphics or view your spreadsheet in 132 columns, SOTAVGA/16 is the right choice.

After all, everything looks better in VGA.

For more information on the SOTA VGA/16 please call 800-237-1713. In CA 408-245-3366.



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DESQview lets you run your favorite programs in windows side-by-side.

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For users of 80386 PCs and PS/2s (or PCs with 80386 add-in boards, such as the Intel Inboard 386), there's DESQview 386 (a combination of DESQview 2.2 and the new QEMM-386 Quarterdeck Expanded Memory Manager, version 4.2).

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LETTERS

and Ask BYTE

PC Power

Congratulations on the much-needed article "PC Power" Parts 1 and 2 by Mark Waller (October and November 1988). There is altogether too much misinformation being cast about by those who pretend to know. Specifications that may sound impressive in a peddling contest often have little to do with the real issues. The problem is confounded by oft-quoted mavens and other self-proclaimed pundits who still speak from the "landmark" studies of the seventies that have long since been determined to be invalid. Some seem to think, however, that it is enough just to know studies were done.

Waller describes many of the shortcomings of surge suppressors and ferroresonant transformers with respect to the needs of modern electronic loads. All I would add to his already good comments is that not only are these approaches not as good as alternatives, but they are actually harmful when misapplied. This point could have been made stronger, I think. All in all, though, Waller's article should go a long way toward broadening the respect for the subject, if not the understanding.

Having said all that, there are two technical points that should be corrected or clarified. In Part 1 on page 280, under the heading "A Better Solution," the text reads, ". . .a couple of capacitors and a MOV across the secondary. . . ." In actuality, the clamping circuit (MOV or otherwise) belongs on the primary side of the transformer.

A second point concerns Waller's suggestion in Part 2 of a marriage between a true power-line conditioner and a standby power system. This is fine. But the recommendation to put the transformer-based power conditioner on the output of an SPS needs to include one important caveat—you shouldn't drive a transformer with a square wave, or even a pseudosquare wave.

AC power is a very complex phenomenon. But, like music, it has been made so readily available that nearly everyone takes it for granted.

David Fencl
Oneac Corp.
Libertyville, IL

I do not agree that the MOV and filter must be on the primary side of the transformer. Although Oneac puts them there, other manufacturers put them on the secondary side. I'm not sure it makes much difference except that the components tend to be protected by the transformer if they are on the secondary side.

I did not raise the issue of reliability because establishing it from one product to the next is extremely difficult, and I thought a discussion of it was beyond the scope of the article. Implicitly we all want the most reliable product. I'm not sure I understand if you are referring to components, design, or one manufacturer against another.

I do not agree with you that "you shouldn't drive a transformer with a square wave, or even a pseudosquare wave." I know of no compelling reason for this except that if the filter elements are on the primary side of the transformer, they might heat up and fail handling the noise and harmonics from the wave shape, remembering that this occurs only when utility power fails. This further implies that the personal computer can handle this better than a transformer. Again, I don't agree. The whole point of the article was to urge people to buy products with a clean sine wave output so that these concerns are eliminated. The isolation transformer should always be the last line of defense—the closer to the computer, the better.—Mark Waller

WE WANT TO HEAR FROM YOU. *Please double-space your letter on one side of the page and include your name and address. We can print listings and tables along with a letter if they are short and legible. Address correspondence to Letters Editor, BYTE, One Phoenix Mill Lane, Peterborough, NH 03458.*

Because of space limitations, we reserve the right to edit letters. Generally, it takes four months from the time we receive a letter until we publish it.

continued


EVEREX

While the software was entertaining and unusual, we found three areas worthy of comment.

The first thing we noticed was the flashy interface complete with startling sound effects. In fact, the sounds bore an uncanny resemblance to the sounds that can be heard emanating from the computer room when our sons are down there playing.

The authors of the software seem to have made the same mistake that a lot of other authors make—that is, to confuse a flashy interface with a user-friendly interface.

While the screen layout was very good and the exploding windows fit in very well, the actual text was anything but user-friendly, and the sound effects were distracting, even annoying, in this context. (Besides, we kept wondering whether they were lifted directly from the code of some commercially available games.)

The second area we noted was the thrust of the programs themselves. We should be flattered that the Soviets think that the general intellectual level of the American public is so high, but the truth is that the programs will have limited appeal in the general market, aside from the ivory-tower crowd.

Finally, we were interested in the Soviets' use of the English language. We are impressed with their command of English but surprised that they didn't at least go the additional step of hiring someone to check spelling, syntax, and idioms. While Americans put up with manuals that are written in some oriental language and then translated into English because the hardware works, when the use of the product itself is impaired because of the language problem, we doubt Americans will be so tolerant.

Overall, we give the Soviets a good grade for a first effort at competing in the U.S. market, but we still don't think they're here yet. We also doubt their claim that, "Without any exaggeration, the NeMo-Tec can be considered as an example of the software technology of the next decade—or even of the next century."

Chris Bidstrup
Idaho Falls, ID
Kent Howcroft
American Fork, UT

Intuition and Theory

I would like to add to R. N. Bracewell's terse dismissal (Letters, November 1988) of the point made in John C. Polasek's letter (August 1988). Polasek's point is born of good intuition and has also been put forward by some of my colleagues in the past. This being the case, I thought it might be useful to offer what might constitute a reconciliation of intuition with theory.

On the surface it would appear, from a functional analysis point of view, that the function set $(\cos(wt)+\sin(wt))$ does not form a complete basis set, even for real functions. In other words, the null space of the Hartley mapping is by no means empty, and while it is true that neither of the basis functions represented by the complex Fourier kernel is orthogonal to the "cas" kernel, many functions formed by linear combinations of them seem to be. For example, one might ask, What is the Hartley transform of the set $(\cos(ut)-\sin(ut))$, which is orthogonal to the "cas" kernel. . .or is it?

This valid question, on further examination, leads to a very simple reconciliation: The Hartley transform and its inverse are mapped into ± frequency and

continued

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± time. The "negative frequency" component of the transform does contain the information represented by the basis set $(\cos(ut)-\sin(ut))$. That is, $(\cos(-wt)+\sin(-wt))$ is identical to $(\cos(wt)-\sin(wt))$. Thus, the seemingly missing, orthogonal component of the Hartley kernel is represented by the mapping into a "negative frequency" range, with the phase information preserved in the relative positive and negative components at each frequency.

Though, in this case, you find an intuitive oversight, I think it illustrates the importance of submitting a formal proof to the acid test of good scientific intuition and resolving any apparent contradictions. All too often, only the reverse is done.

Ron G. Walters
Cleveland, OH

No Coprocessor

In "Floating-Point without a Coprocessor, Part 2" (October 1988), Rick Grehan says to use Taylor-series expansions for the implementation of the trigonometric functions. I disagree. Taylor functions have terrible convergence when high decimal-point accuracy is desired. Instead, I recommend the following approximation for the sine function (x is in radians):

$$\mathrm{Sin}(x \times \pi/2) = c_1x + c_2x^3 + c_3x^5 + c_4x^7 + c_5x^9$$

where

$c_1 = +\ 1.5707\ 9631\ 847$
$c_2 = -\ 0.6459\ 6371\ 106$
$c_3 = \ \ 0.0796\ 8967\ 928$
$c_4 = -\ 0.0046\ 7376\ 557$
$c_5 = +\ 0.0001\ 5148\ 419$

This approximation has an error of ±0.0000 0000 5, which translates into eight-decimal-place accuracy. I coded this function on my Apple Lisa and obtained the comparison ($f(x)$ is the approximation) in table 1.

For a very good list of function approximations, I recommend *Approximations for Digital Computers* by Cecil Hastings Jr. (Princeton University Press, 1955).

David Craig
Wichita, KS

You are correct in that the Taylor-series expansion I gave (actually, it should be called a McLaurin series, since that's the term used for a Taylor series expanded around x = 0) for sine(x) isn't the best for digital computer use. Though it might be passable for some computations as long as the argument is restricted to the first quadrant, a truncated McLaurin series with accuracy on the order of your example's 10^{-8} would require two additional terms beyond the series I showed.

There are a number of techniques for deriving an approximating polynomial's coefficients, ranging from least-squares regression to minimax polynomial approximations. F. R. Ruckdeschel, in BASIC Scientific Subroutines, Vol. 1 *(BYTE Books, McGraw-Hill, 1981) presents a least-squares analysis program and uses it to generate an approximating polynomial for sine(x) good to an accuracy of about 10^{-14}. The results are shown in table 2. Keep in mind that, when working with series approximations like this, you should construct your algorithm so that the higher-order terms are accumulated into the result first so that you avoid errors generated by adding small numbers to large numbers.*

Also, I don't think your definition of radians is quite right, since 1 radian is 57° and sine(π/2 radians) = 1.0.

—Rick Grehan

Table 1: *Coefficients for sine(x) accurate to eight decimal places.*

x	f(x)	Sin(x × π/2)	Delta
0.000	0.00000000000	0.00000000000	0.000e+ 0
0.200	0.30901699496	0.30901699437	−5.813e−10
0.400	0.58778525441	0.58778525229	−2.1173− 9
0.600	0.80901699004	0.80901699437	4.333e− 9
0.800	0.95105652100	0.95105651630	−4.707e− 9
1.000	1.00000000531	1.00000000000	−5.310e− 9

Table 2: *Power series approximation for sine(x) accurate to approximately 10^{-14} over the range $-\pi/2 <= x <= \pi/2$.*

Coefficient	Value
c_1	−0.1666666666671334
c_2	0.00833333333809067
c_3	−0.000198412715551283
c_4	0.0000027557589759762
c_5	−0.00000002507059876207
c_6	0.000000000164105986683

Impressive Workstation

Thanks for an interesting look at the NeXT workstation. The system is impressive in all respects except for the slow access time of its primary storage device. I wonder whether common sense was overridden by the call of the wild, especially since Unix performance is directly related to disk performance.

I don't wish to belittle the importance of the new optical drive. The removable high-capacity read/write optical disk is truly revolutionary, particularly in its implications for data backup and security. A drive failure means that you could swap out the bad hardware, slide in the backup media, and boot back up, drastically cutting downtime. Sensitive data can now be physically removed from the system (not just erased) and locked up at night. The 60-megabyte streaming tape drive would no longer be the standard backup device since the backup could be from disk to disk. Sounds great.

I hope NeXT decides to give the marketplace a break and offer a more conventional high-performance (5-millisecond?) hard disk drive as the primary drive. Nowhere is it written that you cannot be radical and open-minded at the same time.

Peter Matsunaga
Aiea, Hawaii

Where to NeXT?

Thank you for your complete report on NeXT's cube computer ("The NeXT Computer" by Tom Thompson and Nick Baran, November 1988). I am a computer engineer at the University of Oklahoma, and the machine sounded perfect for me, so I immediately set out to buy one.

I asked the people at our purchasing office about the machine, and they said they had not been contacted by NeXT and would not carry the company's ma-

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chines unless approached. I could understand that, so I called NeXT to get the ball rolling. When I finally got a representative, I told her that I was interested in purchasing one of NeXT's machines but that the university's purchasing office had not yet been contacted. To my dismay, the representative told me that the university must contact NeXT to start any student purchases. That was my first catch-22.

NeXT's purpose was to sell to universities and major research institutions. Oklahoma University is a major university, and I still can't get a NeXT computer. I asked NeXT if there was any other way I could purchase its machine. I was told that I could try purchasing one from another university. Great! Except the person I spoke with wouldn't tell me the name of any university that was selling NeXT machines. Another catch-22.

I started out really wanting this computer. It still is an incredible piece of hardware, but until NeXT changes its policies, I would not recommend anyone buying one.

Scott Fields
Norman, OK

One Year Later

In the April 1988 issue of BYTE, you published a letter of mine suggesting the use of the work by Michael F. Barnsley and Alan D. Sloan ("A Better Way to Compress Images," January 1988) for the compression of databases.

The method did indeed compress the databases about 100 to 1 in most cases. However, Barnsley and Sloan's method is lousy, and as a result the data when decompressed was not exactly the same as the data before decompression. However, the method did discover underlying patterns in the data.

The discovery of these patterns allowed the development of a better access system to the data, sometimes increasing access to connected pieces of data by 50 to 1. More important, it allowed the discovery of connections within the data structure that were not thought to exist.

The ability to discover connections within a database does have interesting implications for expert systems and rational database structures.

Robert McLaughlin
Arlington, VA

Buy the Book

I greatly appreciated Jerry Pournelle's generous comments in his December 1988 Computing at Chaos Manor about our new book, *LaserJet Unlimited, Edition II*. Unfortunately, although the name of the publisher was listed, no city or mailing address was included. For those readers who would like to buy a copy, it's available for $24.95 plus $3 shipping from Peachpit Press, 1085 Keith Ave., Berkeley, CA 94708, (415) 527-8555.

Ted Nace
Berkeley, CA

An Orphan Variable

While discussing the creation of contour plots in the computational statistics class I'm teaching this semester, my class and I tried to duplicate figure 1a in Paul D. Bourke's June 1987 article entitled "A Contouring Subroutine." The article claims that the figure is a contour map of the function

$$f(x,y) = \sin((x^2+y^2)^{1/2}) + \frac{.5}{\sqrt{(x+3.05)^2+y^2}},$$
$$-2\pi \leq x,y \leq 2\pi.$$

When we were unsuccessful in producing a plot similar to the one in the article, a careful inspection of its listing 2 revealed the line

```
d(i,j)=SIN(r)+.5 / SQR((ix+3.05)^2
 +iy^2)
```

Unfortunately, the variable *iy* is never defined in the program (presumably Bourke intended to type *jy* instead of *iy*). This has the effect of leaving out the y^2 in the second term of the function, which has a drastic effect on the function. I do not consider myself a programming purist, but an experience such as this goes a long way toward making me want to use languages that require all variables to be declared before they can be used.

H. Joseph Newton
Professor of statistics
Texas A&M University

Time-slicing

I am writing with regard to Mark Minasi's "OS/2's Multitasking Dashboard" (OS/2 Notebook, November 1988). Minasi is unable to explain why background processes get proportionately more CPU time as the time slice is increased (table 3 in the article). As far as I can work out, it is because when you increase the time slice, the ration of `maxwait` to `timeslice` decreases.

As an example, say there is one foreground and one background process running with `maxwait=1` (second) and `priority=dynamic`. If the time slice is

continued

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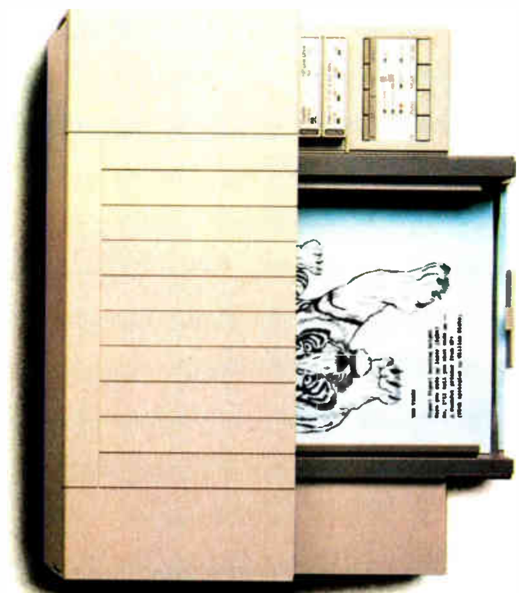

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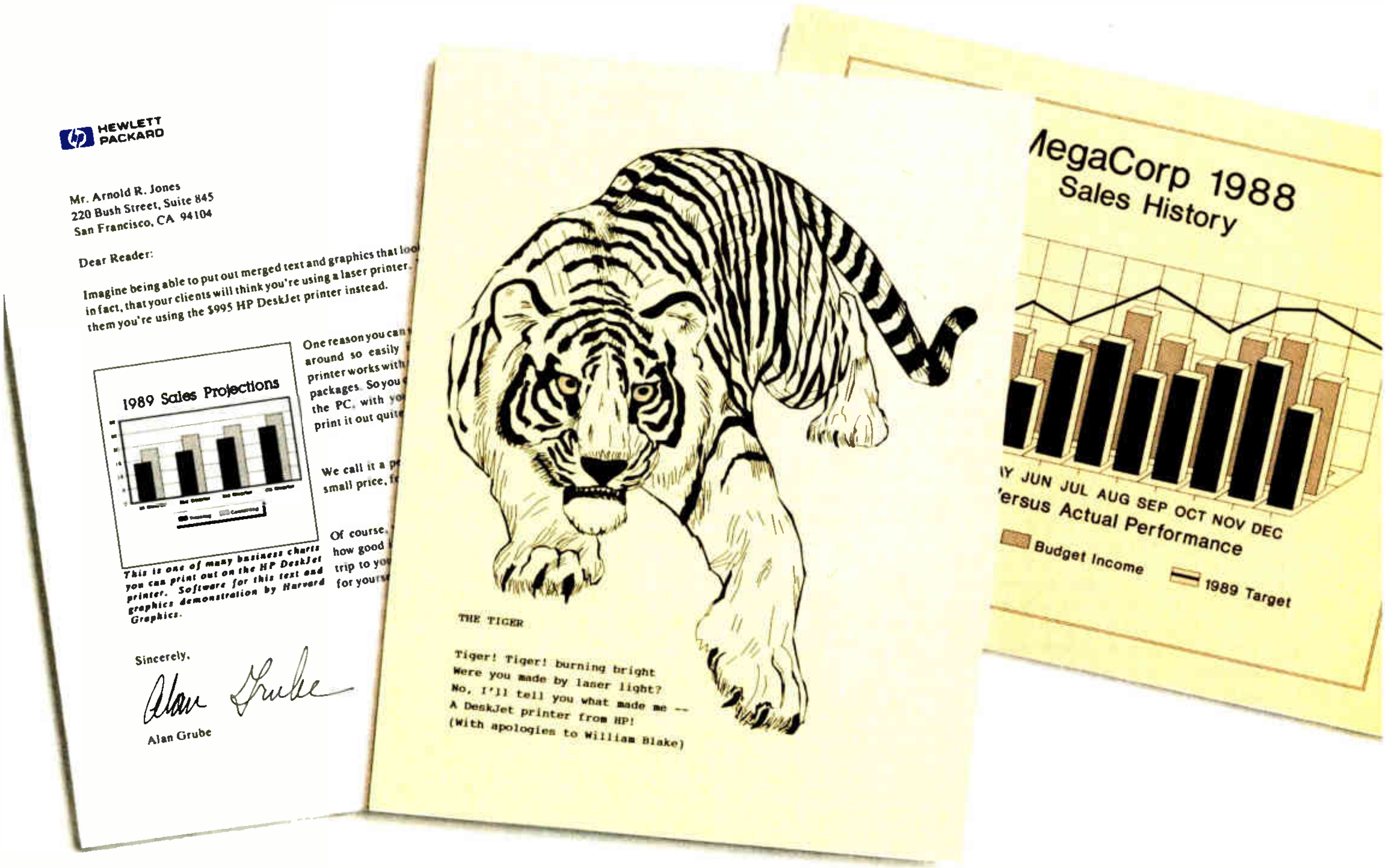

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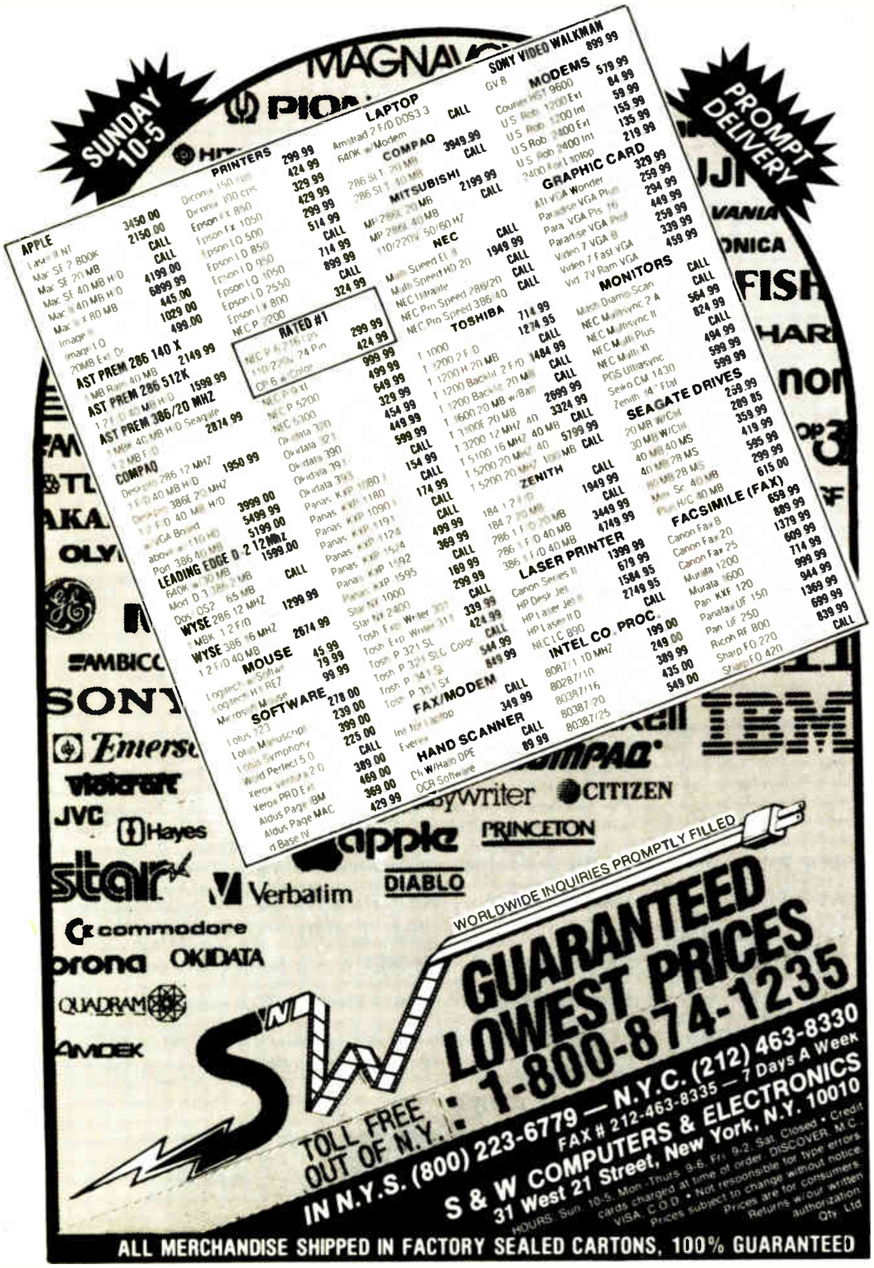
set to 512 milliseconds, the foreground process will get two time slices before the background process receives a priority boost and runs a time slice. So the processor is split 2 to 1. When the time slice is increased to 1024 ms, the foreground process will now get one time slice before the background is boosted and gets a time slice. Now the processor is split 1 to 1. A similar thing will happen with more background processes. To show the task switch overhead, both `timeslice` and `maxwait` should be increased in proportion.

An OS/2 problem that I have is with IBM's 3363 Optical WORM (write once, read many times) drive. The application that we at my university are developing uses an IBM PS/2 Model 80 for imaging work, and we required a removable mass storage medium. Floppy disks have too small a capacity (about five images). The 3363 is the only such device that IBM supports for the PS/2 range. The trouble arises when you also try to use OS/2. OS/2 does not support the 3363, and IBM has no plans to release a device driver for it (as far as I have been able to find out). You cannot run the 3363 in the DOS box because it is a direct-access device driver, and these have to be written under OS/2 device driver rules.

IBM's suggestion to me (after many inquiries and much waiting) was to reboot the system into DOS after image acquisition and back up the images from the hard disk drive, then reboot back to OS/2—hardly idiot-proof, and it doesn't let you browse through the 3363 drive when using the system. Do you know of any other Micro Channel removable media that are large enough to hold many images (256K bytes each) and have an OS/2 device driver rather than a DOS driver? If there are no others, publicizing this deficiency may at least shake Big Blue up a bit.

Nathan Sidwell
Bristol, U.K.



The Akerman Function

Christopher Greaves (Letters, November 1988) challenges the readers of BYTE to deliver to him the value of Akerman(5,5). The answer to this is simply Ack(5,5).

More seriously, Ackerman's function can be viewed as a definition of generalized arithmetic functions where the first argument is constant (value 2), the other has an offset of +3, and the value is offset by −3.

Thus, if op_m denotes function number *m*, where op_0 is the successor function (first argument ignored), op_1 is addition, op_2 is multiplication, op_3 is the "power of" function, and so on, then

$$\text{Ack}(m, n) = (2\ op_m\ (n+3)) - 3.$$

For the first four familiar functions:

$$\begin{aligned}\text{Ack}(0, n) &= (2\ op_0\ (n+3)) - 3\\ &= s(n+3) - 3\\ &= n+1\end{aligned}$$

$$\begin{aligned}\text{Ack}(1, n) &= (2\ op_1\ (n+3)) - 3\\ &= (2 + (n+3)) - 3\\ &= 2+n\end{aligned}$$

$$\begin{aligned}\text{Ack}(2, n) &= (2\ op_2\ (n+3)) - 3\\ &= (2 * (n+3)) - 3\\ &= 2n+3\end{aligned}$$

$$\begin{aligned}\text{Ack}(3, n) &= (2\ op_3\ (n+3)) - 3\\ &= 2^{n+3} - 3\end{aligned}$$

For op_4 I'm not sure of the standard nota-

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tion, but I think Donald Knuth has used ↑↑; that is,

$$x \text{ op}_4\, y = x\uparrow\uparrow y,$$

where $x\uparrow\uparrow y$ is partially defined by

$$x\uparrow\uparrow 2 = x^x,$$
$$x\uparrow\uparrow y = x^{(x\uparrow\uparrow(y-1))}.$$

The function op_5 is defined in the same way in terms of op_4; that is, the general relationship is

$$x \text{ op}_1\, y = x + y,$$
$$x \text{ op}_m\, 2 = x \text{ op}_{m-1}\, x,$$
$$x \text{ op}_m\, y = x \text{ op}_{m-1}\, (x \text{ op}_m\, (y-1)),$$

for $m > 1$. An alternative answer, in terms of the more familiar arithmetic functions, is therefore

$$\text{Ack}(5, 5) = (2 \text{ op}_5 (5+3)) - 3$$
$$= (2 \text{ op}_5 8) - 3$$

but that would be a matter of taste.

It is worth noting that this number is so large that, for example, conversion to binary form is impossible.

Alf P. Steinbach
Ringstad, Norway

Kludge Reduction Exercises

Recently, I wrote a parsing function that would accept up to eight filenames from within my program, storing each filename within an array of a string (i.e., `Array [1..8] of string`). I opened the files ({$I−}) and examined them to see if they existed. I tossed out incorrect filenames. I processed the remaining files and gave the processed files new names ending in .FIL to indicate that they had been "filtered." I won't show anyone the kludge I came up with, but with Dick Pountain's `Before()` and `After()` from "Untangling Pascal Strings" (December 1988), the whole mess reduced to only a few lines of code.

Although, as Pountain correctly states, the Turbo `Val()` function is happy accepting leading blanks, the same is not true when assigning filenames for `Reset()` and `Rewrite()`. For example, opening the file ' IN.DAT' is not the same as opening 'IN.DAT'. If a user decides to separate the files by two or more spaces, the `After()` function no longer works as desired. Instead of calling `Noblanks()` for each filename within my string array, I added another function, `DoubleCheck()`. This function (see listing 1) need only be called once to remove all multiple separators from the input string.

S. Balch
Lucan, Ontario, Canada

Listing 1: *Reader Balch's function to remove all multiple separators from the input string.*

```
Function DoubleCheck(S:String;
                     C:Char):String;
  Begin
    Repeat
      S := Before(S,C+C)
           + C + After(S,C+C);
    Until After(S,C+C) = '';
    DoubleCheck := S;
  End;

Example One

...
Readln(Tmpstr);
Tmpstr := DoubleCheck(Tmpstr,' ');
...
```

ASK BYTE

Disappointing Hard Drive

I wonder if you could advise me on available hard disk drive upgrades for my IBM PS/2 Model 50, which came with a disappointing CMS 20-megabyte 80-millisecond unit using an ST506 interface.

Antony Perakis
New York, NY

Unfortunately, independent companies have yet to penetrate the Micro Channel market. This is no accident. IBM has made it difficult for other vendors to support Micro Channel applications. The Model 50, in particular, causes difficulties for developers. The IBM BIOS supports only 17 sectors per track. Vendors cannot offer high-performance interfaces like run length limited or ESDI. They would either have to convince customers to throw out the IBM BIOS, or they would have to integrate a BIOS chip on the controller board. Since the market is limited to those Model 50 users who wish to upgrade from the included hard disk drive, it's a lot to ask of a vendor. Unlike the Model 60, which offers an extended CMOS RAM where developers have a window for modifications, the Model 50 offers no such luxury.

As of this writing, Adaptec is sampling a SCSI Micro Channel host adapter for OEMs. This product would not only provide an interface for hard disk drives, but it could also be used to add printers,

continued

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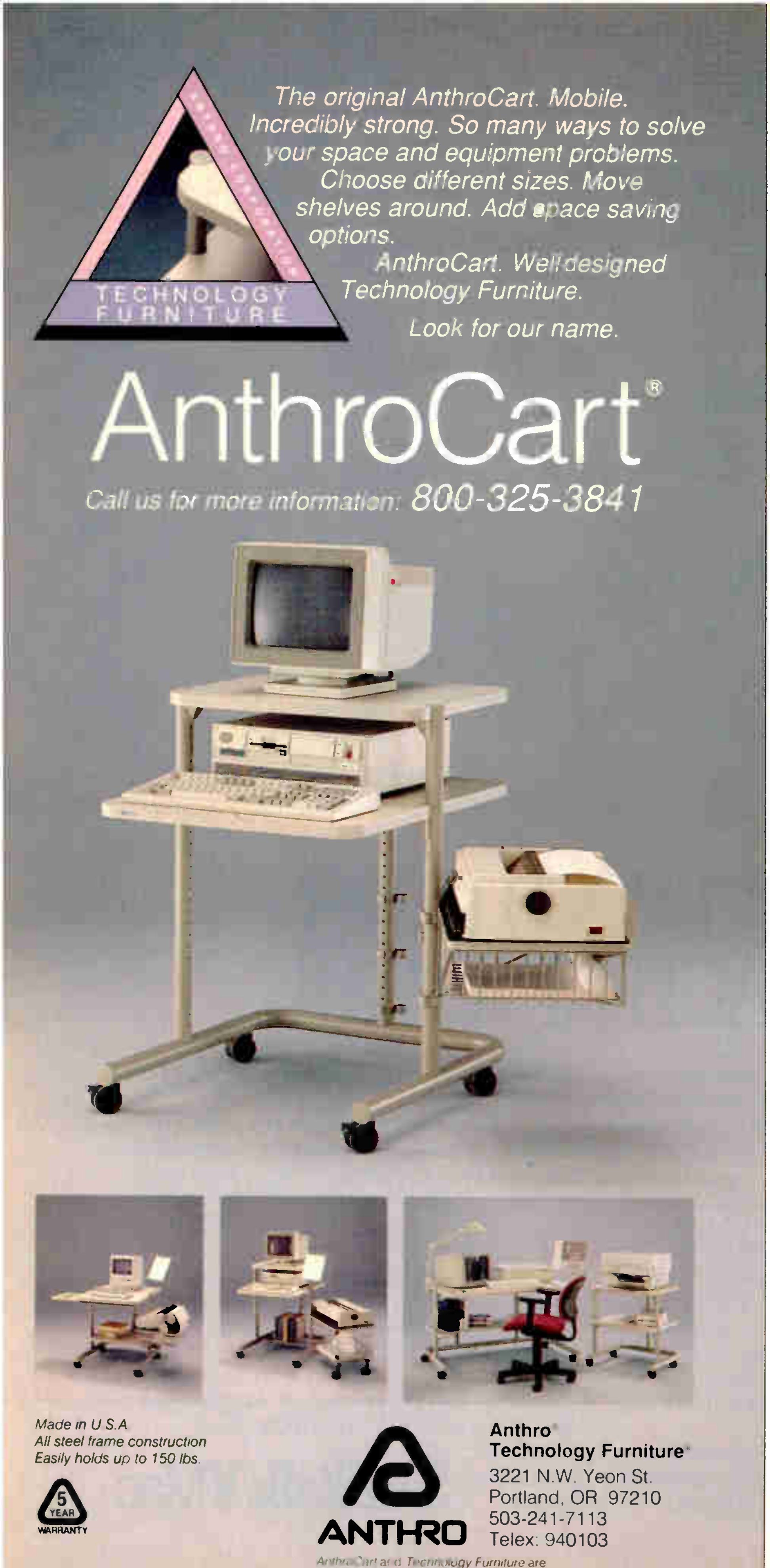

tapes, or scanners. Another product might let you hook to a network system through the SCSI port. Other Micro Channel products should be appearing soon, but they have been slow in coming. IBM does offer an upgrade path for your Model 50's hard disk drive: a 27-ms, 60-megabyte hard drive for $1695.—S. A.

Monochrome EGA

There are several expensive multiscan monochrome monitors that can be used with a conventional EGA card. Are there any *low-cost* monochrome EGA monitors?

Do special EGA cards that are capable of gray-scale shading on TTL monitors offer a good-quality display? Do they suffer from flicker, partial screen display, or distorted aspect ratio on any EGA modes?

Victor G. Apter
Buffalo, NY

EGA supports modes for monochrome display of both text and graphics. Therefore, you have a wide variety of low-cost monochrome monitors to choose from. I prefer the 14-inch models with a flattened screen. The September 1988 BYTE lists (in "Monitor Makers") contact information for the companies that manufacture such monitors.

I really like the output of gray-scale monitors, particularly the NEC Multisync GS (see "Review Update," September 1988 BYTE). The GS will accept TTL or analog video input and is compatible with a wide variety of video adapter cards. Our equipment did pick up excessive jitter, but the display is easier on the eyes than a no-frills TTL monochrome adapter and monitor. Not only is the gray-scale display visually appealing, but it adds functionality to programs that use color menus or extensive graphics. The monitor sells for under $300.—S. A.

From VIC to PC

In 1983, V. J. Georgiou, Ph.D., published the *VIC-20 Interfacing Blue Book*. It contained information on programs and inexpensive hardware for connecting resistance and capacitance meters and a few other nice peripherals. The one I found most interesting was the digital thermometer. It used a 555 timer, a few resistors, capacitors, and a thermistor. The book included the wiring diagram and programs to operate the thermometer. Even the Radio Shack part numbers were included.

Well, my VIC-20 is gathering dust somewhere in my basement, but I would

continued

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still like to run some time-versus-temperature programs. I currently own an IBM PC XT clone, and I seem to remember a "Blue Book" for the PCs, but I have been unable to find such a publication. I have even started to plow through some books on the 8088 processor, but nothing there seems to be leading me toward my thermistor circuit. I realize I could buy a $300-to-$1000 add-on system to read temperatures into data files, but I don't require that level of sophistication. Do you know of any publications that might help?

Wayne A. Holmes
Monroe, CT

I've found two references that should help you.

Interfacing Your Microcomputer to Virtually Anything *by Joseph J. Carr (TAB Books, Blue Ridge Summit, PA: 1984) is not only a source of useful circuits, it's a good introduction to linear (analog) circuits.*

Handbook of Software and Hardware Interfacing for IBM PCs *by Jeffrey P. Royer (Prentice-Hall, Englewood Cliffs, NJ: 1987) should tell you how to put together your own interface boards for the PC and—most important—how programs can communicate with the interface.*

—R. G.

A Problem Solved

Regarding Lee Rose's letter (January Ask BYTE), there is a very viable solution to his problem of running IBM PC AT and Apple programs on the same machine. An Apple IIe or IIGS with Applied Engineering's PC Transporter card will provide most of the compatibility he is looking for. This card is available in a 768K-byte (Apple mode)/640K-byte (IBM mode) configuration. The CPU is a high-performance 16-bit V-30 microprocessor operating at 7.16 MHz. An 8087-2 math coprocessor slot (with an 8-MHz clock rate) is included.

I have used this card on my IIGS for some time now, and I have found the product easy to use and 100 percent compatible with MS-DOS 3.3, GWBASIC, dBASE II Plus, WordPerfect, Framework II, and Microsoft Windows applications. Also, I have easily converted Macintosh files to the IIGS using a modem. This setup, while not as inexpensive as an IBM clone, offers medium-priced compatibility with all three major formats—hard to beat!

Dene R. Francis
Charleston, SC

Thanks for the information.—R. G. ■

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CHAOS MANOR MAIL

Jerry Pournelle answers questions about his column and related computer topics

Don't Knock Unix

This letter is a rather long flame engendered by your comments on Unix, which are summarized, I believe, by the following quote from your September 1988 Computing at Chaos Manor:

"This whole situation puzzles me. I've had a dozen people try to explain why you can't simply fire up Unix and use it as the master operating system to run multiple DOS programs, and the usual answer is, 'You can, but nobody's done it.' None of them can answer the next question," which you asked in the previous paragraph and which follows.

"Of course, I can also run standard Unix programs, but why bother? All the Unix programs that do the things I want to do have been pretty small potatoes compared to what's available on DOS."

Your comments are arrogant and nonsensical, and as a result, your readers might be deprived of the opportunities afforded by Unix.

To answer your first question, try out the Sun Microsystems 386i. Multiple DOS tasks can be run effortlessly or optionally menu-driven in a Unix environment. The cost for the mind-boggling capabilities of the 386i is about what you would pay for one of the more familiar 80386 systems with comparable hardware, with or without Unix.

The answer to your next question, "Why bother?," is necessarily more personal. Like you, I have a single-user system, a lowly 80286. Like you, I write as part of my living. Unlike you, I run Unix and would not touch DOS. Why do we differ? I imagine because we want different functions from the computer. For example, when I write, I must have copious references that are absolutely accurate, inserted with text citation styles that differ widely between publishers.

Under Unix, it is easy to take references from a commercial database with any format and incorporate them into a paper with any citation style and any bibliographic style. The programs that you link together to do this are part of the standard Unix operating system or in the public domain.

Under DOS, there are commercial programs to handle reference database reformatting, citation insertion, and references. But they are indeed overpriced and not flexible enough to satisfy the bizarrely idiosyncratic requests of scientific publishers.

The widely used DOS word processing programs that you appear to favor, such as WordPerfect, have been or are announced as being ported to Unix. Perhaps the reason you haven't seen them is that the Unix user typically does not like them. But they are indeed available. As for myself, there's no way that I would ever return to WordStar, which I used from the late 1970s through the mid-1980s.

Unix is an extremely comfortable computing environment. As evidence, consider the number of people you know who have voluntarily shifted from Unix to DOS, compared with the number who have become Unix users. You can easily tailor Unix to your wants. The standard Unix tools are renowned for their power and variety.

You may not need a multiuser, multitasking system, but it sure is nice, even for a "single user" like myself. Not everyone has the same needs and preferences. But I imagine that among your readers there are also some, perhaps many, who have needs like mine or for other reasons would be better satisfied by Unix than by DOS. Give them correct information, and after that let them decide for themselves.

John Rupley
Tucson, AZ

Thank you for your kind words.

I am well aware that the Sun workstations can do wonderful things; indeed, I

continued

Jerry Pournelle holds a doctorate in psychology and is a science fiction writer who also earns a comfortable living writing about computers present and future. He can be reached c/o BYTE, One Phoenix Mill Lane, Peterborough, NH 03458, or on BIX as "jerryp."

think I wrote at length about some of the work one of my friends did at Bellevue Hospital using a Sun. However, I haven't tried a Sun386i myself because I don't have one and because, up to now, those big Sun workstations have been well beyond the financial limits I generally impose on equipment I'll write about. I don't write about VAXes for the same reason.

Your statements about the 386i with Unix being about what you'd pay for "one of the more familiar 80386 systems with comparable hardware" is true only in the sense that you can load up anything. My Big Cheetah with everything aboard has a list price of only about $10,000. If I added Unix software, which is very expensive, that price would certainly rise.

Finally, your letter is typical of those I get. "Unix is wonderful, and I'm using it right now, and I can do all these terrific things." Fine, say I, and I invite Unix experts to come over here and set something up on one of my machines—and I have a lot of them.

The result so far has been a lot of good excuses. I have nothing against Unix, but I will not change my rules, which are that I write about what I'm using and that what I use has to work on equipment here at Chaos Manor and get done the jobs I have to do, such as writing books and columns and doing my taxes.

I'm glad you're happy with your system and that you like to grep *and* urk.

—Jerry

Exploit the Space Bar

Dear Jerry,

Recently, you've been trying to get us to use better keyboards. I have long wondered why designers haven't made better use of the space bar.

When there are 100 keys on a board for eight fingers, why is there one bar for two thumbs? In the search for more functions, why can't the bar be split in two—the right half for the right thumb, the left for the left thumb?

I'm all thumbs, but I bet I could train my right thumb to strike the right bar to move the cursor forward and my left thumb to strike the left bar to backspace.

Please put this suggestion in the public domain before someone claims "look and feel."

James T. Oitzinger
Houston, TX

That sounds like an interesting idea. I've been watching, and I hardly ever use my left thumb for anything. Fascinating. Thanks for the suggestion.—Jerry ■

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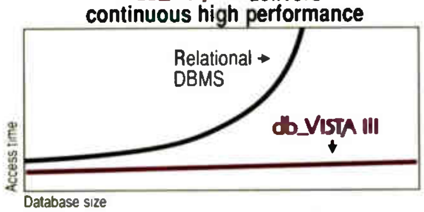

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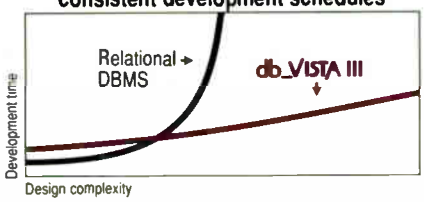

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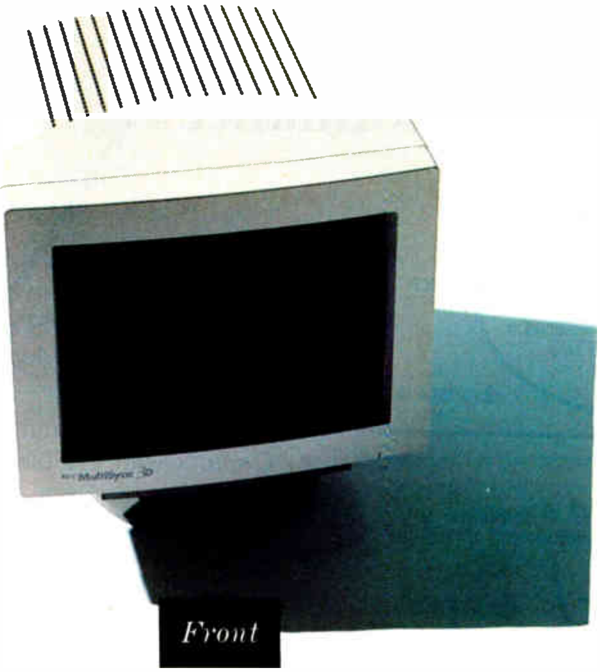
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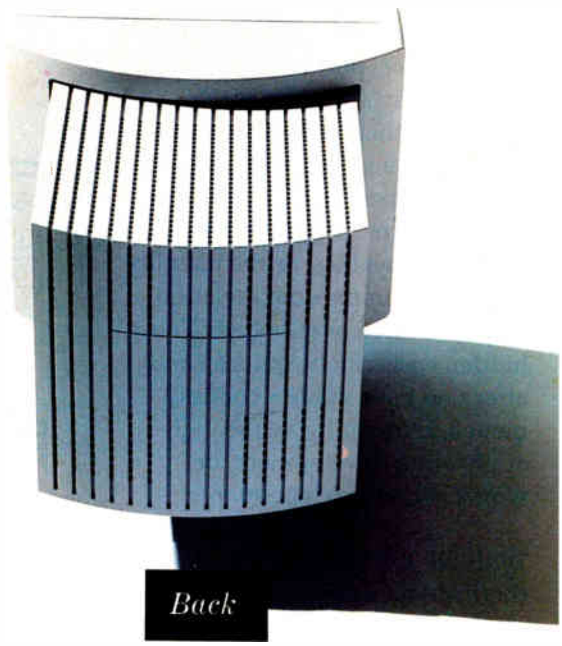
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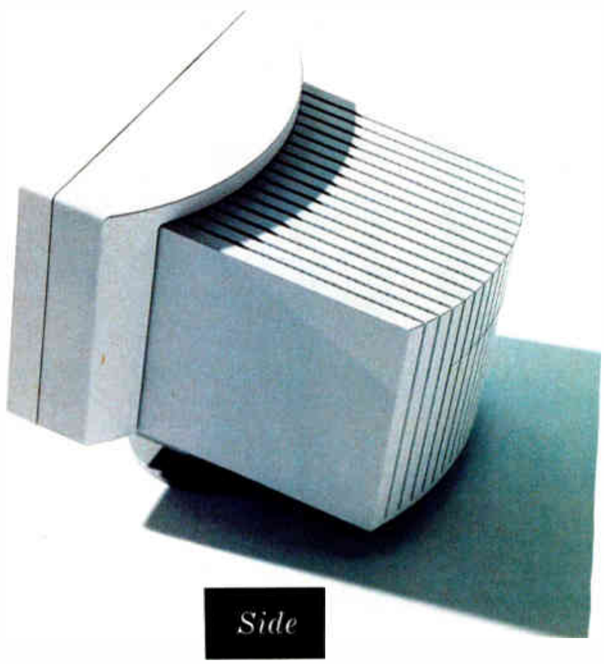

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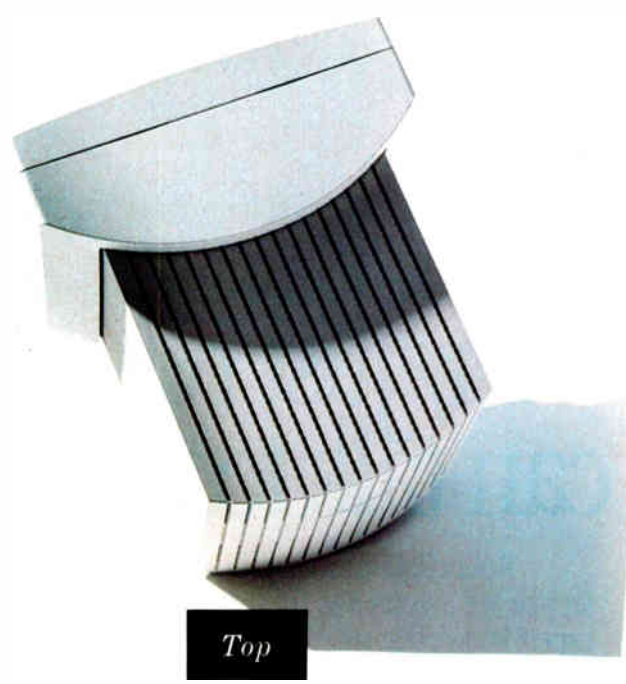

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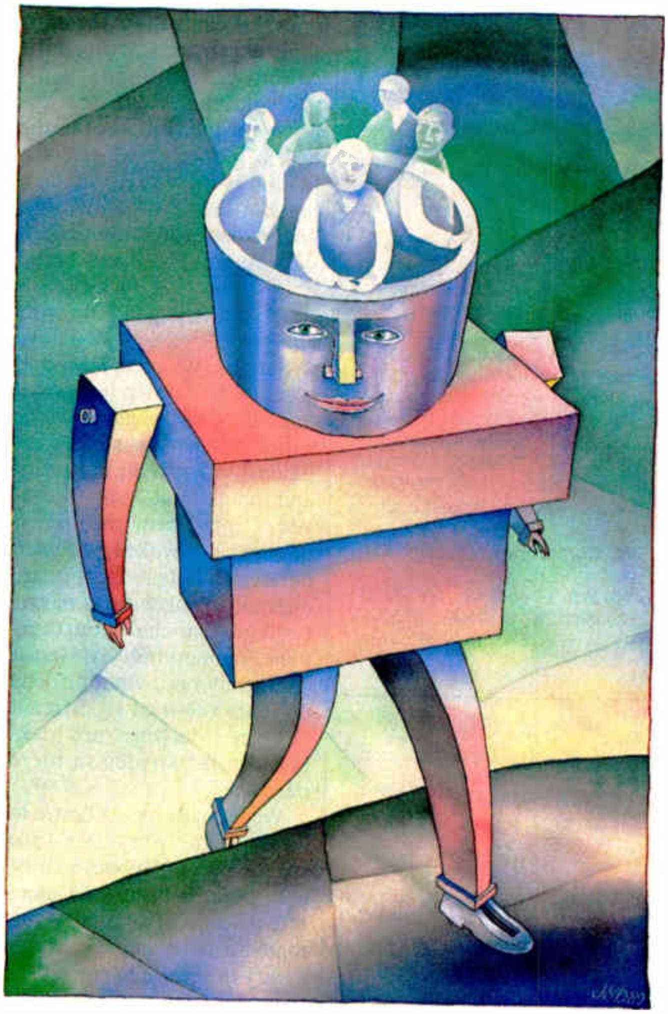
Mind Children: The Future of Robot and Human Intelligence
by Hans Moravec

Harvard University Press, Cambridge, MA: 1988, 186 pages, $18.95

Reviewed by Eric Bobinsky



Although Marvin Minsky was using the phrase "meat machine" in the early 1960s to describe the human brain, this mechanistic viewpoint dates back to the industrial revolution and perhaps much earlier. For years, science fiction writers have constructed worlds in which robots equaled and even improved on human intelligence.

The idea that we humans will eventually build superintelligent machines that might replace us is hardly new, yet the AI community, which used to confidently proclaim it, has lately retreated to a much more conservative stance. After all, it is difficult to extrapolate the superhuman potential of our machines when, after 30 years of AI research, some of the simplest tasks performed by the "meat machine" cannot even be approached by our most advanced creations.

Thus, it is somewhat surprising when the director of Carnegie-Mellon University's Mobile Robot Laboratory writes a book on AI and robotics that stretches the limits of imagination. Hans Moravec's *Mind Children: The Future of Robot and Human Intelligence* goes far beyond the science fiction writers' view of superhuman robot intelligence and, without apology, dogmatically presents the author's ideas on the evolution of human and machine intelligence. It is one of the most fascinating books on the subject ever written.

Mind Children is the kind of book that makes readers react. They will argue with at least some of the author's ideas (I argued with quite a few). But that's not surprising, given the book's main premise that the biological evolution of humanity is complete, that the future will consist of a postbiological world dominated by our robots, and that we will eventually transfer our own minds directly into the machines we build. When speaking of current robot technology, Moravec writes that he sees "the beginnings of awareness in the minds of our machines—an awareness that I believe will evolve into consciousness comparable with that of humans."

Transferring our conscious selves into the minds of robots is an idea that might be safely relegated to the distant future—but coupled with it is the author's unequivocal statement that computers will have attained the processing power of the human brain within 40 years, which Moravec justifies by extrapolating from the rate of development of digital hardware since the time of Charles Babbage. So we aren't necessarily looking as far into the future as might allow us to comfortably contemplate this potential loss of human identity—perhaps only a few thousand years or less.

But creating a 10-teraops processor—which Moravec calls a "human equivalent computer"—isn't the ultimate goal. He goes on to discuss the possibility of protein robots that are genetically engineered to assemble circuits at the nanometer scale, allowing the creation of artificial brains millions of times more powerful than the human mind. Robots equipped with these brains might in turn be capable of using the ultradense matter of neutron stars to create processors that are one million million million million million times more powerful than the human brain by sidestepping certain currently accepted physical limits on the switching speeds of microcircuitry.

After taking the reader on a brief tour of theoretical physics and having tried to establish the feasibility of such ultrapowerful robot brains, Moravec moves into the realm of psychobiology. How will we be able to coexist with fellow creatures—for that is what our robots will become—that are clearly far superior to us in every respect? And, if that doesn't seem possible, should we even continue trying to develop them? What are the justifications for engineering our own obsolescence? Moravec writes, "The answer, I believe, is that we have very little choice, if our culture is to remain viable. Societies and

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The Dreams of Reason: The Computer and the Rise of the Sciences of Complexity

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economies are surely as subject to competitive evolutionary pressures as biological organisms. Sooner or later, the ones that can sustain the most rapid expansion and diversification will dominate. Cultures compete with one another for the resources of the accessible universe. If automation is more efficient than manual labor, organizations and societies that embrace it will be wealthier and better able to survive in difficult times and to expand in favorable ones."

Suppose we accept this and continue to develop our megamachines. What happens? For a time, there is a symbiotic relationship between humanity and robot. "But," says Moravec, "intelligent machines, however benevolent, threaten our existence because they are alternative inhabitants of our ecological niche." Furthermore, humankind "evolved at a leisurely rate, with millions of years between significant changes. Machines are making similar strides in mere decades."

We humans have a desire to expand our presence into space. But our robots will be able to do it faster and more effectively. "Eventually humans . . . will become unnecessary in space enterprises, as the scientific and technical discoveries of self-reproducing superintelligent mechanisms are applied to making themselves smarter still. These new creations, looking quite unlike the machines we know, will explode into the universe, leaving us behind in a cloud of dust."

The matter doesn't end there. Humans have a propensity for trying to better themselves, and what better way to do that than to have the mind transferred into a far superior, immortal robot body? We are immediately confronted with the mind-body problem of philosophy: Is the me in the new body really the old me? Or is it a perfect copy of the old me—and does it really matter? Two sections, "What Am I?" and "Awakening the Past," delve deeply into the problem.

Are there demons in this paradise of immortality and superintellect? "If the world of artificial machinery has seemed disease-free so far, it is only because our machines have been too simple to support mechanical parasites," writes Moravec in a section called "Trojan Horses, Time Bombs, and Viruses." In these pages (which are particularly interesting to read in light of the recent invasion of a nationwide Unix network by a computer science student's program), the author explores various types of digital fauna that can be made to infect and damage computer systems.

If robots can be made as complex as Moravec maintains, then we can easily imagine how susceptible they would be to equally complex parasitic programming. Furthermore, if we ourselves inhabit the robots, then the virus analogy becomes even more apropos. Here we have an entire system of artificial lifeforms: robots, human-robots, and the parasites infecting them—a concept Moravec calls "freely evolving digital wildlife." Parasitism, far from being undesirable, is necessary for triggering the mutations that will allow this system to continue to evolve. In other words, the transition from biological system to postbiological system is complete and irreversible.

Thus, we have built machines that are our postbiological successors. We have looked upon them and seen that they are good. And we have finally abandoned our bodies and moved to inhabit our machines. This has taken many years, but maybe not as long as we anticipated. We have had ample time to thoroughly explore our own universe, and probably others as well. But the universe must ultimately wind down—a victim of entropy death, tragically cutting short our reign of intellect. But not to worry—we are

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now even capable of forestalling the end of the universe by storing energy and releasing it at the proper moment. So our robots, which may be ourselves, have finally attained the stature of gods. But they have done so through what Moravec depicts as a necessary and logical progression in the evolution of the human brain, so perhaps the concept isn't as far out as it seems.

Mind Children is destined to be a controversial book. The margins on every other page of my copy are covered with question marks and exclamation points; it is impossible to read the book and be impartial. It has the accuracy of a college text and the can't-put-it-down appeal of a good novel. Moravec has turned the flights of mind of one of the world's foremost roboticists into hard copy. And he has written a tremendously good book in the process.

The Dreams of Reason: The Computer and the Rise of the Sciences of Complexity

by Heinz Pagels

Simon and Schuster, New York: 1988, 352 pages, $18.95

Reviewed by David A. Mindell

Computers are symbolic machines. So stunning is their appetite for symbolic manipulation, however, that what we used to think of as the "meaning" behind our signals is often obscured by the proliferation and seeming self-replication of machine codes. From bar code to source code, from ISDNs to ICBMs, our culture seems headed for a digital meltdown. Antiquated modes of thought may soon be unable to extract signals from the parasitic noise of the once-hailed Information Society.

Heinz Pagels presents a perspective that would prevent such confusion. In his book *The Dreams of Reason: The Computer and the Rise of the Sciences of Complexity*, he explores how, through computers, we are acquiring the insights required to reverse the tide of cultural entropy and find order in what was previously perceived as chaos. Pagels uses the term "complexity" to describe recent and startling developments in the sciences that have the potential to displace earlier scientific models. These developments are largely a result of the advent of the computer as an instrument of inquiry, and they also display an amazing similarity to the workings of nature itself.

Pagels, who died last year, was executive director of the New York Academy of Sciences and a physicist at Rockefeller University. In *The Dreams of Reason* he thoughtfully explores some of the new "sciences of complexity": chaos theory, computational biology, computer simulation, neural networks, and the increasingly complex web of financial computer networks. He goes on to a thorough and detailed consideration of the philosophical and cultural implications of a scientific paradigm of complexity.

The term "complexity" is itself the first complex concept of the book, because it has numerous meanings and implications and simply cannot be tied down to any one. Pagels's loose definition is that complexity lies between order and chaos. On one hand, a crystal, with its regular rows of atoms, is "ordered" and can be easily known or determined. Completely chaotic systems like gases, on the other hand, can be well understood because "we can apply the laws of statistics to them with great effect."

Complexity could be said to lie between determinism and statistics, a difficult and shady region without clear boundaries. One measure of the

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complexity of a system is "algorithmic complexity," or the size of the system's minimal description. Pi, for example, though an infinitely long real number, is not particularly complex because you can write a relatively simple program to compute it. Another kind of complexity is "computational," which would measure not the size of the program but how long it would take to run.

The problem with these and other quantitative descriptions of complexity that Pagels explores is that they do not lie solidly between order and chaos. The "algorithmic" measure, for example, would ascribe a higher degree of complexity to a random string than to one with some inherent order. Of course, all these measures fail with language, while simple and ordinary sequences of words can be used to describe extraordinarily complex concepts: "To be or not to be." Pagels fails to discover an adequate and binding definition for complexity. Instead, he presents, in a suitably complex mode, several qualities or "themes" shared by complex systems:

- They tend to be selective systems, employing, like evolution, the principle of "survival of the fittest."
- They tend to emphasize parallel over serial processes.
- They often discover new principles based on nature.

Despite the difficulties of numerical or verbal description (there is no adequate theory of complexity; that's why it's new), Pagels's chapters on the sciences of complexity are rigorous and spirited enough to convey to the reader an intuitive sense of the nature of complexity. For example, on mathematics, Pagels comments, "I believe that it is because of the possible complexity arising out of a simple logical system that mathematics acquires its quality of independence and autonomy from the mind."

The entire first half of *The Dreams of Reason* reads like a review of recent popular scientific literature (although Mandelbrot and his fractals are curiously absent). Pagels reviews the startling new theory of chaos, as laid out in James Gleick's recent book. Chaos theory has been used to describe everything from global weather patterns to leaky faucets, and recently even the behavior of computer networks. Such systems are characterized by extreme nonlinearities, which results in an almost infinite sensitivity to initial conditions. Thus, even if the laws governing a chaotic system are known, such a system cannot be simulated without defining its initial state to anything other than infinite precision, which is impossible.

The next chapter is on cellular automata, sophisticated versions of the popular computer game called Life that can simulate complex biological systems. With such tools, scientists watch the evolution of pseudobiological systems and have observed some fascinating phenomena: a) The "cells" of the simulation tend to get trapped in a given configuration, but not necessarily in the optimum one for survival; b) when they do achieve an optimum form, they may not be able to maintain it in the face of recurring mutation; and c) a large amount of spontaneous order arises in the evolutionary process. How well these simulations match the operations of natural evolution can be debated, but they certainly provide insights into the workings of more artificial parallel and selective complex systems.

The second part of *The Dreams of Reason* is dedicated to Pagels's philosophical musings on the implications of the sciences of complexity. He addresses the most complex system known to man: the human brain. He insists that a fundamental understanding of the brain and human cognition

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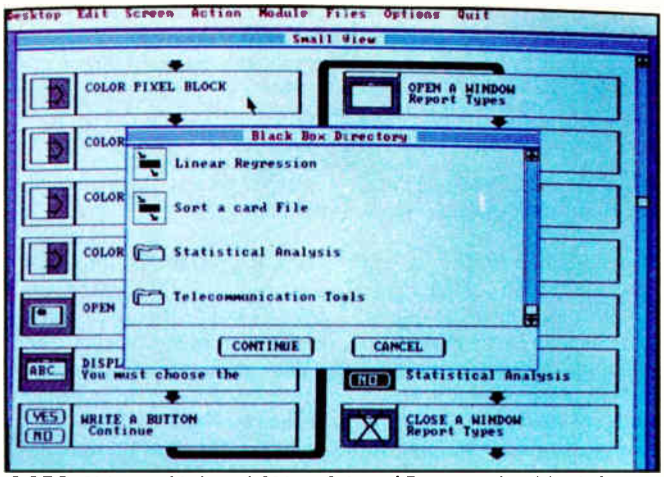

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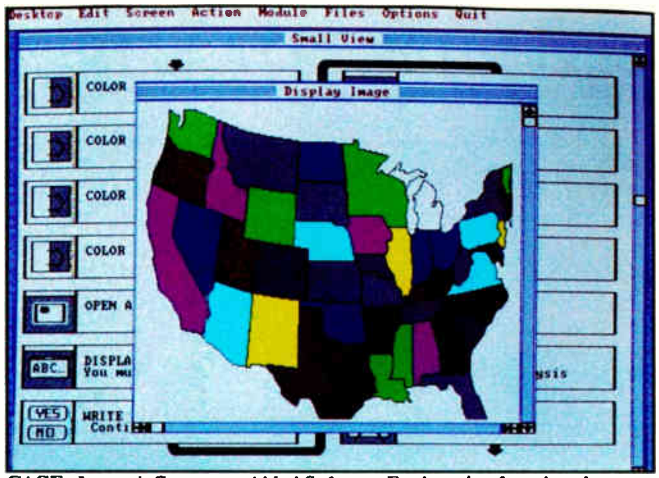

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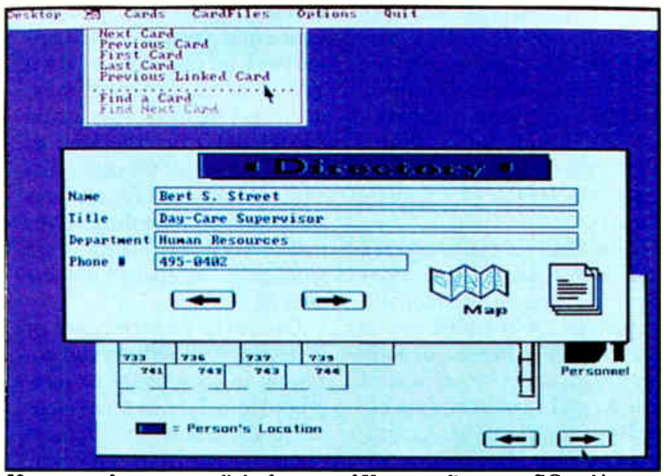

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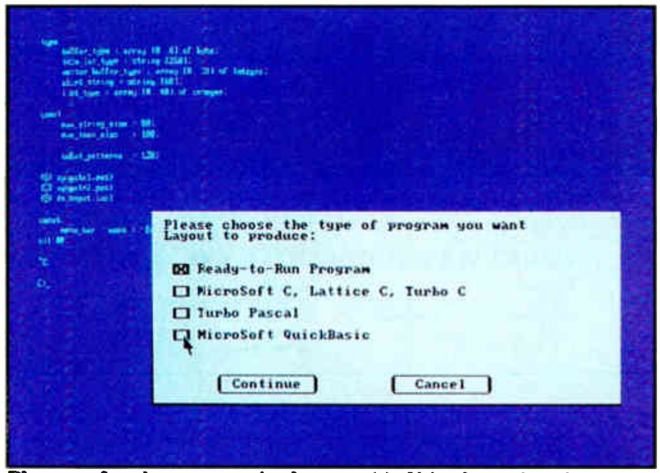

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can come only through a "material understanding" (i.e., one that begins with physical laws and then works its way upward through complexity to thought). According to Pagels, until such an understanding is reached, all other models, such as those of cognitive science, philosophy, or literature, will remain merely "intellectual fashion." He is consistently and harshly critical of the conclusions of anyone he deems to be less than a practicing scientist. His staunch belief that science and only science expresses universal truth limits and eventually unravels his thesis that a theory of complexity must come wholly out of scientific inquiry.

Science, like evolution, is a selective system. It functions by "hypothetico-deductive" reasoning. A scientist comes up with a hypothesis, usually an inspiration or educated guess, and then performs experiments to verify it. According to Pagels, however, a scientific theory can never be proved true, it can only be proved wrong.

Revolutionary theories, like those of relativity or quantum mechanics, acquire a certain credibility if they survive long enough without being disproved, but they still cannot be proved positively. Thus, the evolution of science is like the evolution of life; the fittest survive the tests of time, but not necessarily as the optimum configuration. This is a valid and logical explanation of the progress of science. The problem, however, is that Pagels's writing is simultaneously infused with the opposite view—namely, that science expresses absolute and unshakable truth, knowing nature in a positive sense from "the bottom up." This belief is at odds with the evolutionary view of science, which tells us that, like cellular automata, scientific theories do not necessarily settle in the optimal configuration, only one that will survive. And, as Pagels himself says, "Survival, of course, is not the same as truth."

In response to this criticism, Pagels would admit that science is a world constructed by man, but he still sets it apart and above other worlds like music, literature, and law because "it was not determined exclusively by us." Pagels says that as a scientist he remembers only "concepts and facts," as opposed to "humanists" whose thought is "dedicated to political opinion, taste, and style...and intellectual gossip for its own sake." If, as Pagels believes, the structure of thought could be derived from the laws of physics, then all the other disciplines for which Pagels feels such contempt are themselves materially determined and are legitimate "sciences" in their own right.

Overall, *The Dreams of Reason* is well written and engaging in its attempt to integrate a broad range of developments into a new "synthesis of science." The irony, however, is that Pagels's refusal to recognize ideas from those people who are not practicing scientists grounds his discourse in a deterministic, noncomplex paradigm. His difficulty with defining complexity, for example, would certainly be eased by considering other postmodern thinkers, such as philosopher Gilles Deleuze, historian of science Michel Serres, or even novelist Thomas Pynchon. The sciences of complexity are interdisciplinary. A vision that integrates computers and biology, as well as neurology and quantum mechanics, should be able to accommodate the complex philosophies of thought and language in its quest to select a theory of the shining but presently uncharted region between order and chaos. ■

CONTRIBUTORS

Eric Bobinsky works at the NASA Lewis Research Center in Cleveland, Ohio. **David A. Mindell** is a technical writer and consultant living in Aspen, Colorado.

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Greenleaf SuperFunctions	265	189
PC-lint	139	101
PC YACC	395	359
TimeSlicer	295	279
w/ source	1000	899
Turbo C TOOLS 2.0	149	115
vLIB	99	89
w/ source	149	129
C GRAPHICS		
Essential Graphics	299	229
w/ source	595	509
GraphiC	395	322
GSS Graphic Dev. Toolkit	595	509
HALO '88	325	229
HALO '88 for MS Devel.	595	399
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C SCREENS/WINDOWS		
Curses (Aspen)	159	145
w/ Source	349	319
C-Scape	299	282
w/ Look & Feel	399	379
C-Worthy	195	165
w/ Forms & Source	495	439
Greenleaf DataWindows	295	209
Greenleaf Makeform	125	90
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JAM	750	684
JYACC FORMAKER	495	453
PANEL Plus	495	395
Vermont Views	395	CALL
w/ Source Code	790	CALL
Vitamin C	225	162
VCScreen	149	109
COBOL LANGUAGE		
Micro Focus:		
COBOL/2 w/ Toolset	1800	1465
COBOL/2 Toolset	900	733
PC-CICS	1495	CALL
Personal COBOL	149	121
COMMUNICATIONS		
Carbon Copy Plus	195	135
Close-Up (Support)	245	222
Close-Up (Customer)	195	116
Co/Session (2 user license)	249	227
Support	175	157
Application	125	116
MIRROR III	100	90
RELAY Gold	250	229
RELAY Silver	150	139
dBASE LANGUAGE		
CLARION	695	599
Clear +	200	165
Clipper	695	439
dBASE III Plus	695	399
dBASE IV	795	485
dBASE Graphics for C	90	69
dBASE ON LINE	69	60
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dBASE Tools for C	90	69
dBASE Tools for Pascal	90	69
dBFast	100	75
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Periscope IIx	145	129
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Command Tips	90	80
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hTest: hFormat	90	80
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MKS Toolkit	199	165
Norton Commander	89	56
Norton Utilities	100	61
Norton Utilities, Adv. Ver.	150	101
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Vcache	50	47
Vfeature	80	75
Vfeature Deluxe	120	111
Vopt	50	47
Xtree	70	60
Xtree Pro	129	111
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ZYINDEX PROFESSIONAL	295	265
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BRIEF	195	CALL
Epsilon	195	151
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ME	89	79
w/ source	189	169
MKS VI	149	125
Multi-Edit	99	90
Norton Editor	75	70
PC/EDT +	295	269
PI Editor	149	129
Slick Editor	195	155
SPF/PC	245	185
VEDIT PLUS	185	115
Vq	270	CALL
FILE MANAGEMENT		
Btrieve	245	185
Xtrieve	595	459
Report Executive	145	109
Btrieve/N	595	459
Xtrieve/N	595	459
Report Option/N	345	279
CBTREE	159	141
C-Index for MSC	195	175
C-Index for Turbo C	100	90
C-Index Plus	395	349
c-tree	395	318
d-tree	495	395
r-tree	295	241
c-tree/r-tree bundle	650	523
CQL Query System	395	332
dBC III	250	CALL
dBC III PLUS	750	CALL
db FILE	395	309
db_RETRIEVE	395	309
Essential B-Tree	99	89
w/ source	198	179
XQL	795	599
FORTRAN COMPILERS		
F77L	477	429
GRAPHER	199	179
Lahey Personal FORTRAN 77	95	89
MS FORTRAN	450	299
RM FORTRAN	595	479
SURFER	399	359
FORTRAN LIBRARIES/UTILITIES		
FITLIB	350	305
FFTLIB	350	305
Grafmatic	135	119
MINIPACK1-LIB	350	305
Plotmatic	135	119
SPARSGEM	350	305
Spindrift Library	149	135
Tekmar Graphics Library	195	169
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OPTLIB	49	45
OPTLINK	125	109
Plink86plus	495	279
Plib	195	149
PolyLibrarian I	99	90
PolyLibrarian II	149	131
.RTLink	195	185
MODULA-2		
JPI Top Speed Modula-2	100	90
LOGITECH Modula-2 Development System	249	199

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Solid B + Toolbox	100	90
Stony Brook Modula-2 Development Package	345	309
OBJECT-ORIENTED PROGRAMMING		
ACTOR	495	423
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C Talk Windows	450	399
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OPERATING SYSTEMS		
Microport:		
System V/AT (complete)	649	549
AT Runtime System	249	209
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SCO:		
XENIX Sys V (comp.)	1295	999
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WENDIN:		
Operating System Toolbox	99	80
PCNX	99	80
PCVMS	99	80
Wendin-DOS	139	109
OS/2 DEVELOPMENT TOOLS		
Btrieve for OS/2	595	455
Epsilon for OS/2	195	151
Greenleaf DataWindows	395	CALL
GSS Dev. Toolkit for OS/2	CALL	CALL
LOGITECH Modula-2	349	279
MKS AWK (OS/2)	179	159
MKS Toolkit (OS/2)	495	439
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NeWS/2	495	CALL
Panel Plus for OS/2	495	395
PolyAWK for OS/2	199	179
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Windows for Data (OS/2)	395	349
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Pascal ASYNCH MANAGER	175	137
PCX Toolkit	90	80
POWER SCREEN	129	99
Turbo Analyst	99	79
Turbo ASYNCH MANAGER	129	99

NEW RELEASES

Vermont Views 1.0
Vermont Creative Software's new generation of Windows for Data. User-interface library for forms, windows, menus, help, and key-board handling. Menus in any style. Forms can be larger than display window, have choice lists, context-sensitive help, flexible decimal, date, time, and toggle fields. Mini word processor. International language portability.
List: $395 **Ours: CALL**

Microsoft QuickC V. 2.0
Now features incremental compilation and linking, resulting in greatly improved compilation speed. Also now includes a built-in, in-line assembler. Hypertext technology has been utilized in QuickC's new on-line reference system.
List: $99 **Ours: $69**

.RTLink
Fast, new overlay linker. User RTLs (Run Time Libraries) to prevent repetitive storage, on disk, of code that is common from one .EXE file to the next. Use of .RTLs results in significantly smaller .EXEs resulting in reduced disk space requirements.
List: $195 **Ours: $179**

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This Month's Specials *from* MORTICE KERN SYSTEMS

	LIST	OURS
MKS AWK	99	82
MKS LEX & YACC	249	207
MKS Make	149	125
MKS RCS	189	157
MKS SQPS	495	479
MKS Toolkit	199	165
MKS Trilogy	119	99
MKS VI	149	125

	LIST	OURS
Turbo Geometry Library	150	135
Turbo MAGIC	199	179
TURBO META-MENU	149	139
Turbo Plus v. 5.0	100	80
Turbo POWER TOOLS PLUS/5.0	149	115
Turbo Professional 5.0	125	99
SCIENCE/ENGINEERING SOFTWARE		
CSS	495	459
Derive	200	179
DesignCAD 3-D	299	219
EXP	150	129
HiWIRE-Plus	895	805
MathCAD	349	279
MICRO-CAP III	1495	1269
Microstat-II	395	349
Systat	595	549
Tango PCB Series II	595	569
TECH•GRAPH•PAD	395	359
TRANSLATORS		
Bas_C (Commercial)	375	323
Bas_Pas (Commercial)	280	242
BASTOC	495	399
Brooklyn Bridge	130	CALL
dB2C	299	272
dBx TRANSLATOR	550	469
PROMULA.FORTRAN	450	409
w/ Library Source	745	669
386 SOFTWARE		
386 ASM/LINK	495	399
386MAX	75	66
386MAX PROFESSIONAL	129	115
Concurrent DOS 386 (3 User)	395	349
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MS Windows/386	195	130
NDP C 386	595	529
NDP FORTRAN-386	595	529
SCO XENIX		
Operating System	695	589
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VP/IX (2-USERS)	495	399
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VM/386	245	209
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OTHER PRODUCTS		
FLOW CHARTING II	229	207
MuLisp-87	300	219
MuLisp-87 Interp/Compiler	400	299
PC Scheme	95	79
Pfinish	395	215
PolyDoc	199	179
Source Print	97	80
Tree Diagrammer	77	70
BLAISE COMPUTING		
ASYNCH MANAGER	175	137
C TOOLS PLUS/5.0	129	99
EXEC	95	76
Key Pilot	50	45
PASCAL TOOLS / TOOLS 2	175	137
POWER SCREEN	129	99
RUNOFF	50	45
Turbo ASYNCH MANAGER	129	99
Turbo C TOOLS/2.0	149	115
Turbo POWER TOOLS PLUS/5.0	149	115
BORLAND		
Eureka: The Solver	167	126
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Quattro	247	185
SideKick Plus	200	149
Sprint	200	149
Superkey	100	75
Turbo Assembler/Debugger	150	112
Turbo Basic	100	75
Database Toolbox	100	75
Editor Toolbox	100	75
Turbo C 2.0	150	112
Turbo C 2.0 Professional	250	187
Turbo Pascal 5.0	150	112
Turbo Pascal 5.0 Professional	250	187
Database Toolbox	100	75
Editor Toolbox	100	75
Gameworks Toolbox	100	75

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Graphix Toolbox	100	75
Numerical Methods Toolbox	100	75
Tutor	70	55
Turbo Prolog	150	112
GREENLEAF SOFTWARE INC.		
Greenleaf Bus. MathLib	325	229
Greenleaf Comm Library	229	169
Greenleaf DataMath Interface	75	69
Greenleaf DataWindows, DOS	295	209
OS/2 Version	395	299
Greenleaf Functions	209	149
Greenleaf MakeForm	125	99
OS/2 Version	170	135
Greenleaf SuperFunctions	265	189
Greenleaf ViewComm	495	CALL
LAHEY		
F77L	477	429
F77L-EM/16	695	639
F77L-EM/32	895	805
Lahey/AI OS/386 OP/SYS	195	179
PERSONAL FORTRAN 77	95	89
MICROSOFT		
MS BASIC/6.0	295	199
MS C	450	299
MS COBOL V. 3.0	900	599
MS Excel	495	299
MS FORTRAN	450	299
MS Learning DOS	50	40
MS Mach 20	495	329
MS Macro Assembler	150	105
MS Mouse Bus or Serial		
w/Paintbrush & Mouse Menus	150	105
w/EasyCAD	175	129
w/Paintbrush & Windows	200	149
MS OS/2 Prog. Toolkit	350	229
MS Pascal	300	199
MS QuickBASIC	99	69
MS QuickC 2.0	99	69
MS Windows	99	69
MS Windows/386	195	130
MS Windows Dev. Kit	500	319
MS Word	450	285
NOVELL		
Btrieve Standalone	245	185
Btrieve for DOS 3.1 Networks	595	459
Btrieve for OS/2	595	459
Btrieve for XENIX	595	459
Btrieve Multitasking Add-on	345	279
Btrieve IBM PC Network Add-on	345	279
NetWare C Interface for DOS	295	239
NetWare MHS	100	79
NetWare MHS Interface Guide	145	129
NetWare RPC	950	759
NetWare SQL	595	459
NetWare System Calls for DOS	195	159
Report Executive	145	109
XQL	795	599
Xtrieve	595	459
RAIMA CORPORATION		
db_FILE	395	322
Single user w/source code	890	725
Multi-user	595	485
Multi-user w/source code	1390	1133
db_RETRIEVE	395	322
Single user w/source code	890	725
Multi-user	595	485
Multi-user w/source code	1390	1133
WKS Library	195	179
SYSTEMS & SOFTWARE		
LINK & LOCATE	350	309
LINK & LOCATE + +	395	359
SoftProbe II/TX	395	359

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Developers require programming tools and services that address their changing development needs. Novell® supplies tools that shorten the application development cycle. **Btrieve®** set the file management standard for PC applications. **Xtrieve™** and **Report Executive®** provide query and report writing capabilities. Now Novell offers **NetWare SQL™**! Combined with XQL, you have an unbeatable set of tools for implementing applications with relational data management capabilities.

NetWare SQL NetWare SQL is an open interface relational database engine providing back-end database services to a wide variety of front-end applications. Integrated with the NetWare operating system, NetWare SQL's server-based technology enables efficient, reliable, and high performance distributed data management. By using NetWare SQL, your applications will run faster and deliver higher levels of reliability and data integrity. Plus, your applications can share data with popular packages being offered by major software vendors who are also utilizing NetWare SQL. **List: $595 Ours: $459**

XQL XQL gives you the convenience and power of a 4th generation language by allowing you to use SQL for relational data management within your application programs. Your XQL applications can run stand-alone, on DOS networks, or you can combine it with the NetWare SQL database server for distributed processing capabilities. **List: $795 Ours: $599**

NetWare System Interface NetWare System Calls and the NetWare C Interface, offer over 300 system-level application programming interfaces (APIs) for direct access to NetWare operations such as network security, fault tolerance, network management, and network accounting. **NetWare System Calls List: $195 Ours: $159**
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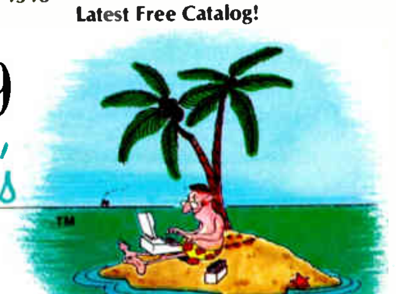

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Network Station Designed Around X Windows

The NCD16 is a 12.5-MHz 68000-based personal computer with 1 megabyte of RAM. It's built in the tradition of the low-priced, intelligent personal computers that boot from an Ethernet host.

This intelligent workstation, however, is designed to also run X Windows software, an MIT-designed software concept that has been endorsed by IBM, DEC, Hewlett-Packard, and others. X Windows lets the NCD16 support multiple applications running on hosts under the Unix and VMS operating systems; it runs the applications between the hosts and the NCD16 at the 10-Mbps Ethernet data rate. (Compare that to the maximum ASCII terminal-to-host rate of 38.4 kbps.)

Once you've downloaded X Windows from your host (or you've booted up with an optional PROM), you address particular hosts with the mouse by clicking on the representative windows. The 16-inch monochrome display with 1024- by 1024-pixel resolution promptly responds with a 105-dpi bit-mapped graphic, something available before only on stand-alone PCs, proprietary LANs using data rates afforded by optical fiber technology, and higher-priced workstations.

The Ethernet adapter fits into the NCD16's only slot.
Price: $2550; PROM, $300.
Contact: Network Computing Devices, Inc., 350 North Bernardo Ave., Mountain View, CA 94043, (415) 694-0650.
Inquiry 1151.

The X Window-based NCD16 computer has an Ethernet link.

This 80386 System Eliminates the 80286 Price Advantage

The Power 386-20 might just be the least-expensive 80386-based personal computer on the market. And it's not short on equipment. It comes standard with 1 megabyte of RAM, a 30-megabyte hard disk drive, a 1.2-megabyte 5¼-inch floppy disk drive or a 1.44-megabyte 3½-inch floppy disk drive, and a 12-inch monochrome monitor.

There's also a Hercules-compatible graphics card, a 101-key Key Tronic keyboard, and room for five 16-bit and three 8-bit add-in cards.

Other standards include a 1-to-1 interleave controller for two hard disk drives and two floppy disk drives, a parallel port for your printer, and room for two half-height internal peripherals and three half-height external drives.
Price: $1995.
Contact: Micro 1, Inc., 557 Howard St., San Francisco, CA 94105, (800) 338-4061; in California, (415) 974-5439.
Inquiry 1153.

A Desktop with a Mainframe Punch

The Unisys Micro A is a redesign of the PW^2 Series 800 with a mainframe processor.

Basically, Unisys took an off-the-shelf 80386-based workstation, converted the 80386 processor into an I/O and maintenance subsystem, and added a 48-bit mainframe A-series processor, an 80286 data communications coprocessor with four ports, and a Z80 SCSI coprocessor.

With the 16-bit 80386 acting as an I/O system, Unisys limits the power of the 48-bit main processor, company officials admit. Similarly, an 80286-based data communications processor with four RS-232C communications ports is overkill.

However, Unisys says that the goal was to design a desktop system that would bring down the price of a Micro A, whose predecessor is priced at about $100,000. Upgrades with X.25 capabilities are planned.

Without modifications, the PW^2 Series 800 runs both OS/2 on the 80386 and the Series A Master Control Program/Advanced System (MCP/AS), which is the mainframe operating system designed for the A Series. It can also run as an 80386 with OS/2 alone.

At the heart of the Micro A processor is the single-chip A-Series mainframe processor, a 2- by 2-inch multichip package that contains the equivalent of 10.3 million transistors. The processor sits on a thick (two-board-width) AT add-in board within the 16-bit 80386-based motherboard. The processor board contains 2.5 megabytes of static RAM plus 12 megabytes of system RAM. The 80386-based motherboard contains 3 megabytes of RAM.
Price: $25,365; software, $5000.
Contact: Unisys Corp., P.O. Box 500, Blue Bell, PA 19424, (215) 542-6512.
Inquiry 1150.

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We'd like to consider your product for publication. Send us full information, including its price, ship date, and an address and telephone number where readers can get further information. Send to New Products Editor, BYTE, One Phoenix Mill Lane, Peterborough, NH 03458. Information contained in these items is based on manufacturers' written statements and/or telephone interviews with BYTE reporters. BYTE has not formally reviewed each product mentioned. These items, along with additional new product announcements, are posted regularly on BIX in the microbytes.sw and microbytes.hw conferences.

continued

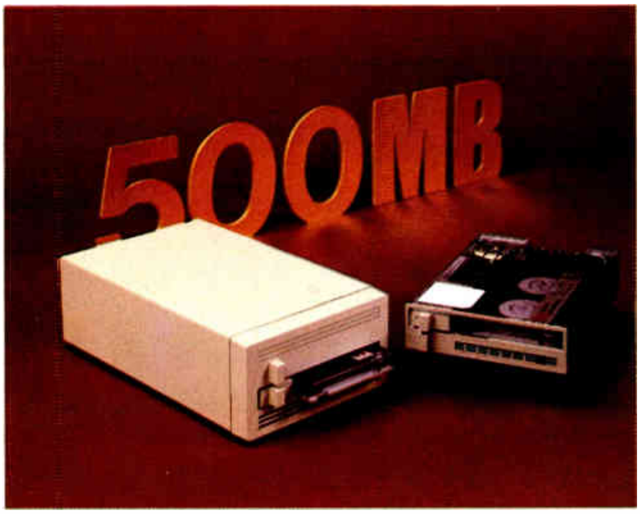
Please, Squeeze My Data

When first approached with a real-time compression algorithm that would effectively double the number of bits it could squeeze into its QIC-40 streaming tape drives, Colorado Memory Systems said "No, thanks" to Stac of Pasadena, California.

But on further consideration, the company that made the QIC-40 a de facto standard decided to work with Stac to use the algorithm, as well as to promote it as the perfect data compression software for streaming tape drives.

The company also decided that the length of the tape could be increased from 600 feet to 1000 feet because, unlike standard audiocassette tapes, the drive mechanism that pulls the tape through the heads isn't the tape itself.

The result is the QFA-500, with 500 megabytes of memory backup capacity. It sits in a 5¼-inch form factor and backs up data at 4 to 6 megabytes per minute, depending on the data. Each QFA-500 needs 512K bytes of system RAM.
Price: $1395; external, $1795; XT/AT adapter, $150; PS/2 adapter, $300; tape cartridge, $43.40.
Contact: Colorado Memory Systems, Inc., 800 South Taft Ave., Loveland, CO 80537, (303) 669-8000.
Inquiry 1155.



Data compression makes the QFA-500 a 500-megabyte tape drive.

Lundy Monitor Features 1600 by 1200 Resolution

The Lundy 1612 is a 1600- by 1200-pixel color graphics monitor for the IBM AT, PS/2s, and compatibles. It comes with a high-speed graphics controller and support for leading software packages.

Besides the 19-inch CRT unit, the Lundy 1612 includes an interface board installed inside the host computer, and an external box (typically sitting below the monitor's swivel stand) that holds the graphics controller and video RAM on two separate circuit boards. Interfaces are available for both the 16-bit AT bus and the Micro Channel bus, and Lundy plans to announce boards for other systems later this year. The standard AT board comes with a megabyte of video RAM.

The graphics controller uses proprietary ICs and the 50-MHz Texas Instruments TMS 34010—a 32-bit graphics processor that TI says is capable of drawing at the rate of 6 MIPS. The controller/monitor combination can display up to 16 colors simultaneously from a palette of 4096 at 1600- by 1200-pixel resolution, or, with software reconfiguration, 256 colors from a palette of 16 million at 1024- by 768-pixel resolution. The video RAM is up to 8 megabytes.

The company claims compatibility with more than 100 software packages.
Price: $9950; MCA version, $10,150.
Contact: Lundy Electronics & Systems, Inc., Computer Graphics Division, One Robert Lane, Glen Head, NY 11545, (516) 671-9000.
Inquiry 1157.

Dot Matrix Just Got Better

The Proprinter X24E and XL24E are 24-wire, bidirectional dot-matrix printers rated at 288 characters per second in 12-character-per-inch draft mode. That's about 20 percent better than their predecessors, IBM says.

The print buffer has been enlarged to 14K bytes. The FontSet option provides for 11 additional fonts, and there's now a display panel for setup where there used to be DIP switches. You also get more paper-handling, paper-width, paper-weight, and programmable features than you probably care to have.

The printer's computer interface is parallel or, optionally, RS-232C or RS-422.
Price: X24E, $899; XL24E, $1199.
Contact: Consult your local telephone book's white pages for IBM Corp. or call (800) 426-2468.
Inquiry 1156.

Replace Mac's Mouse and Keyboard with Speech

Move down! Move right! Double-click!

You've just told your Macintosh to open an application. And you didn't need to use the mouse. You did it by speaking into a microphone.

That's what the new Voice Navigator from Articulate Systems can let you do. After teaching the Voice Navigator a basic vocabulary of commands, you can run applications and perform complex operations entirely by voice.

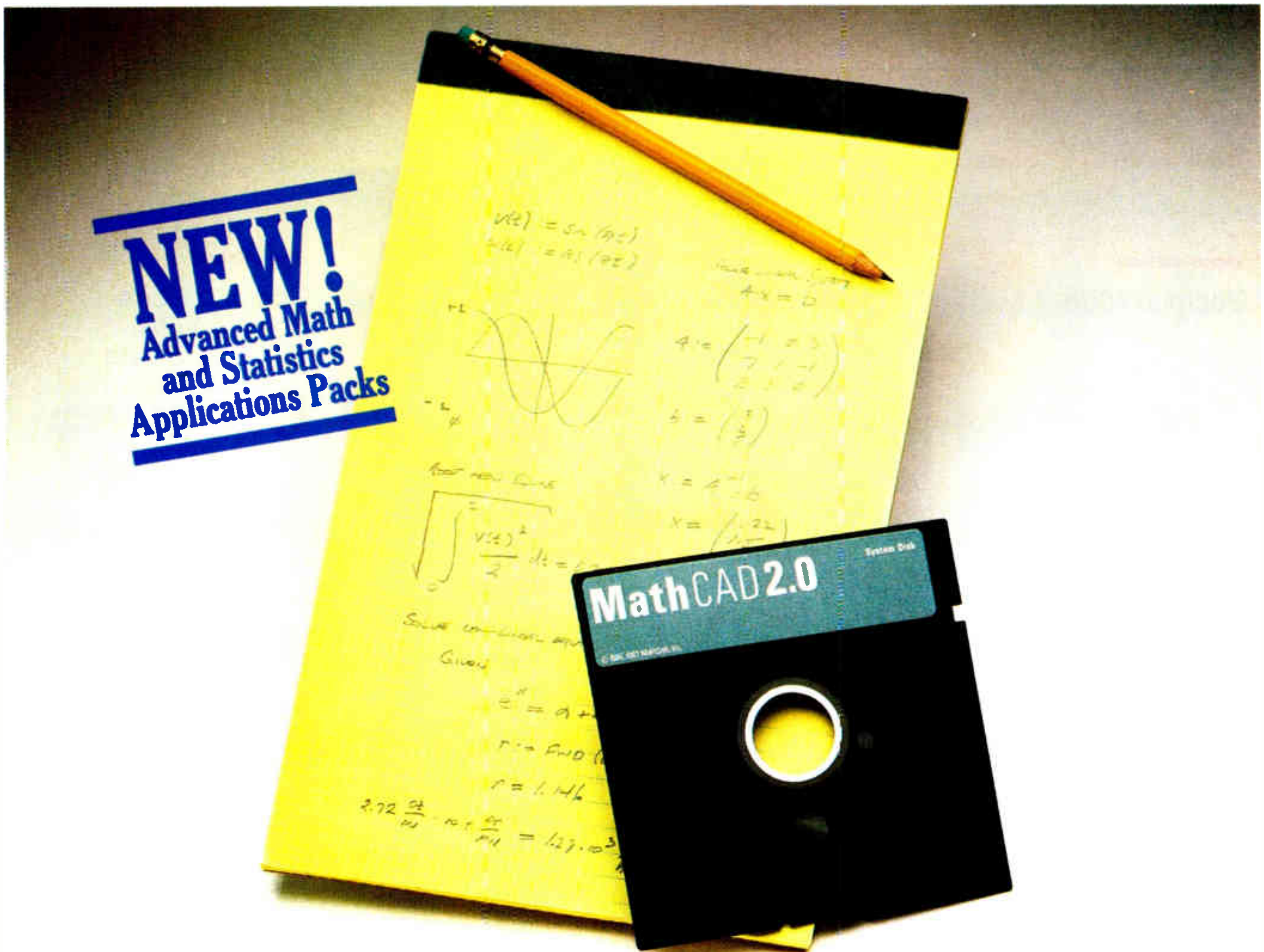
Voice Navigator consists of a hardware/software combination that includes an A/D converter and voice recognition software. The hardware is contained in a 9-inch-square box that plugs into the Mac's SCSI port.

The system comes with a built-in microphone, speaker, and sound controls, and it can also be used with virtually any external microphone/headset combination. On the software side, the Voice Navigator can be used as a desk accessory or INIT file so that voice control is always available.

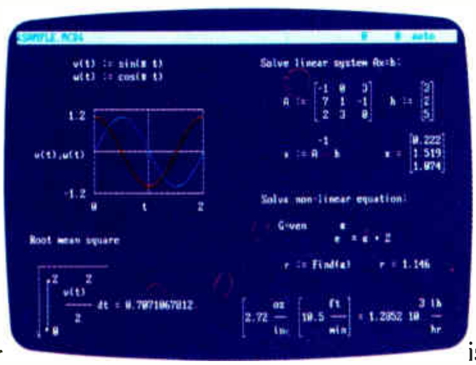
You can control the mouse cursor or any command in any application using the Voice Navigator—to control a HyperCard Japanese "language lab" application, for example. The Voice Navigator can also be used with Apple's MacroMaker.

A telecommunications option for later introduction involves a modem control so you can call up your Mac and tell it what to do.
Price: $999.
Contact: Articulate Systems, Inc., 99 Erie St., Cambridge, MA 02139, (617) 876-5236.
Inquiry 1158.

continued



Your pad or ours?



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MathCAD® MathSoft, Inc.

MathCAD®

MathSoft, Inc., One Kendall Sq., Cambridge, MA 02139

Circle 165 on Reader Service Card

Zoom to 4096 by 1792 Pixels

The second-generation Spectrum/8 video card for the Mac SE/30 provides a 1024- by 768-pixel display with pan and zoom features that allow you to see a part of the screen at a resolution of 4096 by 1792 pixels.

That resolution is designed with 1 bit of information corresponding to 1 pixel on the screen. At 2 bits per pixel, you can get up to four colors at a resolution of up to 2048 by 1792 pixels. At 4 bits per pixel, you rely on 16 colors and 2048 by 896 pixels. And at 8 bits per pixel, you get 256 colors and a resolution of 1024 by 896 pixels. Output can be color, gray-scale, or National Television System Committee–standard RGB.

The Mac SE/30 has an 030 Direct Slot connected directly to the 68030 microprocessor. The slot supports a 32-bit data and address bus and provides access to 32-bit ROM routines.

The Spectrum/8 will work with SuperMac, Apple, NEC MultiSync, and compatible monitors.

Price: $1895.

Contact: SuperMac Technology, 485 Potrero Ave., Sunnyvale, CA 94086, (408) 245-2202.

Inquiry 1162.

The Spectrum/8 video card zooms and pans.

RasterOps Board Aims to Make QuickDraw Quicker

Perhaps the biggest criticism of the Macintosh II as a potential engineering workstation is its graphics performance. For this reason, RasterOps Corp. has introduced the ColorBoard 118, a graphics accelerator board that, according to the company, can run applications up to 60 times faster than standard QuickDraw. (QuickDraw is a toolbox of routines for drawing graphics primitives; it's driven by the Mac's 68020 processor.)

RasterOps' 8-bit acceleration board uses a vector-processing chip, developed by Advanced Micro Devices (AMD), to intercept and accelerate the execution of certain QuickDraw commands intended for the 68020.

RasterOps' high-speed processing circuit, which incorporates the AMD chip, is called the Quad Pixel Dataflow Manager (QPDM). The company plans to implement it on its future boards using an AMD 2000 RISC processor.

The company claims 100 percent compatibility with QuickDraw.

Price: $3195.

Contact: RasterOps Corp., 10161 Bubb Rd., Cupertino, CA 95014, (408) 446-4090.

Inquiry 1163.

Compaq Goes Beyond VGA with New Board

Compaq has introduced a high-resolution graphics board aimed at PC users who are not satisfied with IBM's VGA resolution.

The new Advanced Graphics 1024 Board, built around the 50-MHz Texas Instruments 34010 graphics processor, can display 16 colors out of a palette of 16 million at resolutions of up to 1024 by 768 pixels.

With special drivers, the 1024 boosts AutoCAD operations by as much as five times compared to a VGA system, Compaq claims. Compaq is working with a number of vendors to develop drivers for the new graphics system, including CADKEY, Evolution Computing, and Graphic Software Systems. The board will work with many high-resolution monitors.

An optional 512K-byte memory board adds the capability to display 256 colors simultaneously.

Price: $1499; memory board, $599.

Contact: Compaq Computer Corp., 20555 FM 149, P.O. Box 692000, Houston, TX 77269, (713) 370-0670.

Inquiry 1164.

continued

Dictate to Your 80386 with Dragon Systems' Board

DragonDictate is an AT-compatible board that turns your 80386-based system into a large-vocabulary dictation machine. And for the first time, Dragon Systems says, you don't have to be specially trained to use it.

DragonDictate is based on the DragonWriter speech recognition system. Even with a 5000-word vocabulary, DragonWriter lets you dictate at near real-time rates.

The control and display interface is the key to its ease of use, Dragon Systems says. When a new word is spoken, the system relies on abbreviated keyboard entries to enter text. As it builds the acoustic models of the vocabulary, it shifts to real-time speech recognition to accelerate text entry. The interface controls the display of most likely words from the dictionary and the active vocabulary. It also places the voice- or keyboard-selected word into the text of the document.

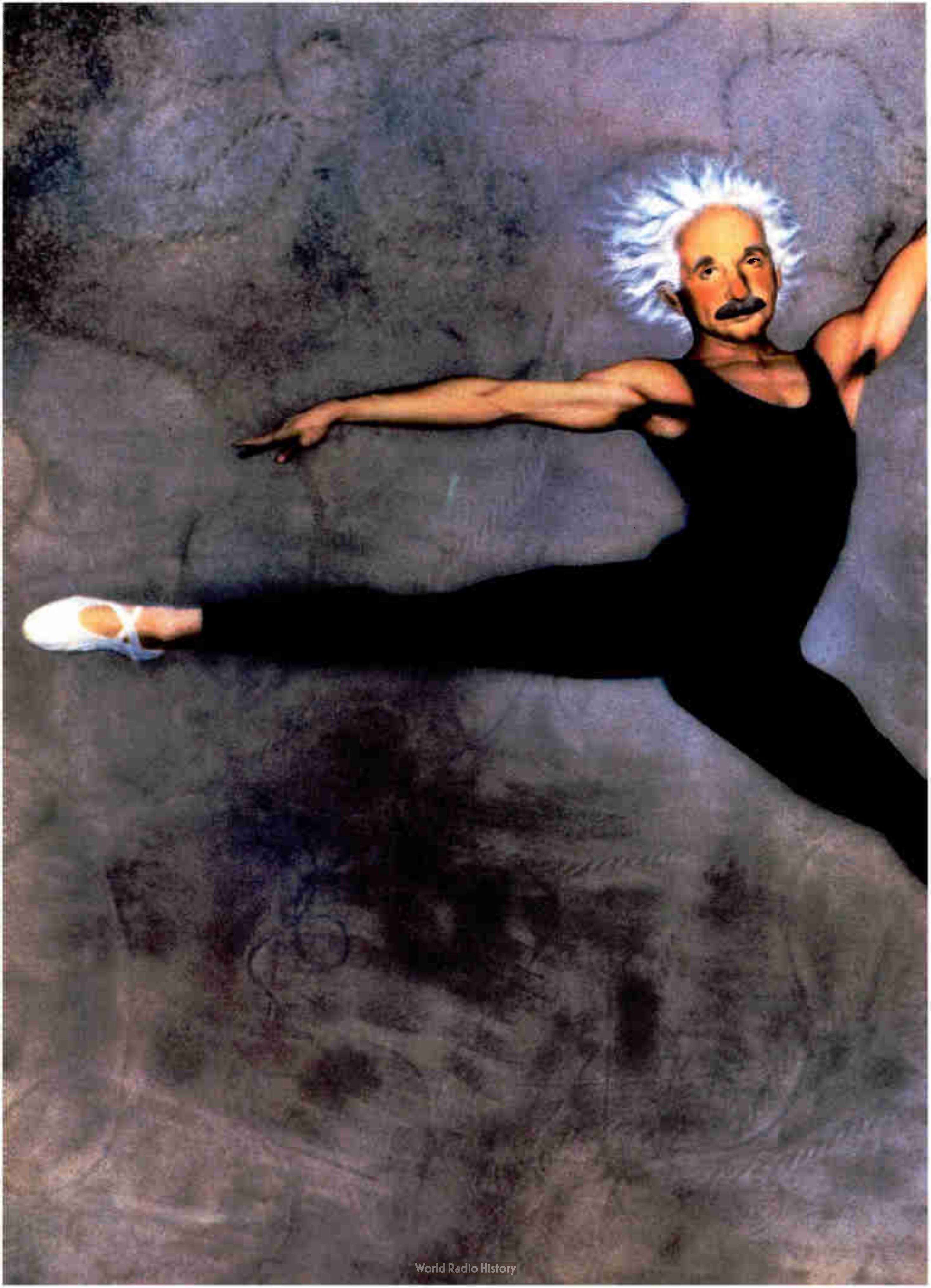
You need an 8-bit slot, a 1.2-megabyte 5¼-inch floppy disk drive, a 40-megabyte hard disk drive, 640K bytes of system RAM, a half-megabyte of expanded memory, and 4 megabytes of extended memory.

The board also comes with software, a headset microphone, and an instructional VHS videotape.

Price: DragonDictate, $9000; DragonWriter, $4500.

Contact: Dragon Systems, Inc., 90 Bridge St., Newton, MA 02158, (617) 965-5200.

Inquiry 1165.



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Circle 311 on Reader Service Card (DEALERS: 312)

Plastic Optical-Fiber LAN Eases Installation Hassles

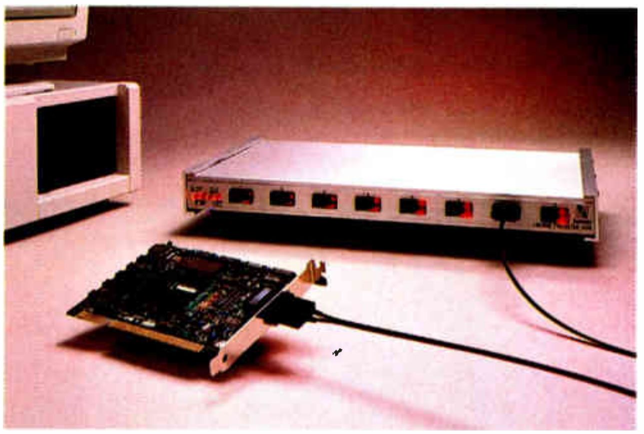
The Fiberstar PC network, the first commercially available plastic optical-fiber LAN, couples the installation ease of twisted-pair cabling with some of the communications advantages normally associated with glass optical fiber.

This takes plastic optical fiber beyond the realm of illumination of automotive instrumentation and into the office. At about $1000 per node, the 2-Mbps Fiberstar PC network makes plastic fiber a cost-effective alternative to twisted-pair copper wiring for local-area networking all the way to the desktop. And it brings with it glass fiber's often-touted advantage of immunity to electromagnetic interference.

Installation hassles are almost nonexistent with the Fiberstar PC LAN. This is because the Mitsubishi Rayon-manufactured optical fiber that Netronix uses is roughly 16 times the diameter of the glass optical fiber used in LANs. The plastic fiber is also less expensive and tougher than glass, which cannot be wound tighter than about a foot in diameter without degrading the signal or breaking the glass.

Installing and terminating glass optical fiber has always been the domain of telephone company technicians, who are said to use the expertise of an electrician and the precision and instruments of a jewel cutter. The plastic optical fiber used here, however, can easily be installed by the average office worker.

There's no need to polish the end of plastic fiber, and the plastic connector (by Amp, Inc.) simply snaps into its place on the XT-compatible add-in cards in your computer



Fiberstar, the plastic optical-fiber LAN from Netronix.

and on the Netronix networking hub.

Each add-in card includes a 650-nanometer LED source and a positive-intrinsic-negative photodiode receiver. Optional cards conform to the TCP/IP.

The 16-port Fiberstar active hub is configured with 650-nm LEDs for plastic optical-fiber transmission distances of up to 500 feet, with 850-nm LEDs for glass optical fiber for transmission up to 5000 feet, or with combinations of different LEDs for the different media. (Active hubs have repeaters; passive hubs simply switch the signals.) In a star configuration, mul'iple hubs can support up to 2 0 nodes.

Through Netronix bridges and broadband adapters, Fiberstar PC hubs can hook into standard baseband networks like Ethernet and StarLAN, and into standard broadband networks as well.

Price: Card, $595; card with TCP/IP package, $895; 16-port hub, $2195.
Contact: Netronix, 1372 North McDowell Blvd., Petaluma, CA 94952, (707) 762-2703.
Inquiry 1159.

Sync Your PC with Telecommunications

The Network Access Controller from Sync Research gives many types of terminals a 64-kbps clear channel for data transfer. Standard support is available for PC and other asynchronous (including asynchronous X.29 hosts) and synchronous devices. A 3270 emulator is included to allow your PC to emulate 3270 terminals, both bisynchronous and System Network Architecture. (SNA is the set of specifications governing IBM networks; it's analogous to the Open Systems Interconnection reference model.)

About the only networking protocol left is TCP/IP, commonly used for Ethernet LANs, and Sync Research says that it plans to upgrade the Network Access Controller for TCP/IP functionality next.

The 64-kbps channel is possible through the X.25-standard packet-switched network.

The enabling device within the controller is a packet assembler/disassembler (PAD) that assembles packets of data for transmission with other packets on the X.25 telecommunications infrastructure. (Voice is carried through the same infrastructure but is not compressed into packets.)

Unlike the PAD modem recently introduced by Hayes, the Network Access Controller works only with leased telecommunications lines, not with dial-up, circuit-switched lines. That means you must lease the line from your telephone company at a premium price. But because you'll own that connection, there's no need to dial a number, and you'll never get the standard holiday message, "All circuits are busy; please try your call again."

Each Network Access Controller supports up to eight simultaneous sessions per terminal, and the standard Network Access Controller has four terminal ports. A separate control unit (available from several vendors) gives you multidrop capabilities with bisynchronous and SNA terminals for as many as 32 terminals per session. Based on the number of sessions and the multidrop capabilities, a Sync Research spokesperson estimated that between 40 and 100 people will use a single Network Access Controller simultaneously.

To tie it all together, you'll need to add network management hardware and software from Sync Research.

The network management hardware consists of an 80286-based machine with 2 megabytes of RAM, a 71-megabyte hard disk drive, a port for Sync's proprietary AT-compatible communications board, and an asynchronous terminal. The 80286-based machine runs Xenix and the Sync software.

Price: $5880; four-port expansion, $2500 each; network management hardware, $12,500; software, including Xenix, $16,240.
Contact: Synch Research, 13891 Newport Ave., Tustin, CA 92680, (714) 669-8020.
Inquiry 1160.

continued

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IC Programmer Expanded for Logic Devices

The JE680 Universal IC Programmer is an EPROM programmer that has been upgraded to program many types of logic and memory-type programmable devices, with or without computer support. Now it can also program memory devices such as MOS/CMOS EPROMs, MOS/CMOS EEPROMs, PROMs, and bipolar PROMs. It will also program programmable-array-logic devices.

The computer interface is RS-232C. There's a parallel printer port, and the JE680 supports standard, intelligent, and quick-pulse programming methods.

A pin-check function with pulse-reflection technology lets you examine individual pins. You can use up to 18 data formats, and the JE680 is compatible with many of the software packages written for other EPROM programmers. Or you can use the software option package—including Boolean conversion, autocompiling, and fuse map generation—for logic design applications.

After programming, you can use the JE680 to do an automatic self-test, an insertion and backward-device check, and other test functions.

Price: $1799.95; software, $29.95.

Contact: Jameco Electronics, 1355 Shoreway Rd., Belmont, CA 94002, (415) 592-8097.

Inquiry 1166.

The JE680 Universal IC Programmer works for memory and logic devices.

EPROM Eraser Shields You from Shortwave UV Light

The EE128 EPROM Eraser uses a 4-watt, 254-nanometer ultraviolet light source to erase up to nine EPROMs in less than 30 minutes. But you won't get any UV exposure with the snaplock drawer and safety switch that help block the escape paths for these harmful rays. There's even a lamp-on indicator for extra protection.

Price: $79.95.

Contact: Ultra-Lum, Inc., 217 East Star of India Lane, Carson, CA 90746, (213) 324-2247.

Inquiry 1167.

Firmware Prototyping Made Easy

The Analogica-T is a software and hardware development tool designed for prototyping computer control interfaces to scientific instruments and industrial equipment. It looks like a full-length board for your PC (it's XT or AT compatible) and a hardware design unit that's just about the length and width of your briefcase.

An 8255-A provides 24 lines of parallel I/O, and switching allows address selections as required for such things as direct memory access and serial I/O.

Circuitry is mostly wire-wrap, Westcoast Technical & Hobby says, so you can make custom modifications. It also lowers the price for using printed circuit boards with copper on only the top and bottom layers.

Driver software is supplied in .EXE and .ASM formats on a 5¼-inch floppy disk drive. Executable 8088 code and an 8255-A driver are also included, as are source code skeletons.

The design board includes buffered system signals brought to terminal strips adjacent to four solderless breadboards.

Each kit comes with assembly instructions and schematics for installation in about 30 hours, depending on the options.

Price: $721; assembled and tested, $1203.

Contact: Westcoast Technical & Hobby, P.O. Box F110-415, Blaine, WA 98230; or call the headquarters in Surrey, BC, Canada at (604) 591-1624.

Inquiry 1168.

Mac Digitizing System Works in 3-D

Forget mice; forget touch panels. At the MacWorld Expo, Mira Imaging introduced a digitizing system that uses three dimensions. The new HyperSpace system consists of a small table that can detect the position of a pen-like stylus in three-dimensional space. Company representatives demonstrated the system by digitizing a bust of an Egyptian pharaoh.

You place the stylus on a point on the surface of the bust and then press the mouse button. A group of electromagnetic sensors located in a small table under the bust then determines where the stylus is located and passes the information to the Macintosh.

Once a sufficient number of points is determined, the System software can then group the points into triangular planes to create a surface. The software can then soften this jagged surface into a smoother one. Once the surface has been generated, you can shade the surface and change the angle of lighting by moving the stylus.

The company claims that the system has a resolution of 0.03 inch at a distance of 15 inches. As expected, the resolution gets worse with increasing distance. In addition to the *x*, *y*, and *z* coordinates of the stylus, the system can also measure its pitch, yaw, and roll. Because it uses electromagnetic waves, the HyperSpace system can be used only with nonmetallic models.

Price: $5300.

Contact: Mira Imaging, Inc., 969 Logan Ave., Salt Lake City, UT 84105, (801) 485-6765.

Inquiry 1169.

continued

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Microprocessor	80386SX 16MHz 16KB, 82385 Cache	80286 12MHz 1 wait state	80386 20MHz 64KB, EE 82385 Cache	80386sx 16MHz	80386 25MHz 64KB,EE 82385 Cache	80386 20MHz 32KB Cache
Bench Mark*	3.36 Mips	1.93 Mips	4.78 Mips	2.49 Mips	6.07 Mips	4.60 Mips
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Storage 1.44MB, 3 1/2" FD	1	Optional ($275.00)	1	Optional ($275.00)	1	Optional ($275.00)
1.2MB, 5 1/4" FD	Optional ($225.00)	1	Optional ($225.00)	1	Optional ($225.00)	1
Fixed Disk Opt	40MB< 28ms	40MB<30ms	40MB<35ms, 120MB<28ms	20MB<29ms, 40MB<30ms	40MB<35ms 120MB<28ms	40MB<30ms, 110MB<25ms
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Circle 14 on Reader Service Card (DEALERS: 15)

Graphics Software Engineering

Using the MetaWindow graphics driver from MetaGraphics, Turbo Meta-Menu provides a user interface for any graphics application program with Turbo Pascal versions 4 and 5. You can create menus, pop-up messages, button menus, and more.

The Turbo Meta-Menu utilities package consists of a library with over 70 procedures that help you create menus. Two additional programs are also included: CurEdit is an icon editor, and MakeMenu is a Turbo Pascal code generator.

To run the program, you need MetaWindow graphics, an IBM PC with a video graphics adapter, a mouse, and a hard disk drive.

Price: $149; MetaWindow, $95; source code, $75.

Contact: Island Systems, 7 Mountain Rd., Burlington, MA 01803, (617) 273-0421.

Inquiry 1120.

FORTRAN Text Editor for the Mac

Developing FORTRAN codes and models on the Macintosh has been difficult in the past due to the cumbersome editors. FREDitor is a text editor with the standard features along with some special functions, such as multiple windows, global regular-expression parser (GREP) search and replace, custom auto-wrap, on-screen column markers, and the ability to generate tables for export to spreadsheets.

FREDitor was developed at Battelle's Pacific Northwest Laboratories and is being published and marketed by TechAlliance (formerly A.P.P.L.E. Co-op).

FREDitor runs on any Macintosh, according to TechAlliance.

Price: $79.95.

Contact: TechAlliance, 290 Southwest 43rd St., Renton, WA 98055, (206) 251-5222.

Inquiry 1121.

Modula-2 on the Mac

The MetCom Modula-2 integrated programming environment for the Mac includes a multiwindow text editor, a one-pass compiler, and an interactive debugger.

The MetCom Editor uses information from the compiler to show various positions in the source program where syntactic errors occur. You can also shift blocks of text, indent text as you enter it, or display windows as tiles or stacks. You can open multiple windows and handle files of any size.

The one-pass compiler generates native code for the 68000/68020 processors, and the code needs no explicit linking, according to the manufacturer. Each compilation produces two files: an object file used by the linker for execution, and a reference file used by the source-level debugger. A dialog box also lets you know how the compilation process is going.

You can view the execution environment at run time with the Runtime Examiner. If an error occurs, the debugger is called and displays several windows that show the source statements being executed, the modules and procedures called, and the values of module and procedure variables, as well as the addresses of the various loaded modules.

A variety of libraries and Macintosh interface modules is included with the program.

MetCom Modula-2 runs on the Mac Plus, SE, and II with System version 4.1 or higher and two 800K-byte floppy disk drives. A hard disk drive is recommended but not required.

Price: $245.

Contact: Metropolis Computer Networks, Inc., The Trimex Building, Route 11, Mooers, NY 12958, (514) 866-4776.

Inquiry 1124.

Adding Graphics to Unix the Convenient Way

The Convenience Plus Unix front end is a graphics interface for Unix from SoftScience. The program lets you perform file management, operating-system commands, and other administrative functions. It offers you a graphical tree display of your stored files, which you can traverse with arrow keys or a mouse.

You can call up windows to display and interact with a graphical image of file storage. The window can also list files or running applications. Other features include a hexadecimal editor/viewer, a search function, and utilities for manipulating files in groups or individually. You can also move files across directories, and you can create and delete directories with the interface.

The program is compatible with Sun, AT&T, and other Unix systems.

Price: $399.

Contact: SoftScience Corp., P.O. Box 42905, Tucson, AZ 85733, (602) 326-4679.

Inquiry 1122.

Forth in the Public Domain

To encourage programmers to use Forth to develop large applications, the Silicon Valley Chapter of the Forth Interest Group has donated F-PC 2.25 to the public domain. The Forth development environment is derived from F83, an earlier public domain version of Forth.

F-PC 2.25 comes on four 360K-byte disks with most files archived. The object code takes 400K bytes of RAM, while the source code and documentation take up about 3 megabytes of disk space.

Some of the features offered by F-PC 2.25 include a command-line interpreter, a high-level procedure compiler/decompiler, an 8086/87 assembler/disassembler, a multitasker, a single-step debugger, core image dump, source code listing and indexing, text searching through files, a turnkey application generator, and a meta-compiler for system regeneration. A collection of applications includes floating-point packages, object-oriented procedures, databases, graphics, mathematics, games, music programs, and more.

The program runs on the IBM PC. A graphics card and hard disk drive are recommended.

Price: $25; user's manual, $20; technical reference manual, $30.

Contact: Offete Enterprises, Inc., 1306 South B St., San Mateo, CA 94402, (415) 574-8250.

Inquiry 1123.

continued

INSTANT MAINFRAME. JUST ADD SCO.

Not too long ago, a few dozen people sharing the same programs, resources, and information on a single computer at the same time meant only one thing—a mainframe.

Powerful, big, expensive, and proprietary.

More recently, the same people could be found doing exactly the same things—simultaneously sharing programs, resources, and information—on a minicomputer.

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Then along came the latest generation of personal computers. And now, the same people are more and more likely to be found doing exactly the same things—simultaneously sharing programs, resources, and information—on a PC.

And not a whole officeful of PCs networked together, either, but *a single PC powering the whole office at once.*

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Or even letting users integrate full-featured multiuser productivity packages *of their choice* under a standard, friendly menu interface.

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Circle 255 on Reader Service Card

Digital Signal Processing

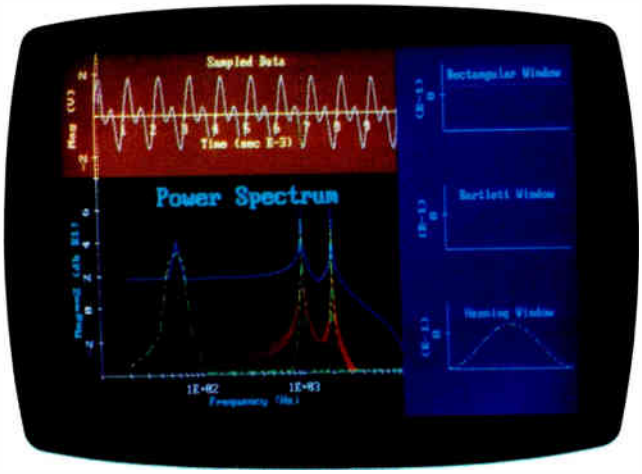
PC Data Master, a DOS environment for signal processing and display, has been enhanced with separately compiled modules for systems with and without a math coprocessor, a multisignal plot utility, and an enhanced DOS shell.

The program combines graphics routines, real- and complex-data-file math routines, digital signal processing utilities, test-data generation routines, data sampling routines, and binary data pipes to create a DOS-based DSP system.

Most graphics boards are supported, and you can integrate data-analysis functions into the program using most compilers or assemblers compatible with DOS 2.0 or higher.

The shell that is at the heart of PC Data Master lets you implement independent DOS console and graphics screen windows. When the shell is active, you can interact with DOS application routines in the console window without disturbing screen graphics, according to the manufacturer. The shell also provides binary data pipes for linking data-processing steps, implemented as independent executable files into multistage data transformations. The pipes are distinct from the DOS pipes and don't affect the standard input and output logical devices, according to Durham Technical Images.

A waveform module is also included with PC Data Master. You can display individual or multiple data files using the plot system's auto-configuration capabilities. You can use pop-up menus and forms, and you can adjust each plot on the display for size, placement, colors, titles, labels, assignment of data files and channels to axes, and more. You can also store display templates with display designs, and you can print hard copies of data displays in portrait and landscape orientation using dot-matrix or laser printers.



PC Data Master 2.0 is an enhanced DOS environment for signal processing and display.

Version 2.0 comes with an augmented set of DSP utility modules. Operations include forward and inverse fast-Fourier-transform and fast-Hartley-transform routines, convolution, correlation, window generation, FIR filter design, and test-data generation.

You can implement many multistage transformations by combining these basic operations with data-file math routines in data pipes, according to Durham Technical Images. Data acquisition modules for MetraByte analog input products are also included, and you can integrate routines for other analog I/O products.

PC Data Master 2.0 runs on the IBM PC with 256K bytes of RAM and DOS 2.0 or higher. A hard disk drive and a math coprocessor are recommended. Shells are provided for CGA, Hercules, AT&T, EGA, and VGA graphics.

Price: $135.

Contact: Durham Technical Images, P.O. Box 72, Durham, NH 03824, (603) 868-5774.

Inquiry 1126.

Stats Packs Added to MathCAD

Two recently released application packs for MathCAD 2.0 cover tests and estimation, and modeling and simulation, respectively. MathCAD lets you use a PC like a scratch pad, according to MathSoft. You can enter and calculate equations, create plots, and enter and edit text. The program also lets you integrate math, text, and graphics on- and off-screen.

The Tests and Estimation pack lets you implement standard test procedures, create your own test procedure, simulate experiments, and model data from within your MathCAD document. A set of standard routines including parametric and nonparametric techniques is included.

The Modeling and Simulation pack includes techniques for modeling data and carrying out simple Monte Carlo simulations. Each pack contains 16 standard procedures.

These application packs are the second and third in a series for use with MathCAD. The first in the series was the Advanced Math Applications Pack. You can purchase the packs separately or bundled together with MathCAD 2.0.

MathCAD 2.0 runs on the IBM PC with at least 512K bytes of RAM and DOS 2.0 or higher. The company recommends a math coprocessor.

Price: Statistics I: Tests and Estimation, $59; Statistics II: Modeling and Simulation, $69; I and II, $99; MathCAD 2.0, $349.

Contact: MathSoft, Inc., One Kendall Sq., Cambridge, MA 02139, (800) 628-4223; in Massachusetts, (617) 577-1017.

Inquiry 1128.

continued

Building Chemical Structures on the Mac

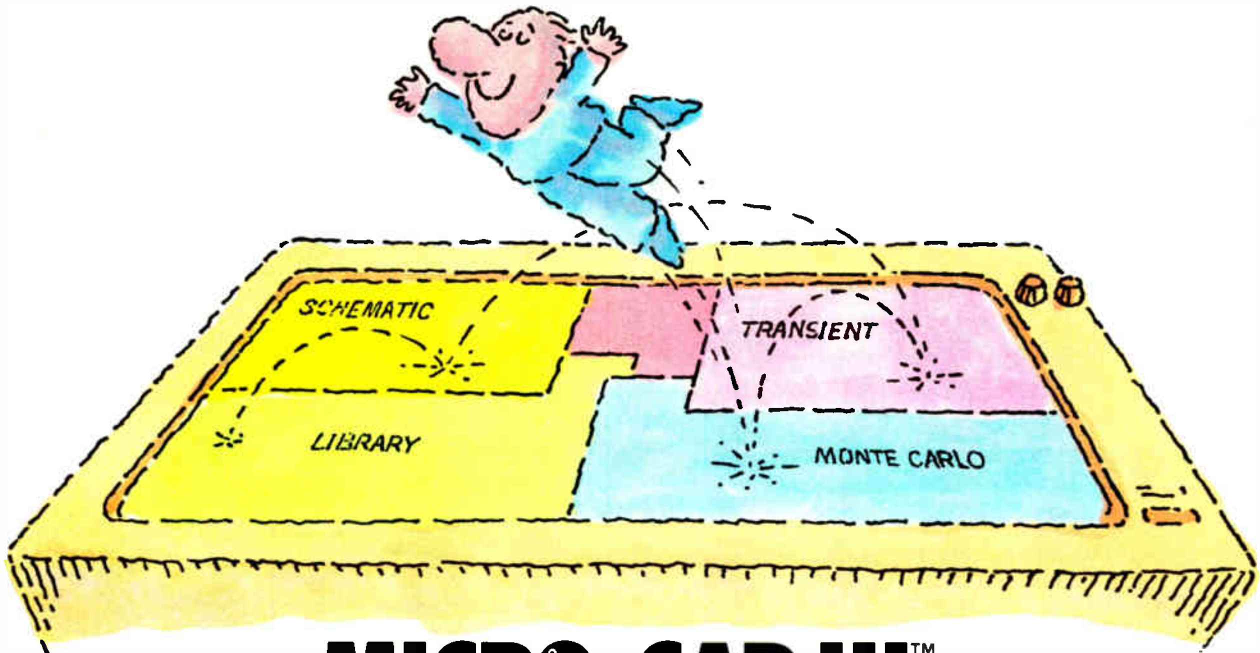
Chemists involved in searching STN International's chemical structure database may find that ChemConnection simplifies the process of constructing a chemical structure. You can draw a query structure off-line using the same drawing capabilities as in the ChemIntosh Desk Accessory, according to SoftShell. You also don't need to know all the structure-generation commands used by the Chemical Abstracts Service, SoftShell reports.

ChemConnection runs on the Macintosh with at least 1 megabyte of RAM and a hard disk drive.

Price: $395.

Contact: SoftShell International, Ltd., 2004 North 12th St., Grand Junction, CO 81501, (303) 242-7502.

Inquiry 1125.



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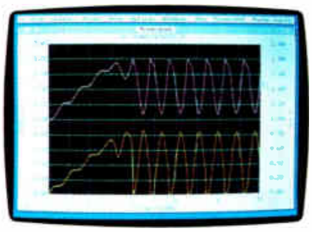
THIRD-GENERATION INTERACTIVE CIRCUIT ANALYSIS. MORE POWER. MORE SPEED. LESS WORK.

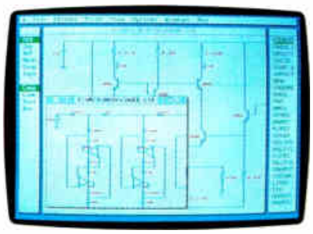
MICRO-CAP III,™ the third generation of the top selling IBM® PC-based interactive CAE tool, adds even more accuracy, speed, and simplicity to circuit design and simulation.

The program's window-based operation and schematic editor make circuit creation a breeze. And super-fast SPICE-like routines mean quick AC, DC, Fourier and transient analysis—right from schematics. You can combine simulations of digital and analog circuits via integrated switch models and macros. And, using stepped component values, rapidly generate multiple plots to fine-tune your circuits.

We've added routines for noise, impedance and conductance—even Monte Carlo routines for statistical analysis of production yield. Plus algebraic formula parsers for plotting almost any desired function.



Transient analysis



Schematic editor

Monte Carlo analysis

Modeling power leaps upward as well, to Gummel-Poon BJT and Level 3 MOS—supported, of course, by a built-in Parameter Estimation Program and extended standard parts library.

There's support for Hercules,® CGA, MCGA, EGA and VGA displays. Output for laser plotters and printers. And a lot more.

The cost? Just $1495. Evaluation versions are only $150.

Naturally, you'll want to call or write for a free brochure and demo disk.

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SYZYGY lets you manage multiple projects in real time.

Processing Words the Mac Way

Paragon has gone one step further with its text editor QUED/M and created Nisus, a word processing program for the Macintosh.

The company says that the word processor stores text and formatting separately, so you can open and edit Nisus documents in any program or desk accessory.

In addition, the program offers a feature called Zap Gremlins, which deletes any surplus characters that might result from importing text from another operating system.

The program's search and replace makes use of GREP, and its find and replace facility lets you search by style and fonts. An Easy-GREP facility features a pull-down menu.

Ten clipboards are included in the program, each of which you can edit, append to, save, or print. An undo facility is included, as is a page-preview feature.

Paragon claims you can have any number of files open at once, and you can tile or stack windows. The program's graphics capabilities let you draw graphics directly into text, place a picture over text, or have text wrap around it. You can use the Clipboard or other applications to draw or paste graphics.

Nisus runs on the Macintosh with 1 megabyte of RAM (2 megabytes under MultiFinder). It supports the LaserWriter and the Imagewriter printers.

Price: $395.

Contact: Paragon Concepts, Inc., 4954 Sun Valley Rd., Del Mar, CA 92014, (619) 481-1477.

Inquiry 1130.

Tools for the Legal Trade

CompareRite, CiteRite II, and FullAuthority, three of Jurisoft's productivity programs, are now grouped together in one package called The Legal Toolbox.

CompareRite is a redlining program that lets you compare two versions of a document; it creates a redlined draft for you, showing the differences between the two.

CiteRite II is a citation program that checks Bluebook and California-style citations without requiring text markers to indicate where the citations

continued

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occur. The program automatically locates citations in legal documents, checks them for proper form, and reports any errors. You can operate it as a pop-up program that checks citations from within your word processor.

FullAuthority looks for citations in briefs and arranges them to create a formatted table of authorities. It lets you choose to sort them into statute, book, law review, or other citation categories.

The Legal Toolbox runs on the IBM PC with 210K bytes of RAM and DOS 2.0 or higher.
Price: $365.
Contact: Jurisoft, Inc., 763 Massachusetts Ave., Cambridge, MA 02139, (617) 864-6151.
Inquiry 1133.

A Mini Version of Solomon III

ProfitWise Basic Accounting is a smaller version of Solomon III Accounting Software for businesses with annual revenues of less than $500,000. The major difference between the two programs is the limitation placed on the number of transactions that ProfitWise will handle. The databases of both systems are compatible, so you can upgrade to Solomon III, the company reports.

ProfitWise includes general ledger, accounts payable and receivable, payroll, invoicing, fixed assets, and address modules. Add-on modules include inventory and job costing, and report and graph designer.

The program runs on the IBM PC with 512K bytes of RAM, 10 megabytes of hard disk storage, and DOS 3.1 or higher. You'll need 576K bytes for the report designer.
Price: $229; add-on modules, $229 each.
Contact: TLB, Inc., Entry Products Division, P.O. Box 414, Findlay, OH 45839, (800) 777-0521; in Ohio, (419) 424-0422.
Inquiry 1131.

Groupware Gives Structure to Projects

Manage multiple projects, people, schedules, and budgets in real time with SYZYGY, a groupware tool from Information Research. The program lets you delegate assignments and monitor workgroups with its hierarchical activity tool. Using the Gantt chart tool, you can show due dates and status of all your projects, and you can zoom in on lower-level tasks.

SYZYGY includes over 50 report capabilities, and the SQL-based query language lets you create your own reports, graphics, and relational database queries.

The interface is object-oriented and offers windows, scroll bars, and cut, paste, and copy tools. SYZYGY files are compatible with DIR, SYLK, XLS, WKS, and WK1 files.

The program runs on the IBM PC with 512K bytes of RAM. It supports monochrome, Hercules, CGA, EGA, and VGA monitors. A NetBIOS-compatible network version is available.
Price: $395; network version for two users, $595; three to 10 users, $995; 11 to 25 users, $1495; unlimited users, $1995.
Contact: Information Research Corp., 2421 Ivy Rd., Charlottesville, VA 22901, (800) 368-3542; in Virginia, (804) 979-8191.
Inquiry 1132.

continued

Power

T5200: 20MHz 386 processor, 2 internal IBM®-compatible expansion slots, VGA gas plasma display with VGA monitor port, 40MB or 100MB hard disk, 2MB RAM standard expandable to 8MB, 1.44MB 3½" diskette drive.

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Each designed to be powerful enough to take on the increasingly complex tasks that face today's sophisticated PC users.

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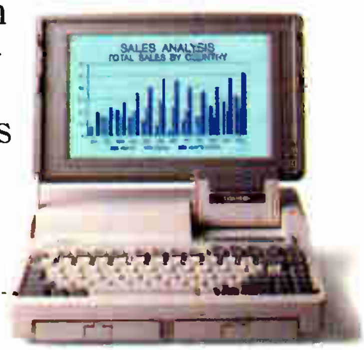
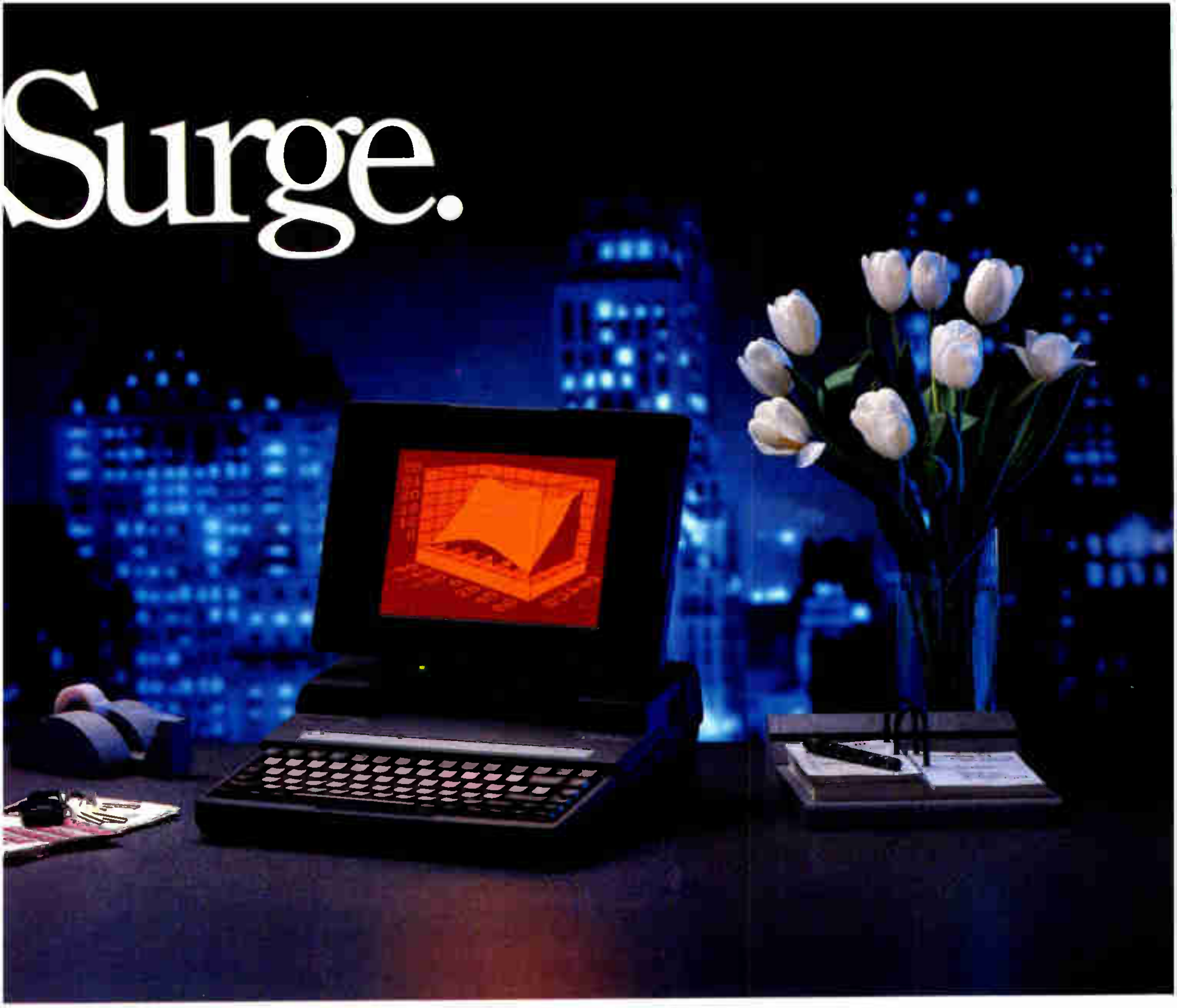
Second, the T3100e, the successor to our most popular machine—the T3100/20. We've made it nearly two pounds lighter and a lot faster—we've even added expansion capabilities. About the only thing we didn't add was more size.



T1600: Battery-powered 286/12MHz, coprocessor socket, 20MB hard disk at 27msec, 1.44MB 3½" diskette drive, 1MB RAM expandable to 5MB, detachable backlit EGA compatible LCD, removable rechargeable battery pack.

Toshiba is the world leader in truly portable PCs and manufactures a complete line of high quality dot-matrix and laser printers. For more information call 1-800-457-7777. All Toshiba PCs are backed by the Exceptional Care program (no-cost enrollment required). IBM is a registered trademark of International Business Machines Corporation. Model T5200 has not been approved by the Federal Communications Commission. This device is not, and may not be, offered for sale or lease, or sold or leased until the approval of FCC has been obtained.

Surge.



And finally, the T5200, which has enough power to replace virtually any desktop PC.

But we haven't just concentrated on power and portability. We've also constantly looked for ways to make our machines more durable, more reliable, and easier to use—down to the 800 number our customers can call for help with any technical question that might come up.

We figure that's what our users demand.

And it's by anticipating the growing needs of our users that we have continually found ways to make our machines weigh less and do more. So you can work wherever you want and however you want.

All of which might make it tempting for some people to abandon their desktop for the convenience of portability. Go ahead.

We've given you the power to do it.

T3100e: 12MHz 286 with 80287 coprocessor socket, internal half-length IBM slot, 20MB hard disk with 27 msec access, 1MB RAM expandable to 5MB, gas plasma display, 1.44MB 3½" diskette drive.

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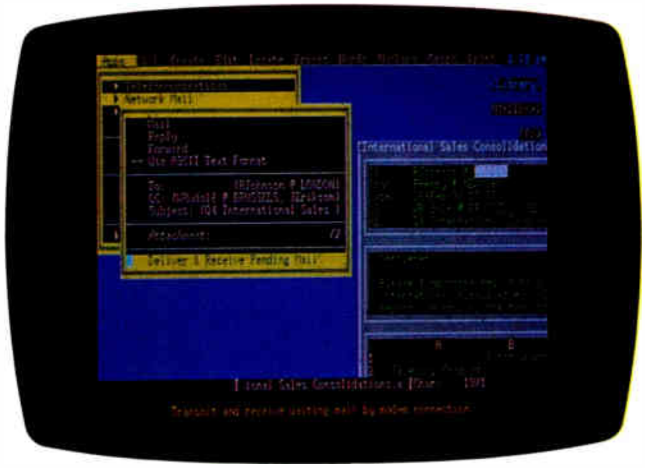
SOFTWARE • CONNECTIVITY

Networked Decisions

Ashton-Tate's integrated software program, Framework, is now available in a network version. Framework III LAN supports five users and includes built-in E-mail, file locking with three file-sharing modes, and peripheral sharing.

Framework includes word processing, database, spreadsheet, business graphics, outlining, and E-mail facilities. The program uses the standard message format, Message Handling Service, developed by Action Technologies and Novell. This lets you communicate with other Framework III users as well as users of other software packages implementing the MHS message format. You can also send mail over phone lines from one LAN to another and from LANs to remote PCs, according to Ashton-Tate.



With Framework III LAN's E-mail, you can communicate with users of Framework III and other mail systems.

Framework III is available in a variety of international languages, and Ashton-Tate reports that the LAN version will be available in translated editions in the spring of 1989. All screens, menus, messages, spelling dictionaries, and documentation will appear in the local language. Optional disks with spelling checkers, hyphenation programs, and thesauri are available in German, French, British English, Italian, Dutch, Swedish, Spanish, Danish, Norwegian, and Portuguese.

Framework III LAN runs on the IBM PC with 640K bytes of RAM. It supports the following networks: Novell Advanced and SFT NetWare/286; IBM PC and Token Ring networks running IBM PC LAN; the 3Com 3+ Network; and the AT&T StarLAN.

Price: $995 for five users.

Contact: Ashton-Tate Corp., 20101 Hamilton Ave., Torrance, CA 90502, (213) 329-8000.

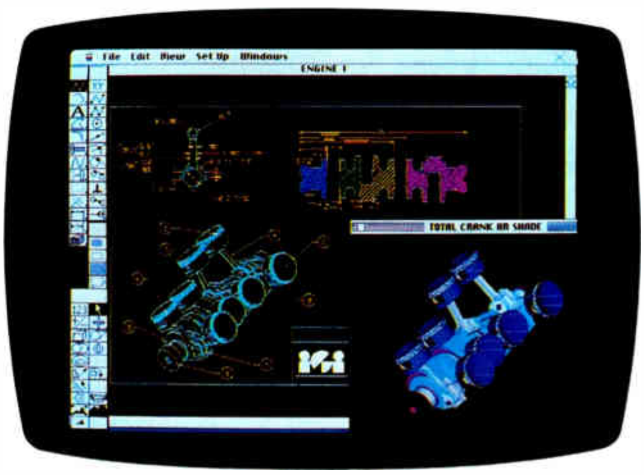
Inquiry 1136.

SOFTWARE • CAD AND GRAPHICS

First Solids Modeler for the Mac

With three-dimensional design and two-dimensional drafting capabilities, Infinite Graphics calls In-CAD the first true solids modeler for the Macintosh II. In-CAD is the first in the In-Vision family of programs for mechanical design, engineering, drafting, analysis, and manufacturing.

In-CAD combines constructive solid geometry capabilites with a WYSIWYG interface, so you can work with the modeler and see the changes take place as you make them. The program differentiates between material and voids, according to Infinite Graphics. Building-block primitives and Boolean operations are used to add and subtract material, and you can view models from any direction with hidden lines removed, shaded, or sectioned. The program also calculates area and mass/volume properties. In addition, you can modify any portion of the model, and the changes will be made automatically throughout the model.



In-CAD gives the Mac three-dimensional design, two-dimensional drafting, and solids modeling capabilities.

Other capabilities include dimensioning, splines, user-definable text fonts and cross-hatch patterns, full geometry creation and editing, subfigures for library parts and components, user-definable attributes and grouping, labeling with balloons and automatic incrementing, variable-width lines, and a programming language. The program also offers associative dimensioning with the ability to update dimensions that are related to a dimension that you've changed.

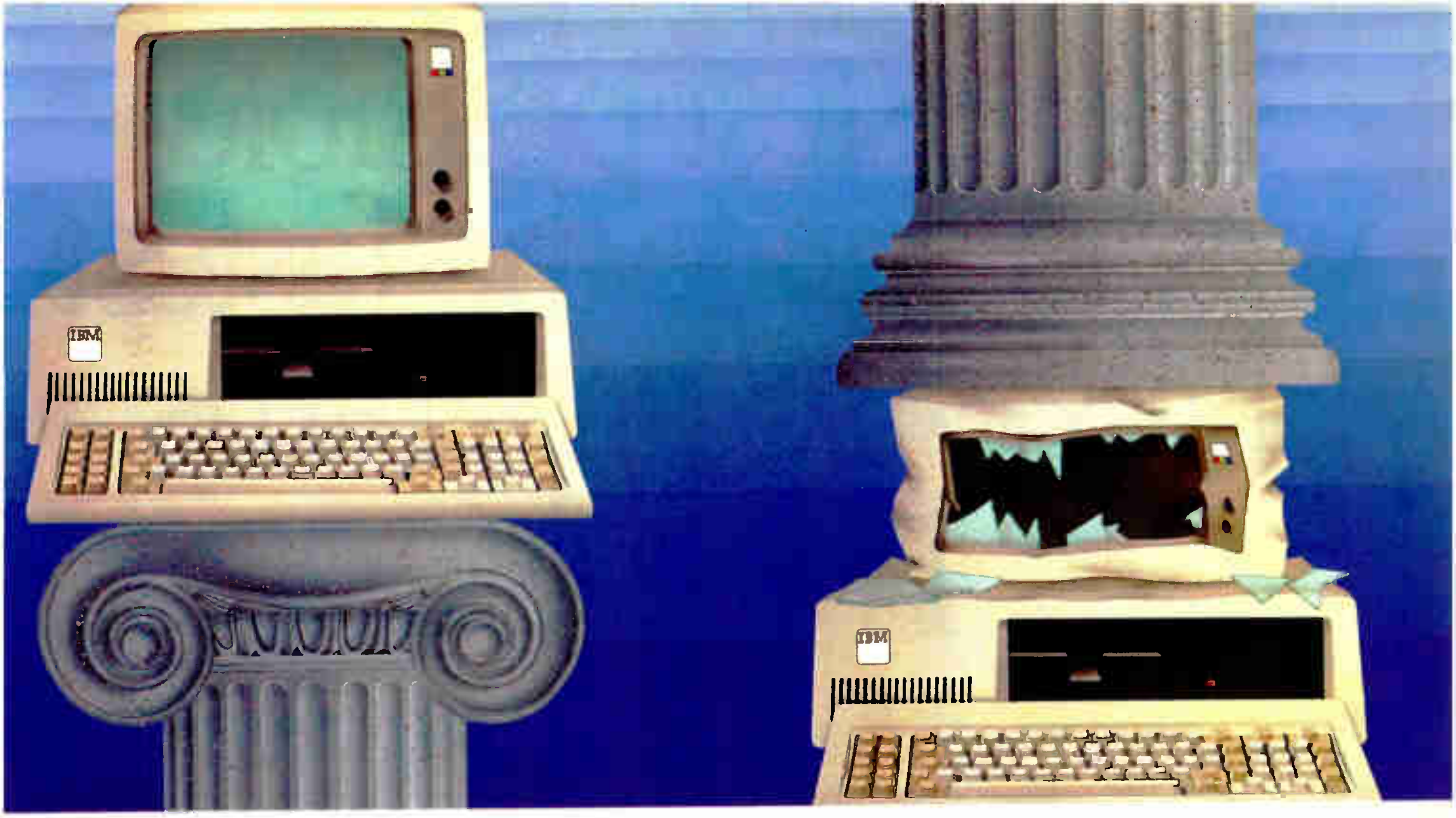
In-CAD runs on the Mac II. It features an Initial Graphics Exchange Specification translator for importing and exporting files between other CAD systems. A translator is also included for AutoCAD DXF files. The program supports Mac menus, desk accessories, and a windowing environment. You can also use a tablet or text commands.

Price: $1945.

Contact: Infinite Graphics, Inc., 4611 East Lake St., Minneapolis, MN 55406, (612) 721-6283.

Inquiry 1137.

continued



QNX® vs. UNIX OS/2

Architecture can make or break a computer system.

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QNX, on the other hand, hums along using an efficient 150K of RAM, yet provides a powerful multiuser, multitasking, and integrated networking environment.

TRUE DISTRIBUTED PROCESSING. A QNX-based PC LAN lets any user share *any* resource on the network—programs, data, devices, even CPUs—without going through the bottleneck of a central file server. With QNX you get mainframe power at micro prices.

ADDED VALUE FOR VARS. QNX is flexible enough to run on *any mix of* PCs, ATs, PS/2s, with or without dumb terminals. Even diskless workstations are supported. So whether you start small or grow to mainframe proportions, you can easily build and maintain powerful, fault-tolerant systems without spending a fortune on hardware.

REALTIME PERFORMANCE. Only QNX combines the performance of a dedicated realtime executive with the convenience of a rich development environment that includes a host of utilities, C compiler, full-screen editor, symbolic debugger, and multiple full-screen windows.

DOS SUPPORT. For those who need their daily dose of DOS, QNX allows a DOS application to run as a single task on each PC on the network.

FREE TECHNICAL SUPPORT. While users of other operating systems have to cough up hefty fees for support, QNX developers enjoy *free* hotline support and *free* BBS access. You'll get prompt answers to your questions and you'll share ideas and insights with fellow developers in the QNX community.

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C Compiler	Standard Kernighan and Ritchie.
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PC-DOS	PC-DOS runs as a QNX task.
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For further information or a free demonstration diskette, please telephone (613) 591-0931.

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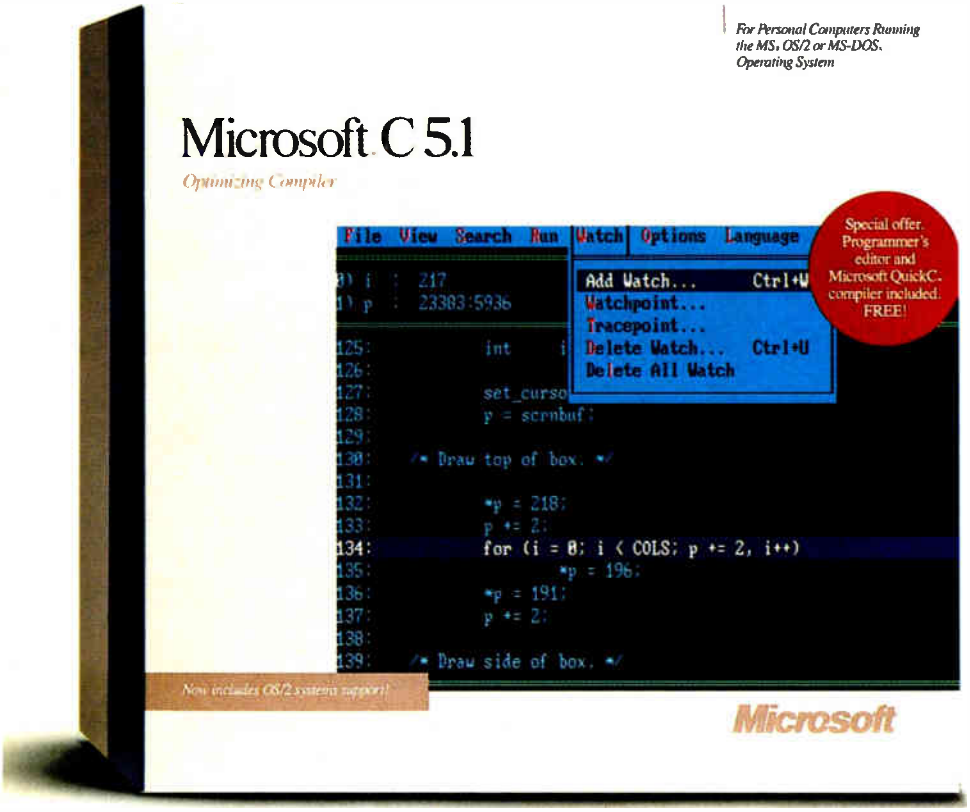


Microsoft C Optimizing Compiler 5.1 Techbox

Compiler
- Optimizations that generate the fastest code for DOS and OS/2 systems.
  - In-line code generation.
  - Loop optimizations.
  - Elimination of common subexpressions.
- Full OS/2-system support to break the 640K barrier.
  - Family API programs that run under DOS and the OS/2 systems.
  - Write multithreaded programs and Dynamic Link Libraries.
- Small, medium, compact, large, and huge memory models.
- Mix models with NEAR, FAR, and HUGE keywords.
- Fast compilation (10,000 lines/minute) with Microsoft QuickC.™
- Fastest math, in-line 8087/80287 instructions, and floating-point calls.
- More complete support of proposed ANSI standard.
- Over 350 library functions, including a graphics library.

Microsoft CodeView
- Full OS/2 systems support.
  - Debug applications of up to 128 MB under the OS/2 systems.
  - Debug multithreaded programs and Dynamic Link Libraries.
- Source-level debugging for precise control over programs.
  - Dynamic breakpoints in the source.
  - Debug programs written in a variety of Microsoft languages.
  - Full symbolic display of C structures.
  - Interactively follow linked lists and nested structures.
  - Watch variables, memory, registers, and flags.

Other Utilities
- Fast linking (twice as fast as the C 4.0 version linker).
- OS/2 incremental linker – up to 20 times faster than a full link.
- OS/2- and MS-DOS reconfigurable programmer's editor.

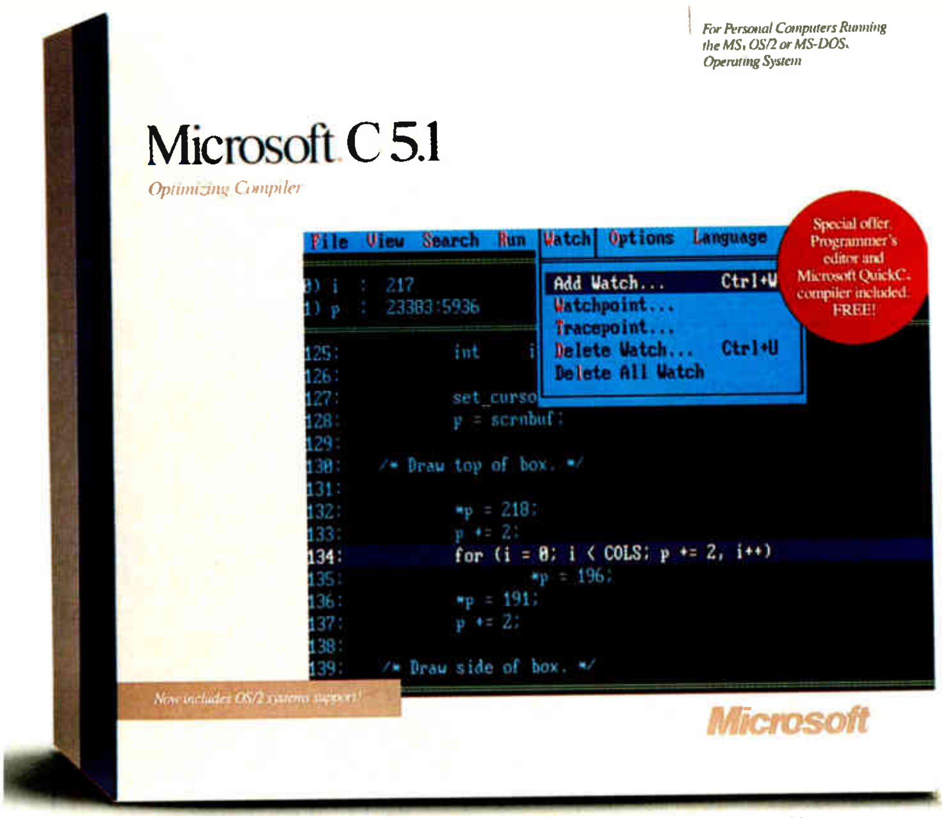
Everything about Microsoft® C Optimizing Compiler version 5.1 is dedicated to the professional programmer.

Fast code. Fast development. Fast debugging. And full support for both MS-DOS® and the OS/2 systems in a single package.

There's no faster C code on a PC, because powerful optimizations, such as in-line code generation and loop enregistering, generate executables that are compact and efficient. The documentation even teaches you special coding techniques to squeeze every last bit of speed out of your code.

Fast code isn't all you get. Under MS® OS/2, the 640K barrier is gone so you can write C programs as large as a gigabyte. You can call the operating system directly. Create more responsive programs (multiple threads allow program operations to overlap). And build Dynamic Link Libraries (DLLs) that can be shared, saving valuable memory. DLLs also allow your main programs to be smaller, so they load faster. You can even write a single Family API program that runs under both MS-DOS and MS OS/2.

The fastest possible way to create the fastest MS OS/2 programs possible.



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Other Utilities
- Fast linking (twice as fast as the C 4.0 version linker).
- OS/2 incremental linker – up to 20 times faster than a full link.
- OS/2- and MS-DOS reconfigurable programmer's editor.

Microsoft Editor is the first reconfigurable text editor for programmers that lets you develop under MS-DOS and MS OS/2. Under MS OS/2, multitasking lets you edit one file while you compile another, which cuts development time. You can even generate multiple compiles that report errors directly back into your source code.

Microsoft CodeView® is the highly acclaimed window-oriented source-level debugger that makes debugging not only fast, but extremely efficient. You can view program execution while you watch variables and register values change. And under MS OS/2 you can debug multi-threaded applications, DLLs, and programs as large as 128 MB. The Microsoft C Optimizing Compiler 5.1, designed for the professional programmer. It's all the speed you need. Call (800) 541-1261.

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Leave the Windows to Your Software

Programming in the Microsoft Windows environment can be difficult and time-consuming because you spend your time writing event handlers and responding to Windows messages. A CASE tool from CASEWorks was designed to make your life a little easier. CASE:W does most of the Windows programming for you, letting you concentrate on the application-specific parts of the program, according to the manufacturer.

CASE:W is an expert system with a knowledge base of Windows code sets and production rules. A prototyper, or front end, is included, which lets you specify the characteristics you want for the program's main window and menu system. Then CASE:W automatically generates the program code files and make files.

CASE:W gives programmers easy access to the compiler command line.

The Windows controls that the program takes care of for you include menu bars, pop-up menus, and dialog boxes.

A Program Regeneration facility is included with the package. It carries forward any code you add from one generation of an application to the next.

CASEWorks also announced plans for a CASE tool for Presentation Manager.

CASE:W runs on 80286- or 80386-based machines with at least 2 megabytes of RAM and Windows. You must have the Microsoft Windows Software Development Kit, the Microsoft C Compiler version 5.0 or higher, a make utility and linker, and a DOS- or Windows-compatible text editor. The company also recommends Microsoft's CodeView debugger.

Price: $1495.

Contact: CASEWorks, Inc., One Dunwoody Park, Suite 130, Atlanta, GA 30338, (404) 399-6236.

Inquiry 1141.

continued

Your compiler needs .RTLink™ to give you the speed and power you've been looking for.

Whatever MS-DOS compiler you use, **.RTLink** can reduce the time you spend developing programs and provide you with vital statistics that will help you optimize your program's performance.

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Also, **.RTLink**'s easy-to-use Run Time Library feature lets you significantly reduce the amount of disk space needed for storage and shipping of your programs.

The **PRFile** feature of **.RTLink** gives you access to program performance statistics in greater depth and detail than ever before available, with no changes to your source code. Now you can see where your program spends its time, and what it is doing, at timing intervals that are adjustable to thousandths of a second. Another utility, to cross-reference the raw statistics for easy analysis, is provided with source code so you can customize the analysis report.

When you consider the speed, power, versatility, and control features provided by **.RTLink** that are not available in any other linker, don't you think it's time you got a new linker?

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Sylva Upgraded

Interactive screen prototyping and COBOL code generation have been added to the Sylva System Developer CASE tool, a program that automates analysis and design techniques.

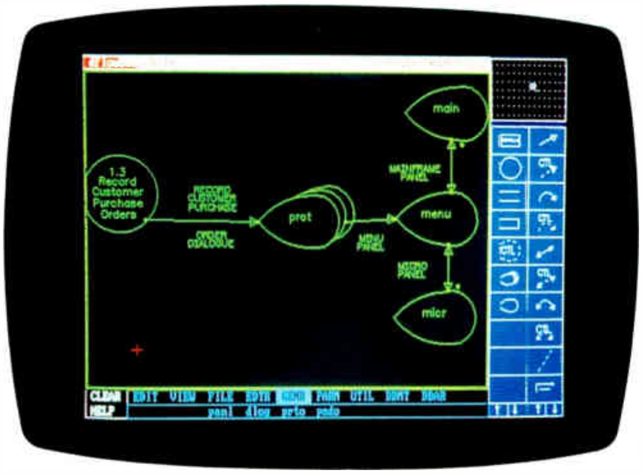
The prototyping facility lets you paint screens and create dialogues that look and behave like an interactive system, according to Cadware. After you've approved of the prototype, you can quickly generate the COBOL screen-handling source code.

To run the Sylva System Developer, you need an IBM PC with DOS 3.0 or higher and at least 640K bytes of RAM. A mouse is recommended along with an EGA board.

Price: $3495.

Contact: Cadware, Inc., 50 Fitch St., New Haven, CT 06515, (800) 223-9273; in Connecticut, (203) 387-1853.

Inquiry 1144.



Paint screens and create dialogues with Sylva's screen prototyping capability.

MicroStep

MicroStep is a CASE tool that produces C source code and executable programs from your graphics specifications, according to SysCorp. The program features an interactive graphic design environment, automatic specification analysis, generation of executable code, and the production of technical documentation. You can use the program to generate PC applications or as a rapid prototyper.

The program features a mouse-driven interface.

MicroStep runs on the IBM PC with 640K bytes of RAM, DOS 3.1 or higher, a 20-megabyte hard disk drive, an EGA or Hercules card, and a mouse.

Price: $5000.

Contact: SysCorp International, 9420 Research Blvd., Suite 200, Austin, TX 78759, (512) 338-0591.

Inquiry 1143.

TCP/IP, OSI/ISO ISDN Tutorials

Berkeley Unix networking, LANs and TCP/IP alternatives, the domain name system, bridges and routers, OSI networking, ISDN, an introduction to TCP/IP, TCP/IP for the Virtual Machine Systems programmer, message handling systems and directory services as defined by the X.400 and X.500 series of recommendations, and network operations and security issues are the scheduled topics for a series of tutorials to be held at the Lafayette Hotel in Boston, April 3 to 6. The ISDN tutorial will be held at the Omni Parker House. All the above tutorials, except for ISDN, will last two days each. The ISDN tutorial will last four days.
Price: $150 registration fee; $500 for each two-day tutorial; $1000 for the ISDN tutorial.
Contact: Advanced Computing Environments, 480 San Antonio Rd., Suite 100, Mountain View, CA 94040, (415) 941-3399.
Inquiry 930.

Electro/89 to Add Automated Design Tools

Electro/89, the IEEE show devoted to the design of electronic products and electronic circuitry, will feature a new section of exhibits on automated design tools for electronics. The conference will be held at the Jacob K. Javits Convention Center in New York from April 11 to 13.

Other exhibits will include electronic components, fiber and electric optics, control systems and benchtop testing equipment, computers, peripherals, software, and microelectronics. The six tracks of tutorials will include new technologies, automated design/semiconductors/systems, video technology, communications, professional and personal concerns, and entrepreneurial activities.
Price: Before March 24: $5 for IEEE members, $10 for nonmembers; at the door, $10 and $20, respectively.
Contact: Electro/89 Preview Program, 8110 Airport Blvd., Los Angeles, CA 90045, (213) 772-2965.
Inquiry 934.

Conference for A Programming Language

The APL (A Programming Language) special-interest group of ACM (Association for Computing Machinery) is sponsoring its annual international conference in New York City from August 7 to 10. The conference theme will be "APL as a Tool of Thought."
Price: Before June 18: $295 for ACM members, $345 for nonmembers. After June 18: $375 and $425, respectively.
Contact: APL89, P.O. Box 4368, Grand Central Station, New York, NY 10163, (212) 877-7733.
Inquiry 933.

continued

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Seminars in Computer Science and Software Engineering

Spring 1989

Programming Languages
Introduction to Programming in C; April 19-21

Advanced C — Data Structures; April 10-12

C++ and Object-Oriented Programming; April 24-25

Common Lisp Programming; April 12-14

OPS-5 Programming; April 13-14

Introduction to Prolog Programming; April 19-21

Introduction to Modula-2 Programming; May 10-12

Unix
Introduction to UNIX; April 3-5

UNIX System: Application Programming; March 30-31

X Window System; May 17-18

Hypertext
Hypertext: A New Way of Accessing & Organizing Information; April 24-25

Data Communications
Internetworking with TCP/IP; April 4-5

PC Networking; May 31-June 2

Database Management
Relational Database Concepts; May 1-3

Relational Database Using 4GL and Application Generators; May 4-5

Software Engineering
Estimating Software Costs; April 3

Real-Time Systems: Information Analysis/ Modeling; April 3-4

Real-Time Systems: Dynamic Analysis and Specifications; April 5-7

Software User Interface Design; April 26-28

Project Management: Managing the Design of the User Interface; May 8

Software Project Management; May 1-2

Establishing an Effective Software Quality Program; May 15-16

Ethics in Technical Professions; May 10

Writing Effective Documentation; May 17-19

Seminars in Data Processing

Spring 1989

IMS/VS Data Communication Interface and Relationships; April 10-14

Structured Methods for System Analysis & Design; April 24-28

DB2 Application Programming & Design; May 1-5

ICF Catalog: Structure & Theory; May 3-5

SQL/DS Application Programming; May 8-11

FOCUS Report Writer; May 15-16

VSAM Record Management & PerformanceConsiderations; June 5-7

Advanced PL/I Programming; June 5-9

The New PL/I; June 12

AS/400 Overview; April 24-26

AS/400 Workshop for the System/38 User; April 27-28

AS/400 Application Development Workshop; May 1-5

AS/400 Control Language Workshop; May 8-12

AS/400 Introduction to RPG; May 15

AS/400 RPG Programming Workshop; May 16-19

AS/400 COBOL Programming Workshop; May 22-26

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June 12–14, 1989 — at the Wang Institute of Boston University

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- An introduction to C++
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- OOP by contract in Eiffel
- LISP based on OOP
- Introduction to Smalltalk
- ... and many more

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AS/400 Communications Programming Workshop; May 31-June 2

Introduction to COBOL Programming; March 27-31, June 5-9

Advanced COBOL Programming; April 3-7, June 12-16

Design, Debugging and Testing; April 10-14, June 19-23

IBM OS Job Control Language (JCL) and Utilities; April 25-28

Introduction to IMS/DB; May 1-4

Advanced IMS/DB; March 6-9, May 8-11

IMS/DC Data Communication Facility for IMS Using the DL/T Language; March 13-15, May 15-17

IBM SNA (System Network Architecture); March 15-17, May 17-19

CICS/VS Command Level Applications; March 20-24, May 22-26

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Connect '89

Connect '89, the second

the Boston World Trade Center from April 18 to 20.
Price: $35 for on-site registration; $25 before March 27. Registration fee allows con-

Hynes Convention Center.
The preconference days will consist of six all-day workshops on Sunday and 17 all-day workshops on Monday.

CASE Conference

National CASEcon is de-

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Spring 1989

Programming Languages
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Connect '89

Connect '89, the second annual technical conference and exposition, will provide a forum for information systems professionals to discuss emerging technologies in networking and systems compatibility.

Micro-to-mainframe links, voice/data communications, LANs, wide-area networks, TCP/IP, Ethernet, Token Ring, ISDN, Open Systems Interconnect (OSI), System Network Architecture (SNA), broadband communications, and terminal-emulation products will be featured at the show, according to the show's sponsors, Cahners Exposition Group.

Connect '89 will be held at the Boston World Trade Center from April 18 to 20.
Price: $35 for on-site registration; $25 before March 27. Registration fee allows conference and exhibit attendance.
Contact: Show Manager, Connect '89, 999 Summer St., P.O. Box 3833, Stamford, CT 06905, (203) 964-0000.
Inquiry 929.

Computers and Education Conference

Lesley College in Cambridge, Massachusetts, will host the 10th annual National Educational Computing Conference from June 20 to 22, with two preconference days on June 18 and 19. The conference will be held at the Hynes Convention Center.

The preconference days will consist of six all-day workshops on Sunday and 17 all-day workshops on Monday. Mitch Kapor, chairman of On Technology Corp., will give the keynote address; Albert Shanker, president of the American Federation of Teachers, will also speak.
Price: $95 for the three-day conference, $120 after May 20; $80 per day for the workshops.
Contact: Sara Burke, Lesley College, Bouma Hall, 29 Everett St., Cambridge, MA 02138, (800) 999-1959, ext. 294; in Massachusetts, (617) 868-9600; or contact NECC '89—ICCE, University of Oregon, 1787 Agate St., Eugene, OR 97403, (503) 686-4414.
Inquiry 931.

CASE Conference

National CASEcon is designed to provide coverage of CASE (computer-aided software engineering) and productivity improvement. It will be held on June 20 to 22 in the Jacob K. Javits Convention Center of New York, concurrently with PC Expo.
Price: Before May 1: $25 at the door, $175 for one day of conferences; after May 1, $30 and $195, respectively. PC Expo: Exhibits only, one day, $30; two or three days, $60. Seminars, $95.
Contact: Peter Brunold, National CASEcon, P.O. Box 1807, Englewood Cliffs, NJ 07632, (800) 444-3976; in New Jersey, (201) 569-8542.
Inquiry 932.

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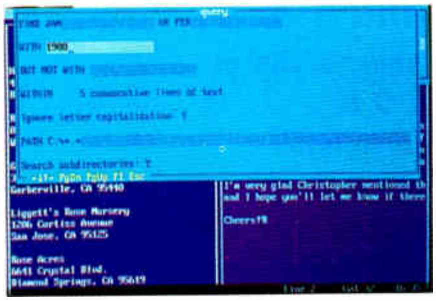
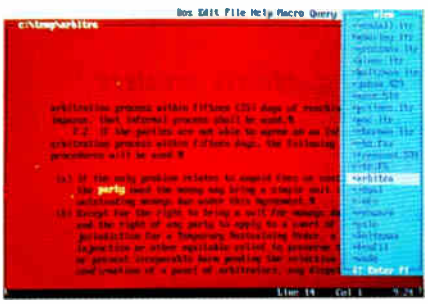


The query panel is Vq's window into your disks.

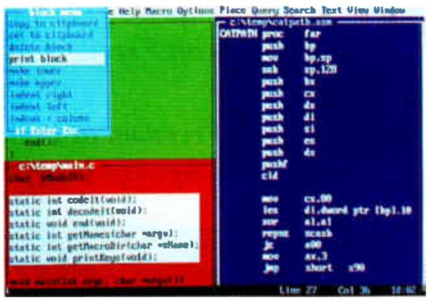
A MATCH FOR YOUR PATH—quickly search a file, a whole disk, or anything in between to find all the files with a match to your search pattern, then display each match *in full context!*



After a query, Vq lists the files containing the target pattern and displays the matching text, highlighted in context.

WHEN YOU FIND IT, USE IT with *Vq*'s full-featured editor or your own word processor, compiler, or custom macro.

A MATCH FOR YOUR CREATIVITY—expand your creativity into a screenful of windows for different files or different parts of a file. Zoom them, compare them, edit them in parallel, or copy-and-paste to a new file.



Vq's Block command menu is pulled down for processing the marked block of text.

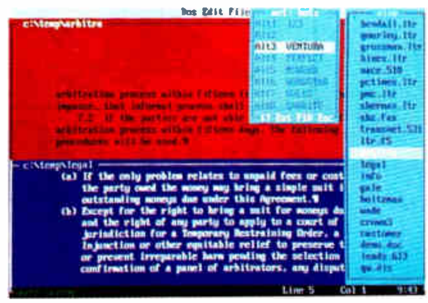
A MATCH FOR YOUR STYLE—everything you want for creating and manipulating text, plus a few things you never thought to ask for—paragraph formatting, text centering, auto-indent, search-and-replace, copy, cut, delete or print blocks—*effortlessly!*

A MATCH FOR YOUR DISPLAY—25, 43, or 50 line display modes, and you pick the screen colors!

A MATCH FOR YOUR MOUSE—full mouse support with pull-down command menus means you don't have to remember *anything.* All *Vq*'s power is delightfully accessible—just point and shoot!

A MATCH FOR SYSTEM FAULTS—while you're working, *auto-save* is working too, so you can automatically start each session where the last one ended—*even if it ended unexpectedly!*

A MATCH FOR YOUR APPLICATIONS—add *Vq*'s access and editing talents to the applications you know and love.



The small-pull down menu lists Vq's preconfigured Hot Links to other applications.

Vq comes with Hot Links to popular applications, and creating more is a snap. Pass your desktop publisher the file you created, format it, print it, then return to *Vq* to find your sales summary and jump-start your spreadsheet program, or write brilliant code and Compile-and-display-next-error. *Vq* shrinks to just 7K bytes during linkage and gives your applications room to run!

A MATCH FOR YOUR CHORES—teach *Vq* whatever you do over and over. *Vq*'s macros learn any input sequence for flawless playback at your command, for infinite customization!

Vq operates with OS/2 or DOS 3.0 or higher in IBM-compatible systems with 256K bytes.

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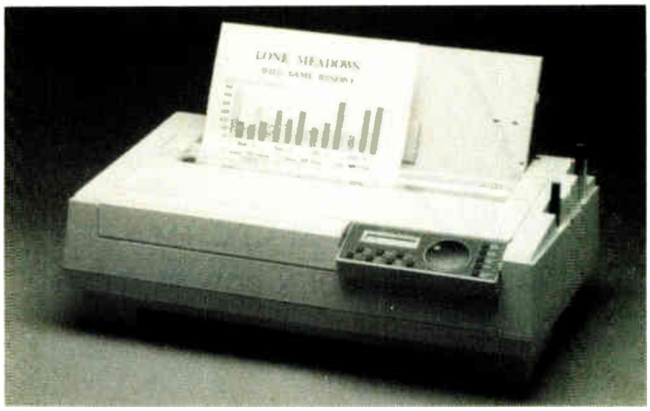

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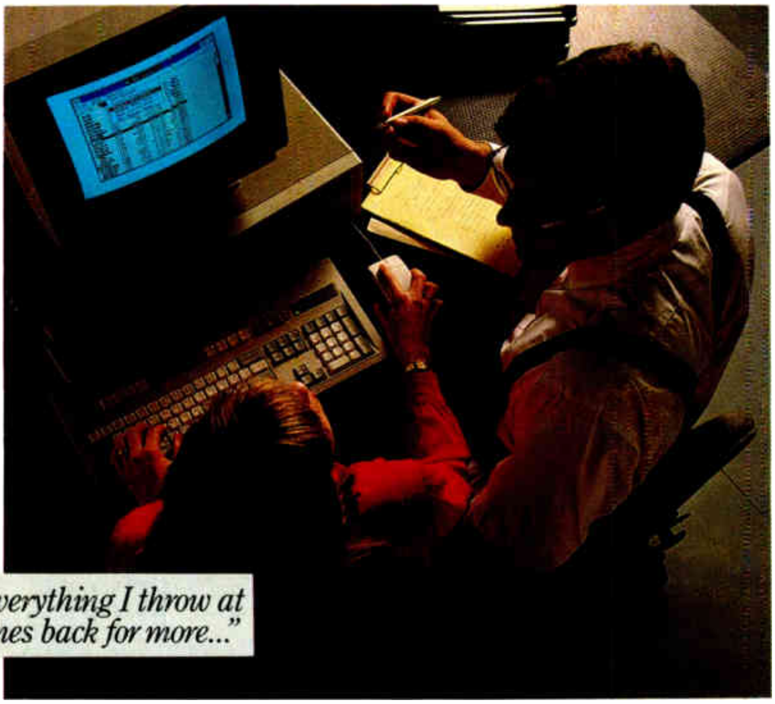

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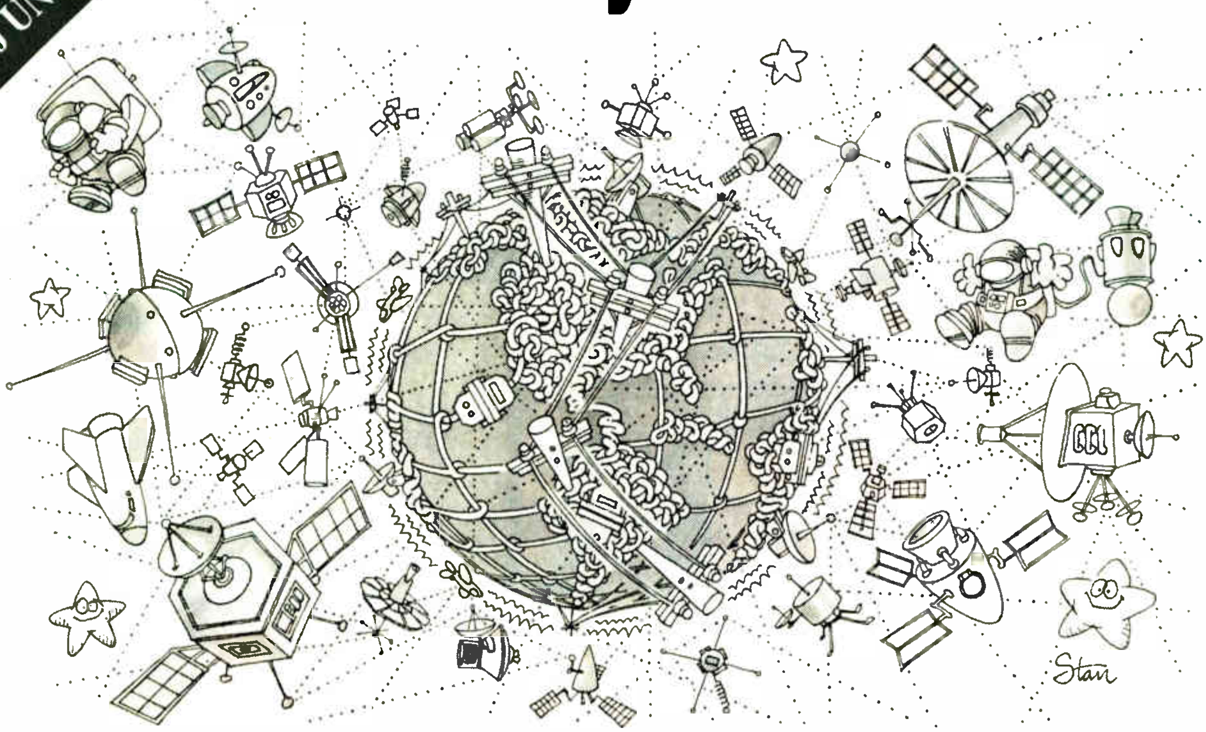

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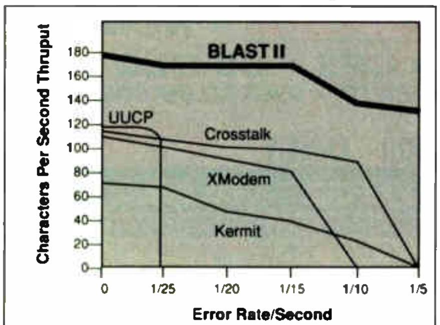

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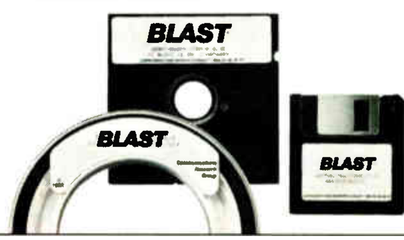

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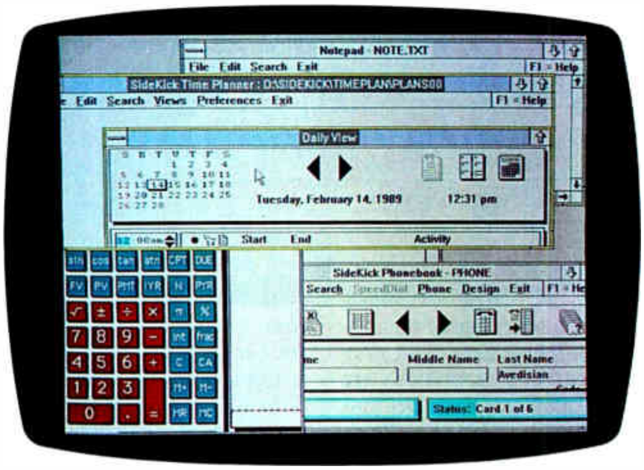
Circle 523 on Reader Service Card

BYTE editors' hands-on views of new products



SideKick for PM: More Than Just a Pretty Face

According to some industry pundits, one of the reasons for the apparently slow acceptance of OS/2 is the current lack of useful applications for Presentation Manager, the operating system's graphical user interface. But IBM has come to the rescue by including a free copy of Borland's new **SideKick for Presentation Manager** in every copy of OS/2 1.1. While it's only a hint of the applications that we'll be seeing for PM, it is a fully functional program that lets you do useful chores as soon as you've installed OS/2.

I found SideKick for PM to be both more and less than its better-known cousin, SideKick Plus.

Installation was a snap. Borland's installation utility copied all the files to my hard disk, edited my configuration file, and added a SideKick group to the group window in OS/2's Start Program application. SideKick for PM isn't a TSR program. This is OS/2, without the RAM-cram problem or the danger of applications bumping into one another.

SideKick for PM is a four-part product, with a calculator, a time planner, a phone book, and a notepad. If you're an experienced SideKick user, it's not hard to see that that's four fewer features than SideKick Plus. SideKick for PM lacks the file manager, clipboard, ASCII table, and outliner of the MS-DOS version. The file manager and clipboard aren't needed, simply because PM has them built in. The ASCII table isn't really necessary either, since PM's main market will be business applications. What about the outliner? A Borland spokesperson told me the company is working on enhanced versions of SideKick for PM.

But it didn't take me long to find that the four existing applications within SideKick for PM more than make up for what I'd thought was a lack of features. This is a completely new product, and it's a prime example of why I believe PM is destined to become an industry-standard interface. Borland's programmers have fully utilized PM's graphical capabilities. Unlike the character-based MS-DOS version, SideKick for PM is full of buttons, changing cursors, icons, and various fonts and font styles. Taken together, they uniquely integrate SideKick into a useful and easy-to-use program.

It would take much more space than I have to even begin to explore the new capabilities of SideKick for PM. But the phonebook and time planner bear special mention because they use the core database engine from Borland's Paradox. You can swap names, addresses, notes, and appointments to and from the Paradox files. And the time planner is a cutting-edge graphics application. I used a time odometer to select open appointment slots, and *child* windows (windows inside the main application window) let me see three different views of my schedule.

Although nonwindowed OS/2 applications have been available for over a year, SideKick for PM is the first application I've seen that truly takes advantage of PM. It's a tantalizing peek at what will be coming down the line. And since it's free with OS/2 1.1, you couldn't ask for a better deal. IBM and Borland have not announced how long their arrangement will last. SideKick for PM isn't currently available as a stand-alone product, but a Borland spokesperson says it will be available, eventually, for $250.

—Stan Miastkowski

THE FACTS

SideKick for Presentation Manager
Free with IBM OS/2 1.1; $250 when available separately

Requirements:
80286- or 80386-based IBM PC, PS/2, or compatible with OS/2 Standard Edition 1.1 or higher, PM, and 3 megabytes of RAM; a mouse is recommended, and a Hayes-compatible modem is needed for dialing.

Borland International, Inc.
1800 Green Hills Rd.
P.O. Box 660001
Scotts Valley, CA 95066
(408) 438-5300
Inquiry 1044.

Industrial-Strength Color Processing

The Macintosh II's color capabilities give it the potential to serve as a color image processing engine. **PhotoMac**, a color-retouching application written by Avalon Development Group and marketed by Data Translation, lets the Mac II achieve this potential. PhotoMac is designed to serve the color prepress industry: It lets you im-

continued

port 24-bit color scanned images and work with them using a set of retouching and design tools. You then save the modified images in several formats, or you can generate process color-separation files.

PhotoMac works on a Mac II or IIx with 2 megabytes of memory and a standard 8-bit color display. Since even small 24-bit color images can easily be larger than 2 megabytes in size, PhotoMac implements a virtual memory system. That is, only the portion of the image actually displayed consumes memory, while the rest of it is held in a disk file until needed. This lets you easily work on files larger than available memory.

How can you work on 24-bit color images reliably when your screen displays only 256 colors? PhotoMac features an adaptive display, where it uses the 240 colors that best represent the 24-bit data for the part of the image that's on-screen. The other 16 colors are reserved to display PhotoMac's tool palette.

PhotoMac imports a variety of color image formats: PICT, 8- and 24-bit PICT2, 24-bit Tag Image File Format, and TARGA or VISTA files. Once you are finished working with the image, you can save it as 8- or 24-bit PICT2 or 24-bit TIFF files. You can print directly to a Tektronix color printer or a Mirus film recorder.

To do color separations, you specify the screen lines, screen angles, gray component enhancement, and gray balance. The separations are saved as CMYK (cyan-magenta-yellow-black) PostScript files, suitable for downloading to a PostScript typesetting system (e.g., the Linotype Linotronic 300). Since the QMS ColorScript 100 laser printer uses color PostScript and not CMYK PostScript, you'll have to import the image file into another application, like Quark XPress, to print it. Since the application is for high-end color prepress, the Apple LaserWriter II printer is not supported.

The tools let you magnify an image (up to 32 times) for precision work or reduce it (to one-eighth its size), and you can rescale it, flip it, or rotate it. You can modify its colors using either RGB or LHS (luminance-hue-saturation) color-correction systems, or you can retouch it with opaque or transparent colors using an airbrush or paintbrush tool. Of course, you can cut, copy, or paste images. A Copy transparent option lets you paste an image into a white region of an existing image. The image pasted is either scaled to fit the white region or cropped, as determined by a key selection during the paste operation.

I tried PhotoMac 1.0 on a Mac II equipped with 5 megabytes of RAM, a SuperMac 19-inch color monitor, and a Spectrum/24 video board. PhotoMac easily imported PICT2 files from PixelPaint and 24-bit TIFF color images made with Howtek's ScanMaster color scanner and MacScan-It 1.0 software. The tools were intuitive and easy to use. For long operations, a dialog box gives you a countdown in minutes and seconds with a Cancel button. Image manipulation seemed a bit slow, but because PhotoMac is working with 24-bit data and uses three temporary files to provide an Undo capability, the delays are reasonable.

The manual is one of the best I've seen: For any surprise that turned up while using PhotoMac, I found an explanation in the manual. It includes a tutorial section that explains how to use each tool, with examples. The reference section follows the traditional by-the-Mac-menu layout—that is, selection by selection through each of the menus.

If you want to do special effects for a publication, crank up the reds in a sunset, or take the green out of the faces of the board of directors, PhotoMac is worth a look.

—Tom Thompson

THE FACTS

PhotoMac
$695

Requirements:
Mac II or IIx with 8-bit color board and monitor, 2 megabytes of RAM, and System 6.0.2/Finder 6.1 or higher.

Data Translation, Inc.
100 Locke Dr.
Marlborough, MA 01752
(617) 481-3700
Inquiry 1045.

The Power of a Wizard

Designed as an electronic organizer, **Wizard** from Sharp Electronics is an 8-ounce pocket computer with 128K bytes of ROM-based software that performs the functions of a calendar, scheduler, memo pad, calculator, personal telephone book, and clock. Additionally, you can get optional "credit card" software packs that contain programs like a to-do list, a time and expense manager, a thesaurus/dictionary, and an eight-language translator.

Wizard comes with its own 32K bytes of RAM and can hold 64K bytes at one time (32K bytes of system memory and 32K bytes with an add-on card). If you buy all the cards, you have a total of 256K bytes; however, you can't use it all at one time.

Wizard lets you store your

continued

notes, appointments, phone numbers, and words and phrases in a user-defined built-in dictionary. This last feature is a godsend in terms of functionality, since the keyboard is sequential rather than QWERTY and typing is generally slow and awkward—a problem when you need to keep your mind on what you're doing instead of the mechanics of data entry. By touching the User Dic key, you call up the dictionary function and can add or recall often-used sets of words.

In my case, a series of verbs (i.e., call, assign, go to, and meet) and the names of BYTE staff members let me schedule and enter most of the things I need to keep track of with about a half-dozen mostly repetitive keystrokes. I can then set the time for the task and tell the Wizard to ring an alarm to remind me of my next appointment.

The intensity-adjustable LCD shows either eight 16-character lines or four 10-character lines. The space is cramped, and the memo pad and scheduler wrap line endings without proper word breaks or hyphenation. What you wind up with, at first, is wasted time while you go back and put things in a readable order. But again, the user dictionary and a little practice give you a better feel for how much to enter before hitting the Return key.

The Wizard also comes with the ability to import and export data through a serial port. When connected to an IBM PC compatible via Traveling Software's Wizard PC-Link, you can, for example, transfer Borland's SideKick data from the PC into Wizard's scheduler. (SideKick Plus transfers did not work with our beta copy of the program, however, and required BYTE's editor in chief to write a short program to bring the data over.)

Another option, according to Sharp, is the ability to create hard-copy output by connecting the Wizard to Sharp's CE-50P printer. Further, the company also offers an optional dubbing cable that it says can connect two Wizards and let them share information.

Wizard's calculator function is enhanced by having its own numeric keypad. It has a "paperless printer," or journal-tape function, that helps you keep track of your entries or edit previously input calculations. You can also perform calculations of data stored in the memo pad (e.g., summing price lists). The large-character display in calculator mode is also surprisingly handy.

I could do without the world clock function, but I can see where it might be useful for people who often need to know when would be a good time to put through a call to Seoul or London or Lagos. Another function I'd list as thoughtful on the part of the designers, but only potentially useful to me, is the Wizard's secret mode. Using it, you can password-protect data in your schedules, telephone directory, and memo pad.

In its basic configuration, the Wizard has a lot to recommend it. People like myself who normally have lots of discrete tasks to organize and manage—and who spend a lot of time away from their desks and TSR programs—can always use help in keeping track of events. However, the Wizard's price makes it a question mark for me. It retails for $299. Additionally, the extra software packs run from about $100 to $130 each, the Wizard

continued

THE FACTS

Wizard, $299; software packs, $100 to $130;
PC-Link, $179;
printer, $169.99

Sharp Electronics Corp.
Personal Home Office
Electronics Division
Sharp Plaza
Mahwah, NJ 07430
(201) 529-8874
Inquiry 1046.

Circle 319 on Reader Service Card (DEALERS: 320)

PC-Link costs $179, and the printer goes for $169.99. Once you start adding on, the price adds up.

I've used Wizard enough to know that I could eventually rely on it as easily and as naturally as a pocket notepad—and because of its automatic alarm capabilities, it's at least one cut above pencil-and-paper-based organizers.

On the other hand, the price is a barrier right now. I'm not sure how much I'd be willing to pay just to have my notepad kick me awake for a meeting or to go harass some poor editor about a deadline. Of course, discounting is a fact of life. I'm making a note in my Day-Timer to check it out again in a few months.

—Glenn Hartwig

Discus Rewritable: The Latest in Storage Technology

One of the big surprises when Steve Jobs introduced the NeXT computer last October was its erasable, rewritable optical disk drive. Now, you don't even have to wait for a NeXT computer to get your hands on the latest in storage technology: A new optical disk drive from Advanced Graphic Applications (AGA) connects to an IBM PC AT and offers 650 megabytes of storage at a cost per byte comparable to hard disk drives.

Rewritable optical technology is the successor to WORM (write once, read many times) technology, which lets you permanently archive data on shiny, durable laser disks similar to audio CDs. The **Discus Rewritable** drive offers the benefits of a WORM drive—huge capacity, long-term stability of data, and portability—with the added flexibility of conventional magnetic media. And it's so fast that in tests with workaday software packages like databases and word processors, it performed nearly as well as (and in some cases better than) my trusty old hard disk drive.

The Discus Rewritable is available either in an internal, full-height configuration or as an external unit with its own power supply and fan. Both versions communicate via a SCSI port designed by AGA that is included with the product. Cables, documentation, and software utilities and drivers are also included. You can daisy chain up to six additional drives to a single controller through an external SCSI port on the back of the external model.

I tested an external model on several different com-

continued

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THE FACTS

Discus Rewritable
$4995 (internal),
$5495 (external)

Requirements:
IBM PC AT or compatible (no minimum RAM requirement) with DOS 3.0 or higher; SCSI controllers also available for IBM PC XT and Micro Channel.

Advanced Graphic Applications, Inc.
90 Fifth Ave.
New York, NY 10011
(212) 337-4200
Inquiry 1047.

puters. The setup and software installation were well explained in the manual and turned out to be very easy. I couldn't get the drive to work on one AT clone that turned out to have a strange direct-memory-access controller (the SCSI port uses a DMA channel), but this problem disappeared on a more fully IBM-compatible system.

The drive uses 5¼-inch magneto-optical disks supplied by 3M. One disk is included with the drive, and additional disks cost $250. Each disk holds 650 megabytes of data, or 325 megabytes per side, but you have to manually turn over the disk to access the other side.

As with conventional hard disks, you have to partition the disk, but this is simplified by a software routine supplied by AGA. (Disks are shipped formatted on side A, but you must perform a low-level format on side B that takes about 25 minutes.) Each side can be configured as a single giant partition. To set up partitions larger than 32 megabytes under DOS 3.0, however, you have to run a DOS patch provided by AGA. (This will not be necessary in the DOS 4.0 version, which was not yet available.)

On the downside, I found the external unit heavy and bulky, and its fan was very noisy. I was also annoyed by a few minor details, all of which I could live with: The Discus Rewritable always has to be turned on after the computer, disks have to be removed with the drive on, and they must be removed to lock the heads for transport. But overall, Discus Rewritable seemed to be a solid, reliable, and well-designed product.

Perhaps my greatest pleasure was the sensation of space I felt every time I saw the DOS DIR listing that said "322, 830,336 bytes free." It must have felt the same way to be a pioneer in the Old West and come upon an undiscovered territory.

—*Andy Reinhardt*

Talk to Me, DOS, Talk to Me

DOSTALK from SAK Technologies is a natural-language interface for MS-DOS-based computers. Instead of having to type in such cryptic commands as COPY C:\ARTICLES*.DOC C:\BACKUP, you simply enter the phrase "copy all the files in the Articles directory that have the extension Doc to the Backup directory" (capitalizing *only* the names of files and directories.) DOSTALK then translates that into the correct DOS commands and executes them.

Installing DOSTALK is fairly easy; an automatic installation program creates a subdirectory called DOSPEAK and copies several files from your DOS directory into it. The next time you reboot, DOSTALK will be available any time you press F2.

DOSTALK does all the easy things: list filenames, copy files, and so on. I wondered, though, how it would do with fancier commands. Plain old DOS lets you invoke "switches" at the end of many commands to provide added features.

When I said "show Article.doc one screen at a time," DOSTALK correctly invoked the MORE option of TYPE. "All right," I thought. "I'm impressed."

Unfortunately, DOSTALK then managed to mess up what I consider a fairly easy request: When I typed "show all the files that end in Doc," DOSTALK replied "Subdirectory Doc NOT FOUND." Not until I asked it to show the files that end with a Doc extension did I get what I wanted. This is supposed to be plain English?

There were other times when DOSTALK would get confused and go off on a long search for something I hadn't intended. For example, if you want to use a plain old DOS command, you have to type a $ first. I once typed DIR T*.* without the $, and DOSTALK asked "What should I do with the T*.* directory?" I replied "show them," and DOSTALK answered "Files T*.* NOT FOUND," even though there *were* files in that subdirectory that began with *T*. DOSTALK then proceeded to go looking for *all* the files on my hard disk that began with the letter *T*—a long list, and one that I didn't need to see. Unfortunately, DOSTALK doesn't let you interrupt once it has started on a long trek. (Sometimes, however, you can hit Control-C to abort an action.)

Yet another time, I asked DOSTALK to "print the names of the files," to which it replied "Name of list device [PRN]:." How many novices are going to know the correct response, LPT1? It then proceeded to print the *full text* of the files, rather than the directory list that I wanted. Urk! Where's the power switch on that printer?

DOSTALK does have some nice features, like the ability to undo the last command. And if you ask it to erase an entire subdirectory, it will ask you if you want to erase the files in it first. DOS just tells you that it's an "Invalid path, not a directory, or directory not empty."

One obvious omission from DOSTALK, though, is an on-line help facility. Any program (especially one that will be used by novices) that doesn't respond to a help cry with *some* kind of response deserves to go back to the shop.

Who needs DOSTALK? Certainly not someone who has been using a DOS machine for any length of time. Once you've learned the basic syntax, you can probably make DOS do what you want—faster, easier, and more flexibly than with DOSTALK. People with absolutely no DOS experience might find DOSTALK useful, but they'll probably need help from a DOS user to set it up. Even beginners will probably feel constrained by DOSTALK after a while.

—*Ken Sheldon* ■

THE FACTS

DOSTALK
$129.95

Requirements:
IBM PC with 300K bytes of RAM and DOS 2.1 or higher.

SAK Technologies, Inc.
1600 North Oak St.
Suite 931W
Arlington, VA 22209
(703) 522-6425
Inquiry 1048.

32.5 MHz and Climbing

Rated for 33 MHz and running at 32.5 MHz, SIA's 386/32 defines a new speed plateau

Just over a year ago, 20-MHz 80386 systems left us slack-jawed by the sheer power they delivered. A few months later, a clutch of 25-MHz machines provoked a similar reaction. Now, Systems Integration Associates (SIA) has begun shipping its 386/32, the first PC-compatible machine running on a 32.5-MHz clock—a unit that sets a new standard in microprocessor speed.

Chicago-based SIA managed to scoop the Compaqs and Advanced Logic Research machines by putting together a system board designed for 32 MHz with an Intel chip rated for 25 MHz and some additional hardware to keep things running smoothly. At the heart of the system is a high-speed cached memory design and cooling equipment dedicated to keeping chips close to their rated temperature while running above their rated clock speed.

Integration by Parts

While not a from-the-ground-up manufacturer like some of the bigger names in the industry, SIA has assembled an impressive machine by combining a good collection of subassemblies and adding a few touches of its own to cover up the seams. The basic system, which lists for $13,100, includes the system board stuffed with 4 megabytes of RAM, a case, a 200-watt power supply, a hard disk drive controller, a serial/parallel card, and a keyboard. The fully loaded evaluation unit weighed 70 pounds. It was mounted in an optional tower case that stands a good 6 inches taller than a PS/2 Model 80. With hard disk drive, tape drive, and coprocessor, the unit lists for $19,830.

Chief among these integrated subsystems is a motherboard built to handle 32.5-MHz operation. The 30 percent increase in frequency from the last generation means that physical problems, like stray capacitance and transmission line effects, become more acute, requiring careful board layout. Also critical to a good design is a memory subsystem that won't bottleneck the processor.

To keep up with 32.5-MHz operation, a conventional design would require 30-nanosecond static RAM. SIA gets around the prohibitively expensive solution of implementing the entire main memory in 30-ns SRAM by using a 64K-byte, direct-mapped, 20-ns SRAM cache. SIA claims zero-wait-state operation and an 81 percent hit rate for the write-through cache, operated by a discrete logic controller. In a direct-mapped design, each memory access involves comparing a *tag* (which specifies blocks within the cache) with part of the requested address. The system uses faster 15-ns SRAM to store the frequently used cache tags, further enhancing performance. Up to 16 megabytes of main memory can be cached; accessing any memory installed beyond this limit will slow the system considerably.

The main memory is made up of relatively fast 70-ns DRAMs. Four banks for dual in-line package memory are available on the motherboard, along with 4 single in-line memory module slots. DIP sockets are compatible with both 256K-byte and the larger 1-megabyte DRAM chips. SIA claims that both SIMM and DIP sockets will be compatible with the not-yet-released 4-megabyte versions of each package. With 1-megabyte parts, you can get up to 8 megabytes on the motherboard; when 4-megabyte components are released, the board will take not 32, but 16 megabytes—a system board limit imposed by the BIOS. An additional 24 megabytes can be added via the single 32-bit I/O slot, but, in any case, the upper addressable limit is 32 megabytes.

The processor itself is a 25-MHz Intel 80386 that has been tested for operation at 33 MHz by SIA. Both the 25-MHz Intel 80387 and Weitek 3167 coprocessors are supported.

The greatest difficulty in running a device beyond its listed rating is chip heating, as heat generation increases with clock speed. To keep things cool, SIA has mounted a cross-flow blower just above the processor and coprocessor socket, which, the company claims, keeps the chip case very close to ambient temperature. The CPU is also mounted slightly elevated (3/32 inch) from the socket, allowing air to flow underneath as well. I checked temperature differences with a digital thermometer. The chip case got as high as 11°F above ambient (i.e., 86 degrees) but never showed any adverse reaction to the heat.

SIA plans to offer early buyers of the 386/32 an upgrade to the 33-MHz CPU once Intel makes it available. The swap will cost those users a maximum of $500 with the return of the 25-MHz chip. SIA had not determined the exact upgrade charge at this writing.

Except for the high-speed modifications, the board looks like any number of 25-MHz 80386 motherboards currently available (see photo 1). It also interfaces just as easily, because the one 8-bit and six 16-bit I/O slots can run at a compatible bus speed of 8 MHz. The BIOS, designed by American Megatrends International (AMI), includes ROM-based setup and diagnostics and allows video BIOS relocation. I found that video BIOS relocation meant a threefold performance increase in some graphics functions, but it

continued

PHOTOGRAPH: PAUL AVIS © 1989

Table 1: *Benchmark results of a comparison between the SIA 386/32 and the ALR FlexCache 25386. For indexes only, higher numbers reflect better performance.*

CPU	SIA	ALR
Matrix	2.10	2.60
String Move		
Byte-wide	15.54	16.20
Word-wide:		
Odd-bnd.	17.78	21.97
Even-bnd.	7.78	8.13
Doubleword-wide:		
Odd-bnd.	13.14	15.93
Even-bnd.	3.87	4.03
Sieve	11.02	14.02
Sort	8.26	10.50
☐ **Index:**	**5.99**	**5.07**

FLOATING-POINT	SIA	ALR
Math	3.79	4.90
Error[1]	0.00E+00	0.00E+00
Sine(*x*)	1.23	1.54
Error	2.00E−09	2.00E−09
e^x	1.20	1.81
Error	1.00E−09	1.00E−09
☐ **Index:**	**14.20**	**10.55**

DISK I/O	SIA	ALR
Hard Seek[2]		
Outer track	3.33	1.64
Inner track	3.31	3.33
Half platter	6.65	6.67
Full platter	9.10	8.35
Average	5.60	5.00
DOS Seek		
1-sector	10.23	6.93
32-sector	18.77	15.35
File I/O[3]		
Seek	0.04	0.06
Read	0.85	0.49
Write	0.76	0.78
1-megabyte		
Write	2.89	2.91
Read	4.28	2.92
☐ **Index:**	**2.36**	**2.74**

VIDEO	SIA	ALR
Text		
Text average	3.21	4.60
Graphics		
Graphics average	1.96	1.94
☐ **Index:**	**3.06**	**2.57**

Application Indexes	SIA	ALR
Word processing	4.41	4.41
Spreadsheet	4.07	4.13
Database	2.02	2.83
Scientific/ Engineering	6.24	5.80
Compiler	3.94	4.08
☐ **Index:**	**20.67**	**21.24**

All times are in seconds. Figures were generated using the 8088/8086 and 80386 versions (1.1) of Small-C.

[1] The errors for Floating Point indicate the difference between expected and actual values, correct to 10 digits or rounded to 2 digits.

[2] Times reported by the Hard Seek and DOS Seek are for multiple seek operations (number of seeks performed currently set to 100).

[3] Read and write times for File I/O are in seconds per 64K bytes.

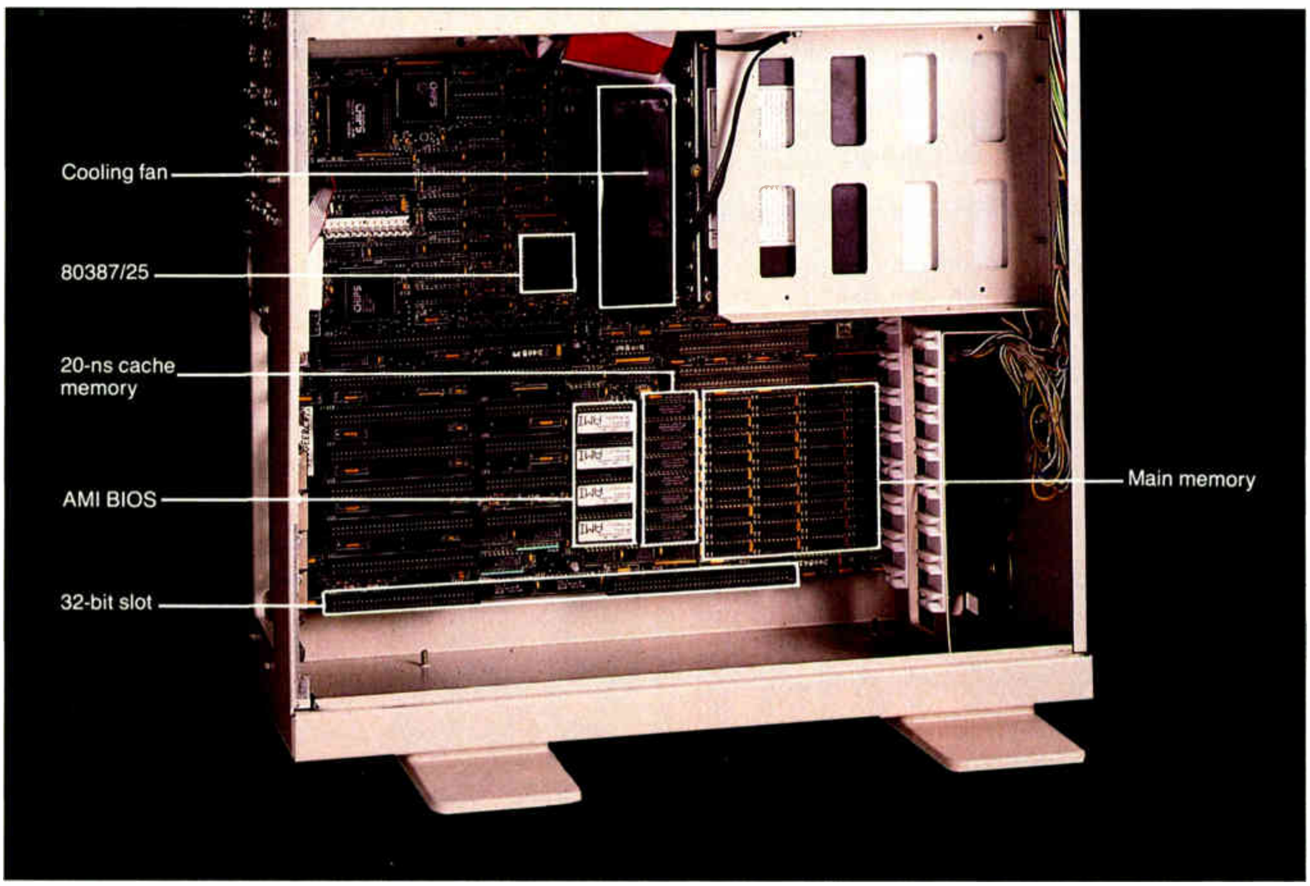

Photo 1: *The SIA 386/32's interior. The most striking departure from conventional systems is the presence of a dedicated CPU fan (CPU is mounted behind fan).*

will cost you 256K bytes in RAM above 640K bytes. The BIOS also allows you to toggle between 32.5- and 8-MHz CPU speed and to enable or disable caching with a hot-key sequence.

The disk subsystem, of course, often causes a bottleneck when combined with a high-performance CPU. SIA went a long way toward solving that problem by providing an ESDI controller as part of the standard system. The controller, an Adaptec ACB-2322, supports two hard disk drives and two floppy disk drives. With a data transfer rate of 900K bytes per second, the controller is well matched with the rest of the system. The unit I looked at included a very quick (16.5-millisecond rated access) 150-megabyte Control Data hard disk drive (which is not part of the standard configuration), adding to the system's impressive specs.

The unit also included a 150-megabyte tape backup system, two floppy disk drives, an 8-bit VGA controller, and an analog monitor. The monitor display is crisp, and the keyboard has an excellent feel. Unfortunately, the system unit interferes with the monitor when they are close together, and the standard monitor cable is quite short. The unit is FCC Class A–approved for business but not home use.

Proof in Performance

I test-drove the SIA 386/32 with BYTE's standard benchmark suite. Not surprisingly, the system set new performance records in most of the tests (see table 1). That word *most* is of critical importance; what's interesting is that this 32-MHz system could not quite out-distance the entire the 25-MHz pack. For the purpose of comparison, table 1 lists the SIA 386/32's benchmark numbers next to those for ALR's 25-MHz FlexCache.

The low-level CPU and FPU benchmarks reveal the kind of performance you'd expect—the SIA is consistently faster than the FlexCache. While the times vary slightly from test to test, the SIA's overall CPU index shows a performance increase over the FlexCache of about 18 percent. FPU performance increase is about 35 percent. These numbers are impressive indeed, considering ALR's 386/25 was the fastest unit we'd tested until now, but the relative CPU performance doesn't quite measure up to the 30 percent difference in CPU clock speed.

Disk speed was good, and the Adaptec/Control Data ESDI combination certainly did not result in any noticeable waits. Unfortunately, the disk subsystem

Processor speed is not an end-all measure of performance.

was outperformed by ALR's similar ESDI unit. While write operations and seek times are roughly equivalent, the SIA's disk-read scores are poor.

I tested the 386/32 using the video shadow-RAM feature, which made graphics operations fly. ALR's 16-bit VGA adapter had outstanding graphics speed, which proved unreachable for the SIA 8-bit card, but the SIA came out with a higher overall score.

These minor shortcomings in disk and graphics performance significantly hampered the 386/32's application performance, and the overall application index is actually slightly lower than the 25-MHz FlexCache's. Disk-intensive database operations were disappointingly slow.

High speed can mean software incompatibility if floppy disk drives run too fast, but that wasn't the case with the 386/32. The system had no problems with copy-protected Lotus 1-2-3, an application that usually weeds out systems with incompatible floppy disk speeds.

Final Thoughts

The SIA 386/32 promises excellent processing speed, and its high clock rate and cached memory design deliver. With the aid of the interior blower, the 25-MHz-rated chips run without difficulty. When armed with a high-speed coprocessor, there probably isn't a faster PC-compatible number cruncher available today.

Processor speed is not, however, an end-all measure of performance, and number crunching is not a computer's only function. Some of the peripheral components could not keep up with the CPU, and application performance suffered. These units can be replaced and indeed are not even part of the basic system. Replacements are unlikely to save money—this top-shelf system requires top-shelf parts; it's hard to imagine a 386/32 that fully exploits its system board power for under $17,000. ■

Steve Apiki is a testing editor for the BYTE Lab. He can be reached on BIX as "apiki."

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LANGUAGE SOJOURN

Jerry explores the highways and byways of programming choices

It won't be finished here, but I'm beginning this column on the island of Molokai. To be exact, I'm at the Kaluakoi Resort, which is on the northwest corner of the island. Like most people, the only thing I knew about Molokai—which is written locally Moloka'i and pronounced Molokay-aye—is that there is a leper colony here, but you're supposed to say Hansen's disease.

Actually, while there is a hospital community on the Kaluapapa Peninsula and some of the former residents live there, the isolation of Hansen's disease patients—begun here by decree of King Kamehameha V in 1865—hasn't been the law for years, since Hansen's disease is now completely controllable.

I'm here for the conference on Grand Challenges to Computational Science, where I'll be the dinner speaker. I also expect to learn a lot about the future of supercomputing. More on that later.

Languages Reconsidered

Before I left, I did a lot of work on Mrs. Pournelle's Reading Program. This was originally begun by Bruce Tonkin in Microsoft BASICA. Tonkin writes many of his programs for TNT Software in BASICA and sells some amazing products, complete with source code, at quite reasonable prices.

Then about a year ago, I began converting the Reading Program into Compiled BASIC. I tried Borland's Turbo Basic, then I tried Microsoft QuickBASIC 3.0, and I went from there to QuickBASIC 4.0.

I don't get to do a lot of programming. This project was certainly the biggest one I worked on last year. Since I use real projects to learn, get used to, and evaluate other programs and products, I was more than once tempted to abandon BASIC and work with another—and presumably more modern—language.

There were a number of contenders. I'm still fond of Modula-2. Logitech's Modula-2 system has an excellent debugger and a good working environment for PCompatibles. Workman & Associates has FTL Modula-2 for both PCompatibles and the Atari ST, so I'd have at least that much portability for the code. There are many good libraries of program modules that go a long way toward overcoming Modula-2's rather primitive I/O and string-handling capabilities.

Another possibility would be Borland's Turbo Pascal 5.0; certainly that would be hard to beat for sheer popularity, and the new Borland Turbo Debugger is neat and fairly easy to learn. One—perhaps the main—reason Modula-2 hasn't caught on as fast as I thought it would is that Turbo Pascal 5.0 incorporates many of Modula-2's major features while remaining easy to learn. It also compiles like lightning.

Finally, Turbo Pascal has become popular enough that literally dozens of programmers have developed packages of thoroughly tested and debugged subroutines you can incorporate into your programs; it's not necessary to invent the wheel each time. For example, Robert Jourdain's *Turbo Pascal Express* (Brady Computer Books, 1988) comes with a whole slew of useful assembly language routines. You can also get the Peabody on-line help utility to make learning and using Turbo Pascal even easier.

Something Completely Different...

Of course, I already more or less know Pascal, Modula-2, and QuickBASIC. For a while there, I toyed with the idea of using this project as an opportunity to learn something entirely new.

One choice would be Ada. I keep hearing contradictory tales about that language. One group says it was designed by a committee and shows it: there are too many features, so that the resulting language is too big and too slow and just not useful for practical programming.

A second group says that's all nonsense: Ada is a splendid language, no more complex than many others, especially if you don't use some of the special features. They say the RR Software Ada Compiler for PCompatibles is plenty good enough for production work, and it produces code that's competitive in speed and size with any other language's output. After all, RR's Compiler is written in Ada and compiles itself nicely.

It would be instructive to find out which group is right.

There are certainly differences in philosophy between Ada and Modula-2. For one thing, Ada incorporates exception handling, something that's anathema to Modula-2's designer Niklaus Wirth ("anyone who needs exception handling just doesn't know how to program"). The designers of Ada, on the other hand, wanted a programming language that would write code to handle real-time events, and not all of those can be predicted; they thought they had to have capabilities to handle problems no one had foreseen, since Ada was to be used for all Department of Defense programming.

On the other hand, Ada has modularity and variable hiding like Modula-2, and in fact there's more similarities than differences between the languages. They're both nearly self-commenting, too. Certainly they resemble each other more than either one resembles BASIC or FORTRAN. I haven't had enough experience with Ada to get any real feel for it, but from what Ada code I've read, it wouldn't be that hard to learn Ada and get used to working with it.

Another possibility would be Turbo Prolog. Mrs. Pournelle's Reading Program presents lessons and elicits re-

continued

sponses. Then it evaluates the response and does something appropriate, like show a graphics reward, play a tune, go back to review the lesson, or present new material. Since this is all done according to logical rules, it seems to me that Prolog would be a very appropriate language to work in. We expect to develop a whole family of educational programs once the Reading Program is finished, and using Prolog could make that easier.

Finally, there's Trilogy, a new language that combines many of the procedural features of Pascal with the logical operations of Prolog. I haven't had a chance to learn Trilogy thoroughly, but from the little experience I've had with it, I suspect it has great potential for both experimental and production work. It's very fast, and it's actually easier to learn than Prolog; I've been quite favorably impressed, and indeed, if I were going to change languages, Trilogy would be a very strong contender.

It's Still QuickBASIC

After all the thought about new languages, I finally stayed with QuickBASIC. There were several reasons, but chief among them are that QuickBASIC now has all the data structures and algorithms of any other procedural language—and I already know the BASIC syntax. With all the others, including Turbo Pascal 5.0, I'd have to do some relearning and lose old habits.

I'd also have to translate about 20,000 lines of code. That's not necessarily bad. I have to recode about half the program anyway, since it was originally written by several people who didn't use the kind of top-down programming structure I like. Still, it's easier to recode from BASICA to QuickBASIC than it is first to learn Prolog or Trilogy, then to figure out how to organize the program along logical rather than procedural lines.

Actually, even that's not necessarily true. Given a solid block of time to sit down and work on the program, I still think I'd be better off starting over, probably with Trilogy; but the trouble is, I don't *have* a solid block of time. I *never* get a chance to just sit down and program for several days. I've been working on Mrs. Pournelle's Reading Program in fits and starts, often while on the road; and while the Zenith SupersPort 286 is more than adequate for programming while traveling, there is a limit to the amount of paperwork and documentation I can haul around. Hotel rooms are not an ideal place for learning new languages.

Thus, I stayed with QuickBASIC, despite its bugs; and bugs it does have.

One of the most annoying bugs showed up only when I ran the compiled Reading Program on a fast 80386 machine that had an 80387 chip in it. The interpreted version—one of QuickBASIC's most attractive features is its ability to run in interpreted mode so that you can do a lot of interactive debugging—ran fine on my Big Cheetah 80386/80387. The code compiled fine. It ran fine on the Kaypro 386i with an 80287 math chip; but when I ran the compiled code on Big Cheetah, I got overflow errors in converting stored graphics images. It wasn't a problem with Big Cheetah, because we got those same errors on a Compaq Deskpro 386 with an 80387. There was something wrong with Microsoft's code generator.

Fortunately, that bug and a number of others were fixed when Microsoft brought out QuickBASIC 4.5. For reasons I don't understand, Microsoft did *not* inform all the registered QuickBASIC 4.0 owners of the 4.5 update; that is, they not only didn't tell me (and I do send in registration cards, even for review software), but they didn't tell a number of my readers who have written to me about it. So, if you have QuickBASIC 4.0, be sure to get it upgraded.

Version 4.5 also has some bugs—I can still manage to get run-time errors in compiled versions of code that ran fine in the interpreted mode—but there are far fewer of them. Moreover, the documents are better, and the programming environment has been simplified. All in all, version 4.5 is a distinct improvement.

In fact, it's enough of an improvement that, while I still believe that Prolog or Trilogy would be better for the type of program we're producing, I find QuickBASIC good enough; and in line with my view that better is the enemy of good enough, we're doing the full production model of Mrs. Pournelle's Reading Program in QuickBASIC.

A Cautionary Tale

QuickBASIC 4.5 has modularity and separate compilation; that is, you can write parts of your program as distinctly separate modules, complete'y debug them, and generally check them out; compile them; and put the compiled object code into a program library. You can then incorporate those procedures and

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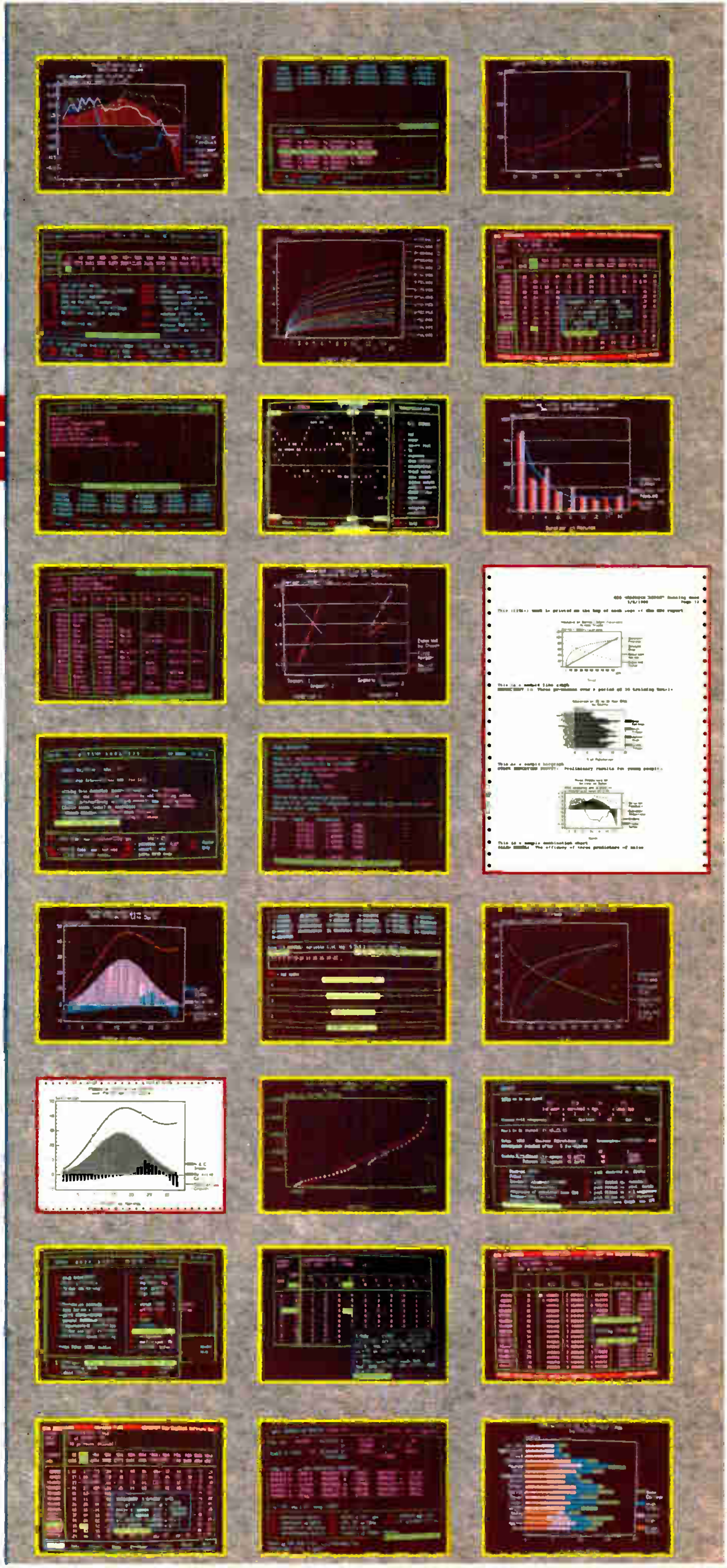

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functions into new programs. Provided that you really did have things properly debugged, you'll never have to fool with your old code again. When it comes time to compile a program that incorporates your precompiled procedures, first compile the new parts, then tell the Linker where to find the proper library.

This is one of QuickBASIC 4.5's more attractive features. The general notion is that when you write a program, you ought to write it such that much of the code can be used again in other programs. This saves a lot of work and lets you get new programs going faster.

There's one problem. The Microsoft program to build libraries isn't especially easy to use. It's not complicated, but building a library of many different routines can be quite tedious; I've often wished for a program that would make it simpler to make new libraries or change the contents of old ones.

Then I got QuickBASIC Tools from Project X Software. This looked to be the answer to my prayers. Not only did the product include a library-construction program, it also had a whole bunch of precompiled and presumably debugged procedures and functions that I could incorporate into my QuickBASIC program. There were over a hundred of these subprograms, and nearly every one of them appeared useful.

There were routines to check on the existence of a file. Routines to get and change the date of a file. Routines to open dialog boxes and get responses. Routines to get characters from the keyboard and check to see if those characters were in a previously specified set (so that you can, for instance, demand that the user enter either *Y*, *y*, *N*, *n*, *Q*, or *q*, and the routine will ignore any other input). Routines to open, close, rename, and copy files. Routines to make different beeps and chirps. Routines to manipulate dates and return day of year or Julian day, or reformat the date from U.S. to European style.

All of these are available as callable subprograms or functions. No need to write your own: simply use the library manager to choose the routines you want, and let that manager program not only build you a library containing only what you want, but also build you an include file of the proper DECLARATIONS. Some of the functions duplicated stuff I'd already written, but they looked to be more compact. Others were things I'd wanted to include in the program but either hadn't gotten around to or had reluctantly decided I wouldn't have time to

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write myself. Consequently, QuickBASIC Tools looked to be exactly what I'd been needing for a long time.

(Unfortunately, after this column was written, Project X Software went out of business. QuickBASIC Tools is no longer available.)

First Problems

Of course, I had a rather old (six months or so) copy of QuickBASIC Tools, but so what? It was supposed to be thoroughly tested. So, not long before Christmas, I sat down to build myself a library with a dozen of the most useful tools. While I was at it, I'd also use the library manager to link in a couple of general-purpose functions I'd written myself.

The library manager trundled away for a while. I could understand why it took a while. According to Project X, the library manager examines each library function to see if that one calls some other function; if it does, it includes that new function and looks to see if *it* calls something not already on the library list. I could see how that feature alone could save me a lot of time.

Eventually, the library manager finished. Rather eagerly—this looked like it was going to make it much easier to finish up Mrs. Pournelle's Reading Program—I invoked QuickBASIC in a way that included the resulting Quick library. (When you build libraries for QuickBASIC 4.5, two are constructed, a regular LIB that's linked into your compiled code, and a Quick library that you use when running QuickBASIC 4.5 in the interpreted mode.) Then, without using any of the new functions, I started up my program. I didn't anticipate any problems. After all, it ran fine the last time I'd used it.

It wouldn't run. There were "unresolved external calls," meaning that the program was looking for functions that were not in its library.

I rather angrily called Project X and got the president (and chief programmer). I explained who I was. He explained first that I had a "very old" copy of their product; and second, that they had just learned that when Microsoft changed from version 4.0 to version 4.5, they made a number of internal undocumented changes in the program. In particular, they changed the names of some of the internal subroutines. Some of these were called by the Project X tools and by the library manager.

However, he said, they had just solved that problem, and they would send me, via Federal Express, the revised product that had been out for several weeks and the newest revisions that would enable it to work with QuickBASIC 4.5. Since there were no changes in the tools themselves, only in some internal stuff, everything should work just fine.

Two Good Days

The new tools came on Friday. Early Sunday morning, we were scheduled to catch the plane to Molokai. Mrs. Pournelle's Reading Program was very nearly done. The only things that remained were polishing it and adding some administrative stuff like keeping records of which students had completed what lessons and a way to set the pass/fail percentage for each student. I was thus eager to get to work, so I opened the revised version of QuickBASIC Tools the instant it arrived.

There were extensive revisions from what I had: lots of new tools and thoroughly new documentation (which, alas,

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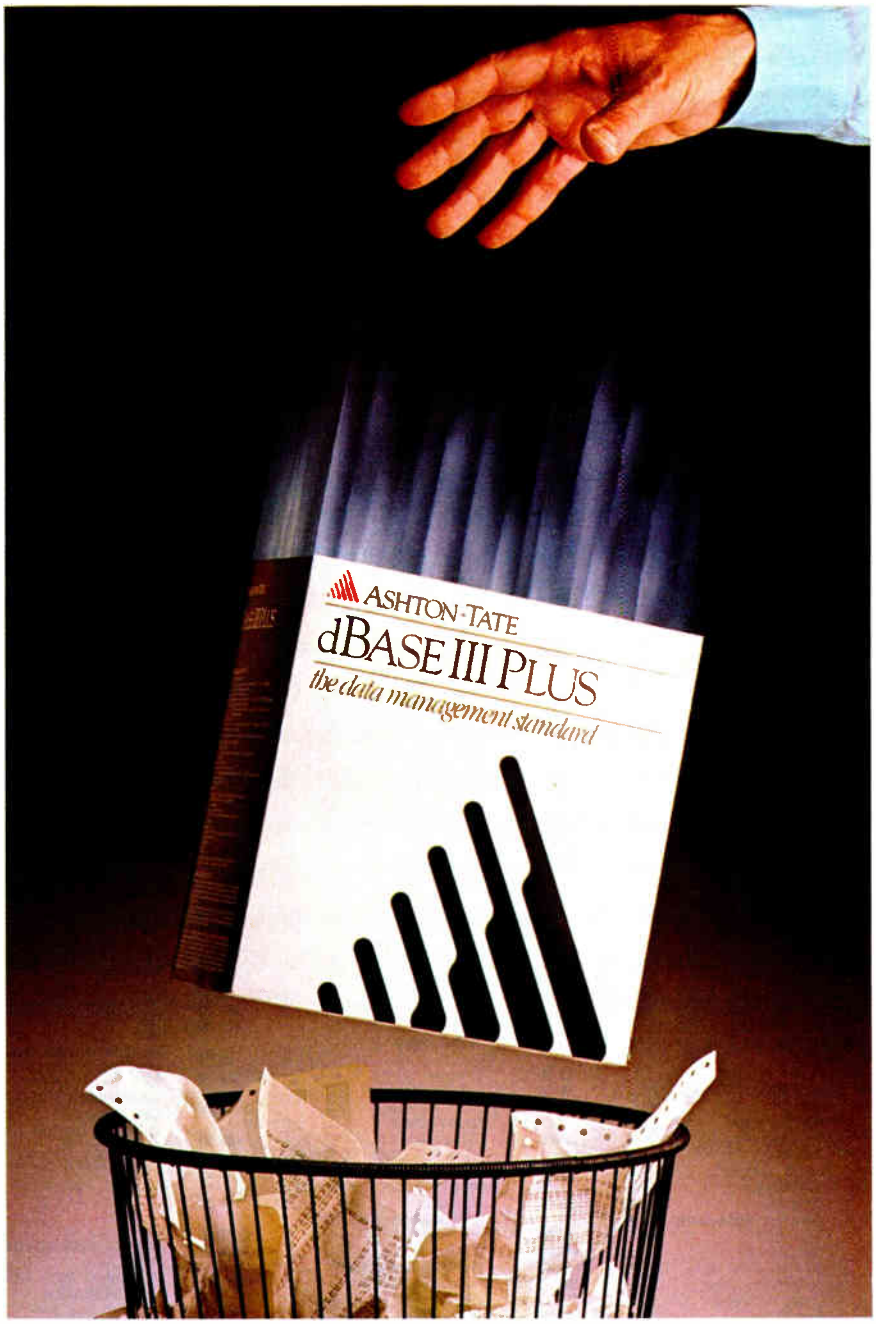

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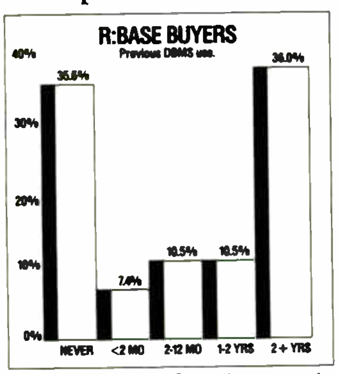

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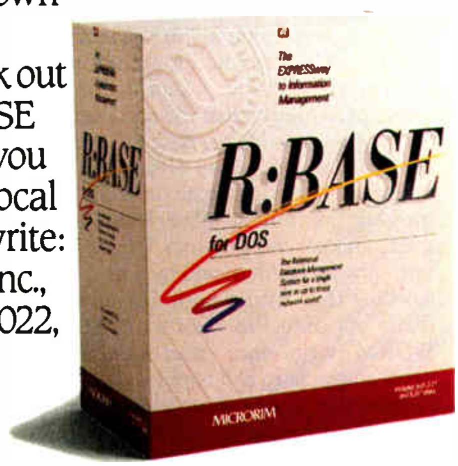

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was misprinted, not so badly as to be unusable, but badly enough to be frustrating). The library manager was rather different, too, but it was quite easy to use. I got to work building up a new LIB and Quick library.

This time, everything seemed fine. I added a whole bunch of new stuff, including the FILEXISTS function (it rather cleverly looks at the file's date rather than attempting to open it; that way, it avoids having to trap QuickBASIC's "File Not Found" error) and input routines. The Reading Program ran fine in interpreted mode. I kept adding features, particularly disk file stuff and also some provisions for running the program on multiple 360K-byte floppy disks (it takes just under a megabyte to hold the program, administrative files, lesson files, and all the graphics files). I kept testing what I added, and by golly, it was working fine. Friday night I saved everything off onto the Maximum Storage WORM (write once, read many times) drive and went to bed happy.

Saturday I was supposed to pack. Also, Jim Ransom came over to work on our SSX (Space Ship Experimental) briefing. All that was secondary for me, though; I was about to *finish* the Reading Program and get that sucker *out the door*. A great feeling. I worked a good part of the day, paused for dinner, and had at it again. By 10:00 at night I had it done. Two good days of work.

Disaster

There were two more things to do. One, I'd been running under interpreted mode, and I'd have to compile the program. Second, while I'd been working on the computer program to present the stuff, Mrs. Pournelle had been revising the lessons themselves. In doing it, she'd revised their order and added a couple in the middle of the sequence. The upshot was that of the 70 lessons, about 35 needed to be renumbered. Each lesson is accompanied by two graphics files, so I was really talking about copying and renaming some 105 files. While I could do this by hand, I sure didn't want to, especially since I could describe the changes I needed with a couple of FOR...NEXT loops in BASIC.

The only problem is that BASIC does not have a FILE COPY utility. The only way to copy a file in BASIC is to open it, read it, open an output file under the new filename, and write to that. This isn't all that slow on a Priam 330-megabyte hard disk drive, but it's not all that speedy, either; more important, writing the code at 10:00 p.m. after a long day wasn't very attractive.

However: the Project X tools include a FILE COPY utility.

Wonderful, thought I. First things first: I copied the original lessons and graphics files to the K partition of my Priam hard disk. I already had copies on the WORM drive, and of course Mrs. Pournelle had copies downstairs, both on floppy disks and on the Priam hard disk in her Kaypro 386i, so my latest copy was really gilding the lily; but anyway, I did it. Then I wrote a BASIC program that looked something like the following:

```
FOR i% = 68 TO 34 STEP -1
  j% = i% - 1
  PRINT "Copying Lesson";i%
  COPY
(F:\QB4\READ\LESSON.i%,
G:\READ\LESSON.j%)
  COPY
(F:\QB4\READ\BANNER.i%,
G:\READ\BANNER.j%)
NEXT
```

and so forth. The notion was that I'd build up the correct files on drive G, then copy the whole mess back into drive F, after which I'd be done. The whole thing shouldn't take more than 10 minutes, and the worst that would happen would be that I'd have to go get the stuff off K and start over.

I checked the code several times to be sure it would do what I wanted and told the program to run.

Nothing happened. Nothing at all. After a while, it was obvious that nothing was going to happen.

I hit Control-C. Then Control-Break. Then Ctrl-Alt-Del. None of those produced any result whatever. Finally, I hit the hardware reset on Big Cheetah.

It wouldn't boot. In a mild panic, I turned off the machine, waited a full minute, and turned it back on. It still wouldn't boot.

It was now 11:00 p.m. on a Saturday night. At oh-dawn-thirty we were due to catch an airplane to Hawaii. And Big Cheetah was thoroughly dead.

Corpse Revival

It was probably a good thing that I had to catch a plane in the morning, because otherwise I'd have tried working all night, and I'd probably have made things much worse than they were. Still, I got out my emergency floppy disk, Startup Master, and put that in Big Cheetah's drive A and reset. Nothing happened.

By now, I was in a real panic. Rationally, I shouldn't have been. After all, everything up to late Friday evening was backed up on the WORM drive. True, what I'd worked on all day was probably lost, but heck, it wasn't *that* much, and besides, I could pretty well remember what it was I'd done. It wasn't as if I'd been doing creative writing—and yes, I know that sometimes programming can be quite creative, but what I'd been doing hadn't been like that at all, it was just file management stuff, easily recoverable work. All I'd lost was some time.

Still, I was annoyed, and while I didn't see how some software program could permanently harm Big Cheetah, he sure was dead, which was a little scary.

It was now about midnight. I called my son Alex. After all, he's in the data-recovery business. What's the use of having kids with degrees in computer science if you can't bug them in the middle of the night? Actually, he was up, since he keeps about as weird hours as I do.

"Have you looked at the RAM BIOS entries?" he asked.

I blushed to say I hadn't.

"Run the Setup program," he said.

"How can I do that when it won't even look at the floppy?" I muttered, but then I realized that with an Award BIOS, as Big Cheetah has, the Setup program is in ROM; all you need to do is press Ctrl-Alt-Esc during the boot-up sequence and you're automatically put into Setup. I did that.

"Nothing," I reported. "Every entry is blank. Even the time."

"That must have been some COPY utility," Alex said.

I reset the clock and told Big Cheetah that he had a 1.2-megabyte floppy disk in drive A, a 360K-byte floppy disk in drive B, and a "Type 9" 330-megabyte hard disk drive as drive C. Then I tried to boot up.

Big Cheetah looked at drive A, but since the door was open, he tried to boot from drive C. "Bad or missing Command file," I was told. Not good news, but at least we were making progress. I put the DOS 3.3 Startup Master in drive A and reset. Up he came. I couldn't access drive C, or any of the other hard disk drives for that matter, but at least I knew Big Cheetah was alive. At that point I went off to bed. Next morning I caught the plane.

Supercomputers

Hawaii was great. Not only was the conference extremely interesting, but by getting up at 5:00 a.m., while it was still dark, and going out on the golf course in my pajamas, I was able to see Alpha Centauri, the nearest star (and the origin of the Fithp invaders in *Footfall* by Larry Niven and Jerry Pournelle).

It used to be that the only supercomputers were at the National Weapons Laboratories, namely, Lawrence Livermore and Los Alamos. Then some really bright people got to thinking that even when supercomputers become more widespread, there won't be many people who know how to use them because there's no way students, or faculty members for that matter, could get any time on them. From those thoughts grew the National Science Foundation (NSF) Supercomputer Center program.

I am not normally a big fan of government programs. Most not only don't work very well, but they often produce more of the "problem" they were designed to solve. However: let me be the first to say that the nation nearly always benefits from the investments made by the NSF in general, and that the NSF Supercomputer Center program may be the most spectacularly successful government investment since Isabella hocked the crown jewels.

Indeed, if you've a mind to write letters to your Congresscritter, you might mention this program: that it has worked wonders, and the only problem now is that it was so successful that it's underfunded. It needs more resources to put more time in the hands of more people. More important, though, the whole point of the program is to make available the absolutely latest in supercomputer technology, and the Centers won't be able to do that and expand their grant program at the same time.

We're not talking about billions here. A hundred million dollars a year works miracles. And do understand, we're talking about *investments*, with real and visible payoffs. We don't have any U.S. equivalent of the Japanese Ministry of International Trade and Industry to help U.S. industry against overseas competition, and that's probably just as well: the last time the Department of Commerce got into the act to "help" with the DRAM-chip situation, they darned near ruined the industry. Consequently, the NSF grant programs, which enable our students and faculty members to stay out at the cutting edge of technology, are by a long shot our most effective weapons. They really are important, and they really do work miracles.

I got to see some of those miracles in Hawaii. What has happened is that while supercomputing is still confined to a fairly small community, that community has grown spectacularly in the past few years—and every scientific discipline that supercomputing touches gets revolutionized. Biology, hydraulics, aeronautics, physics; the list goes on.

There's still a lot to be done. There are real problems with operating systems

continued

and languages—most scientific work is done in FORTRAN, and as one physicist put it, after you've written 40,000 lines of FORTRAN, you're too tired to do physics any longer. There need to be software tools and easier user interfaces. Some of that is already happening, as for example at the Illinois National Supercomputer Center, where Director Larry Smarr has interfaced the supercomputers with all kinds of machines, including the Macintosh.

You Too Can Play

I'll have a lot more on supercomputers in the future, but for now, the news is that any legitimate student or faculty member of any U.S. university, college, or junior college who wants time on a supercomputer can get it.

Write Janice Friedland, User Administration Coordinator, John von Neumann National Supercomputer Center, P.O. Box 3717, Princeton, NJ 08593. Ask for either the Education Grant Form or the Research Institution Grant Form. The education grants are for 2 hours' time and are nearly automatic; they're for the purpose of getting familiar with supercomputers. The research grants go up to 20 hours, and they require a brief proposal for educational demonstrations or small research projects. Review is very rapid.

Janice Friedland can also be reached on Bitnet as FRIEDLAND @ JVNCC or on the Internet network as FRIEDLAND @ JVNCA.CSC.ORG.

Fill out the forms; if you're a student, get a faculty member to countersign; and the time is free. (You may have to pay for long-distance access, although many academic institutions are already connected through one or another network, and those may be free.)

Incidentally, the National Supercomputer Centers find it amazing that only 1 percent of the academic use of their machines is by computer science and mathematics departments. Most is by engineering departments, followed by physics. Biologists use more time than computer science departments.

Restoring Big Cheetah

Hawaii was fun, but I brooded all the way back, and although we got in late at night, I couldn't wait to work on Big Cheetah.

My first move was to get out Norton Disk Doctor (NDD), put it on a floppy disk, and see what it said about the hard disk drive.

The result was interesting. NDD reported a number of problems, including a damaged partition table, and it offered to fix them. I told it "thank you, no," until I could think it over. The surest way to recover any data off Big Cheetah's hard disk would be to let Alex have at it. Workman & Associates has been able to work miracles in data recovery, but it would be no favor to them to try home remedies first.

On the other hand, Alex is busy, and he wouldn't be able to get at it for a day or so; and after all, I did have WORM drive backup copies of just about everything on that disk. What the heck, I thought, and told NDD to go ahead.

It trundled for about a minute, then it asked me to reboot the machine. This time Big Cheetah admitted he had a drive C—and reported a bad copy of COMMAND.COM. That was easily fixed. By now, Big Cheetah was healthy enough to boot from a 1.2-megabyte floppy disk. I transferred the ESDI disk driver—with a 330-megabyte hard disk drive, you re-

continued

You No Longer Have to Share the Lower 640K With Your Debugger

Periscope I's new board uses ZERO memory in the lower 640K. Yet it has plenty of room to safely store all debugging information, like symbols, as well as the powerful Version 4 software.

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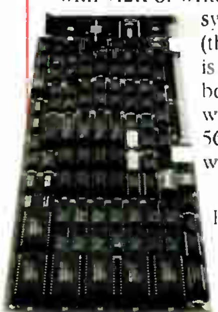
The break-out switch lets you break into the system any time. You can track down a bug instantly, or just check what's going on, without having to reboot or power down and back up. That's really useful when your system hangs! The switch is included with Periscope I, Periscope II, and Periscope III.

The NEW Periscope I memory board keeps all debugging information out of the lower 640K. Can be used in PCs, ATs, and 386s with both EGA/VGA and EMS boards installed. The Periscope break-out switch enables you to recover from a hung system. Included with Models I, II, and III.

Periscope I has a **NEW** board with 512K of write-protected RAM, user-expandable to 1MB, for the Periscope software, symbol tables, and all related debugging information. Normal DOS memory (the lower 640K) is thus totally freed up for your application, and Periscope is protected from being overwritten by a run-away program. The new board's footprint is only 32K, so you can use it in PC, AT, and 386 systems with EGA/VGA and EMS boards installed (not possible with the previous 56K board). It can also be used with Periscope III to provide additional write-protected memory.



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David Nanian, President of Underware, Inc. (of BRIEF fame) says this about the new Periscope Version 4:

"Periscope has always been an unbelievable assembler-level debugger. Version 4 has turned it into a terrific source-level debugger as well. Aside from major enhancements like the source-level improvements, all the little changes make a really big difference, too. For instance, symbol lookups and disassemblies are noticeably faster, and highlighting the registers that have changed really makes life easier. Once again, Periscope has raised the industry standard for debuggers!"

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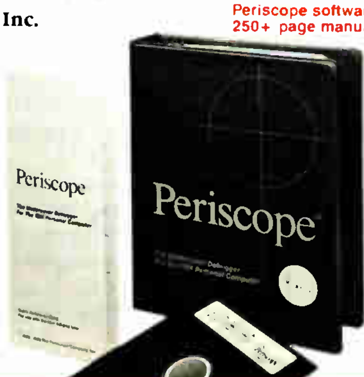

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- **Periscope II-X** includes software and manual (no hardware) for $145.
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Due to the volatility of RAM costs, prices on board models are subject to change without notice.

REQUIREMENTS: IBM PC, XT, AT, PS/2, 80386 or close compatible (Periscope III requires hardware as well as software compatibility, thus will not work on PS/2 or 80386 systems); DOS 2.0 or later; 64K available memory (128K at installation time); one disk drive; an 80-column monitor.

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"Module Too Large." That one was simple to fix. I broke the code up into several modules and compiled each. Worked fine. Linked them. Worked fine. Ran the code.

"String Space Corrupt."

A blooming wonder is what I consider Mathematica to be.

If you look that up in the Microsoft reference manual, it tells you that you've probably made some kind of error in a COMMON statement. I had only one COMMON statement in the doggone program, and all it did was pass a number of universal constants, such as TRUE and FALSE. I could have experimented to see what was wrong now, but I didn't. It took only 5 minutes to rearrange modules so that I didn't need any COMMON statements at all.

Done!

Five minutes later I was done. Mrs. Pournelle's Reading Program is finished. There's about 100K bytes of code. I'll be able to trim it quite a bit by eliminating line numbers and other diagnostic hooks, downsizing arrays, and generally tightening things up; the program should run in a 256K-byte machine. At the moment, it takes about 340K bytes. It also requires either a color or a Hercules graphics monochrome video card.

There's still some polishing to do. The documentation is Mrs. Pournelle's problem, but she pretty well hammered that out in beta testing. I may add touches like help files. What we do have right now, though, is a stable program that will enable just about anyone who can read English to teach just about anyone else.

The highly structured program uses intensive sequential phonics. In addition to the program and a computer, there must be an instructor present to read the on-screen lessons and instructions. The instructor need not be a trained professional, or even an adult.

There are 70 lessons. Each takes a *minimum* of 20 minutes. (Since the program is self-paced with built-in rewards, we can't specify a maximum.) At a normal pace of one lesson a day, with reasonable time for weekends and review, that's 90 days to full reading ability.

Of course, it will take longer in special cases, and we suppose there must be cases (particular pupils or combinations of instructor and pupil) where it won't work at all—although we've never seen one. It does take patience and persistence, but then, all education does.

Mrs. Pournelle's Reading Program (IBM PC version 1.0) is available from Roberta J. Pournelle, 3960 Laurel Canyon Blvd., Suite 372, North Hollywood, CA 91604. The current price is $100 postpaid. We haven't the remotest idea of what the final price will be or, for that matter, who the publisher will be.

Winding Down

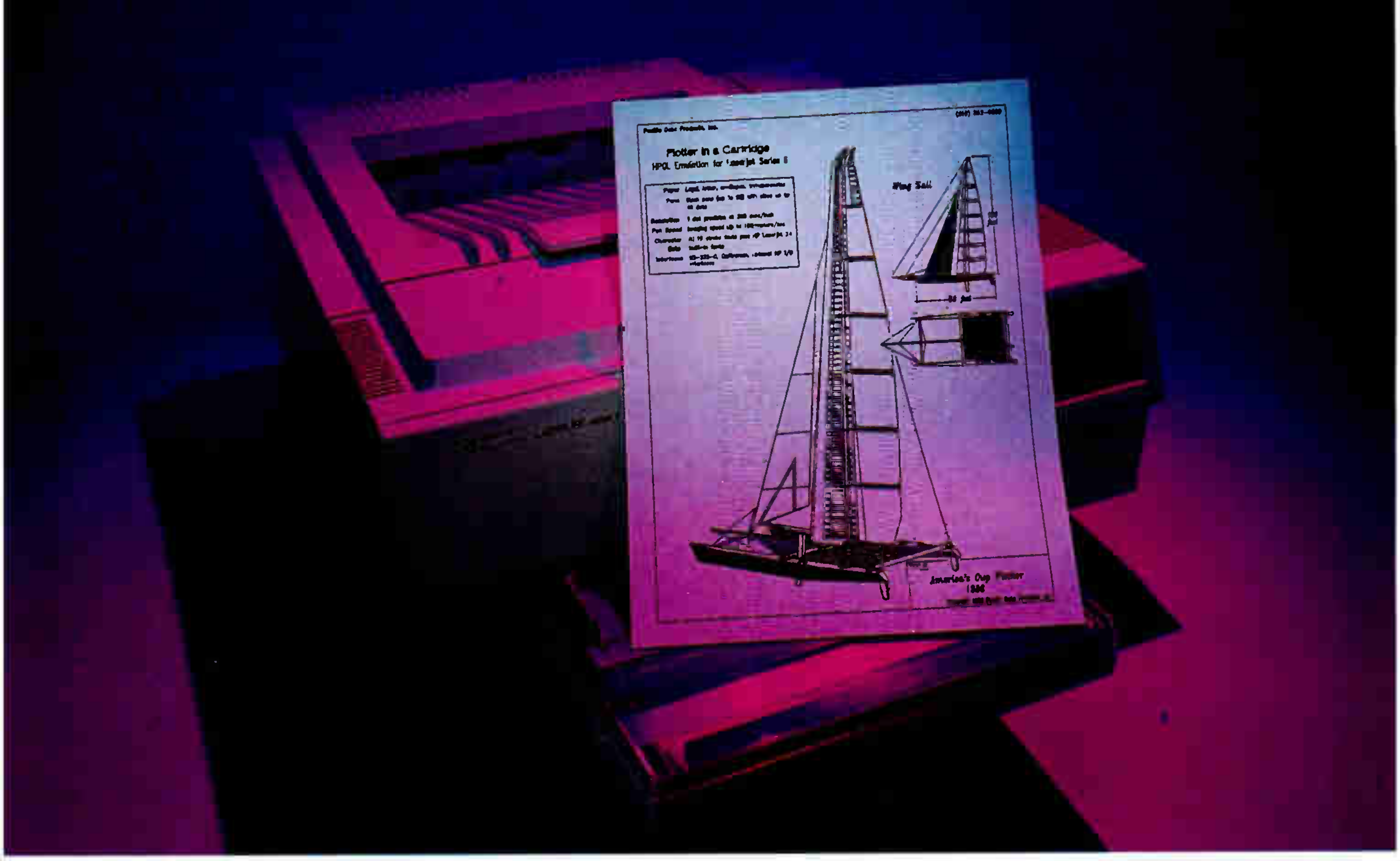
I'm writing this on the SupersPort 286 in the San Francisco Hilton. We're up here for the annual meeting of the American Association for the Advancement of Science, followed by MacWorld Expo. A lot of exciting things are happening in the world of science. One that's particularly relevant to computer users is Mathematica from Wolfram Research. As of this week, there's an 80386 version, as well as versions for the Mac and the NeXT machine. The program is a blooming wonder. More on it next month.

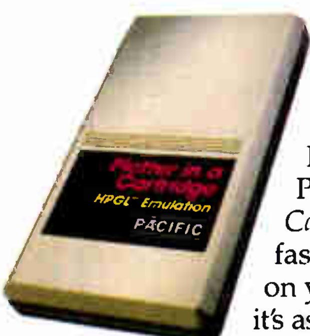
The book of the month is Edith Efron's *The Apocalyptics* (Simon and Schuster, 1984). This is a long but fascinating scientific detective story that should be must reading for anyone intelligently concerned about environmental quality, which, I hope, means every voter. As Efron says, laypeople can't make scientific judgments; but we do have to understand the costs and benefits of rules and regulations.

Now I have to get back to MacWorld Expo. Tonight, we drive back to Hollywood. I confess I can't wait to get back. I've thought of a couple of final touches that I can put on Mrs. Pournelle's Reading Program, and I want to play with Mathematica. ■

Jerry Pournelle holds a doctorate in psychology and is a science fiction writer who also earns a comfortable living writing about computers present and future. Jerry welcomes readers' comments and opinions. Send a self-addressed, stamped envelope to Jerry Pournelle, c/o BYTE, One Phoenix Mill Lane, Peterborough, NH 03458. Please put your address on the letter as well as on the envelope. Due to the high volume of letters, Jerry cannot guarantee a personal reply. You can also contact him on BIX as "jerryp."

The Wait is Over.





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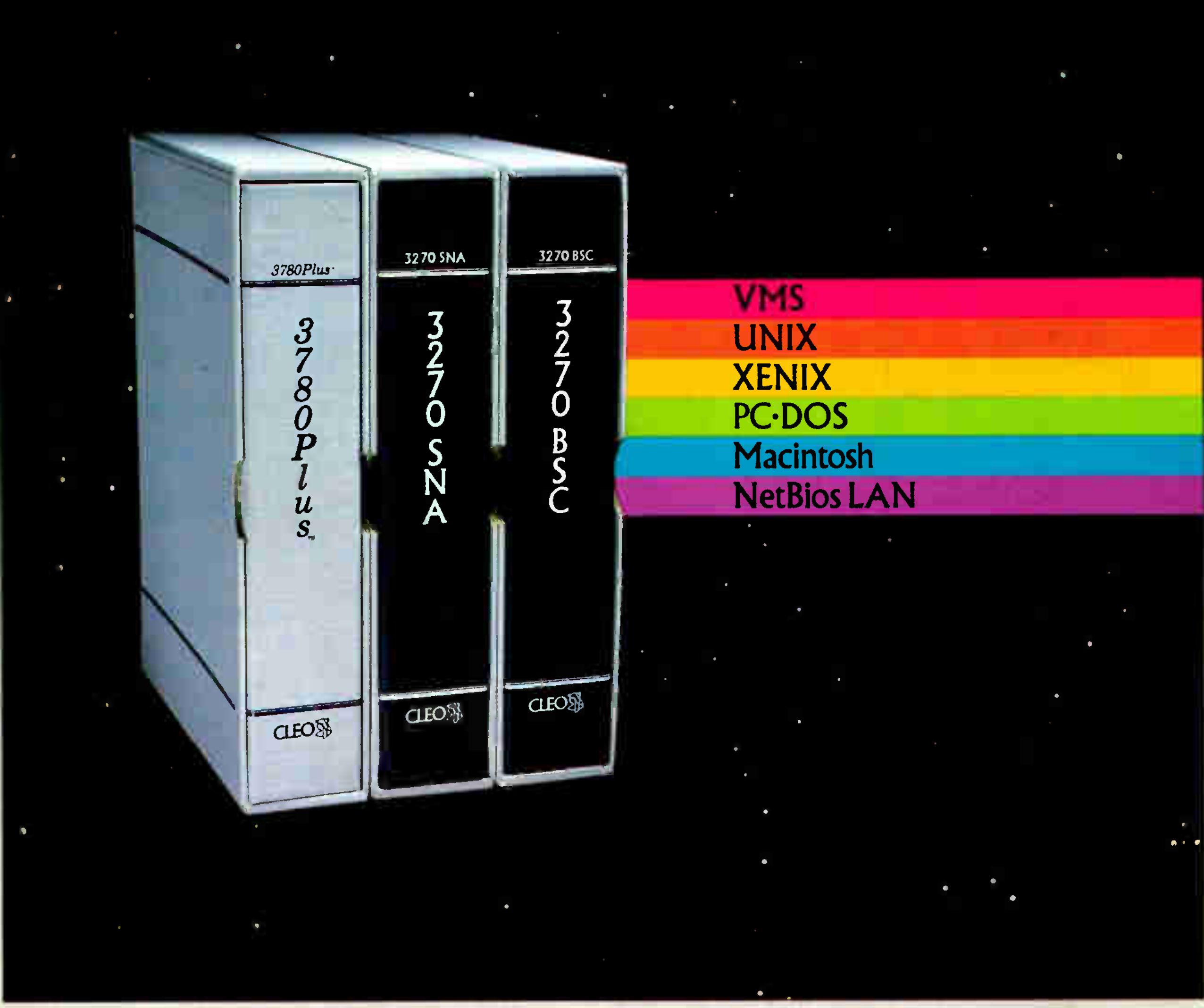

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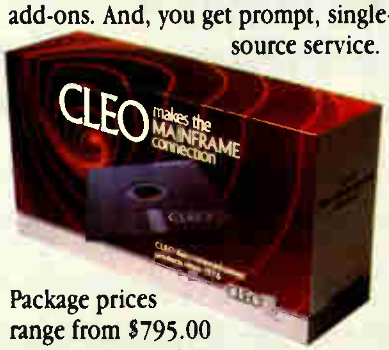

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ANSWERS TO MY MAC MESS

Readers rise to defend the Mac and offer solutions to system snafus

In my December 1988 column, I commented that my Macintosh system had become as overburdened and fragile as my MS-DOS system, with frequent crashes and the accompanying sense of insecurity and fear. I pin this problem on a number of factors.

First, there is the proliferation of INITs and cdevs, programs that are loaded at boot-up and modify the available functions of the operating system. Many of these programs are essential to me (e.g., Vaccine, a public domain virus-protection program, and QuicKeys, the keyboard macro program from CE Software), but they occasionally conflict with application programs, the operating system itself, and each other.

Second, Apple's upgrades to the operating system are often somewhat buggy. The maintenance releases that follow major overhauls seem to fix most of the problems, but invariably Apple also changes the rules for software compatibility just enough to make using older applications a risky proposition.

Third, the pressure to bring new products to market sometimes forces developers to release programs before they're thoroughly debugged.

Finally, the increasing size of programs and the dangers of negotiating the minefield of conflicts with other products make bulletproof debugging nearly impossible. Sooner or later, no matter how exacting a vendor's quality-control procedures, some customer somewhere will come up with a set of INITs, desk accessories, and other programs running under MultiFinder that will bring the system to its knees.

I don't blame anyone for this mayhem, and it's certainly not unique to the Mac universe. I note that Apple and the community of Mac developers have done a good job compared to the confusion that infected the MS-DOS world when memory-resident programs appeared. However, my complaining sparked a spate of correspondence on the subject.

Safe Strategies

One group of letter writers offered a practical solution to my headaches: Don't upgrade the operating system or applications until you're absolutely sure that everything will run amicably. This is really the only strategy that makes sense; and I endorse it wholeheartedly, whether you're running a Mac, an IBM PC, a VAX, or anything else.

If you can possibly do it, stay one or two releases behind the most current offerings and let other people serve as the guinea pigs. Unfortunately for me, it's my business to act as a guinea pig, and thus I consciously place myself in jeopardy with more willingness than I would were I merely using software rather than evaluating it. I stick my neck out lest others get their heads chopped off.

Other correspondents took umbrage with my bashing of Apple's System software releases as "more bug-laden and crash-prone than Microsoft's MS-DOS updates." I stand by the point, but it's really immaterial; the evil synergy occurs when a number of components are in contention. Even if the Mac operating system was completely solid, which I dispute, the hassles arising from running the mélange of operating system, MultiFinder, new applications, old applications, desk accessories, INITs, cdevs, and so on, stem from the interaction rather than any one piece of the puzzle.

The best advice is to test each component independently before attempting to incorporate it into a complex environment. By being careful and testing exten-

continued

ILLUSTRATION: KERRY GAVIN © 1989

sively, wrote Matthew Dixon Cowles, "I have not, by any means, had to become 'accustomed to the sporadic system crashes that characterize life on the Mac.' And neither have my clients."

OK, fine. I yield. If you adopt a conservative approach, you'll be safe. If you want to waste your time testing every piece of software you own, singly and in combination, or hire a consultant to do the same thing, that's your business. Me, I think this says something about the nature of personal computing these days. Something ugly.

Back to Backup

Of course, everyone chided me for not being adequately backed up, and a few went so far as to say I got what I deserved when I inadvertently trashed my hard disk. Well, I'm not entirely foolhardy, and I'm not going to get caught again. A handsome new Irwin Magnetic Systems' tape unit now sits next to my Mac. This little devil, the Model 5080, crams almost 80 megabytes onto a data cassette, and I was able to back up roughly that amount in a little under an hour. It costs $1695, Irwin's formatted cartridges go for $35 a pop, and the peace of mind is worth every penny. The slickest thing about it, though, is the EzTape software.

EzTape allows tremendous flexibility in backup procedure; just about anything you want to do is possible. You can back up or restore an entire disk volume, or you can specify folders, individual files, or types of files, in any sort of combination. You can name backup sets, save rule criteria as parameter files, and perform incremental backups by date or by date of last backup.

The program will run in the background under MultiFinder, and you can configure it to run automatically at a certain time or regularly at a specified period. It will even restore MS-DOS tapes to a Mac disk, or vice versa. (When acting as a file transfer system from Mac to MS-DOS, EzTape will discard the meaningless Mac resource forks and create legal filenames and directory names.) In the past, I've used HFS Back Up, Diskfit, and Central Point Software's PC Tools backup module. This program is as good as any of them.

If you're going to attempt unhealthy maneuvers in an environment that's not rock solid, buy some insurance.

Good-bye, Mousie!

My search continues for an acceptable alternative to the mouse. This month's entry is the MouseStick from Advanced Gravis Computer Technology, a deluxe joystick that lists for $169.95. I've been experimenting with the Mac Apple Desktop bus (ADB) model, but other versions are available for older Macs, the Apple IIGS, and PC compatibles. It's a nifty gizmo, well built and responsive, but I've had a few trivial snags that temper my otherwise enthusiastic endorsement.

The unit itself is a bit more complicated than I would have expected. A 4-inch contoured, foam-padded stick rises from a base that's 6½ inches wide, 4¾ inches deep, and 1¼ inches high. The base has rubber feet; that fact, along with its size and weight, makes it difficult to tip in most situations. With the two large thumbwheels front and rear, you can adjust stick tension from stiff and springy down to totally limp. There are three "fire" buttons, two on the base to the left of the stick and one on top of the stick. All three buttons are initially set to generate single mouse-clicks.

A cable leads from the pedestal to another box, about a third smaller than the base, which houses the device's programmable electronics. A 16-character LCD indicates mode settings that enable nearly total control of resolution, tracking, and button function. An ADB cable leads from this GMPU (Gravis MouseStick Processing Unit) to the Mac.

continued

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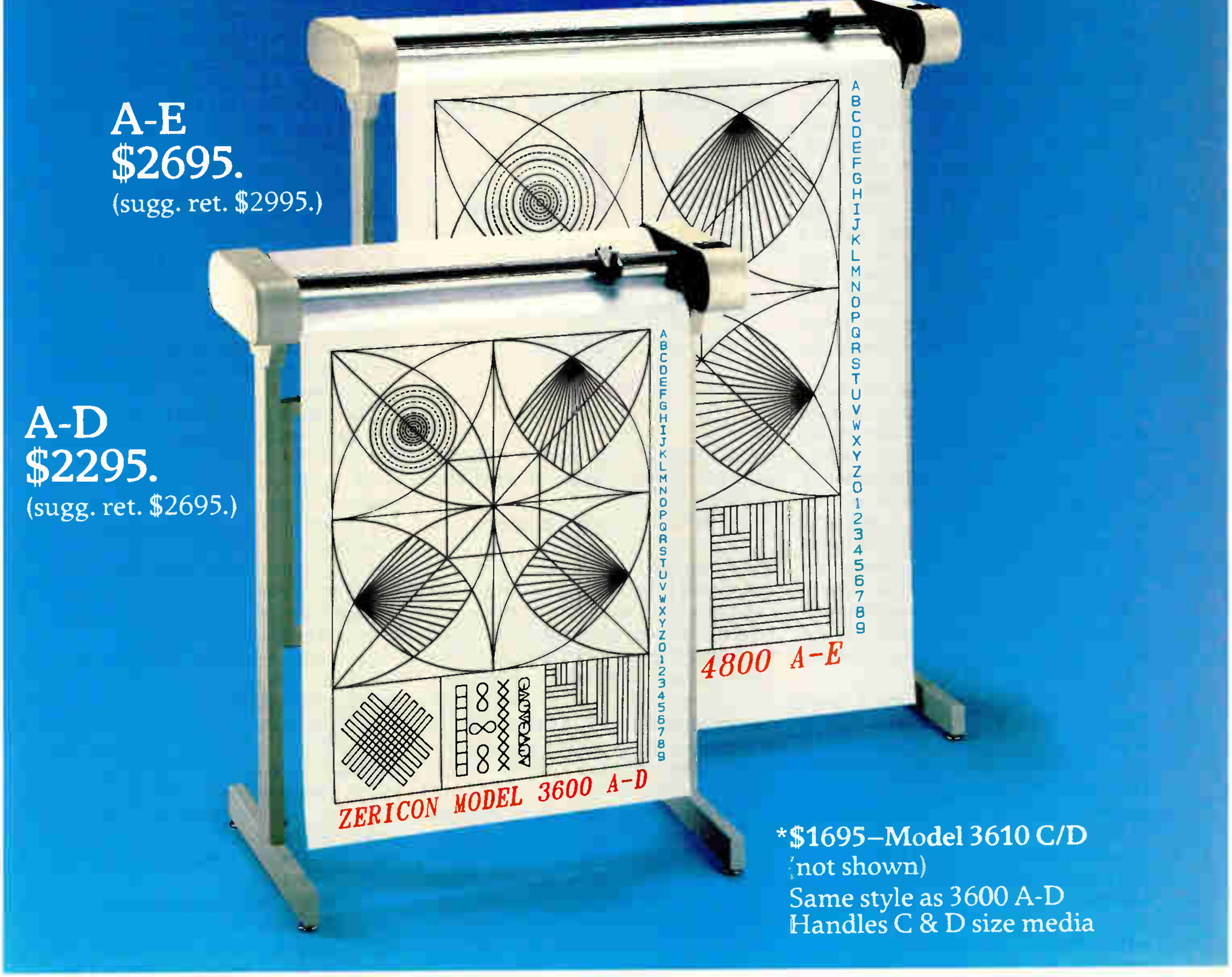

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After I had used it for a few minutes, I was convinced that my instinctive approach to the MouseStick, which was to grab the stick in my fist and click the top button with my thumb, like a jet pilot, was useless for anything but game playing. You need far more precision for even the simplest Mac operations, like clicking on an icon or setting the cursor; I felt about as coordinated as someone trying to fill out tax forms with one of those fat crayons they give you in grade school. I had also adjusted the joystick for the highest level of tension, figuring that would give me the fastest, most sensitive response. Instead, I found myself struggling against the stick to hold the cursor in position.

By thoroughly rethinking the way I held my hand, though, I was able to achieve a degree of control I have not experienced with either a mouse or a trackball. Resting my index finger on top of the stick and grasping the shaft between my thumb and remaining fingers, the way you might hold a saltshaker to tap out small quantities of salt, gave me the ability to make tiny, accurate movements with the cursor. I also loosened the tension wheel five notches, to the lowest amount of spring allowable for automatic centering.

Using Aldus FreeHand and Cricket Paint, I actually knocked out a couple of drawings as close to pen and ink as I've ever produced with a computer. And I could sketch quickly, rather than laboring over every line.

With the mouse, you draw with your shoulder and elbow, using your fingertips only to click the mouse button. The trackball cuts down the large muscle movements, but there's still very little wrist involved, and the second gesture necessary to click a button is often awkward and throws off your cursor position. The MouseStick puts it all in your wrist and fingertips; it's very tight and very comfortable. I haven't had enough experience with a stylus and a bitpad to make a knowledgeable comparison, but I'd guess that a skilled MouseStick operator could come close to the effects achieved with a digitizing tablet.

Now for the bad news. The GMPU draws its power directly from the Mac's ADB; there's no internal battery. Any configuration settings you program into it are wiped out when you turn off the Mac. Though running through the setup routine every time you boot the computer is inconsequential, it's irritating. This is pretty dumb; a little bit of RAM and a lithium battery are not too much to ask at this price.

Further, the unit is designed for the game player who wants to play rocket jockey, so even if the MouseStick is built for abuse, the ergonomics don't favor the serious computer user. I'd love to see a reworked MouseStick with a slimmer base, a shorter shaft, and a convenient way to rest your wrist on your work surface.

I've never been particularly opposed to copy protection.

But for now, I'm very pleased.

Bitten Again

Just when I thought it was safe to go back into the water, I was attacked by that old nemesis, the copy-protection shark. This happened three times in the course of a two-week period, which was quite a surprise. Copy protection has pretty much vanished, particularly in the category of business software, so finding three protected programs all at once is worth a few comments.

I've never been particularly opposed to copy protection; in fact, I've gone on record as something of a hawk. I believe that software companies and authors are entitled to just compensation for their work, and should they choose to combat unauthorized distribution via copy protection, so be it.

Software piracy is rife; almost everyone I know engages in casual swapping on a regular basis. And coping with the various protection schemes is a petty annoyance that has been blown vastly out of proportion. Plugging in a key disk or running through an installation procedure is a momentary hassle at worst. On the other hand, a petty annoyance is still an annoyance. As a user, I choose to avoid the issue by steering clear of protected programs. This is what's known as a "marketing reality." Most vendors have chosen to acknowledge this attitude and unprotect their programs.

Two of the three cases I discovered can be dismissed easily. One was a game that merely requested I type in a certain word from the manual. Rather mild stuff, and certainly reasonable. The second was a $5000 package that's normally sold as part of a fully integrated system; it required a hardware key (what's known as a *dongle*) to be plugged into the Mac's modem port. I figure that if the consumers of this product are willing to pay that kind of price, they'll be willing to put up with this kind of nuisance.

The third case was much more disturbing, because the copy protection is attached to a major software package that could be one of the most significant desktop publishing products of 1989. The software is Letraset's LetraStudio for the Mac, a $495 typographic manipulation program that lets you kern, distort, color, and otherwise make headlines perform tricks you didn't think were possible on a microcomputer.

The resulting modified type is stored in Encapsulated PostScript format, ready for inclusion in files created by most word processing and page layout programs. Compared to the dismal effects you get when you try to blow up standard PostScript-text alphabets to headline size, this is spectacular stuff. Anyone involved in serious desktop publishing, advertising, graphics, package design, and so on will be drooling for this one.

LetraStudio itself is not copy-protected, but this is a classic case of razor and blades. LetraStudio is the razor; because it can work only with special typefaces sold by Letraset, the typefaces are the blades. And the blades are copy-protected. Four reversible installations, but protected. You get two blades with the package and two more when you send in your registration card—anything else you've got to buy.

This hits my fence-straddling position right smack-dab where it hurts. I can't really complain about this scheme on moral or ethical grounds, because I believe Letraset has the right to protect its interests however it sees fit. On the other hand, I need and want this program, and I also want lots and lots of blades for it. While I have never had a single disk mishap due to this sort of protection, these typefaces will be the only things on my hard disk that are copy-protected. Which makes me very, very nervous.

Something to think about, you bet. ■

Ezra Shapiro is a consulting editor for BYTE. You can contact him on BIX as "ezra." Because of the volume of mail he receives, Ezra, regretfully, cannot respond to each inquiry.

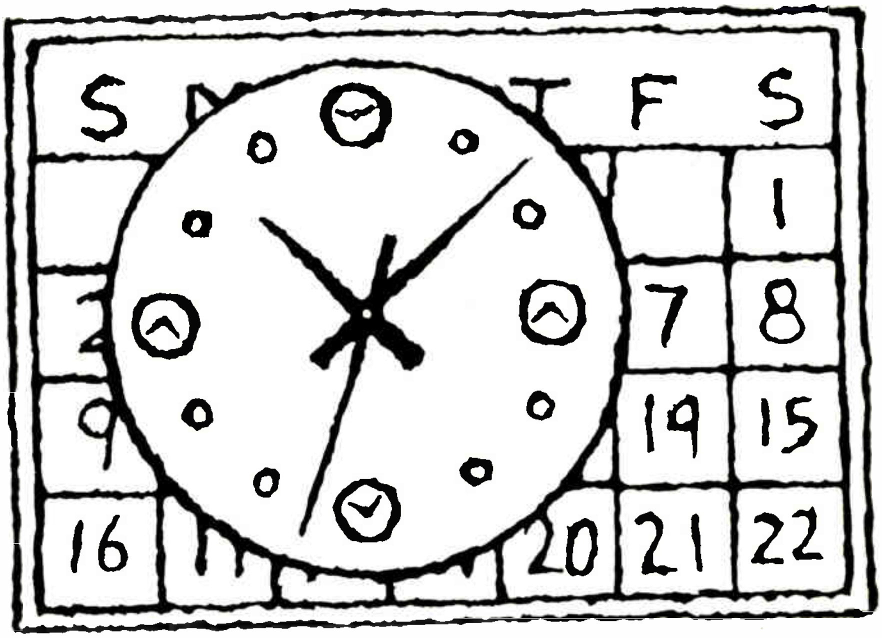
Your questions and comments are welcome. Write to: Editor, BYTE, One Phoenix Mill Lane, Peterborough, NH 03458.

GROPING FOR GROUPWARE

Group productivity software is designed to make life a little less complicated for busy executives

Group productivity software, or "groupware" as it's affectionately known, is designed to enhance the functioning of a group in much the same way that individual productivity software helps the individual. The difference is that groupware, to be effective, must enhance the interaction of the people in a group.

Because of the nature of a group of people, groupware faces several challenges. First, people who work together are not necessarily located together. They can be spread across many floors, between buildings, or even in separate cities. Second, a group consists of individuals who may have their own ideas about what work they need to do and how it should be accomplished.



The Parts of the Group

People in a working group interact with each other in two ways that software can help. The first is simple communications. This function is met through the use of a telephone or by E-mail. People also tend to gather in meetings, so they need to set up those meetings. An electronic scheduling package can help.

E-mail is reasonably familiar to most computer users. A good E-mail package should be easy to use, even for inexperienced users, but still capable enough that you can use it to forward word processing and graphics files. It should also allow time and date stamping, multiple addressees, copies, and forwarding. Mail should be password protected, so that someone else cannot send mail while pretending to be you.

An electronic scheduler allows group members to see when others in the group are free. It should have some way of notifying others that you want to have a meeting, and it should be able to automatically find a clear time for all members of a specified group so that they can schedule a meeting. Of course, the ability to do this doesn't mean that it can be used in every case. Not everyone likes to have others schedule his or her meetings.

Looking at Groupware

For this column, I looked at two packages of group productivity software: WordPerfect Office from WordPerfect Corp., and Higgins from Conetic Systems. These are designed to work on a LAN and to support the users on the network. Higgins has been around for years and is widely used, while WordPerfect Office was introduced only last year.

Both packages provide E-mail and group scheduling and are designed to provide a shell from which you can perform most other functions, such as word processing. Each contains additional productivity tools designed to make the busy executive's life less complicated. Whether they do this for you depends on whether your life matches what the company thinks it should be.

I used both packages on an Ethernet network running Novell NetWare 2.12. Both packages are designed to run on most other common LANs, including 3Com, Banyan, and most NetBIOS-compatible networks. Both packages reside on the file server.

WordPerfect Office

WordPerfect Office is the latest entry in what seems to be WordPerfect Corp.'s continuing effort to take over the world. The package is based on a shell that has a two-column screen of choices (see photo 1). Along with the shell, you run a small memory-resident program that will alert

continued

ILLUSTRATION: MARCEL DUROCHER © 1989

you when it receives mail messages or schedule requests.

The standard shell that comes with WordPerfect Office includes choices for all the included groupware programs, as well as for WordPerfect, PlanPerfect, and DataPerfect (not included; you can easily change the entries for these if you don't have that software). The shell lets you create a menu with up to 20 entries and provides a command line that lets you run anything else you need. For example, you need the command line to perform DOS functions, since none of these are provided on the standard Office menu. Of course, you can add them to the menu yourself, if you wish. The shell requires only about 40K bytes of memory, and it didn't seem to interfere with any other software.

When the shell loads, it is able to tell who you are from the network software, so you don't have to enter your name again, though you do have to enter passwords for the mail and scheduler systems. Otherwise, you can remain in the shell and perform your day's work.

In addition to the scheduler and E-mail, Office contains a calendar program that works in conjunction with the scheduler. This means that people can see whether you're busy, but they can't see the details of your calendar or who you're meeting with. Office also includes a file manager, which lets you search for and copy DOS files; the Notebook, which is a flat-file data manager; and a calculator. There is also a macro editor for WordPerfect and a program editor for batch files and the like. For those long lunch hours, Office also contains a game called Beast, which is nice, but not nearly as nice as Novell's multiuser Snipes game.

WordPerfect users will feel right at home with WordPerfect Office. Most of the common keystrokes are the same, and the flow of the program is familiar. The editors are quite similar to WordPerfect. The E-mail editing screen follows a message format, but most of the familiar WordPerfect keys work there, too.

Higgins

When you start running Higgins, it lets you know right away that it's groupware: The first thing you see is the group scheduling screen (see photo 2). Other functions appear in windows on your screen, but your calendar is always right there at the top. If you have a busy schedule that you need to refer to a lot, this is really handy.

Likewise, Higgins shines when it comes to E-mail. You can buy optional packages that let you send mail to external systems, to other networks, and to mainframe mail systems. You can even send mail to people who don't have E-mail: If you specify it, Higgins will print the mail message so that you can send it via paper mail.

At the bottom of the screen, a Lotus-like menu gives you access to several applications. Higgins includes additional productivity tools in the form of a calculator, a scratch pad, and an expense program. The expense program seems to be useful, though many companies require specific software or expense voucher forms that would limit the usefulness of the software contained in Higgins.

Unlike WordPerfect Office, Higgins doesn't let you incorporate specific external software into the menu system. Instead, you invoke a command that gives you a command line from which you can run external software. This makes Higgins somewhat less convenient for inexperienced users.

If Higgins has a problem, it's security. When you start up Higgins, it asks you for your name and password. It does not take your name from the network software. Once you enter the password, you need not do it again. This means that if you are going to be away from your desk, you need to leave Higgins, since anyone with access to the computer could enter your mail and scheduling system. WordPerfect Office, on the other hand, requires you to enter a password each time you access mail and scheduling. This means that you can start up the Office shell and stay there all day, since you can run software from there and since others still won't have access to your mail and schedule systems.

On the other hand, Higgins will allow you to have two passwords for your account. You can have one for yourself that allows full functionality and another with partial functionality for your secretary. Higgins also lets you arrange for your nonprivate mail to be forwarded to someone else while you're away.

In general, Higgins is easy enough to operate, although I did find it awkward to use the space bar, rather than the arrow keys, to navigate through the Lotus-like menus.

Grouping the Differences

I found both packages useful. WordPerfect Office suited me better, but as in the

continued

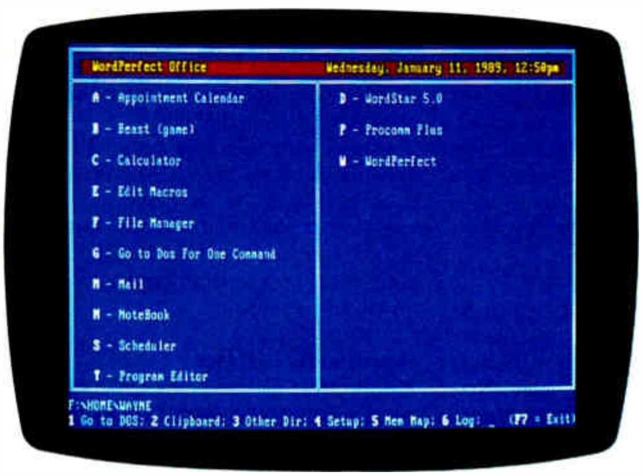


Photo 1: *WordPerfect Office's menu lets you choose from several built-in programs or create menus of up to 20 choices from your own application programs.*

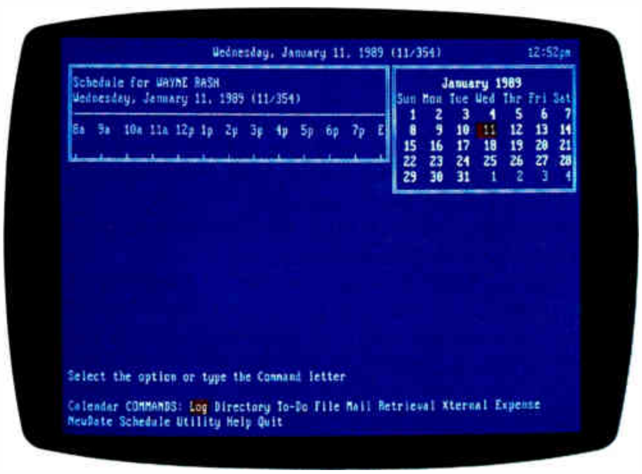


Photo 2: *Higgins places the group scheduling window prominently on the screen and includes a Lotus-like menu at the bottom that gives you access to other applications.*

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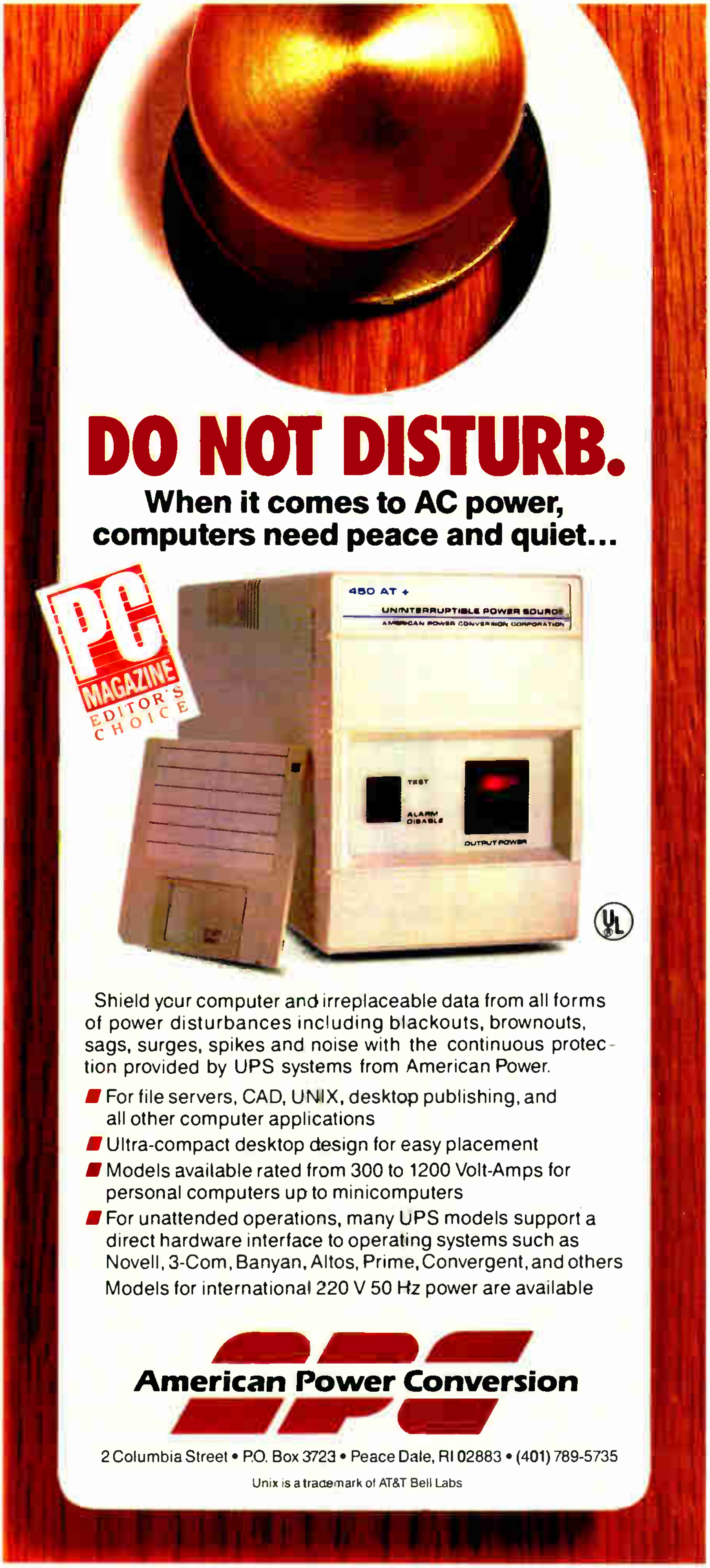

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Higgins exchange
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Conetic Systems, Inc.
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San Leandro, CA 94577
(415) 430-8875
Inquiry 982.

WordPerfect Office
per server......................$495
per workstation $150
WordPerfect Corp.
1555 North Technology Way
Orem, UT 84057
(801) 225-5000
Inquiry 981.

case of word processing packages, this is to some extent subjective. Still, I liked being able to load the shell once and let it stay there all day, and being able to add other programs to the menu. I also liked the similarity to WordPerfect's other products. There's no question, though, that the powerful mail capabilities of Higgins are important if you need to handle communications over a variety of systems.

Both packages are expensive compared to the normal run of single-user software. WordPerfect Office costs less for small systems, but it is priced in such a way that it costs a great deal more for really large systems, because a set amount is charged for each workstation. On really large LANs, this can be significant and has knocked Office out of the running in a couple of cases with which I'm familiar. As is often the case, you probably get what you pay for, but there is the question of whether you need all of what you're getting. ■

Wayne Rash Jr. is a consulting editor for BYTE and a member of the professional staff of American Management Systems, Inc. (Arlington, VA). He consults with the federal government on microcomputers and communications. You can reach him on BIX as "waynerash," or in the to.wayne conference.

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edge) and it was easier to figure out what to do next, since I could rely on the Macintosh menus across the top of the screen. With Smalltalk-80, I had to keep referring back to the manual, or, through trial and error, try to remember which combination of mouse clicks and Shift-Option-Command keys was needed to activate a particular pop-up menu.

Even window scrolling was easier to learn and use with Digitalk's Smalltalk/V Mac since its windows use standard Macintosh horizontal and vertical window scroll bars. In short, the Smalltalk/V Macintosh interface was easier to master than Smalltalk-80's generic workstation interface.

Perks for Programmers

The tutorial examples provided with Smalltalk/V Mac are better suited to an old procedural programmer like me. The examples often give you a side-by-side comparison of a Pascal routine next to the solution implemented in Smalltalk. This kind of side-by-side comparison is invaluable if you're learning object-oriented programming (OOP) and Smalltalk for the first time and already have

What compromises were made to Smalltalk/V to keep it so small?

programming experience with procedural languages like Pascal.

Here's a short sample taken from the Smalltalk/V Mac manual that illustrates the point nicely. One side of the page lists a simple Pascal program for counting the frequency of each alphabetic character in an input stream, as shown in listing 1. On the manual page opposite the Pascal listing is the equivalent Smalltalk program (see listing 2).

Both the Pascal and Smalltalk programs use predefined routines (Pascal used functions, Smalltalk used objects), called `asLowerCase` and `isLetter`. Both Smalltalk-80 and Smalltalk/V Mac can use primitives similar to `asLowerCase` and `isLetter` written in other computer languages. However, any Mac Pascal development system would be hard pressed to incorporate library code written in Smalltalk.

Smalltalk-80's manual does not provide these kinds of tutorial examples. Instead, it focuses more on a pure programming-language learning approach to OOP and Smalltalk. For me, the Smalltalk/V approach was easier to follow, but it could be less so for new programmers. They could very well favor the purer environment and teaching method adopted by Smalltalk-80.

My evaluation of Smalltalk/V Mac is too preliminary to tell if it could substitute for Smalltalk-80 in my planned Smalltalk course and in my own Smalltalk exploratory programming work. I'll need to learn a lot more about the Smalltalk/V environment and figure out the compromises made to keep it so small and inexpensive. One thing's for certain: the Smalltalk/V documentation is much better suited for the course I have in mind, composed of second-, third-, and

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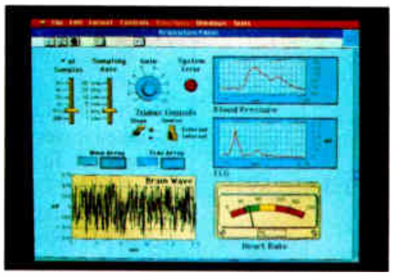

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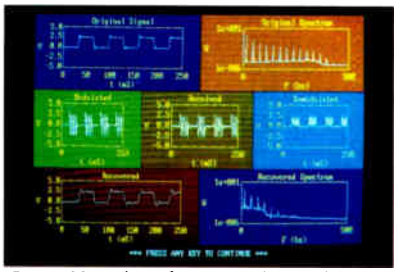

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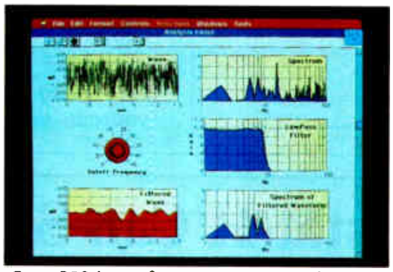

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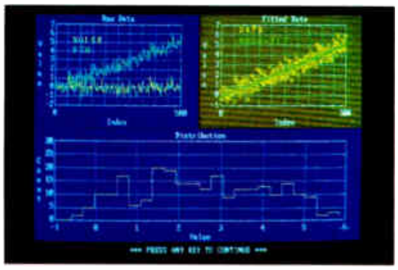

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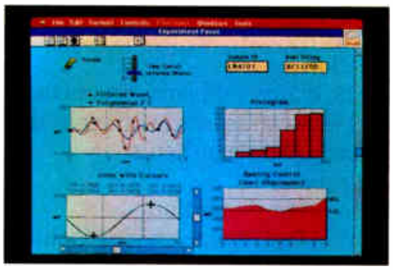

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Smalltalk-80 version 2.3 ...... $995
ParcPlace Systems
2400 Geng Rd.
Palo Alto, CA 94303
(415) 859-1000
Inquiry 1102.

Smalltalk/V Mac version 1.00 .... $199
Digitalk, Inc.
Suite 604
9841 Airport Blvd.
Los Angeles, CA 90045
(213) 645-1082
Inquiry 1101.

fourth-year undergraduates. The ParcPlace Smalltalk-80 manual would likely overwhelm them.

MultiDisk: Disk Partitioning Utility

Last month, I mentioned that I would periodically discuss some of the best utility programs that I've stumbled across. One of these handy programs is MultiDisk, a useful and inexpensive little disk partitioner from ALSoft, a company that makes several useful utilities, including Font/DA Juggler, and my favorite, MasterJuggler.

MultiDisk works like other Macintosh disk partitioning programs, allowing you to create smaller logical volumes out of large hard disks. Such partitions help you use disk space more efficiently, keep Finder operations (like file copying) speedy, and protect you from data loss due to damage to the volume directories.

But MultiDisk combines some special features that I haven't found on competing products. Among these are noncontiguous partitions (i.e., disk partitions don't need to occupy contiguous free disk space) and expandable partitions (you can make disk partitions larger without having to erase and then recreate them).

Individual features that I've found elsewhere, but usually not all together in a single partitioner, include: partition password protection and full partition encryption, desk accessory access to partitions, and TOPS and AppleShare network accessibility of partitions. MultiDisk partitions have all the usual Finder features that are associated with normal physical volumes, so they are very easy to manage.

MultiDisk does a great job at a good price. I can't wait for ALSoft's next utility. I'm sure that once I have it, I'll find that I can't live without it. That's certainly been the case with MasterJuggler and MultiDisk. ■

Don Crabb is the director of laboratories and a senior lecturer for the computer science department at the University of Chicago. He can be reached on BIX as "decrabb."

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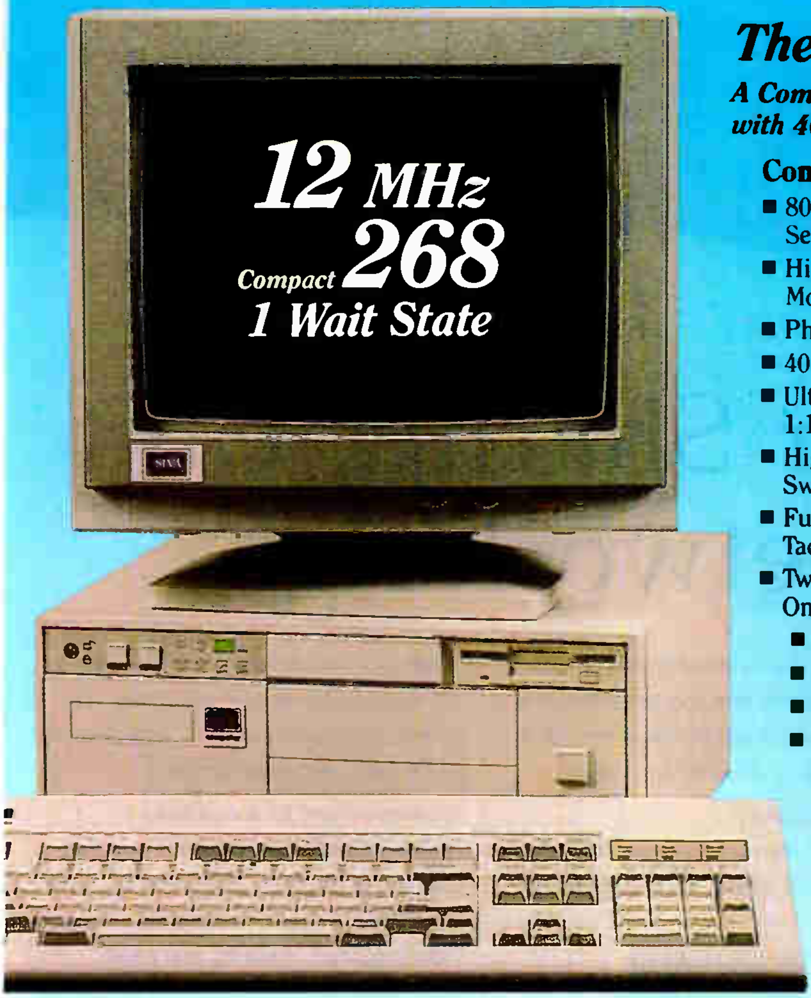

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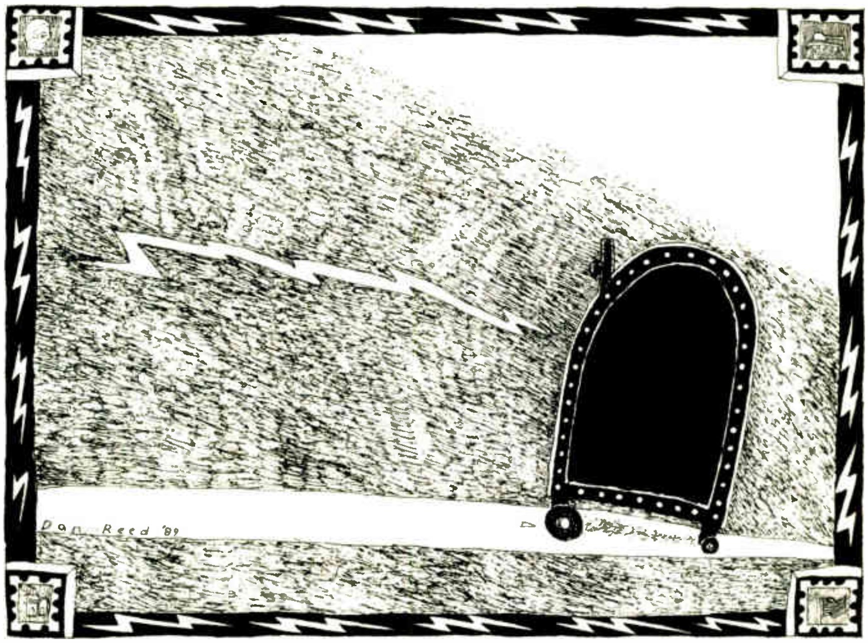
E-MAIL ECONOMICS

Subtle differences in E-mail services will affect your choice of the "perfect service"



The bulldozer and the backhoe are still more important to our economy than the personal computer. The VCR and video camera are still more fun. For all the hyperbole tossed around about the information age, such prosaic industrial-age inventions as cars, cement, and diet soft drinks currently have more effect on our daily lives.

There are 87 telephones, 65 cars, and 65 TVs in the country for every 100 people. Yet the percentage of PCs hovers between 10 percent and 15 percent, depending on which expert is available for a quote that afternoon.

But don't sneeze. The type of ubiquity that it took telephones 75 years to achieve, the car 70 years, and the TV 30 years will take the computer another 10 years (or less) to achieve. And one of the best measurements of this occurrence is the overwhelming growth of the E-mail industry.

Last year close to a billion electronic messages were transmitted, according to various market research groups. That's almost five times as many messages as were transmitted in 1984.

According to Mike Cavanaugh, executive director of the Washington, DC-based Electronic Mail Association, business-related use of E-mail accounts for 90 percent of all E-mail volume. E-mail's growth in the consumer area is increasing at a slower rate than for business uses, but Cavanaugh thinks this lag is only temporary.

"People who use E-mail at the office are going to want to use it at home, too. Like the telephone in its early years, it was first used primarily for business, but then people decided they wanted to use it to call their friends," Cavanaugh said. "The trouble was, not everyone had a phone in those early days. The same applies for the E-mail industry. Not everyone has an E-mail account or even a personal computer, for that matter."

For many businesses and consumers, E-mail use has become routine. Yet as the use of E-mail ramps up, so does the confusion. The variety of E-mail services available brings to mind the state of today's telephone industry. Picking the right E-mail service for your needs is no less confusing than choosing a long-distance telephone company.

When choosing an E-mail service, you should compare features such as rates, incentive services (e.g., database access), and customer support. One of the first things you'll want to check out is rate structures—not all services are created equal.

What follows is a look at the bottom line of four popular E-mail services: CompuServe's EasyPlex, MCI Mail, Western Union's EasyLink, and BIX (see table 1).

EasyPlex

EasyPlex is CompuServe's E-mail system. CompuServe does have an alternative deluxe-type service called Executive Mail Service that offers some advantages, such as an electronic news-clipping service, but EasyPlex is more popular. And with 350,000 customers, CompuServe can accurately boast that its E-mail service has the largest number of subscribers in the world.

Charges for using EasyPlex are based on the time you spend on-line and are billed in 1-minute increments. The charges are the same as when using any of CompuServe's offerings; there is no special charge for using EasyPlex.

EasyPlex's hourly rates are in effect 24 hours a day, 365 days a week. For

continued

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1200-/2400-bps access, you'll pay $12.50 an hour; if you're still using 300-bps, it will cost you only $6 an hour. However, CompuServe also tacks on an extra 25 cents per hour to each of the above rates for access to its system via its packet-switched network.

MCI Mail

MCI Mail is unique among these four services because its rates are not time-based; you don't pay for any of the time you spend on-line. You are charged only according to the amount of messages you send. This means you can draft letters on-line or read your E-mail on-line without being charged a penny.

MCI Mail gives its users a free-access phone line in more than 50 major metropolitan cities. If you happen to live in a city that doesn't have one of these local-access phone lines, you can access the service through an 800 number or via Tymnet. There is no charge for using the 800 number, but MCI Mail charges a fee of 25 cents per minute if you use Tymnet.

MCI Mail charges you by the "MCI ounce," which equates to 7500 characters. The first ounce costs you a flat rate of 75 cents. Every additional ounce costs $1. So a message of 15,000 characters will cost you $1.75. A message consisting of 7501 characters will cost you $1.75. If you exceed the 7500-character ounce limit by even one character, you're charged for another ounce.

Recently, MCI Mail acquired the assets of RCA Mail, an E-mail service from RCA Global Communications. Users of RCA Mail have been given the opportunity to move their E-mail box to MCI or simply drop E-mail altogether.

EasyLink

Western Union's entry into E-mail was a "do or die" kind of venture. For years, the mainstay of this company's revenue was telex transmission. Beginning in 1983, Western Union funded EasyLink with $115 million of start-up funding, including $45 million for advertising.

EasyLink charges $21 per hour for 300-bps service (not so strange when you consider that Western Union telex users are used to 110-bps transfer rates). For 1200-bps access the cost is $30 per hour. These rates apply during prime time (from 7 a.m. to 6 p.m.). The rates drop some 40 percent from midnight to 7 a.m. Like MCI Mail, EasyLink is accessed via a local phone number. If your city doesn't have an EasyLink phone number, you can use a nationwide 800 number. However, if you use this 800 number, tack an additional 30 cents per minute onto your bill.

Table 1: *Charges for the various services discussed. Note that the EasyLink $25 charge is a monthly minimum usage charge. If you don't use $25 worth of the service, you're still charged $25, but there is no subscription fee per se. N/A denotes not applicable.*

Service	Sign-up fee	Hr. rate (1200 bps, prime time)	# Subscribers
BIX	$39 (one-time)	$12	27,000
EasyLink	$25 per month	$30	200,000
EasyPlex	$39.95 (one-time)	$12.50	350,000
MCI Mail	$25 per year	N/A	100,000

BIX

The Byte Information Exchange (BIX) is an electronic extension of BYTE. BIX is made up of hundreds of conferences with specialized information on computer-related or general topics in each one. A few special services have been added for variety, including two daily news-wire reports, Microbytes Daily, the McGraw-Hill Executive News Service, and a real-time chat mode called Cbix. There is an E-mail capability, too, commonly referred to by its users as "BIXmail."

BIX is accessible via Tymnet. The nonpeak (7 p.m. to 6 a.m.) hourly rate is $11. Of that $11, the actual BIX charge is $9; the Tymnet charge is $2. The peak (6 a.m. to 7 p.m.) hourly rate is $20. That breaks out to $12 per hour for BIX and $8 per hour for Tymnet charges. The rates listed in table 1 are for 1200-bps access. A special 2400-bps access rate tacks on an extra $2.50 per hour during peak times and $1.50 per hour during nonpeak times.

(For the sake of clarity, I should note that I have a professional affiliation with BIX both as a group moderator and as a contributor.)

E-Mail Benchmarks

To ferret out the bottom line for each of these systems, I've used three typical E-mail examples: a one-page memo of about 2500 characters (see table 2), a four-page letter of about 10,000 characters (see table 3), and a nine-page report of about 22,500 characters (see table 4).

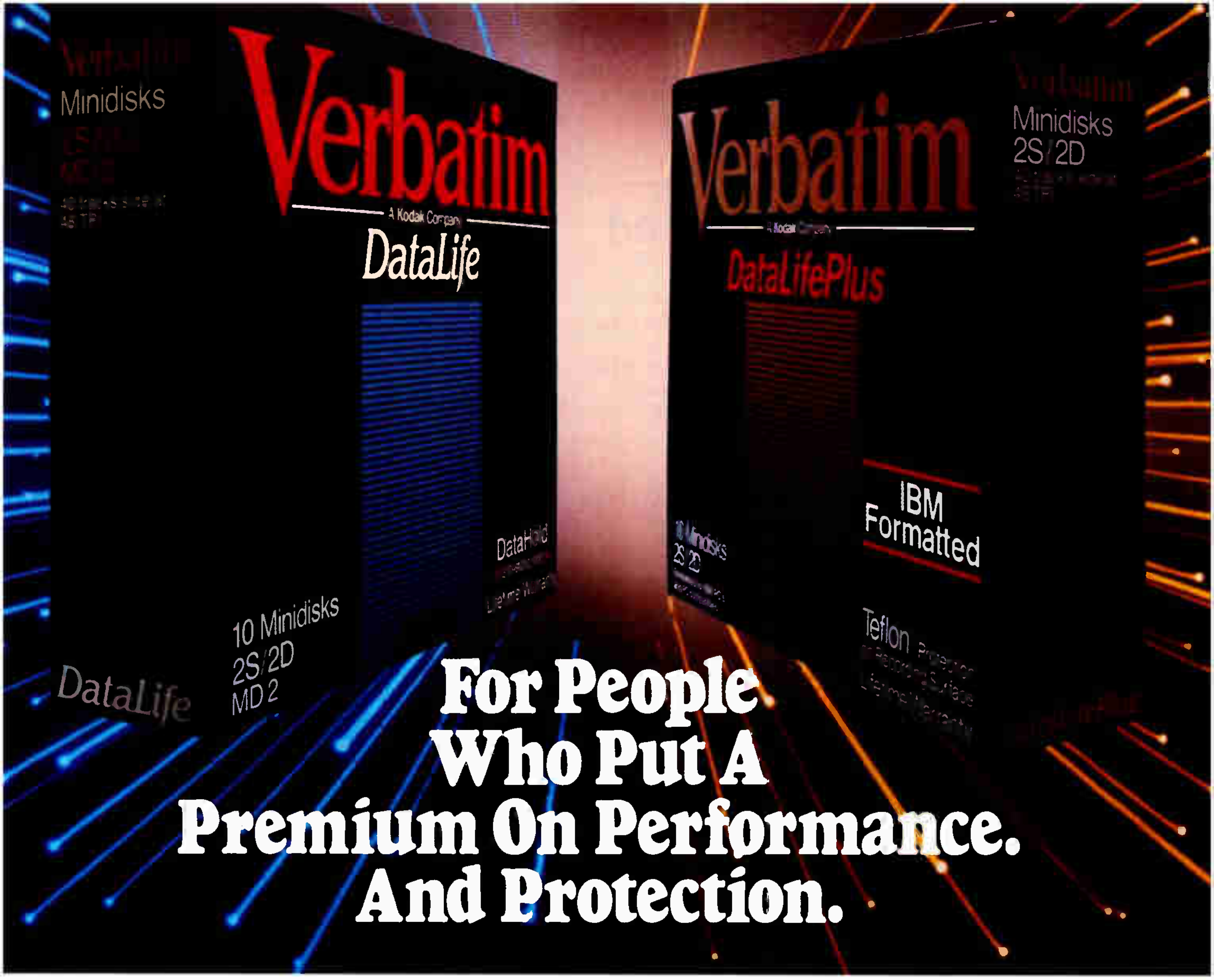
I prepared each of these examples off-line and uploaded them manually at 1200 bps during normal business hours using a straight ASCII transfer implementing XON/XOFF flow control.

At first blush, the choice for an E-mail service is easy—look at the chart, and go with the lowest overall charges, right? In all four cases, this happens to be CompuServe's EasyPlex. But such reasoning is

continued

Table 2: *A comparison of time and charges for sending messages of various lengths over the services discussed. The documents were prepared off-line and uploaded during normal business hours, at 1200 bps, using a straight ASCII transfer implementing XON/XOFF flow control. Note that MCI charges are based on document length and not on-line time. Times are in minutes:seconds.*

	1-page memo		4-page letter		9-page report	
Service	Time	Cost	Time	Cost	Time	Cost
BIX	0:29	$0.17	1:39	$0.55	3:32	$1.19
EasyLink	0:24	$0.20	1:38	$0.81	3:27	$1.72
EasyPlex	0:41	$0.14	1:55	$0.40	3:35	$0.75
MCI Mail	—	$0.75	—	$1.75	—	$2.75



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Campbell, CA 95008
(408) 559-7434
Inquiry 1072.

EasyLink
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4230 Alpha Rd., Suite 100
Dallas, TX 75244
(800) 527-5184
Inquiry 1073.

EasyPlex
CompuServe
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P.O. Box 20212
Columbus, OH 43220
(800) 848-8199
Inquiry 1074.

MCI Mail
1150 17th St.
Washington, DC 20036
(800) 444-6245
Inquiry 1075.

seductive; the choice isn't that clear-cut.

There are other factors you may want to consider. For example, there's a factor I call "on-line overhead." This is the extra time it takes to actually set up the system to input your letter, report, or memo. Using CompuServe, you have to navigate your way to the E-mail section. You can do this in two ways: by using menus or using a kind of on-line shorthand, called "go" commands.

Using the menu system on CompuServe, you might take a couple of minutes to actually reach the E-mail section; using the go commands, it takes about 30 seconds. But because CompuServe bills in 1-minute increments, you'll pay for a full minute when you use 30 seconds of on-line time. That extra minute tacks on an extra 21 cents to the actual cost of the mailing.

With the exception of MCI Mail, using each of the services mentioned here, you'll rack up some of this on-line overhead. Of course, the more familiar you are with the system, the less time you will need to set it up to input your mail. But regardless of how well you can manipulate your E-mail service, the clock is always running. And here's where MCI Mail shines.

Using MCI Mail, you can stay on-line for as long as you wish, with no on-line overhead. This is significant when you take into account that all E-mail isn't prepared off-line, as my examples were. A simple one-page memo might take you 5 minutes to write on-line.

In addition to on-line overhead, you have to consider whether or not you want a full-service E-mail system. All these services offer extras, such as telex transmission, fax transmission, and access to special on-line information services. Though BIX does not offer telex or fax services, its on-line information service is very robust. Some of these services are priced above and beyond the on-line charges.

Then there's the question of interconnectivity, or the ability to send a message from one system to a subscriber on another system. Of the services mentioned, only MCI Mail and EasyPlex have this feature. With the other systems, you're limited to sending E-mail only to other subscribers of that service.

When choosing an E-mail service, there is an inherent advantage to subscribing to a system that has a large number of users. The rationale is simple; the more people using the system, the more likely it is that you'll be able to contact whomever you need to reach.

Using MCI Mail, you have no on-line overhead to worry about.

Reaching Nonusers

In this day of global communications—and until the E-mail industry becomes truly interconnected—it seems we're stuck with having to subscribe to several E-mail services in order to receive the best coverage possible. One exception is a service called DASnet. The folks there promise that they can "reach anyone, anywhere, with an electronic address."

DASnet operates as a kind of worldwide electronic post office for the information age. While DASnet is sometimes unreliable and has a confusing rate structure, it is the best solution to date for reaching someone via E-mail who is not a subscriber to your particular service.

While you can use statistics to help you decide which system is right for you, your best bet is to use these comparisons as a guide and then write to the E-mail companies that interest you. Ask them to send you an information package. Compare features and talk to friends who have used various E-mail services. Ultimately, you will base your decision on a combination of all these factors. ■

Brock N. Meeks is a San Francisco–based freelance writer who specializes in high technology. You can reach him on BIX as "brock."

Your questions and comments are welcome. Write to: Editor, BYTE, One Phoenix Mill Lane, Peterborough, NH 03458.

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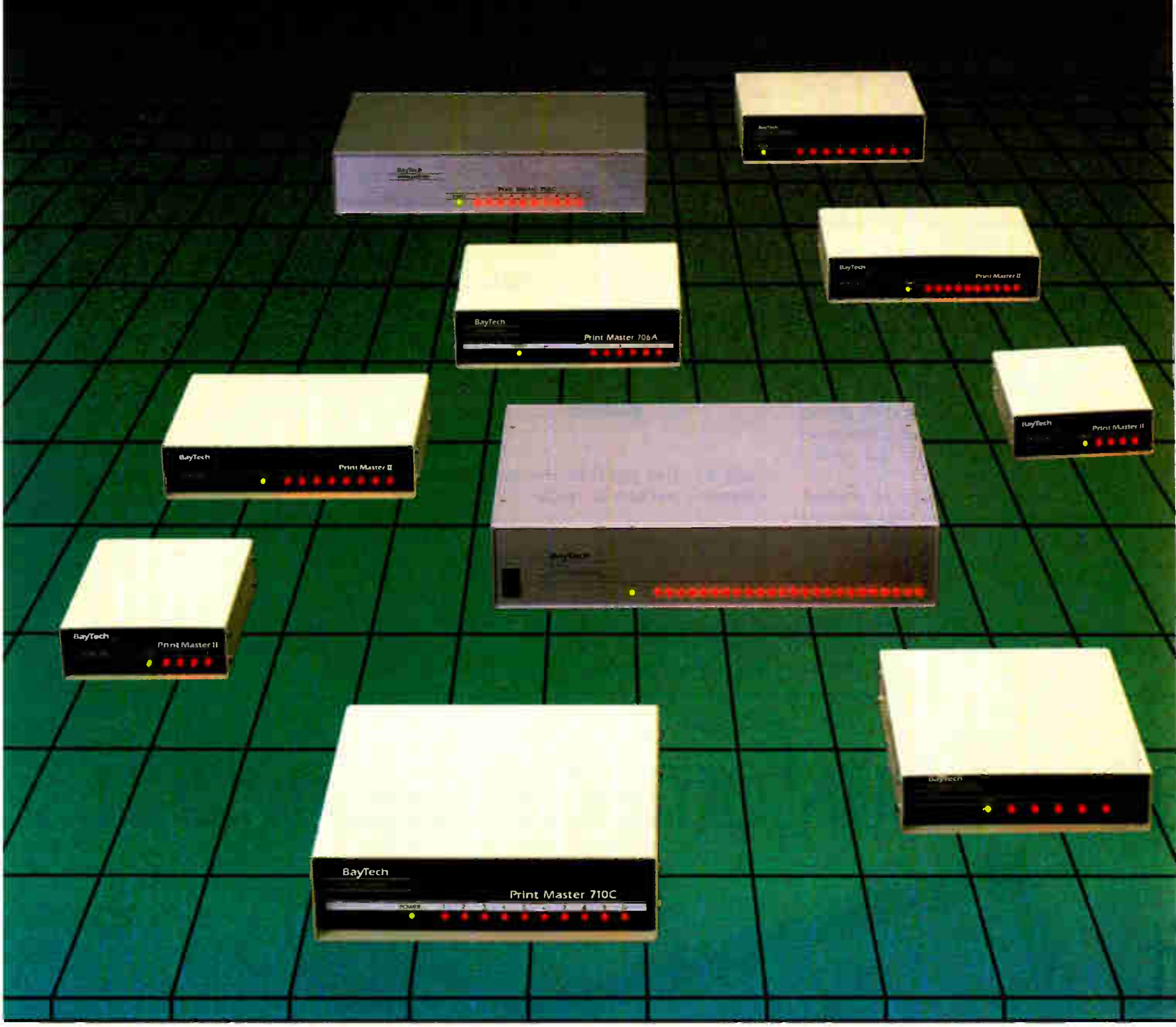

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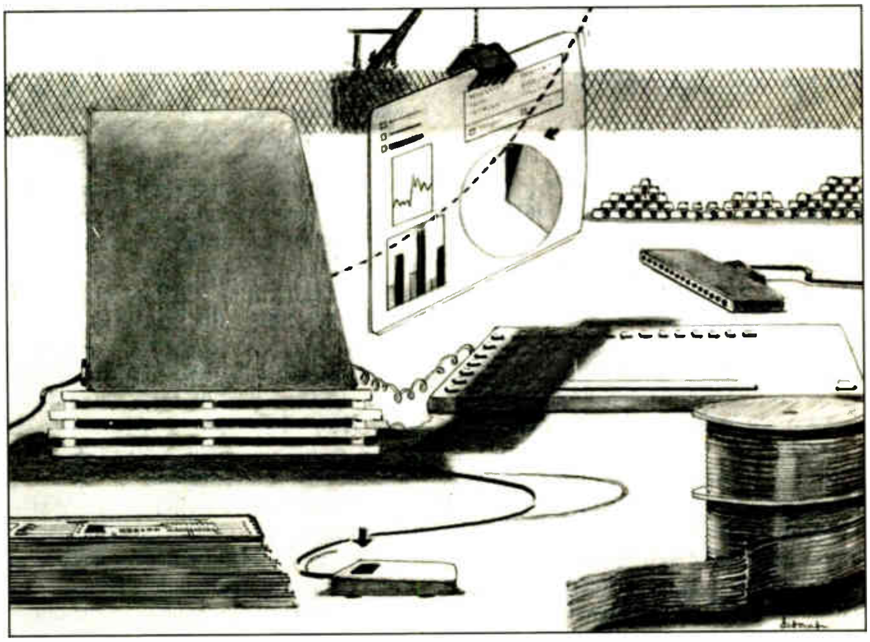
Circle 36 on Reader Service Card

OS/2 FOR CHEAP

Part 2 of a series showing how to put together an inexpensive OS/2 workstation



Before being interrupted by my trip to COMDEX, I was talking about building an inexpensive workstation that supports Presentation Manager (PM). So far, it consists of the following:

- a 10-MHz IBM PC AT clone with 512K bytes of RAM expandable to 1 megabyte (with a DTK motherboard and a Phoenix BIOS), a power supply, and a case
- a 3-megabyte extended-memory card (Everex RAM 3000)
- 3.5 megabytes of 256K-bit DRAMs to fill the card and the motherboard
- a Western Digital WD1003 AT-type hard/floppy disk drive controller
- a Seagate ST4096 80-megabyte hard disk drive

The total cost so far is $2990. I've gotten these things either through a mail-order house or from my local clone boutique.

Serial Ports without 8250s

Next, I'll add printer and serial ports. It makes sense to buy one of those $80 boards with one serial port and two parallel ports, which you'll find in mail-order ads. But you may want a board with two serial ports because you need a mouse *and* a modem, and this way you can support the mouse, modem, and parallel printer with just one slot. A separate mouse board wouldn't cost much, but it *would* gobble up a precious slot.

That's easy enough. But there's one important detail to buying an OS/2-ready serial port. Serial ports and internal modems are built mainly around a single chip called a universal asynchronous receiver/transmitter. The ones seen in most serial ports are an older design called the 8250. It's perfectly good, and it can be run at speeds of up to 115,200 bps, as LapLink has ably demonstrated. A new-and-improved chip, the 16450, is now available. It, too, is a UART.

Current DOS machines may have either an 8250 or a 16450—you can't tell the difference under DOS. As internal modems are also serial devices, they too have a UART. The Hayes internal modems I've seen, for instance, use the 8250. The IBM PS/2 Micro Channel architecture–compatible internal modem uses the 16450. Again, under DOS there's effectively no difference.

Under OS/2, it's another story. OS/2 will talk only to the 16450. There are many serial/parallel add-in cards, but beware: If they have an 8250, they won't even be recognized by OS/2.

If you already have a serial port, you can easily find out whether or not you have a 16450. First, remove the circuit board that provides the serial port function or the internal modem board. It's not hard. After that, remove the cover of your computer. With the power off, remove the screw that holds the serial board in place. Rock it back and forth gently, and the board will come out.

You won't be able to miss the identifying number—this is a large chip. The number may be surrounded by other characters (e.g., S8250N-B), but you'll easily see it. If you're worried about doing this, find someone who's been under the hood of a computer before.

If you're buying a PM-ready workstation, be sure to ask the vendor whether or not the serial ports use an 8250 or a 16450. If you can't get an answer, don't buy from that vendor.

Suppose you already have an 8250-

continued

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based serial port—must it go in the trash? BYTE's hardware expert, Brett Glass, tells me that he has been able to make OS/2 happy by simply replacing the 8250 with a 16450. Many parallel/serial boards mount the 8250 with a socket, so you can replace the 8250 with a 16450 without any soldering. You can probably find a place that will sell you a 16450 in the back of this issue. Jameco Electronics is a longtime BYTE advertiser that handles chips. On the other hand, if your UART is *not* socketed and you're not comfortable with a soldering iron, perhaps it's best to buy a new board.

There are boards around that offer two serial ports and a parallel port for $80. The total now reaches $3070.

EGA or VGA?

The PM doesn't support CGA—well, it does, but not credibly—or Hercules graphics, so the workstation will have either EGA or VGA. There's a number of reasons to go with VGA rather than EGA, and perhaps I'll tackle them in a future column. For now, it's enough to say that the cost difference between EGA and VGA—$150 for EGA versus $400 for VGA—is enough to go with EGA.

What about the so-called EEGA, the "extended" EGA card with the snazzy 800- by 560-pixel resolution mode? There's no real point in buying one of these, as the nonstandard modes aren't supported by the PM anyway. But this may change. Just as Paradise, Genoa, Video Seven, and the rest have written special Windows drivers to show off their cards, perhaps we'll soon see similar drivers for the PM.

My associate, Rob Oreglia, has railed for years now against color monitors. "They're of no use to you," he argues. "Say you get a pretty color screen—how do you get a hard copy?" I can't argue with him.

The bigger problem is that color monitors are expensive. A monochrome monitor is cheap—$70 tops via mail order, $95 for the "paper-white" screens. EGA monitors all seem to start at $350 and go up from there. Additionally, monochrome monitors produce nice, sharp text. So, to really make this cheap, we'll shoot for an EGA card that can support a basic monochrome TTL monitor in EGA resolution by displaying different shades. Simple, you may say—any auto-switching display card will handle that.

That's just the problem, you see. Autoswitching *kills* OS/2.

I've tried a number of video cards with OS/2—ATI's EGA Wonder, Quadram's QuadEGA+, and Paradise's EGA cards

The bigger problem is that color monitors are expensive —$350 and up.

—and every one of them causes OS/2 to lock up when trying to boot if the card has auto-switching enabled. What's needed is a video card that boots up in EGA mode and doesn't squawk about emulating a full 256K-byte EGA on a monochrome monitor.

I've tried out a pile of cards, and the one that seems to be the most trouble-free is the Paradise AutoSwitch Mono EGA Card. Be very careful when buying this, however, because dealers seem to be unaware of its existence. They want to sell you a Paradise Basic EGA Card, or an AutoSwitch EGA 480, or a VGA Professional Card.... Make sure you're getting the right card by specifying Model 02-17. It lists for $279, but I found it at a local dealer for $199. A Samsung TTL amber monitor was $80 with a tilt stand. Now the cost is up to $3349.

The AutoSwitch Mono EGA Card also has other virtues. When running DOS, you can use its MEGA.EXE software to direct the board to emulate a Hercules or CGA video card, so you have most of the bases covered. I don't know of anyone at the moment that offers a similar card for VGA, but someone probably will in time.

Rodent

Some of us think mice should be in laboratories testing vaccines, but the PM needs a mouse. What's a good one? I use the Microsoft Serial Mouse. Yes, you *can* run PM without a mouse, but you have to have a head for the trivial, shall we say? You want to make drive A the default drive? Just press Control-A. Want to move from one window to another? Alt-Tab.

My advice against using the keyboard to control Windows or PM is not based on a small amount of experience; at my desk, I use a mouse. When doing PM classes, however, I often find myself having to use the keyboard, as computer rental companies are often unequal to the task of supplying a working mouse.

OS/2 claims to support several mice, and I guess it doesn't matter which one you use. I like a serial mouse for reasons I cited earlier—you save a slot. The Microsoft Serial Mouse is about $95.

Floppy Disks Required

OS/2, being large, can't be booted from a 360K- or 720K-byte floppy disk. Since it arrives in the 1.2- or 1.44-megabyte flavor, you need a 1.2- or 1.44-megabyte drive A. OS/2 is so large that you don't really boot it upon installation—you just boot a minimum program that is smart enough to load the five disks. Then it tells you to reboot, and at that point you're in. That's part of the reason why the FORMAT /S option does not work under OS/2 1.1.

The 1.44-megabyte floppy disk seems to be the disk of choice for IBM—it was easy to get the PM on 1.44-megabyte disks weeks before it was available on 1.2-megabyte disks—so be sure that whatever machine you buy will support a 1.44-megabyte disk down the line.

Now the tally is up to $3444, and that's final. Of course, you need to buy software, but that's for another day. Summarizing, the workstation ended up including the following:

- a 10-MHz IBM PC AT clone with 512K bytes of RAM expandable to 1 megabyte (with a DTK motherboard and a Phoenix BIOS), a power supply, and a case
- a 3-megabyte extended-memory card (Everex RAM 3000)
- 3.5 megabytes of 256K-bit DRAMs to fill the card and the motherboard
- a Western Digital WD1003 AT-type hard/floppy disk drive controller
- a Seagate ST4096 80-megabyte hard disk drive
- a Paradise AutoSwitch Mono EGA Card
- a Samsung amber TTL monitor
- an I/O card with two serial ports and one parallel port (serial ports with the 16450)
- a Microsoft Serial Mouse

Once the workstation is set up, the *real* fun begins when you start arranging files. When OS/2 installs itself on your disk, it installs itself *all over* your disk. I'll talk about the files that OS/2 drops on your machine next time. ■

Mark Minasi is a managing partner at Moulton, Minasi & Company, a Columbia, Maryland, firm specializing in technical seminars. He can be reached on BIX as "mjminasi."

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Curing the Brownout Blues

Ka-thunk. The lights dim. Your heart stops. For a moment, you are frozen. Time seems to stand still. Then you hear the familiar sound of your computer booting. To you, it sounds remarkably like a toilet flushing your precious data down the drain. If only you would learn to save your work more often. If only . . .

All of us who use computers live on the edge. Yes, you have heard all the adages about saving your work and backing up everything, but do you really do it as often as you should? At some point, you enter the danger zone, where one good lull in the power line could cost you greatly in terms of lost data. Not even an uninterruptible power system (UPS) can promise absolute protection, but it can offer some peace of mind to those who fear the dreaded power brownout.

Consider the following disquieting information. In 1972, 1975, and 1983, IBM published studies on utility power across the U.S. (the 1983 survey also included Japan and Europe), recording incidents—out-of-specification fluctuations above and below line voltage. Though frequency of fluctuations depends heavily on your location, on average, the frequency of such incidents rose from 12 percent to 27.3 percent, and the number of outages increased from 5 percent to 15 percent.

In its *Gold Book*, the IEEE recommends that computer equipment be designed to operate within a steady-state window 6 percent above and 13 percent below normal line voltage. Most computer equipment available today complies. But the largest group of incidents recorded by the IBM study covered sags of 20 percent; the second largest group (and growing) included sags of 30 percent (it tied with total blackouts). More than half of all incidents lasted less than 6 seconds. If your work is intimately tied to your microcomputer, you would be wise to purchase a UPS. (For a primer on UPS technology, see the text box "What Is a UPS?" on page 168.)

The BYTE Lab looked at 12 UPSes (see table 1). Each unit is rated at under 1500 volt-amperes (VA)—designed primarily to support microcomputers or networks. We were looking for units to provide reliable backup power; we did not test for overvoltage protection. While surge and spike suppression are important to line quality and are often offered as enhancements, they don't fall within the traditional domain of the UPS.

UPS Testing

The words behind the UPS acronym suggest that such a product provides power that is, within reasonable limits, uninterruptible. Testing a UPS means determining just how invulnerable to interruption it really is—feeding it less-than-ideal power and looking at how much of the disturbance the unit passes on to your computer.

To provide power disturbances, we used a Variac rated for 120-/240-volt inputs, a 9-A load, and output voltages ranging from 0 V to 280 V. A Variac is a variable autotransformer, a device that let us convert fixed line voltages (from the wall socket) to any 60-Hz voltage within the given output range. The Variac let us do simple but realistic and repeatable simulations of low line voltages.

We monitored UPS inputs and outputs using two line monitors to measure root-mean-square voltage and an oscilloscope to capture actual voltage waveforms. The first line monitor is a BMI GS-3, a low-load instrument that gives a constant reading of RMS voltage when connected to a power outlet. The unit gives reliable readings for sine waves only, so we used it to test only inputs.

To monitor UPS outputs, we used a BMI 2400 Power Scope, a similar but more sophisticated device that gives true RMS readings, even for nonsinusoidal waves. For waveform acquisition, peak voltage measurements, and timings, we used a Hewlett-Packard Model 16530 digitizing oscilloscope.

A line-switching device developed by Emerson rounded out our test equipment. The Emerson box uses a solid-state switch to toggle its output between direct connection to line voltages and connection to line voltages through the Variac. The output is normally connected directly to the power line. The Variac input acts as a variable voltage source or, when set to other than line voltage, as a disturbance input.

When the switching device is activated, the solid-state switch connects the output to the Variac (the disturbance input) for a short time, then switches back to normal line voltage, without disturbing the phase. The test instrument also generates a trigger signal so that we could monitor each disturbance with the oscilloscope. Timing controls let us choose both the duration (from 0.2 millisecond to 683 ms) and the start time (as a position on the sine wave) of the disturbance; adjusting the Variac let us select its magnitude. With the Emerson unit, we were able to replicate most common line faults in both size and duration.

Table 2 shows our results. It includes a single figure for cutoff voltage and restart voltage, and three figures for output voltage. The cutoff voltage represents the input level at which the UPS kicks in—where it generates an input fault alarm and, if necessary, switches to backup power. We measured the cutoff voltage by connecting the UPS being tested directly to the Variac and lowering the voltage until the test unit responded. After recording this cutoff voltage reading, we turned up the Variac until the test UPS switched back on; the turn-on level is the restart voltage.

As a caveat, these two figures can be affected by the impedance of the Variac and the nonlinear current draw of the units being tested. Each UPS was loaded with an IBM PS/2 Model 80 and a monitor (168 VA) connected to its output.

The three output voltage numbers rep-

Twelve UPSes that can help you sleep better at night

Steve Apiki, Stanford Diehl, and Rick Grehan

resent the normal, backup, and minimum steady-state voltages present at the UPS output. To perform the normal output test, we plugged the UPS into the line socket, loaded it with the Model 80, and determined RMS output. We also used the oscilloscope to get a picture of the normal output waveform.

To find the backup voltage, we disconnected the UPS from the line and again measured the output. Minimum output voltage is the RMS reading for the unit just before switching to backup, if the unit is a standby power system (SPS), or the smallest voltage reached while the input is reduced to zero. To read this, we wired the test UPS to the Variac and turned down the input voltage, recording the minimum output value.

We used the oscilloscope to measure the effect of four undervoltage line disturbances that were generated by the Emerson box. The voltage levels were set at minus 30 percent from nominal line; we used ⅛-, ¼-, ½-, and one-cycle durations. Each disturbance began at a positive slope zero crossing. We made qualitative observations and measured peak voltages from the scope trace. Again, we loaded the test units with a PS/2 Model 80.

To determine transfer times and their sensitivity to different loads, we sent one-cycle near-blackout voltages (minus 75 percent) to each of the six SPSes. Using the storage oscilloscope, we captured the resulting waveforms and measured the transfer times (see photo 1). We performed the test twice, once with a small load (the Model 80) on the UPS and once with a moderate load (420 VA).

We supplemented the disturbance tests with a measurement of holdup time (how long a UPS/SPS can provide usable power when disconnected from the wall). Because the holdup time's load response can be nonlinear, we used loads of three sizes. The small and moderate loads corresponded to those used in the transfer-time test.

We also used a large (672 VA) load to put a realistic

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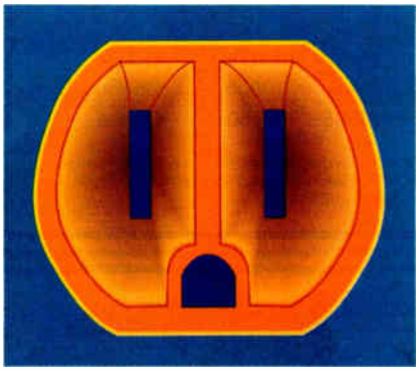

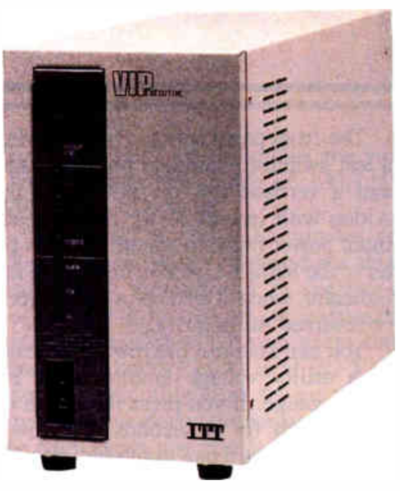

Three of the best: *ITT PowerSystems' VIP 800 (left), Sola Electric's Mini UPS/2 (middle), and the Emerson PC/ET (right) represent products designed for medium-, large-, and small-load requirements.*

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DTK means value in PC-compatible motherboards, add-on and networking cards, and bare bone systems, including FCC Class B-certified 10 and 12 MHz '286 computers. Which is why the two high-speed XT clones named "Best Buys" in *PC World*'s August, 1988 issue, both use DTK motherboards.

The PEM-2000 is a good example—but by no means the only example—of the DTK difference. To get the full story on all our high-performance XT, '286 and '386-based products, contact the DTK office nearest you for specifications and pricing.

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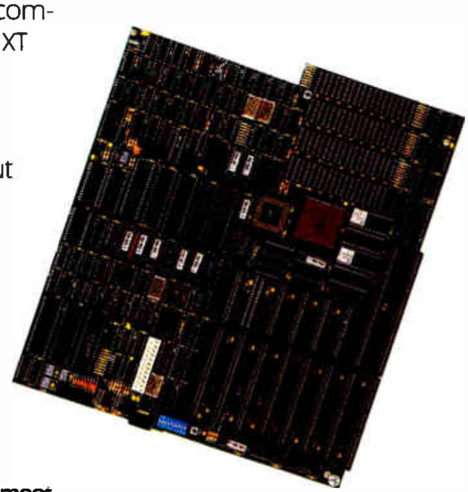

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High-Tech Computing, Cafeteria Style

The Wells American CompuStar 286 packs performance into a design-it-yourself PC

Mark L. Van Name

When it comes to PC engineering, "new" usually means bigger or faster: faster processors, bigger and faster memory caches, bigger and faster hard disk drives. That's why it's nice to see a computer like the Wells American CompuStar 286—a machine that tries something new.

What's new about the CompuStar is that it's the first computer designed to be whatever kind of PC you want. Want an AT clone? The CompuStar can be an AT clone. How about a Micro Channel system—a PS/2 clone? It can be that, too. You can even have both types of PCs in the same box.

You pick your processor, too. You can choose an 8086, a fast 20-MHz 80286, or three different models (16-, 20-, and 25-MHz) of the 80386.

The Secret

The secret to this flexibility is a unique design. The CompuStar base system includes a keyboard and an almost-empty shell: a 24- by 7½- by 26-inch, floor-standing, aluminum-skinned case housing just a 220-watt power supply and what Wells American calls its I/O module. The I/O module supplies two serial ports, one parallel port, PS/2-style 6-pin DIN keyboard and mouse connectors, and both a DB-9 digital monitor connector and a DB-15 VGA analog connector. Circuitry on the I/O module handles VGA (courtesy of a Paradise PVGA1A chip), EGA, CGA, MDA, and Hercules graphics. This board also acts as a disk drive controller that can handle up to four floppy disk drives.

After you order the $1195 base system, you then choose from a list of options. You start with a bus module. You can pick an AT- or PS/2 Micro Channel–compatible bus module; if you choose the PS/2 bus module, you also need a special PS/2 adapter. The AT bus module has seven AT-compatible expansion slots, while the PS/2 module contains five Micro Channel–compatible slots and one AT-compatible slot. Since the only card you need to add to most CompuStar basic systems is a hard disk drive controller, you end up with a lot of free slots.

And there's more. You can have not one, but two bus modules—a primary and a secondary. You can mix and match these any way you want: two AT bus modules, two PS/2 bus modules, or one of each. So, in a single CompuStar chassis, you can have up to 13 AT slots, or 10 PS/2 slots and one AT slot, or a mixed bag of seven AT slots and five PS/2 slots. Talk about expansion space!

After you pick a bus module, you then need to choose a processor, or, in Wells American's terms, a CPU module, which contains a CPU, a socket for a math coprocessor, memory sockets, the ROM BIOS, sockets for two expansion ROM chips, and a battery-backed clock/calendar. Wells American is shipping the 80286 and both 16- and 20-MHz 80386 CPU modules. A company spokesperson said that the 10-MHz 8086 CPU module was scheduled to begin shipping in February.

continued

Wells American also offers a nifty CPU upgrade option. You can trade in your initial CPU module for another and get a purchase credit toward the cost of the new one. In fact, if you trade in your CPU module within a year after purchase, the company gives you its full purchase price as a credit.

The combination of the bus and CPU modules still doesn't give you a complete system. While the 8086 CPU module comes with 512K bytes of memory, the 80286 and 80386 CPU modules do not include any memory. You purchase separately either 512K-byte or 1-megabyte memory modules from Wells American. These memory modules are 80-nanosecond DRAM zig-zag in-line packages (ZIPs) that plug into the eight ZIP sockets on the CPU modules. You can add a 1-megabyte memory-expansion kit to the 1-megabyte memory modules, so, with eight such expanded modules, you can rev your CompuStar up to its maximum 16 megabytes of memory.

Finally, you must add the other necessities: one or more floppy disk drives, one or more hard disk drives, a monitor, and DOS.

This process sounds like a lot of work, but fortunately Wells American sends the system to you fully assembled, with the hard disk drive formatted and ready to go.

A Cautionary Note

If the CompuStar's flexibility, and especially its dual-bus option, seem too good to be true, you're not alone. I felt the same way. I'm still not sure it will all materialize, because as we go to press, Wells American is not yet shipping any of the PS/2 modules, and the company did not get an 80386 CPU module to BYTE in time for this review because it was only recently completed. A spokesperson said that the PS/2 modules were ready, but that Wells would not ship them until it had secured some patents it was seeking. The company projects that it will ship the PS/2 modules in the first quarter of this year.

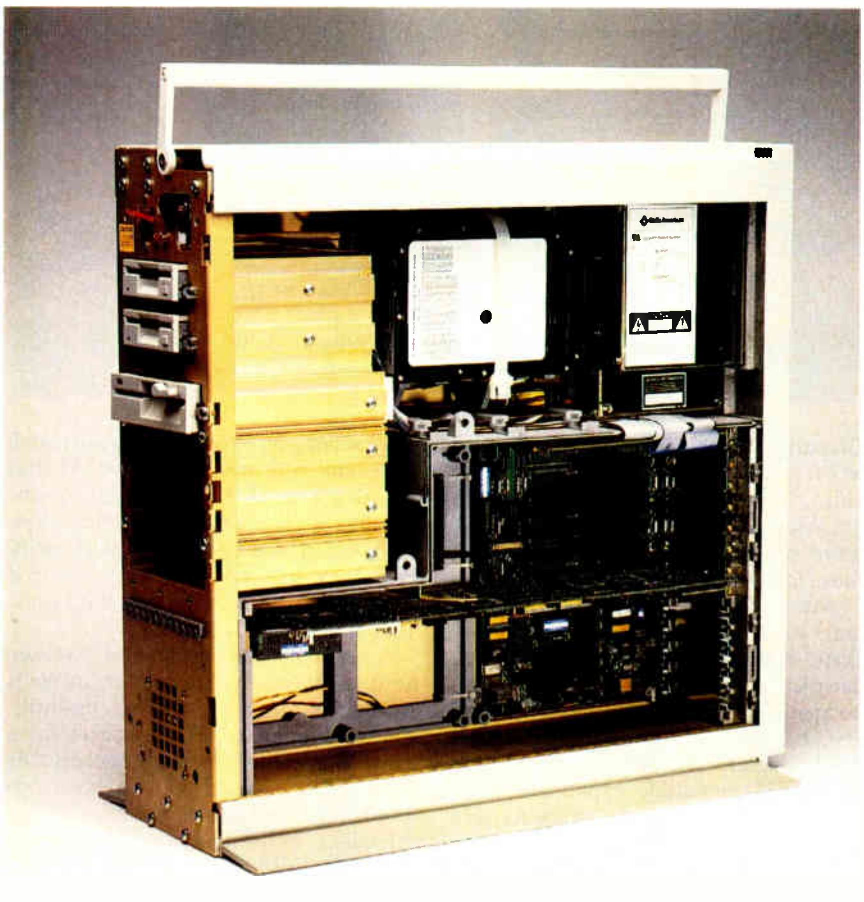

Photo 1: *The inside of the CompuStar reveals the nature of the beast: plenty of expansion room and easy access to components.*

Despite the unavailability of some modules, this machine shows some of the nicest engineering I have seen in a long time. Although Wells American isn't a household name, the company has been around for some time. In the late 1970s and early 1980s, it built microcomputers under the name of Intertec Data Systems, which you may remember for its SuperBrain CP/M microcomputers and its later multiuser systems.

The Evaluation System

My evaluation system came with one AT-compatible bus module, the 20-MHz 80286 CPU module, a 10-MHz 80287 math coprocessor, 1 megabyte of 80-ns DRAM in two 512K-byte ZIPs, two 1.44-megabyte 3½-inch floppy disk drives, one 1.2-megabyte 5¼-inch floppy disk drive, a 150-megabyte hard disk drive, a flat-tension-mask VGA color monitor, and IBM's PC-DOS 3.3. Six of the AT slots were empty, with the hard disk drive controller in the seventh slot (see photo 1).

That's a powerful system, and it carries a hefty price tag: $6570. But you get a lot of performance for the money. In fact, this CompuStar 286 proved to be the fastest 80286-based system that BYTE has tested. Its overall application index was about 9 percent faster than that of the previous 80286 speed champ, the Dell System 220. The CompuStar beat the Dell System 220 on all but the word processing and compiler tests, which it lost by only 3 percent and 2 percent, respectively.

Both systems maximize their performance with interleaved memory banks, so that one bank of memory recharges while the other is ready to go. As you might expect from such a well-engineered machine, the CompuStar offers a nice improvement on traditional two-bank interleaving: If you have four identical memory modules, it can do four-way interleaving, so that three banks are ready while one is recharging. On its 80386-based CPU modules, Wells American combines this interleaving with an Intel 82385 cache controller and 32K bytes of 35-ns static RAM cache to boost performance further.

Wells also borrows a page from most 80386-based systems for the CompuStar 286 by using shadow RAM, a technique that copies the ROM BIOS into RAM at boot time for faster ROM access.

The flip side of performance is always price, and the CompuStar's speed victory over the Dell System 220 would mean a lot less if the CompuStar cost a

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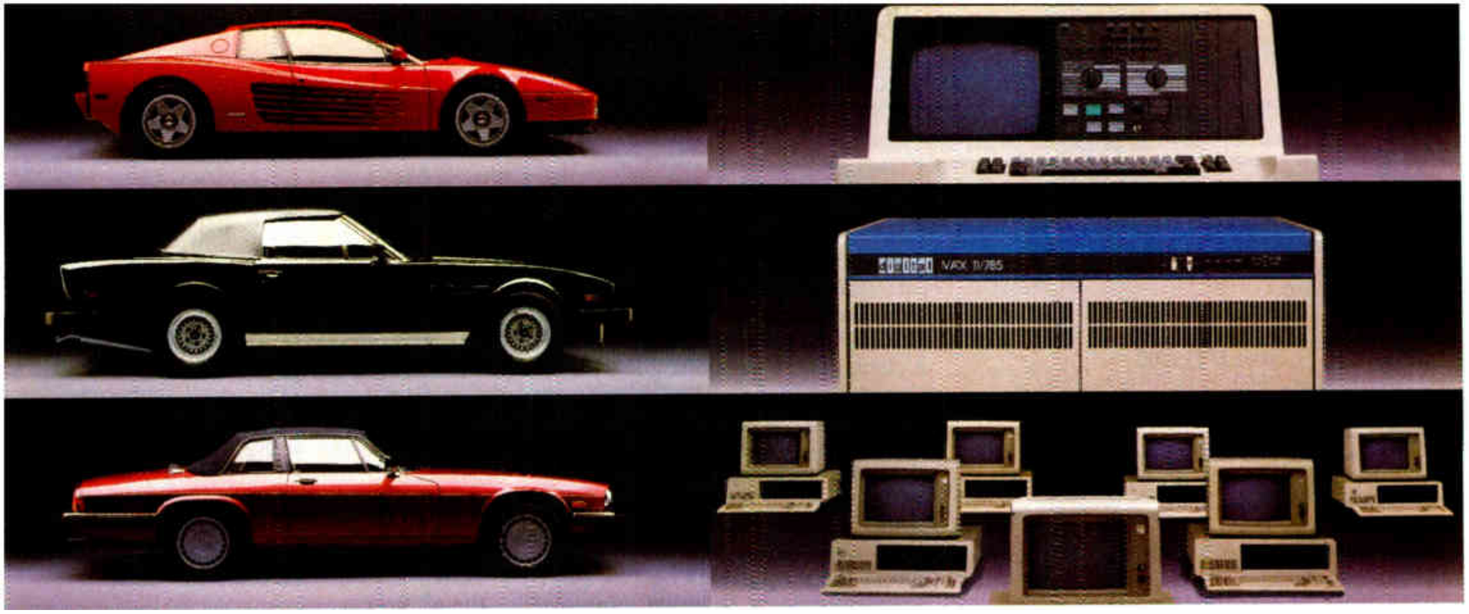

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Unlike bandsaws, washing machines and tv's, computer circuits are hypersensitive to the slightest power variations. Data can be scrambled or vaporized in a few milliseconds. Programs can crash unceremoniously.

Fact is, many problems blamed on hardware or software are, in reality, the fault of raw electricity. Industry statistics show that half the downtime, lost employee and machine productivity, and maintenance costs are the direct result of bad electricity.

A typical computer site experiences about 7 blackouts, over 500 sags and more than 2,000 spikes and surges per year. Plus there's almost continuous line noise at even the best locations.

Power surges alone are credited by one insurance company with $35 million in pc losses just last year.

Any way you look at it, making sure your computer gets premium fuel is up to you. Fortunately, it's easy and affordable.

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In reality, they're also the best power *conditioners* money can buy. They work *continuously,* uniquely providing an impenetrable barrier that isolates your computers from power problems.

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CompuStar 286

Company
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Components
Processor: 20-MHz 16-bit Intel 80286; 10-MHz Intel 80287 coprocessor
Memory: 1 megabyte of 16-bit 80-ns DRAM on 80286 CPU module, expandable to 16 megabytes; 128K bytes of BIOS ROM
Mass storage: Two 1.44-megabyte 3½-inch floppy disk drives; one 1.2-megabyte 5¼-inch floppy disk drive; 150-megabyte hard disk drive
Display: Flat-tension-mask color VGA-compatible monitor; VGA support on the motherboard
Keyboard: 101 keys in IBM Enhanced layout
I/O interfaces: Two RS-232C serial ports; DB-25 parallel port; analog monitor port with DB-15 connector; 6-pin DIN keyboard connector; 6-pin DIN mouse connector; seven AT-compatible expansion slots

Size
24 × 7½ × 26 inches; 66 pounds (weight can range from 50 to 90 pounds, depending on the configuration)

Software
Setup disk, which includes a setup utility, a memory and port management utility, a video mode utility, a utility for setting the processor's speed, utilities for displaying messages on the LED display, and drivers for LIM/EMS memory and additional floppy disk drives

Options
CompuStar Base Model 100: $1195
AT-compatible primary bus module: $195
AT-compatible secondary bus module: $175
PS/2-compatible primary bus module: $295
PS/2-compatible secondary bus module: $250
PS/2 adapter module: $995
8086 CPU module (available as of February): $295
80286 CPU module: $695
80286 memory-extender kit: $55
16-MHz 80386 CPU module: $1395
20-MHz 80386 CPU module: $1695

Documentation
User's manual; Adaptec hard disk drive controller user's manual

Price
System as reviewed: $6570

Inquiry 857.

great deal more than the Dell computer. A Dell System 220 with a 40-megabyte hard disk drive, 1 megabyte of memory, three empty AT slots, and Dell's VGA Plus monitor costs $3299 as I write this. A comparable CompuStar 286 with a 44-megabyte hard disk drive and equivalent VGA monitor runs $4010, or $711 more. For that extra $711, the CompuStar 286 offers more empty slots, a slightly faster overall system, and its built-in flexibility.

But Is It Compatible?

Another concern about any high-speed PC is its level of compatibility. The CompuStar ran everything I threw at it, both hardware and software. I successfully installed an Everex Evercom II 2400-bps internal modem, a Microsoft Serial Mouse, and an Intel Above Board/AT. On the software side, I tested Borland's Quattro 1.0, Reflex 1.14, SideKick Plus 1.0, SuperKey 1.16A, Turbo Basic 1.1, Turbo C 2.0, and Turbo Pascal 4.0; Digitalk's Smalltalk/V 1.2; Kermit 2.30; Lotus 1-2-3 version 2.01, which ran without forcing me to slow the system manually; MicroPro's WordStar 3.3 and 4.0; Microsoft's PC Paintbrush 2.0 and Word 4.0; Quarterdeck Office Systems' DESQview 2.0; the Norton Utilities 3.00; and Symantec's Q&A 1.1.

Wells American sells IBM's own PC-DOS 3.3 and the AT version of IBM's OS/2 1.00, which a Wells spokesperson said runs on the CompuStar. Wells did not include OS/2 with the evaluation unit, however, so I was unable to verify that.

And More Goodies

When you leave the world of external applications and dive into the box itself, you find that the Wells engineers have been at it again. It starts with the fans—one at the bottom front of the unit that blows out enough air that you can feel it if you wear shorts, and one at the top rear inside the power display case. The unit disassembles easily, using Nylatch nylon snap fasteners.

The flexible design also extends to the CompuStar's storage devices: The system can hold up to six half-height devices, all of which you can access from outside the machine, if necessary. Two of these device areas are 3½-inch bays, while the other four can hold 5¼-inch devices. All the devices mount on sliding rails inside the machine.

My evaluation unit had two Mitsubishi 3½-inch floppy disk drives, which DOS saw as drives A and B, in the 3½-inch slots. By using Wells's own special drivers and CompuStar Extended Diskette Drive BIOS, DOS saw my evaluation unit's third floppy drive, a 1.2-megabyte 5¼-inch TEAC model, as drive E.

The CompuStar also includes one other full-height 5¼-inch drive bay inside the machine. The power supply includes seven device connectors, so you can run the system even if you fill this bay and all six half-height bays. In my unit, this internal bay held a Maxtor 155-megabyte, 18-millisecond hard disk drive managed by an Adaptec 10-megabit-per-second ESDI controller in one of the AT slots.

Wells American includes Storage Dimensions' well-respected SpeedStor hard disk device driver, version 5.13b, with the system. The combination of that software and Wells's disk BIOS lets you make a second DOS partition that is larger than the traditional DOS 3.3 limit of 32 megabytes. In my evaluation unit, the C drive was only 2 megabytes, while the D drive was over 150 megabytes. Wells American uses this design to leave drives E and F open for two of the four floppy disk drives that the CompuStar can include.

Wells also offers a slew of other mass storage options, including tape backup systems, a WORM (write once, read many times) drive, and an erasable optical drive from Maxtor.

The CompuStar's interior bay design has one flaw: No hard disk light is visible outside the machine. Wells more than compensates for this omission, however, with a little touch that Dell popularized on its early systems: a four-character LED display on the front of the system. That display shows both diagnostic and system status information. For example, it shows "R" when the system is reading the hard disk and "W" when the system is writing to that disk. If you press the Control or Shift keys, the LED shows the current system speed.

Wells American also includes on its standard setup disk two programs, DISP.EXE and SCROLL.EXE, with which you can display four characters of your choice, either statically or scrolling from right to left, in the LED display.

Wells American also did its own ROM BIOS; my unit included the CompuStar Multi-Processor Convertible Microcomputer V1.05 BIOS.

Like most of today's fastest systems, the CompuStar offers a slower compatibility speed. Unlike many systems, however, it offers five slower speeds. You can run the 80286 at 16, 12, 10, 8, or 6 MHz. Wells implements these speeds by

continued

Wells American CompuStar 286

APPLICATION-LEVEL PERFORMANCE

Wells American CompuStar 286 **12.5***

WORD PROCESSING

	Medium/Large
XyWrite III + 3.52	
Load (large)	:10
Word count	:03/:20
Search/replace	:05/:22
End of document	:02/:14
Block move	:09/:09
Spelling check	:09/1:00
Microsoft Word 4.0	
Forward delete	:13
Aldus PageMaker 1.0a	
Load document	:13
Change/bold	:25
Align right	:20
Cut 10 pages	:18
Place graphic	:05
Print to file	1:46
Index:	**2.62**

SPREADSHEET

Lotus 1-2-3 2.01	
Block copy	:03
Recalc	:01
Load Monte Carlo	:16
Recalc Monte Carlo	:04
Load rlarge3	:03
Recalc rlarge3	:01
Recalc Goal-seek	:03
Microsoft Excel 2.0	
Fill right	:05
Undo fill	1:50
Recalc	:01
Load rlarge3	:25
Recalc rlarge3	:01
Index:	**3.11**

DATABASE

dBASE III + 1.1	
Copy	:59
Index	:18
List	1:14
Append	1:34
Delete	:02
Pack	1:20
Count	:16
Sort	1:04
Index:	**1.65**

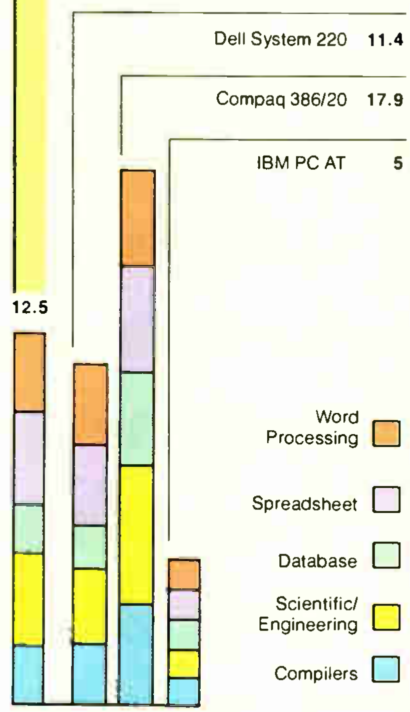
SCIENTIFIC/ENGINEERING

AutoCAD 2.52	
Load SoftWest	1:09
Regen SoftWest	:44
Load StPauls	:11
Regen StPauls	:07
Hide/redraw	14:58
STATA 1.5	
Graphics	:19
ANOVA	:14
MathCAD 2.0	
IFS 800 pts.	:21
FFT/IFFT 1024 pts.	:22
Index:	**3.06**

COMPILERS

Microsoft C 5.0	
XLisp compile	4:37
Turbo Pascal 4.0	
Pascal S compile	:06
Index:	**2.06**

All times are in minutes:seconds. Indexes show relative performance; for all indexes, an 8-MHz IBM PC AT = 1



*Cumulative application index. Graphs are based on indexes at left and show relative performance.

LOW-LEVEL PERFORMANCE[1]

Wells American CompuStar 286

CPU

Matrix	5.20
String Move	
Byte-wide	40.40
Word-wide:	
Odd-bnd.	30.60
Even-bnd.	20.19
Sieve	22.69
Sort	18.95
Index:	**2.64**

FLOATING POINT

Math	23.12
Error[2]	
Sine(*x*)	9.78
Error	
e^x	8.37
Error	
Index:	**2.04**

DISK I/O

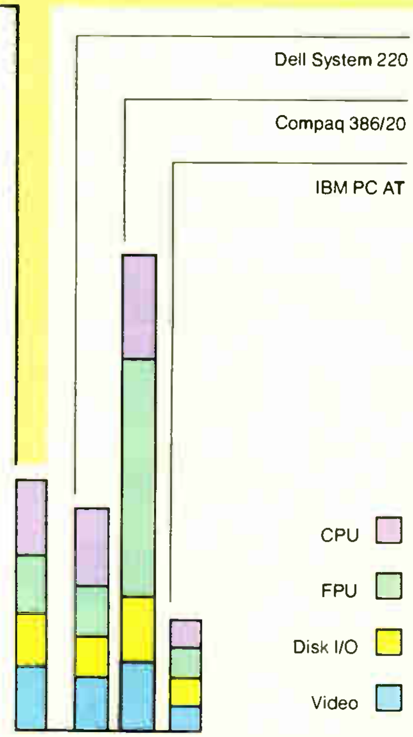
Hard Seek[3]	
Outer track	3.29
Inner track	3.33
Half platter	6.66
Full platter	6.68
Average	4.99
DOS Seek	
1-sector	8.47
32-sector	25.19
File I/O[4]	
Seek	0.11
Read	0.88
Write	0.78
1-megabyte	
Write	4.87
Read	5.02
Index:	**1.90**

VIDEO

Text	
Mode 0	3.95
Mode 1	3.96
Mode 2	3.94
Mode 3	3.92
Mode 7	N/A
Graphics	
CGA:	
Mode 4	1.92
Mode 5	1.87
Mode 6	2.01
EGA:	
Mode 13	3.46
Mode 14	3.74
Mode 15	N/A
Mode 16	3.77
VGA:	
Mode 18	3.90
Mode 19	2.00
Hercules	N/A
Index:	**2.30**

CONVENTIONAL BENCHMARKS

LINPACK	478.68
Livermore Loops[5] (MFLOPS)	0.46
Dhrystone (MS C 5.0) (Dhry/sec)	5000



N/A = Not supported by graphics adapter.

[1] All times are in seconds. Figures were generated using the 8088/8086 versions (1.1) of Small-C.

[2] The errors for Floating Point indicate the difference between expected and actual values, correct to 10 digits or rounded to 2 digits.

[3] Times reported by the Hard Seek and DOS Seek are for multiple seek operations (number of seeks performed currently set to 100).

[4] Read and write times for File I/O are in seconds per 64K bytes.

[5] For the Livermore Loops and Dhrystone tests only, higher numbers mean faster performance.

For a full description of all the benchmarks, see "Introducing the New BYTE Benchmarks," June 1988 BYTE.

using three different crystals/oscillators in the system, one each for the 20-, 16-, and 12-MHz rates. A flip-flop divider off the oscillators provides the three additional slower speeds. You can control the unit's speed from the keyboard with the now-traditional Ctrl/Alt/+ combination to raise the speed, or Ctrl/Alt/− to lower it. The system beeps once each time you lower its speed, and twice each time you raise the speed. You can also use a Wells utility, SPEED.EXE, to set the speed from the DOS command line.

Monitor and Keyboard

In keeping with this abundance of options, you can choose either a "firm touch" or "soft touch" keyboard. Both keyboards follow the IBM 101-key Enhanced layout. My evaluation unit came with the Fujitsu keyboard that is showing up on a lot of machines these days. It has a good, very springy feel with an audible keyclick.

As for the monitor—well, if you haven't seen flat-screen color monitors yet, avoid them at all cost. Once you see one, you'll want one, and they're expensive. Wells charges $895 for the Zenith-built one on my evaluation unit, and it is gorgeous, albeit big and a bit noisy, since it has its own fan.

The Soft Side

The only standard software is Wells American's Setup disk, which comes in both 3½- and 5¼-inch versions. That disk includes Wells's setup program, which is also in ROM and accessible via the Ctrl/Esc key combination; a LIM/EMS driver; a program that lets you set the system's video mode; another that lets you control its port assignments and memory usage, including its use of interleaving and shadow RAM; Wells American's special disk drivers; and the LED and compatibility speed control programs mentioned earlier.

Documentation and Support

The CompuStar includes a single, 100-plus-page user's manual. Its early chapters are for novices, with step-by-step instructions for adding options to the system. Its later chapters and appendixes contain detailed technical information, including data on the jumpers on all the CPU modules.

Unfortunately, even though it's well done, this book just cannot make a novice comfortable installing all the possible options. The task itself is largely unnecessary, however, since Wells assembles the units at the factory.

My unit also came with a user's manual for my Adaptec hard disk drive controller. That complex book is useful only for skilled users who want detailed information about the controller.

When the manuals leave you wanting, you can call the company's technical support. It's a toll call, which is unfortunate since you're likely to have to wait. Every time I called, I had to sit on hold until I either gave up or gave in to the secretary's request for my name and number. When she took that information, however, someone always called me back. The support people with whom I spoke were courteous and very knowledgeable about every aspect of the system. In a rare sweep of competence, everyone with whom I spoke was able to answer all my test questions, which ranged from simple to complex.

You get a one-year limited warranty on parts and labor, which includes all hardware but not software. You have to get your machine, or at least the defective component, to a Wells Authorized Warranty Repair Center. You can buy on-site service in many locations nationwide through Wells's arrangement with General Electric. Wells American sets prices for this service on a monthly basis by component, such as $5 for the base system, $3.50 for the 80286 CPU module, and $26 for the 150-megabyte hard disk drive. Those prices can add up for a whole system; a year of service for my evaluation unit would run around $700.

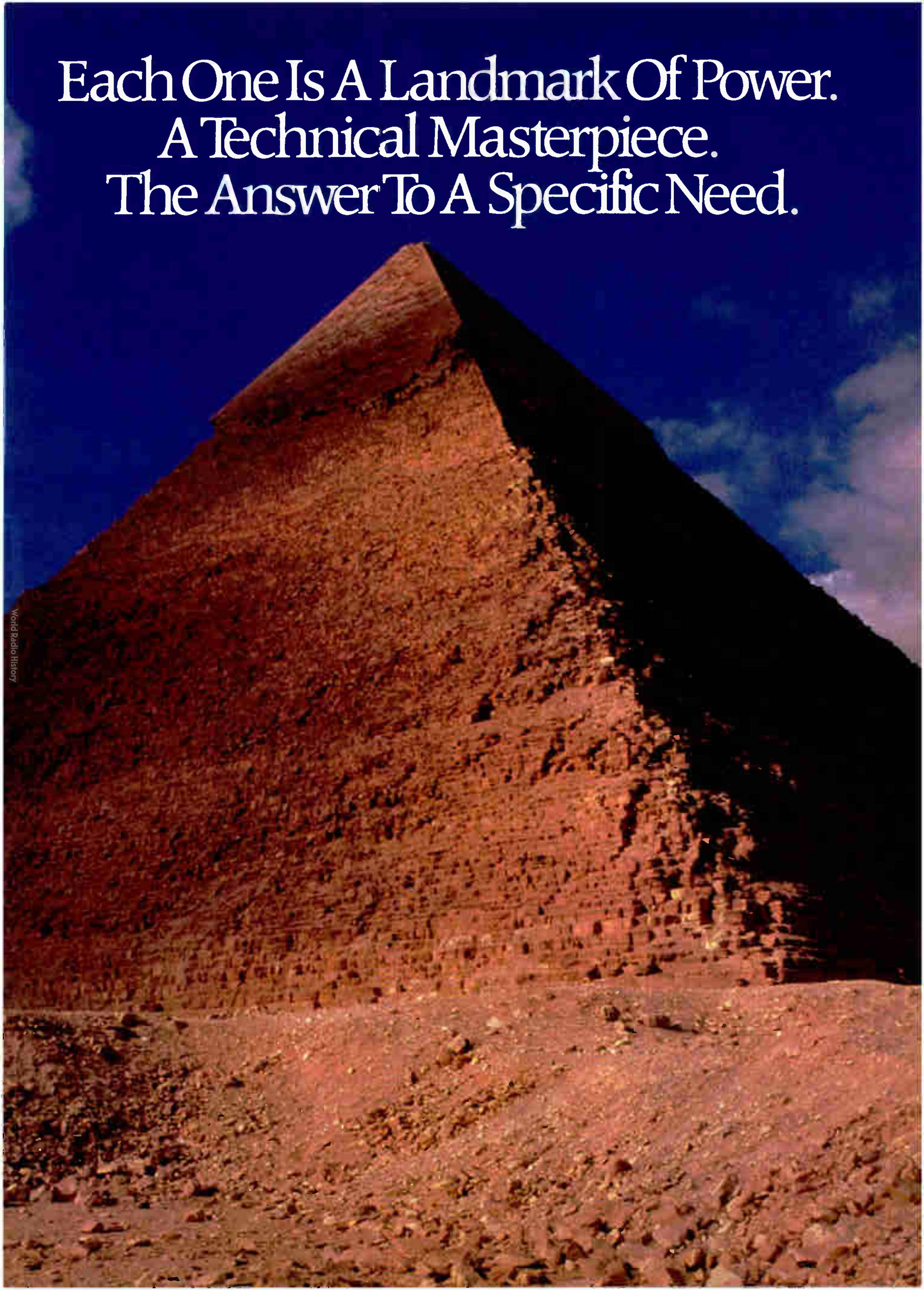
Wells offers another support plan, the C.A.R.E. (components authorized for repair or exchange) program, for which the company has not yet set prices. It lets you quickly replace a defective module. You call with the identity of the module, and the company will send a replacement via overnight delivery service.

A Good System with Great Potential

The CompuStar 286 is the fastest 80286-based system I've seen, and it has as much or more expansion capability as any system I've seen. Those properties, along with a bearable price tag, make it a good machine to consider. The real excitement will come if Wells American delivers its PS/2 module, fulfilling the dual-bus promise of the CompuStar.

I hope Wells meets this challenge, because I like this machine, and I like the engineering behind it. It's nice to see something new in the PC clone business for a change. ■

Mark L. Van Name is a freelance writer and computer consultant living in Durham, North Carolina. He can be reached on BIX c/o "editors."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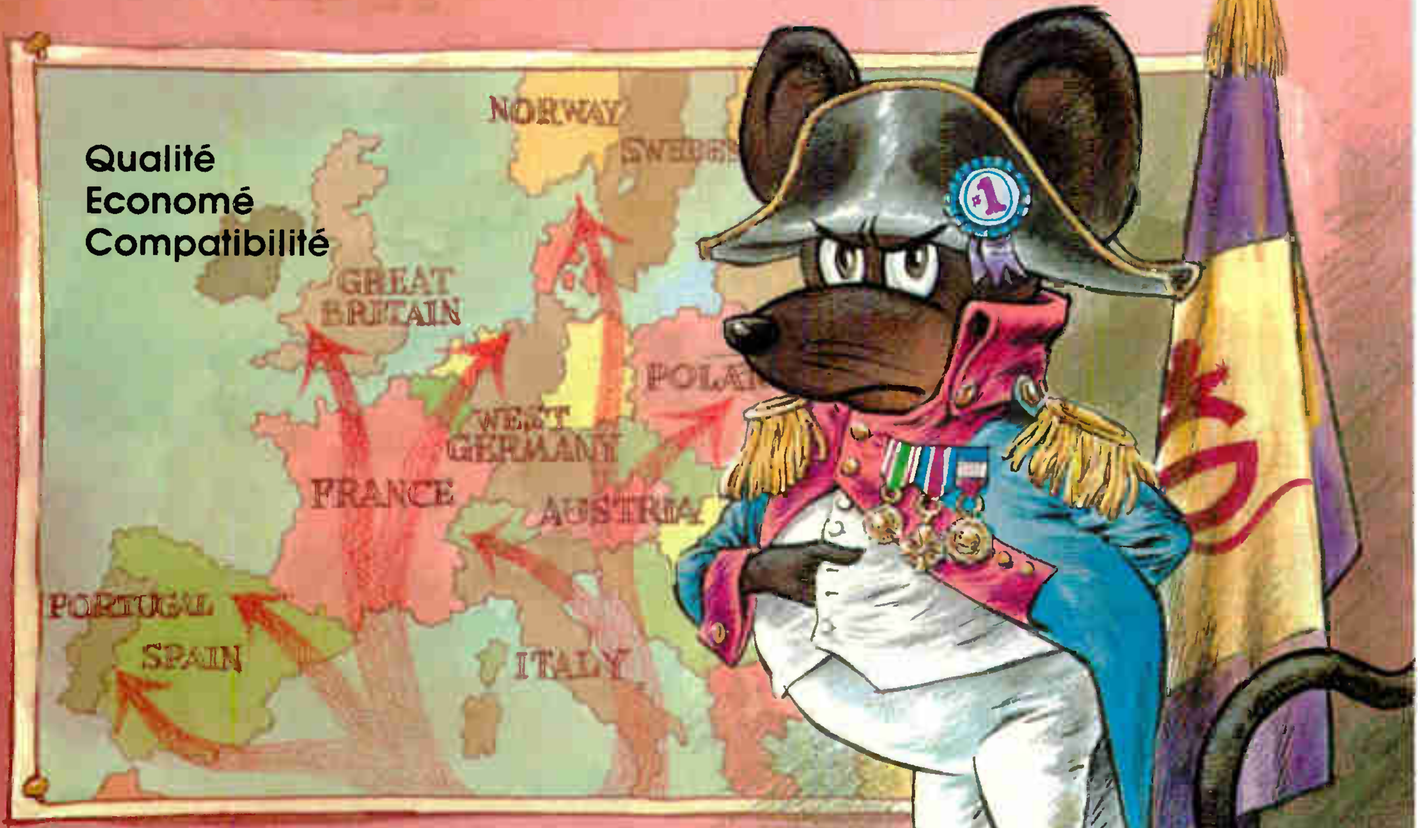

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Full-Spectrum Scanners

The Sharp JX-450 and the Howtek Scanmaster bring affordable color scanning to the Mac II

Tom Thompson

Photo 1 *(below, left)*: *The Sharp JX-450 produces excellent results but requires a substantial amount of memory.*

Photo 2 *(below, right)*: *Howtek's Scanmaster offers superior scanning software on a hardware platform identical to that of the Sharp unit.*

The Mac II's rich set of color graphics primitives and its versatility at manipulating color bit maps make it a powerful image processor. But to take advantage of these capabilities, you've got to get the color images into the computer in the first place.

While you can do this easily enough in shades of gray with existing scanners (see "Bringing the Outside World into a Macintosh" by Laurence H. Loeb, October 1988 BYTE), the ability to achieve this result in color has been a long time coming. That's not to say that it couldn't be done, but the equipment to do it cost dearly—usually tens of thousands of dollars.

Now two vendors, Sharp Electronics and Howtek, have introduced scanning packages that can electronically reduce a color photograph to binary data and convert it into color formats that many Mac II applications can use (for a detailed description of how these scanners capture an image, see the text box "Color Scanning Explained" on page 191). Both packages accomplish this operation in just minutes. But their most important feature is their nonstratospheric prices: Sharp's top-of-the-line JX-450 color scanner package costs $7565, and the Howtek Scanmaster package is priced at $8195. Both scanners also work with the IBM PC and PS/2s.

Mirror Image

It's no coincidence that the Sharp and Howtek scanners look alike: With the exception of the company nameplate, both scanners are identical. Howtek buys its scanner from Sharp and resells it with its own interface board and scanning software (see photos 1 and 2).

The scanners closely resemble a flatbed copier, right down to the hinged cover that holds the original down on the

continued

Sharp JX-450

Type
Flatbed color scanner

Company
Sharp Electronics Corp.
Sharp Plaza
Mahwah, NJ 07430
(201) 529-9500

Features
Scans images up to 11⅝ by 17 inches at 75, 100, 150, 200, and 300 dpi in 24-bit color; optional mirror unit lets you scan transparencies up to 8¼ by 11⅝ inches; can be programmed to scan in resolutions as low as 30 dpi in single steps up to 300 dpi; can scan images at several speeds and degrees of sharpness; PixelScan software drives scanner in various modes and saves images in several formats

Size
21¾ × 20⅞ × 7$\frac{5}{16}$ inches; 55 pounds

Hardware Needed
Macintosh II or IIx with at least 2 megabytes of RAM and a 40-megabyte hard disk drive; NB-GPIB package, which contains NB-GPIB NuBus board, driver software, and GPIB cable

Software Needed
Finder 6.1/System 6.0.2 or higher

Options
Mirror unit to scan transparencies: $500
NB-GPIB package: $570

Documentation
Programming manual

Price
$6995

Inquiry 851.

Howtek Scanmaster

Type
Flatbed color scanner

Company
Howtek, Inc.
21 Park Ave.
Hudson, NH 03051
(603) 882-5200

Features
Scanner unit characteristics are identical to the Sharp Scanner; also includes NB-GPIB NuBus board, software driver, and cable; MacScan-It application drives scanner in various modes and saves captured image in several formats

Size
21¾ × 20⅞ × 7$\frac{5}{16}$ inches; 55 pounds

Hardware Needed
Macintosh II or IIx with at least 2 megabytes of RAM and a hard disk drive

Software Needed
Finder 6.1/System 6.0.2 or higher

Options
Transparency scanning option: $659

Documentation
Operator's guide; MacScan-It user's guide; GPIB installation guide

Price
$8195

Inquiry 852.

image table. Markings on the sides of the image table let you accurately position eight different sizes of documents: U.S. office, legal, invoice, and tabloid, plus the European A3, A4, B4, and B5 standard sheets. If you want to scan slides or transparencies, Sharp's optional mirror unit or Howtek's transparency option must be attached to the scanner so that it redirects the light from the scanning lamps through the film.

Both scanners use an IEEE-488 general-purpose interface bus to communicate with the host computer. This means that, in addition to the scanning unit, GPIB interface hardware is required. Both products use the National Instruments NB-GPIB NuBus board, the NI-488 driver, and a GPIB cable to complete the connection. Finally, you'll need application software that talks to the scanner, retrieves the image data, and saves this information in a disk file. I call this combination of hardware and software a scanning package, since all the components in the package are required to obtain a scanned image.

You don't always get a complete scanning package when you buy a scanner. Howtek provides its Scanmaster scanner, power cable, and scanning software, along with the NB-GPIB interface board, driver software, and interface cable, as a complete package. Sharp, however, sells its JX-450 scanner, power cable, and scanning software as a unit for $6995. To connect the JX-450 to the Mac II, you need to buy the GPIB interface board, driver software, and interface cable for an additional $570.

Workspace Required

You'll need plenty of desk space or a large open spot on your office floor to accommodate these scanners. Measuring 21¾ by 20⅞ by 7$\frac{5}{16}$ inches, they each occupy more space than an IBM AT. The large size is necessary to accommodate up to a 12- by 17-inch document. You'll also need an additional foot of clearance on each side of the scanner, as the scanning bed moves from side to side during operation. Fortunately, the 6½-foot-long GPIB interface cable lets you place the scanner some distance away from the computer.

To take full advantage of these two scanners, you're going to need a hefty amount of resources. The Mac II you intend to use should have at least 2 megabytes of RAM and a high-capacity hard disk drive (I recommend at least 8 megabytes of RAM and a 100-megabyte hard disk drive). Images with 8-bit-deep color pixels need lots of RAM and disk space, so the more RAM and the larger your hard disk drive, the better off you'll be.

As with most Mac II NuBus peripheral boards, installation is a breeze. First, you pop the Mac II's hood, drop the NB-GPIB interface board into an empty slot, close the computer, and reboot. Then you copy the `NB Handler INIT` file to the Mac's System Folder. This INIT contains the NI-488 driver, which installs into the Mac II's system heap when you reboot the machine. Applications that work with the scanner use this driver to communicate through the GPIB interface. If you have more than one GPIB board in your system, the `ibconf` application written by National Instruments and supplied with both scanning packages lets you configure the `NB Handler INIT` for a particular board in the system.

All that's left is to copy the scanning application to the Mac II's hard disk, and you're set. Sharp provides a PixelScan application that can operate the scanner at various resolutions and scanning speeds and save the image in several data formats. Howtek's MacScan-It application has similar features, but it has better color controls and can save data in a wider range of data formats.

Once system setup is complete, you place a photo or document onto the scanner table and switch the unit on. After the scanner warms up, you launch the scanning application. First you select the scanning mode, resolution, and color palette, and then you issue a scan command. The scanning bed moves from right to left, and in about 1½ minutes (at the fast scan setting), a color image fills the window of the scanning application. If you've set your color controls correctly, the results can be spectacular.

Not Created Equal

Although both scanning packages are virtually identical in terms of the scanning unit and GPIB interface, they differ significantly when it comes to the scanning software they supply. Sharp's PixelScan version 1.1a, written by SuperMac Software, provides only minimal capabilities. You can choose from a wide variety of scanning settings: resolution (36, 75, 100, 150, 200, or 300 dots per inch, or adjustable), image size (including U.S. and European formats), brightness correction (normal or lighter), scanning speed (slow, fast, or custom), and image sharpness (normal, sharp, exaggerated, or softened).

You can scan a picture using the Mac II system colors, custom color palettes, or gray scales. You can also specify whether you want dithering performed during the scan. For certain custom palettes, the scanner captures the image in two passes. The first examines the image's colors and then selects (from the Mac II's palette of 16.7 million colors) the 256 colors that best represent the image; the second pass performs the image scan. You can save the image in PICT2, PixelPaint, and MacPaint formats. PixelScan also has a SIZE −1 resource, making it MultiFinder-compatible.

The size and resolution of each scan is limited by available RAM. The only way you can control the size of the scanned area on the scanning bed is through canned document-size settings. The PICT2 file format is the graphics *lingua franca* for exporting color images to other Mac applications, but the MacPaint format, which handles only black-and-white images, makes no sense for a color scanner.

Although PixelScan operates under MultiFinder, it allows little or no background processing during a scan. I attempted an XMODEM download from BIX at 1200 bps using Red Ryder 10.3 in the background. The download timed out while PixelScan captured a large document.

Howtek's MacScan-It 1.0 is the more polished application, although the program and documentation was shipping in preliminary form (Howtek says it will provide free upgrades of the final version to users who buy the preliminary version). The program has the same image-size and image-sharpness menu selections as PixelScan. You can scan at resolutions of 75, 100, 150, 200, or 300 dpi, or at variable resolutions.

MacScan-It lets you scan an image as a positive or a negative, and with continuous colors or gray scales. If you're scanning transparencies or slides, MacScan-It has a large selection of color lookup tables for many types of films (e.g., Kodak, Konica, 3M, and Fuji). Unlike PixelScan, it also has a useful preview mode that lets you look at the image on the scanning table before starting the final scan. You can then resize a preview window to surround the picture entirely or crop the part that you want.

MacScan-It uses the preview window's dimensions to determine the area

continued

Color Scanning Explained

Sharp's color scanner is operationally similar to a gray-scale scanner. It measures the light intensity at certain points along the original image and converts this intensity level into digital values. However, a typical gray-scale scanner detects only 16 different light intensities (or shades), while the Sharp JX-450 scanner detects 256 different light intensities. And while a gray-scale scanner captures an image once, the Sharp scanner must do it three times, measuring the red, green, and blue intensities for each part of the image (for gray-scale images, the scanner uses only the green lamp).

The process is further complicated by the fact that all three RGB measurements must be aligned to the exact same points on the image. Otherwise, the combined color measurement would correspond to several different points on the image, resulting in color fringing or a garbled image. The process is similar to the careful alignment required to print a color picture like those in this magazine: If even one of the colored inks is printed out of register, the image is ruined.

Sharp's solution to this problem is ingenious. The original photo lies on the image table. The image table remains stationary while the JX-450 fires three colored fluorescent lamps (red, blue, and green) in sequence. Red and blue filters mounted over the red and blue lamps help to improve their spectral characteristics. This light then shines through a slit on the image table and onto a band of the image.

The light reflected from the image band is directed by two mirrors onto a charge-coupled-device (CCD) sensor strip. The strip contains 3648 photosensitive elements that measure the intensity of the light that falls on them. The sensor strip has sufficient elements to measure a band across the widest part of a European A3 document (297 millimeters, or about 11¾ inches) at 300 dpi. The light intensity for the particular color component (determined by the color of the lamp illuminating the image) is digitized into 256 levels (8 bits), although Sharp guarantees only 6-bit accuracy for each component.

The scanner sends the intensity information for the image band at this particular color to the host computer. Measurements of the next color component for the same band start when the next lamp fires.

Once all three lamps have fired and all the RGB color information for this image band has been captured, the scanning table moves slightly. This exposes a new part of the image, and the measuring process repeats. In this manner, the entire image is assembled, one band at a time. The only possibility for misalignment is if the scanning table moves inaccurately, but this only causes problems with the alignment of bands of the image, not with their colors.

To scan transparencies, the scanner uses an optional mirror box that directs light from the lamps through the film. Two mirrors in the device guide the light from the image table slit across the scanning table, through the film, and onto a third mirror inside the scanner. This third mirror then reflects the light onto the CCD sensor strip.

While this process might seem extremely complicated compared to the operation of a gray-scale scanner, the color scanner must capture three times as much information—all of it required to produce a realistic image. The Sharp scanner tackles this problem with a simple but effective design. The effort is worth it: The color images look worlds better than anything a gray-scale scanner could generate.

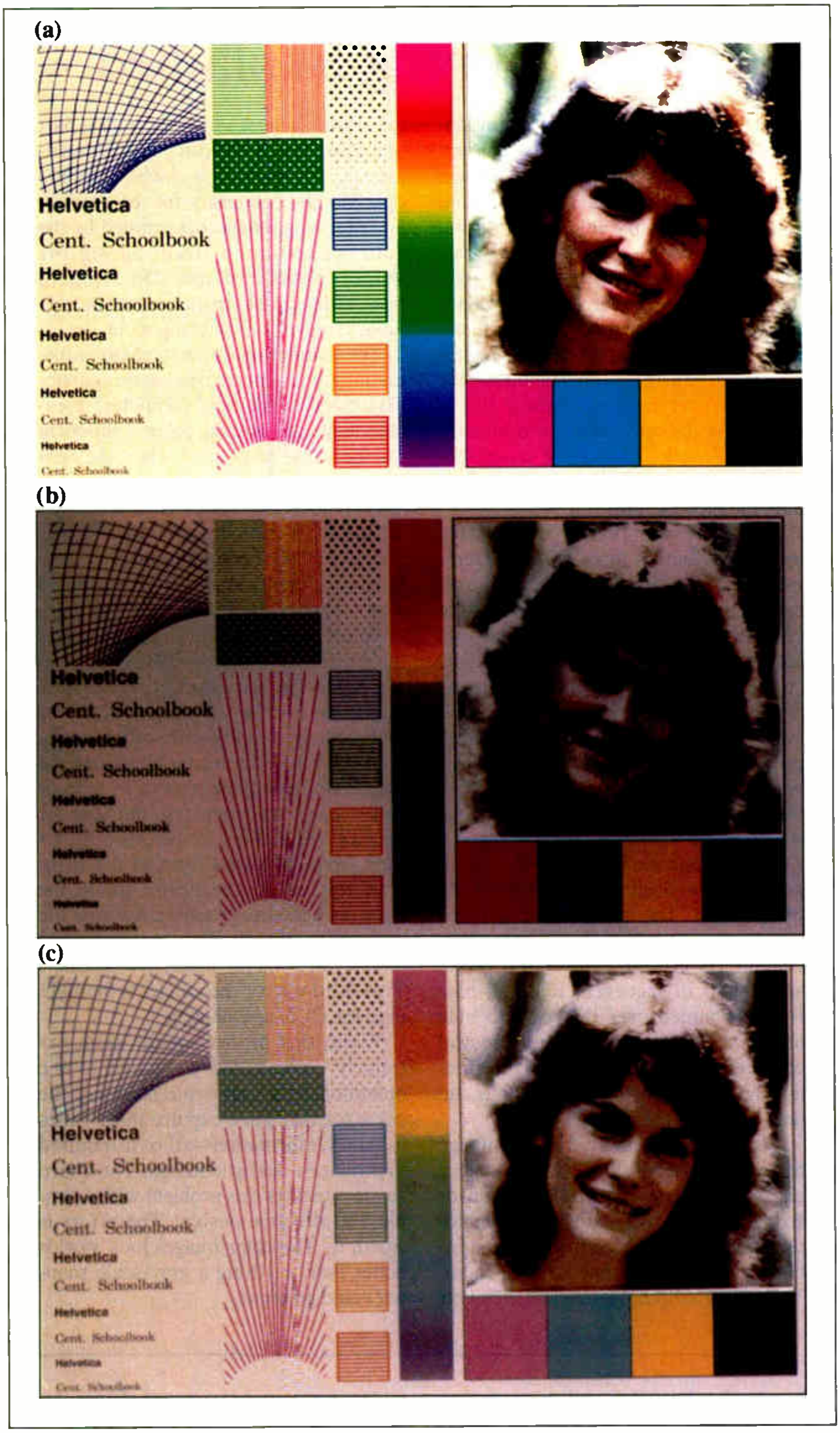

Figure 1: (a) *The BYTE color test pattern for scanner quality.* **(b)** *The test pattern scanned at 300 dpi on the Sharp JX-450.* (c) *The pattern scanned at 300 dpi on the Howtek Scanmaster. All images shown are actual size. I used LaserPaint Color II with the Sharp JX-450, and MacScan-It with the Howtek Scanmaster. To avoid degrading the image quality, I saved the images as PICT2 files and sent them to a computer graphics firm to make direct color separations. The firm experienced some difficulties with the LaserPaint image, so that image was exported as a TIFF file to Avalon Development's PhotoMac application and saved as a PICT2 file.*

to be scanned—a nice touch that saves RAM and disk space. It also saves the data in a larger variety of formats: SIM (a proprietary format that saves all 24 bits of color information), PICT2, 24-bit TIFF (for use with PageMaker and Ready-Set-Go!), and RIFF (for use with ImageStudio).

MacScan-It is stingy with memory. As it obtains 24-bit image data from the scanner, it spools the data to a temporary file on the hard disk. It uses this 24-bit data, as well as any color corrections you've selected, to paint an 8-bit-deep image on the screen. This makes scanning much slower (it took about 3 minutes to scan and view a legal-size document with MacScan-It versus 1½ minutes for PixelScan), but if you use a 2-megabyte machine, you can make large 300-dpi scans without running out of memory.

You also can make certain color corrections (i.e., auto-white, auto-gray, auto-contrast, and custom gamma corrections) once you've scanned in an image. MacScan-It reads the 24-bit color image data from the temporary file as it applies the corrections. This makes additional scans unnecessary, and all color corrections use the same high-quality image data. MacScan-It is also MultiFinder-compatible, and it does a better job of allowing background processing. The XMODEM download completed successfully while MacScan-It worked on a large document.

Neither PixelScan nor MacScan-It can read files. This is understandable, since the primary goal of these applications is to get images into the computer. But MacScan-It's utility would certainly be enhanced if it could read its own SIM files. This capability would enable you to manipulate the image data later without rescanning the image.

I used the BYTE test pattern to judge the quality of the images I scanned (see figure 1). I tested both packages with a Mac II equipped with a SuperMac Technologies Spectrum/8 video board and 19-inch color monitor; I configured the system alternately with 2 and 5 megabytes of RAM. PixelScan distorted or lost portions of the test images at the high dpi settings. I had to resort to LaserPaint Color II to scan in the test pattern on the Sharp scanner.

When I tried to crop the 300-dpi image, LaserPaint didn't have enough RAM to perform the operation; I had to install an additional 4 megabytes of RAM (for a total of 8 megabytes). As figure 1 shows, the test pattern isn't very

continued

large, so, obviously, software that works with copies of an image in memory isn't going to handle large 300-dpi scans. By contrast, with MacScan-It, I was able to crop the image in preview mode, and it performed the scan without a hitch.

Since the scanning hardware is virtually the same, the quality of the test patterns was nearly identical. The only differences, as evident in the continuous-tone portrait, seem to be in the software itself. When both scanners captured the same image, the quality of the image was the same, but the PixelScan image tended to be darker in tone than the MacScan-It image. MacScan-It lets you manipulate the image colors if good color fidelity is required, but with PixelScan, you'll need a graphics application to touch up the image.

Tough Choice

No matter which package you buy, you're going to end up with a Sharp scanner, so your choice is between the two scanning software packages. Is the Howtek package worth the extra $630? Look at it this way: If you buy the Sharp package, plan on using the money you saved to buy decent scanning software, because PixelScan is inadequate. Even though the Howtek MacScan-It software is preliminary, it does a far better job.

Possible candidates to replace PixelScan are LaserWare's LaserPaint Color II (which drives both the Sharp and Howtek scanners) for $595, SuperMac Software's PixelPaint 2.0, also $595, or Imagenesis's ChromaScan software for $195. Registered owners of Letraset's ImageStudio can obtain a special scanning module from Sharp for free.

If you plan on scanning large images at 300 dpi, you might want to factor in the cost of additional RAM. You'll need at least 5 megabytes (preferably 8) to work comfortably with either scanning package (a 4-megabyte RAM-expansion kit from Apple costs $2300).

But in most cases, MacScan-It will let you get by with less memory. First, MacScan-It lets you crop the image area before you perform the scan. You capture only the part of the image you need, not everything that fits within a fixed sheet size. This saves on RAM and disk space and on the cost of a color paint package to crop the image. Second, since MacScan-It spools image data to disk instead of holding it in memory, you can make large, high-resolution scans in 2 megabytes of RAM.

The Sharp scanning unit is an impressive piece of hardware that lets you digitize color images and save them on your computer in a matter of minutes. The results are crisp and snappy. More important, these relatively low-cost color scanners open up new possibilities in the areas of art, scientific, and multimedia applications. For example, a doctor might scan x-rays in gray scale and then use false colors to help pinpoint a tumor or blood clot. Research geologists would digitize satellite photos and enhance their contrast to bring out specific details hidden in the image.

Now that the technology to import color images quickly and easily is available at a reasonable cost, the Mac II is set to fulfill its promise as an image-processing engine. And the scanner's ability to collect 24-bit data makes it ready for Apple's 32-bit Color QuickDraw when it arrives. ■

Tom Thompson is a BYTE senior technical editor at large. He can be reached on BIX as "tom_thompson."

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IN DEPTH

CASE

CASE, or computer-aided software engineering, is a *tool* for programmers, analysts, and systems engineers, as well as for business planners and executives at all levels and for businesses of all sizes, shapes, and structures. To quote Carma McClure, "CASE is software automation." It provides software tools to help corporate planners plan, and to document their work; to support systems analysts in analyzing and designing systems, and to document those tasks; and to take some of the drudgery out of programming while documenting it.

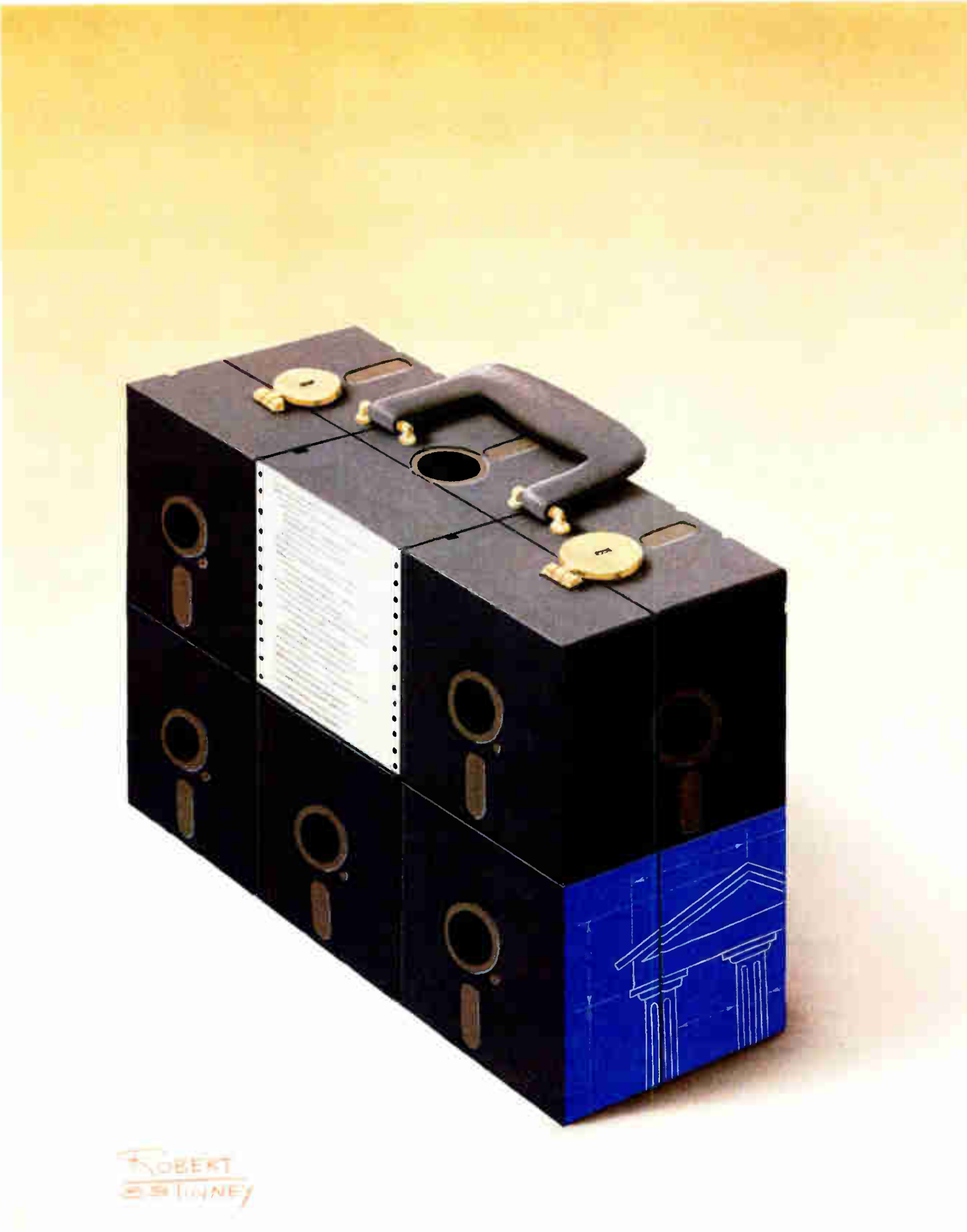
Sounds better than sliced bread, doesn't it? If it is, why haven't you heard more about it from BYTE? Because until recently, CASE tools cost in the five- and six-figure price range and only ran on mainframes. Now, the majority of them run on microcomputers, and many are available for less than $1000.

So, this month we introduce CASE, BYTE-style. The In Depth section has among its authors some of the top people in the field. We begin with "The CASE Philosophy" by Michael Lucas Gibson. This article introduces the concepts and capabilities of CASE and explains what the different elements of CASE are and what they are for. CASE itself is modular, and you may find that some parts of it fit your environment while other parts do not.

You can't talk about CASE without discussing methodology. In a field based on structured methods, there are an amazing number of different approaches to that structure. In the article "Methodology: The Experts Speak," some of the gurus of various methodologies present the approaches for which they are known. This article contains "The Warnier/Orr Approach" by Ken Orr, "The Gane/Sarson Approach" by Chris Gane, "The Yourdon Approach" by Edward Yourdon, "The Entity-Relationship Approach" by Peter P. Chen, and "The Structured Design Approach" by Larry L. Constantine.

In addition, "The CASE Experience" by the renowned CASE authority Carma McClure discusses some of the various CASE tools, toolkits, and workbenches available today for microcomputers—what they do, what CASE functions they deal with, and how they fit into the whole picture. She also includes three CASE success stories. Finally, she provides our resource guide this month, "A CASE Workshop," with a list of contact information for the products discussed in her article.

—Jane Morrill Tazelaar
Senior Technical Editor, In Depth



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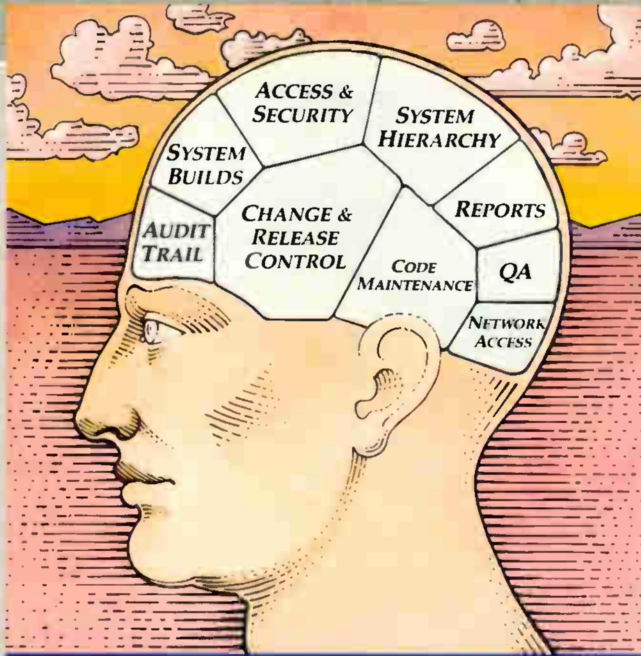

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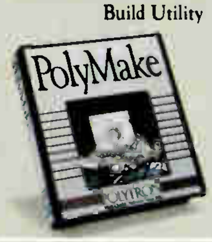

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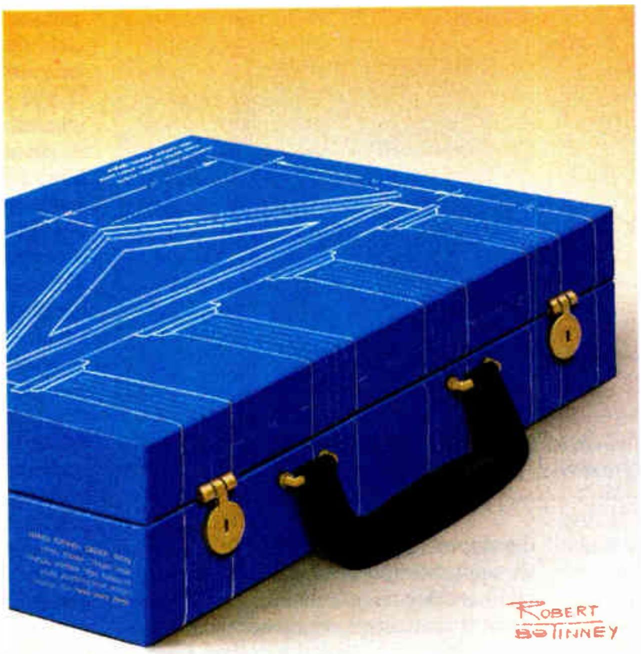
The CASE Philosophy

The whole picture involves integrating corporate plans, systems design, and systems development into one system

Michael Lucas Gibson



Computer-aided software engineering (CASE) represents a comprehensive philosophy for modeling businesses, their activities, and information-systems development. The CASE philosophy involves using the computer as a development tool to build models that describe the business, the business environment, and corporate planning, and to document computer-systems development from planning through implementation.

Components of CASE

Breaking CASE down into component parts makes it easier to understand. However, there is disagreement about whether upper CASE, middle CASE, and lower CASE have any reality. Many CASE vendors and gurus think that this functional separation is just a journalistic device. Perhaps it is, but it is also convenient and provides a familiar frame of reference for discussion.

With that in mind, I will use the terms here to refer to the computer-aided components supporting the various CASE functions. I make no claims about my definitions or even acceptance of these terms elsewhere. Upper CASE, often called computer-aided planning, will refer to a computer-aided component that supports corporate planning. Middle CASE will refer to a component that supports systems analysis and design. Lower CASE will refer to a component that supports systems development.

Another confusion in the CASE arena is that there are no standards. Each vendor includes and omits whatever it wishes based on its own criteria. And each vendor would like its implementation to be accepted as the standard. Such organizations as the Software Engineering Institute at Carnegie-Mellon University and the Center for Advanced Information Management at Auburn University have emerged to provide CASE guidance. A research goal of the CAIM is to arrive at standards for CASE. (See the text box "The Center" on page 211.)

The complete picture of the CASE philosophy prescribes that specifications for corporate plans, systems design, and systems development become fully integrated. This occurs by sharing specifications for the three functions of corporate planning, systems analysis and design, and systems development across CASE components.

In a fully integrated CASE environment, computer systems originate in the corporate-planning function (see figure 1). Corporate planners create computer specifications for developing functional corporate plans using upper CASE, specifications that embody planning requirements for the company's fundamental activities.

continued

tionary specifications you will have.

Every plan the company devises depends on timely information to ensure its success. Upper CASE systems contain planning specifications for functional

how the company functions and what its information needs are.

Very little information about how the company functions is directly related to the software that supports the company's

cations predominantly involve documenting a company's activities and the ways in which information serves it, only a small percentage of these specifica-

continued

IN DEPTH
THE CASE PHILOSOPHY

tions is directly mapped into lower CASE systems. Some people question the need for middle CASE systems, but the expert knowledge contained in the middle CASE specifications provides a common base of knowledge that is invaluable.

Creating analysis and design specifications also involves a lot of clerical work. But once a number of systems have been modeled using middle CASE systems, many design specifications will be reusable. And as you continue to use middle CASE, reusable analysis and design specifications continue to multiply.

Lower CASE

Lower CASE uses a development software component to create a set of systems development specifications ultimately used to generate programs and in user documentation. The CASE development software system also contains a dictionary software system. However, it rarely provides a graphical component, since physically oriented development specifications don't usually need it. Lower CASE systems specifications are usually directly related to programs within the developed system.

Traditionally, a dictionary system documents the characteristics of the real-world entities being modeled. Thus, its specifications usually only provide a comprehensive reference for the modeled phenomena. The CASE development dictionary, on the other hand, is an *active* dictionary, which lets you enter specifications that both describe and influence the development of the modeled object by providing criteria for its development as well as references to its attributes.

An active dictionary comprises three major components:

- a database of empty storage buckets in which to store the characteristics of the computing environment and explicit characteristics of the applications systems;
- frameworks for procedural logic and specific types of procedural commands and modules contained within typical programs in the applications systems; and
- an *activator* capable of combining environmental and application characteristics with selected procedural-command frameworks and modules to produce application programs.

The database of empty storage areas provides the location in which to store the characteristics of the computing environment. Thus, it is less essential to describe the computing environment during applications development. These characteristics are stored in the active dictionary's database during installation of the CASE development system. Subsequently, systems developers enter the characteristics of individual application systems into the active dictionary's remaining storage areas. These characteristics define the attributes of the system being developed.

The frameworks for 14 types of logical routines used in typical business systems are also embedded in the active dictionary. Regardless of program style or language, business programs contain either a single logical routine or a combination of logical routines from within this basic set. Five are batch-processing routines, and nine are on-line processing routines. All involve either information-reporting or data-updating functions. To generate programs, the activator com-

continued

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bines appropriate frameworks and the database of systems and applications characteristics.

Creating development specifications also involves a lot of clerical work. However, once some systems have been developed using lower CASE, many development specifications are reusable. As you continue to use lower CASE, reusable development specifications continue to multiply. Thus, the benefits attributed to lower CASE continue to expand.

Finally, the CASE development system can generate development and user documentation and make it available in various formats.

The Benefits of Using CASE

The benefits of upper CASE are more direct if you usually perform corporate planning. By using an upper CASE system to build an enterprise model, you gain greater insight into the importance of certain functions and how the activities they control affect the entire organization. You can better understand

1. corporate and departmental mechanisms and responsibilities;
2. the goals of the company and its departments;
3. the influence of operations on achieving these goals;
4. their place within corporate and departmental administration and operations;
5. the timeliness and sequence of operations;
6. factors influencing operations and goal achievement;
7. allocation of resources in support of operations;
8. the effect of external influences on the organization;
9. problems facing the organization; and
10. the importance of information relative to the success of the organization.

Using upper CASE systems to develop planning models gives you a clearer understanding of the company's direction and how you can contribute to its success. These planning models let you assess the impact of changing values for certain planning specifications on corporate plans. You can perform "what if" analyses and assess worst-case and best-case scenarios. Thus, you can assess the impact of changing specifications prior to committing to these changes.

Models also provide the basis for project specifications. Many planning specifications can be mapped into project schedules, descriptions of activities and their time durations, and resource allocations, utilization, and costs. Vendors of upper CASE systems are trying to closely integrate their systems with commercially available project management software systems. They are trying to pass planning specifications entered into their systems to these project management systems. Thus, many initial activities performed during project management will be done without human intervention.

A major benefit of middle CASE is that it provides easier methods for changing systems design. It is also easier to determine that the analyst understands the problems and how to solve them. Design is inherently an iterative process:

- Users discuss their information needs with analysts;

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The promise of the CASE philosophy is an exciting one. As CASE continues to evolve, it will provide the framework needed for more timely and tightly integrated corporate planning and systems development. ■

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Methodology: The Experts Speak

Five prominent software engineers discuss the methodologies for which they are famous

Ken Orr, Chris Gane, Edward Yourdon, Peter P. Chen, and Larry L. Constantine

The Warnier/Orr Approach

Ken Orr

It's more or less impossible to write about the Warnier/Orr methodology because, in fact, there is no such thing. While there are Warnier/Orr diagrams, Warnier's methodology (i.e., logical data structure, logical construction of systems, and logical construction of programs), and Orr's methodology (data-structured systems development), there is not, strictly speaking, a Warnier/Orr methodology.

Many software engineers confuse diagrams with methodologies. Perhaps this is natural, since the diagrams are the most visible part of most methodologies; but it's unfortunate, for methodologies are much more than just a set of diagrams and syntax rules.

Within the context of software engineering, a *method* is a procedure or technique for performing some significant portion of the software life cycle. Over the years, techniques have been developed for requirements definition, database design, program design, test-case development, and so on. A *methodology*, in software engineering terms, is a collection of methods based on a common philosophy that fit together in a framework called the systems development *life cycle*.

Methods often use a variety of tools: diagrams, forms, and text for documenting and communicating. Not surprisingly, these diagrams and forms often take on a life of their own. Diagrams, like words, can be used out of context, without understanding the purpose for which they were intended. While the results can be confusing, new possibilities and uses often arise that are quite fortuitous.

People who develop software engineering methods and methodologies attempt to solve problems, observe what others do, and derive, or abstract, patterns from all this. Those patterns ultimately turn into methodologies.

In my experience, my colleagues and I always know *what* works long before we know *why* it works. Software engineering methodologists are skilled at working with experts, such as analysts, programmers, database administrators, and so forth, finding out how these experts do what they do, and putting these findings down in such a way that others can follow them.

The correct name for what many people call the Warnier/Orr methodology is data-structured systems development. DSSD, like most methodologies, is actually the result of many people's efforts, in addition to my own, including my coworkers at Optima, colleagues, and clients. Much of the methodology has come about by taking various component technologies, such as structured programming and relational-database design, and putting them together into a coherent framework.

A Little History

In 1972, Terry Baker's article "Chief Programmer Team Operations" in the *IBM Systems Journal* had a major impact on the field. It brought together several ideas: structured programming, top-down design and implementation, the chief programmer, the chief-programmer team, and the documentation librarian. If there was a shot that started the "structured revolution" in the U.S., Baker's article was it.

In the early 1970s, I became interested in structured programming and in structured design. In applying the principles of top-down design, I discovered that many of my best, most intelligible solutions were those in which the hierarchi-

continued

cal structure of the program mirrored the hierarchical structure of the data the program was processing.

Shortly after this discovery, I stumbled across the work of Jean Warnier and realized that he not only had made the same discovery with regard to data-structured programming but had already built a systematic methodology around it. I also followed Michael Jackson's work, another form of data-structured design.

I already believed that you could and should construct programs hierarchically using only a few basic logical structures. Moreover, I believed that if you were going to build very large things, you should build them in systematic ways based on simple structures. This coincided with design and construction techniques used in fields such as electrical engineering. Structured programming represented a base on which to build; therefore, using the data structure as the framework for building the program structure seemed like the next natural step.

Data-structured programming meant that you could create predictably correct solutions for a wide class of programming problems—problems in which the structures of the input and the output were the same or very similar. But beyond that, Warnier, Jackson, and those of us involved in developing DSSD were able to extend data-structured techniques to arbitrarily complex programs.

To solve these more complex problems, you must recognize that the nature of the problem of complexity is, on one level at least, fundamentally mathematical in nature—that is, complex problems are fundamentally *n:n* (many-to-many) mappings from input to output. To deal with this complexity systematically, you must break the problems down into a series of less complex mappings.

This is what mathematicians have been doing for thousands of years—breaking large troublesome problems into smaller ones for which there are clear precise answers. In the case of data-structured design, this meant developing a scheme in which the physical inputs were mapped into logical inputs; the logical inputs were then mapped into the logical outputs; and, finally, the logical outputs were mapped into the physical outputs.

With this overall program-design framework comes a goal-oriented design strategy—an approach that starts with the structure of the output and works backward, first to the logical, or ideal, input, and then to the physical input.

The data-structured approach to program design has proven to be successful on a wide variety of problems, but it is clearly no panacea. What it does represent is a systematic approach to attacking complex problems (simple problems have a way of taking care of themselves, or, alternatively, becoming complex).

Programming in the Large

At some point in developing techniques for building systems, you realize that the most significant problems in software occur not at the programming level but at the systems level. How do you design entire suites of programs so that they work effectively together? How do you get the right requirements? Where does planning fit into the scheme of things?

Little by little, DSSD moved from a program-design methodology to a systems-design methodology. Over a period of years, the methodology was expanded to deal with database design, require-

continued

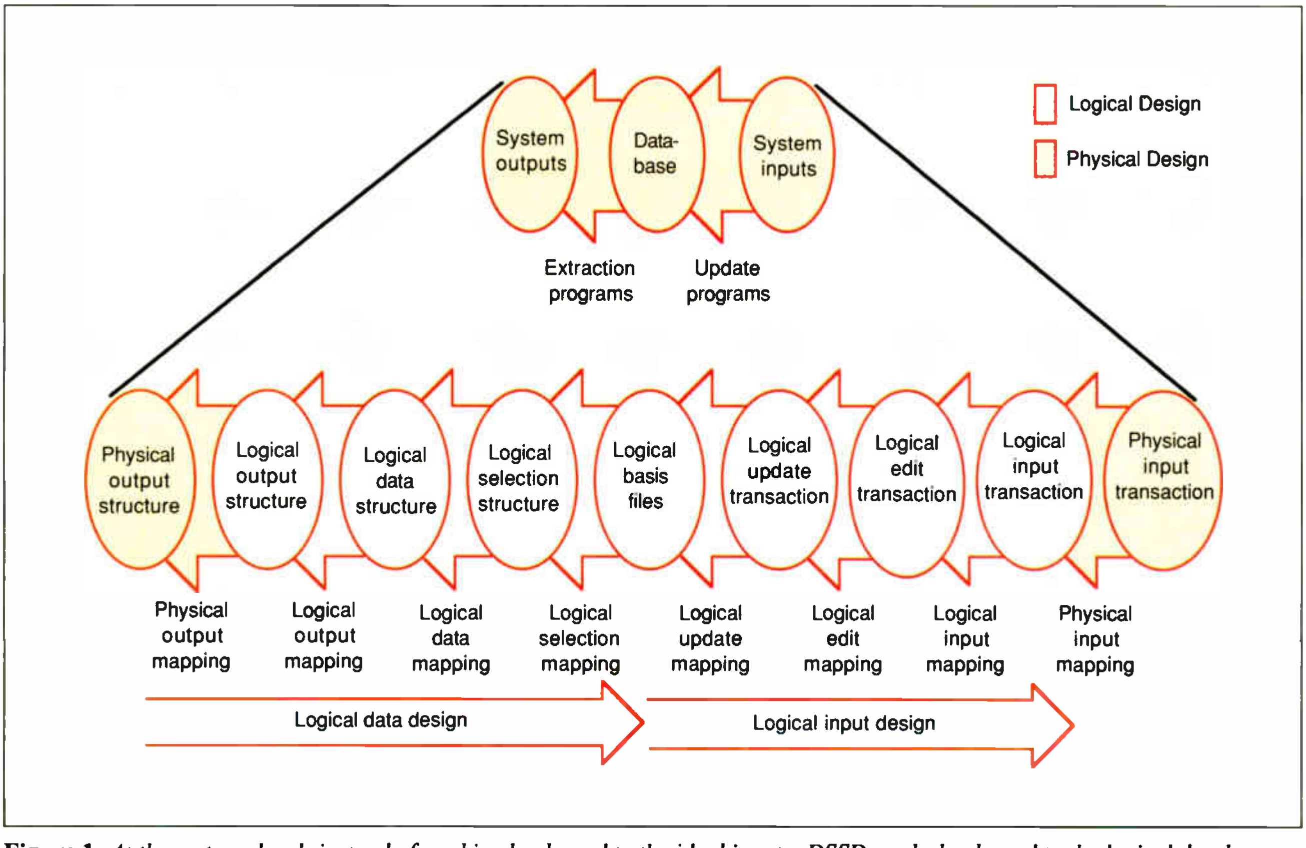

Figure 1: *At the systems level, instead of working backward to the ideal inputs, DSSD works backward to the logical database and then to the inputs.*

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ments definition, and finally systems planning and architecture.

At a conceptual level, DSSD still retains features that characterized it at the programming level. For example, it still focuses (in its design phase) on working backward from outputs. But at the systems level, instead of working backward to the ideal inputs, as it does in the programming methodology, DSSD works backward to the logical database and then to the inputs (see figure 1). The logical database turns out, not surprisingly, to be a normalized relational database.

While a complete definition of the results (outputs plus algorithms) is an excellent point at which to begin the design process, it is not the proper place to start requirements definition. So, over the years, DSSD has been extended to cover first the context, then the functions, and finally the results of the system in question.

Thus, a number of tools were needed to facilitate this process. *Entity diagrams* help you define the systems context, and *assembly-line diagrams* (a modified form of Warnier/Orr diagrams) help you define the functional flow of the system.

Data-structured methodologies have, I believe, a leg up on more process-oriented methodologies, since they are more rigorous and hence provide a better basis for true integration throughout the systems life cycle. DSSD has been used successfully on a range of software systems, from commercial on-line systems to real-time control systems. Thousands of people have been trained and thousands of systems have been built using it.

DSSD is a software engineering approach that has provided a stable framework for incorporating new technologies as they come along. For example, we have incorporated prototyping, on-line, and real-time design into DSSD without sacrificing the rigor or completeness. But there is a catch: To use DSSD successfully, you must invest time in training, use, and automation. In software engineering, as in life, there is no free lunch.

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The Gane/Sarson Approach

Chris Gane

When we think about an information system that doesn't exist yet, our ideas are usually pretty vague and general. This is not an accusation; it's a fact of human psychology.

The purpose of logical modeling is to take these necessarily vague ideas about requirements and convert them into precise definitions as fast as possible. Part of the speed comes from having graphical techniques that enable you to put down the essence of a system without going through the trouble of actually physically implementing it, as you might do, for example, in a prototype.

Several approaches to logical modeling have been proposed. The one outlined here is the current version of the approach set out in a book I wrote with Trish Sarson (see reference 1). It has become generally known as the Gane/Sarson methodology.

Logical Modeling

You can think of logical modeling as a seven-step process. Suppose the users say, "We need a system that integrates sales, inventory control, and purchasing." What exactly does that mean?

• *Step 1.* Develop a system-wide data-flow diagram (DFD) describing the underlying nature of what occurs in the sales, inventory control, and purchasing areas of the business. The simplicity of the DFD comes from the use of only four symbols to produce a picture of the underlying logical nature of any information system, at any desired level of detail.

Figure 1 shows CUSTOMERS (an external entity, something outside the system) sending in a stream of sales orders along the data-flow arrow. Process 1, process sales, handles those orders using product information from the data store called D1: PRODUCTS and puts information about sales into the data store named D3: SALES.

This figure also shows the whole of the business area, depicted using only the four symbols. For each sale, process 1 updates the INVENTORY data store, D2, with the units sold. The data stored in D3 is used by processes 2 and 3 to prepare bank deposit documents and send them to the bank, and to prepare sales reports and send them to management.

At some appropriate time—notice that time is not shown on the DFD—process 4 extracts information about the inventory status of various products from D2 and combines it with information from D3 concerning their past sales, to determine whether a product needs to be reordered. If so, based on information in D4, which describes the prices and delivery times quoted by suppliers, process 4 chooses the best supplier to order from.

Process 4 sends purchase orders to the external entity SUPPLIERS and stores information about each purchase order in D5: POS_IN_PROGRESS. When a shipment is received from a supplier, process 5 analyzes it, extracting data from POS_IN_PROGRESS to determine whether what has been received is what

continued

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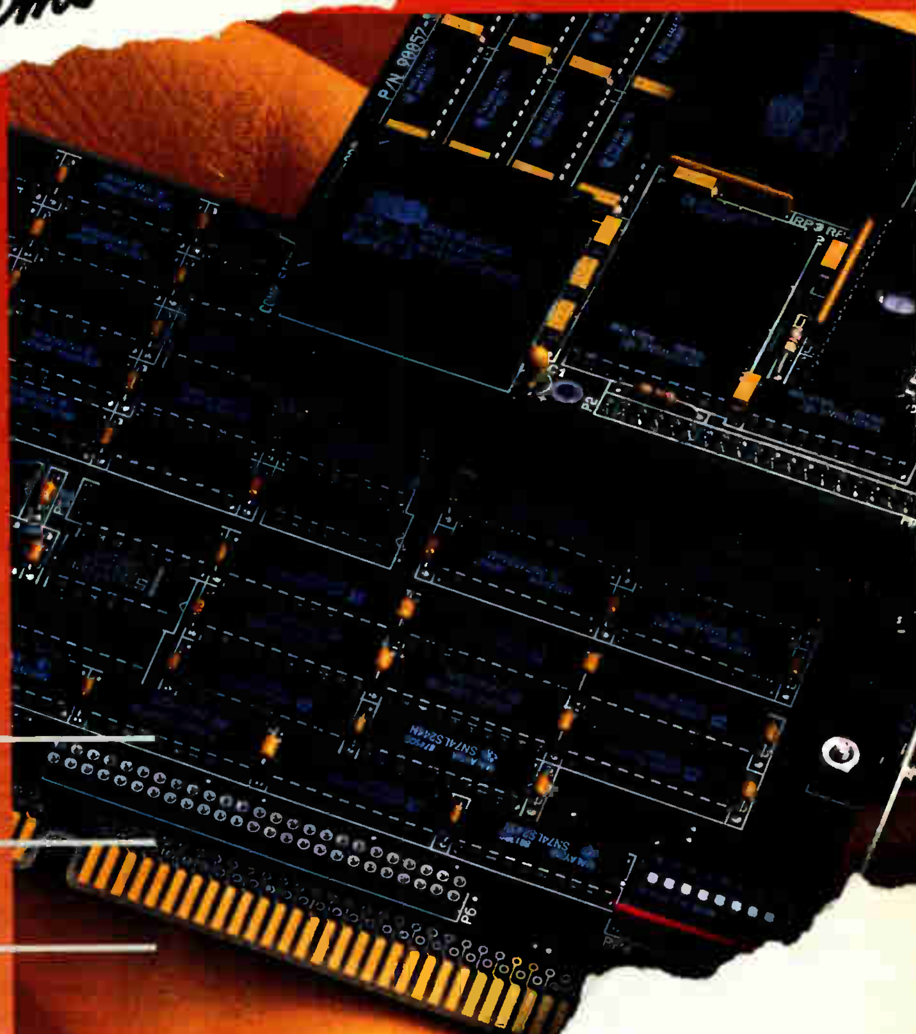

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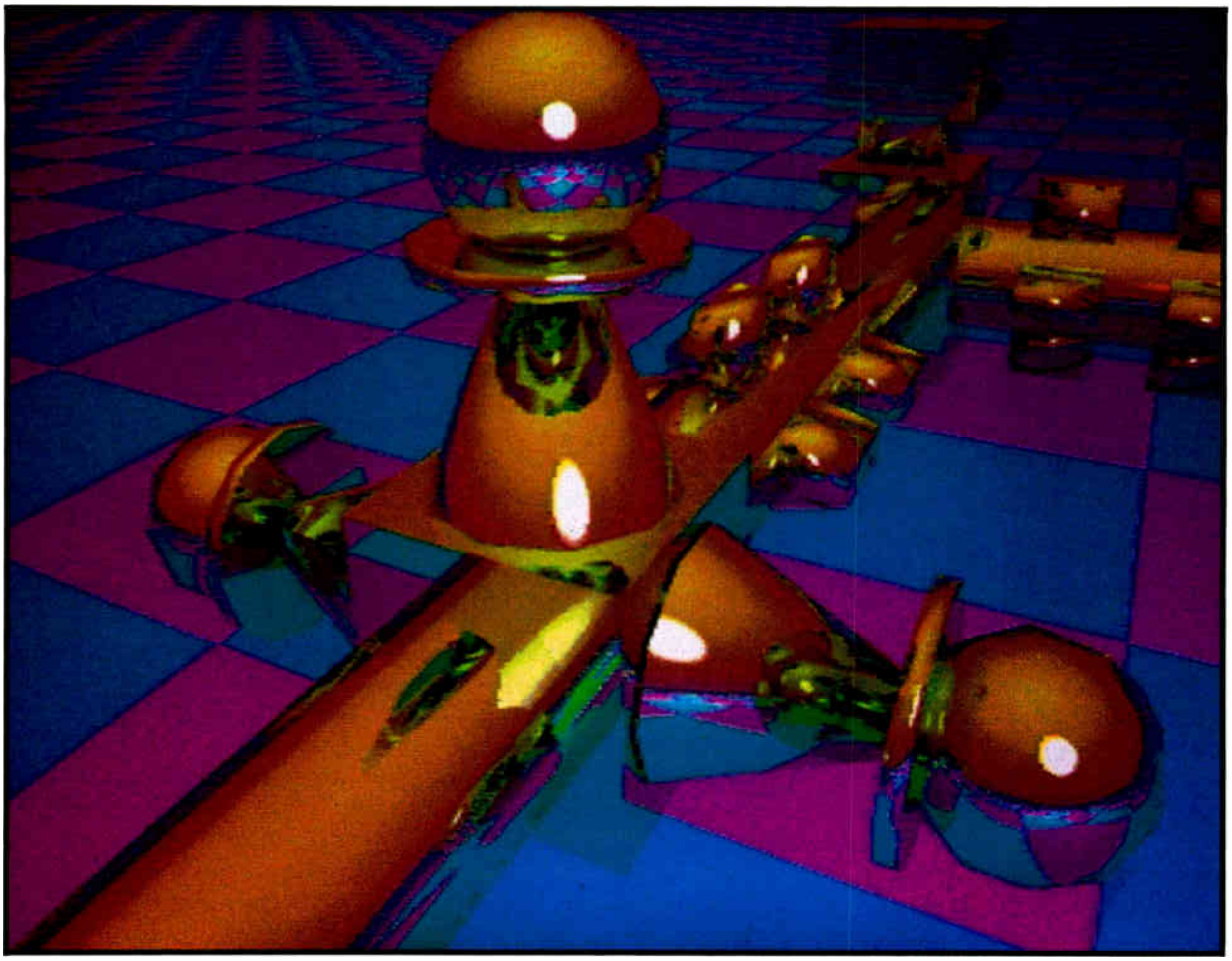

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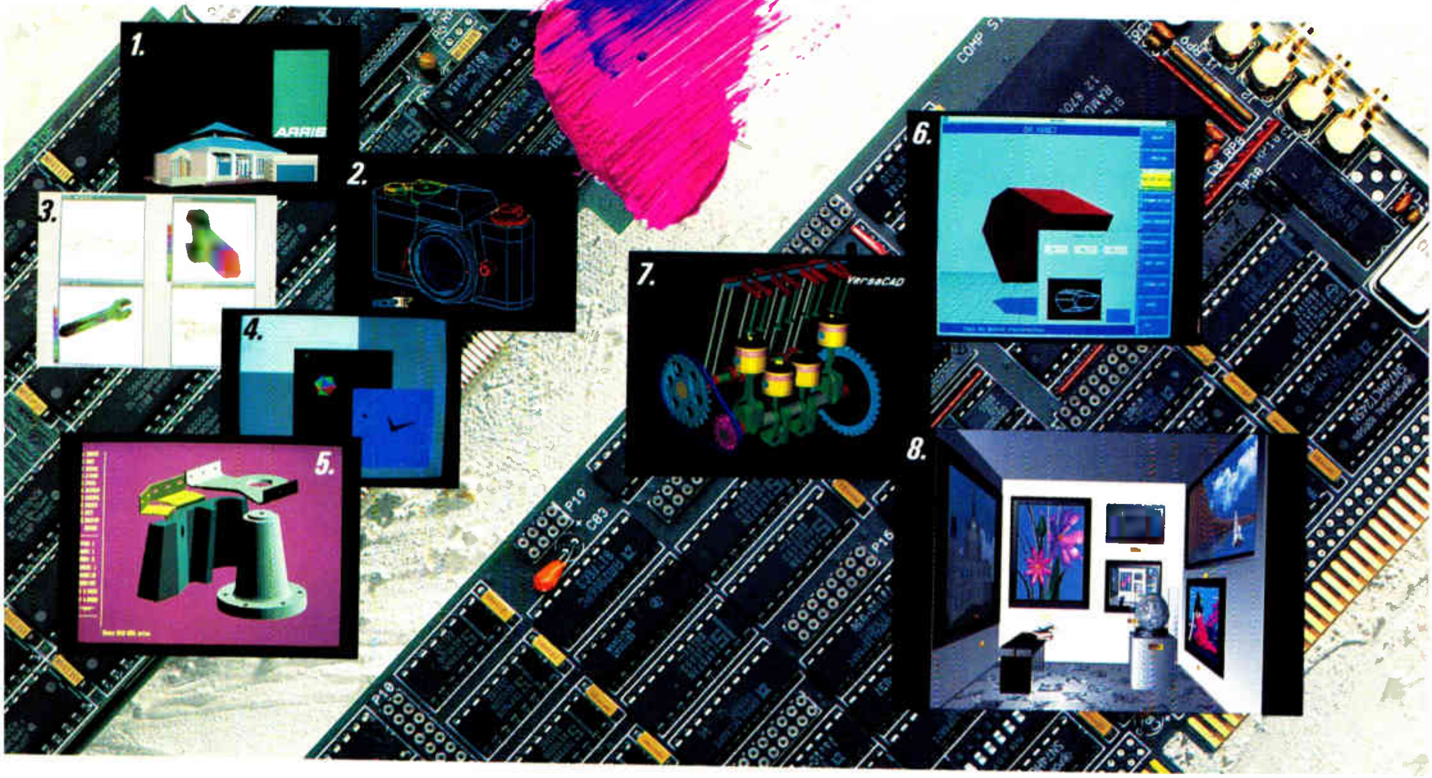

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BYTE

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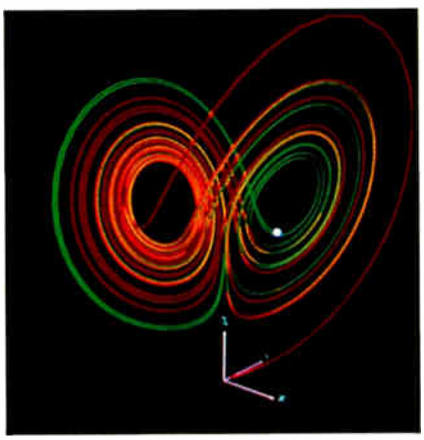

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Graphics Supplement • BYTE 251

A Brief History of Graphics Standards

Graphics standards have been a key element of computer graphics for over a decade. Like all standardization efforts, graphics standards come about because vendors and users realize that their industry is hampered by a profusion of incompatible hardware and software systems. Standards promote portability and device independence for applications software, important qualities in the fast-changing computer industry.

The earliest proposals for graphics standards, Core (1977) and GKS (Graphical Kernel System, 1978), were developed to solve the need for two-dimensional output. They dealt with primitives such as lines, markers, and polygons filled with crosshatch patterns or color. They also had reasonably extensive text capabilities and handled interactive input from several devices.

More recently, the need to produce three-dimensional drawings has been addressed by a standard called PHIGS (Programmer's Hierarchical Graphics System, 1984). This standard addressed three-dimensional lines, markers, polygons, and text, and added the notion of a hierarchical model, which allowed users to manipulate the graphical database (e.g., rotate it in three dimensions, modify the colors, and so on) without having to respecify the entire database from scratch.

Associated with each of these graphics standards has been a series of related proposals to extend their functionality to new situations. GKS spawned CGI (Computer Graphics Interface), a standard for graphics devices that would implement GKS efficiently; CGM (Computer Graphics Metafile), a standard for archiving graphical descriptions in a file; and GKS-3D, an extension of GKS to three dimensions that rivaled PHIGS. PHIGS has recently spawned PHIGS+, an extension to include hidden-line and hidden-surface elimination and simple light sources.

Each standard has been developed through the deliberations of a number of standards bodies such as ACM SIGGRAPH, ANSI, and the International Standards Organization. The trip through the standards pipeline is long—GKS was finally approved as an international standard in 1984, and a C language binding (a formal list of the official C subroutine calls) was finally approved last year.

Graphics standards can also result from industry acceptance of a clean and powerful interface proposed by a single company or group of cooperating companies. De facto standards such as PLOT-10 (a two-dimensional plotting package), X (a window management package), and PostScript (Adobe's page-layout language) have emerged in this way. De facto standards are developed and refined more quickly than official standards, particularly since there is a guarantee that a conforming implementation is available; however, the fact that they are not official sometimes limits their acceptance among competing companies.

permit you to give the renderer a pointer to a subroutine that will expand simple objects into more complicated ones, such as converting a triangle into a fractal mountain or a sphere into a particle system explosion. Using procedural primitives, the modeler can download a very complex model, such as a fractal, to the renderer in a carefully controlled way that sends only the required amount of detail through the interface.

No three-dimensional rendering interface would be complete without hierarchical modeling, and of course RenderMan supports it. There is a full set of transformation operations (e.g., rotate, translate, scale, and skew), which can be pushed onto, and popped from, a hierarchical transformation stack. This permits modelers to easily define articulated models (e.g., a robot), where the position of one piece depends upon the position of another piece further along. Constructive solid geometry, a technique for defining objects in terms of the sums and differences of solids (e.g., a solid block with a cylindrical hole cut out of it), is also fully supported.

Shading Information

RenderMan has a large set of routines for defining shading information. You can set both the color and the opacity of objects, and you can specify colors not

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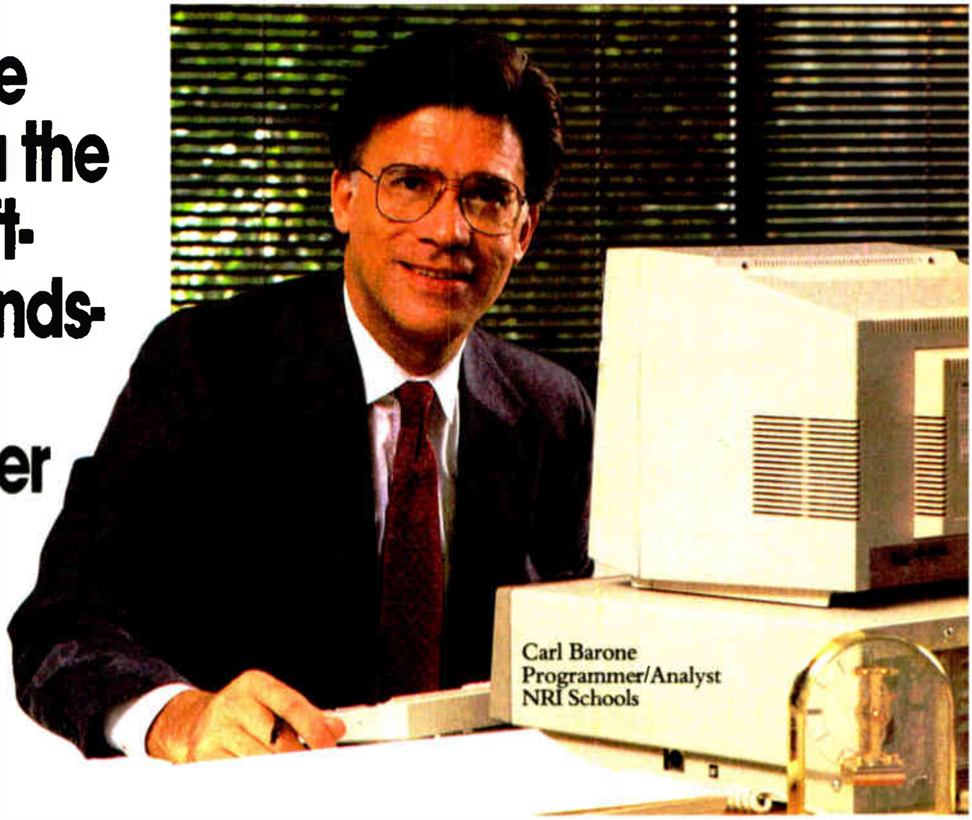

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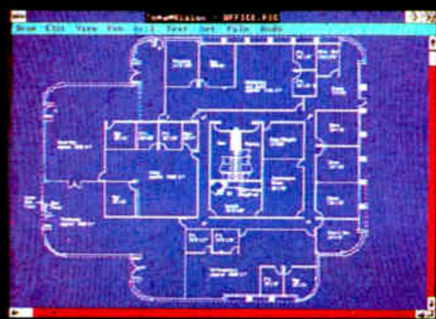

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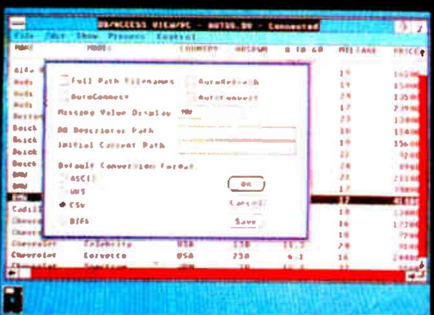

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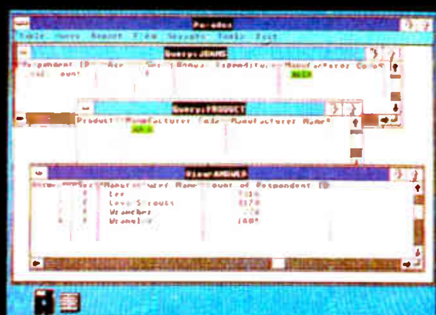

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merely in the RGB coordinates typically used in computer graphics, but also in multichannel spectral colors if the need arises. The use of spectral color permits renderers to more carefully compute the ways in which some objects reflect and refract light at different wavelengths (e.g., a prism).

One of the most important features of the RenderMan interface is its strong support for user-definable material characteristics. You can define up to four separate shading language programs that provide different material characteristic information about each object: a *surface shader*, which determines what color you see when light reflects off the surface; a *displacement shader*, which can move the surface by small amounts to add dents or fillets that are too small or too complex to model geometrically; a *light shader*, which describes how luminous objects emit light; and a *volume shader*, which describes how light is attenuated as it passes through the interior of a translucent object.

While this may seem complicated, it's actually a straightforward way to think about the material properties of objects, particularly once you've seen these shaders in action.

Special Effects

You specify the camera position and orientation using the same hierarchical transformations that describe the positions of all the other objects in the scene. RenderMan lets you specify other parameters of the simulated camera, as well, in order to provide information to renderers that support advanced rendering features.

For example, you can set the shutter time as well as the focal length, focal distance, and f-stop of the camera to simulate motion blur and depth of field. Photography buffs will recognize motion blur as the effect that occurs when a fast object moves across the camera's field of view during the interval that the shutter is open (see photo 2).

RenderMan lets you specify the positions, shapes, and colors of the objects at multiple times during the shutter interval. This is so that sophisticated renderers that can simulate motion blur will know how the objects are moving.

High-quality rendering requires a lot of attention to the sampling and filtering that are performed on the output pixels in order to avoid *aliasing*—the "jaggies." RenderMan offers independent control over the number of shading samples per pixel and the number of hidden-surface samples per pixel, as well as the size and shape of the pixel filter function.

Besides the standard display parameters of output image name, device type, and resolution, RenderMan supports gamma correction and exposure control. These functions compensate for the tendency of a monitor's phosphors to glow with exponentially increasing brightness as voltage increases linearly. RenderMan also contains the new concept of an *image shader*, another shading language program that lets you implement various color manipulations on final pixels just before they are put into the frame buffer or file.

Shading Language

I've mentioned the RenderMan shading language several times. Now I want to take a close look at it. Most renderers have a subroutine that determines the color of the surface of an object. Typically, this subroutine implements a single

continued

mathematical equation that uses a simple model of the reflection of light to calculate the contributions of the light sources and texture maps to the surface color.

The equation often has many parameters (5 to 20, depending on the renderer) that you tweak to control the appearance of different kinds of materials (e.g., plastic, metal, and chalk). Very often, however, you want the surface to have some characteristic you can't achieve with the fixed equation, and you want to use a texture map to modify some parameter that the implementers didn't think you would want to modify. What can you do?

If you are fortunate enough to have the source code, you can add your function and recompile. If not, you are out of luck. RenderMan changes this situation. It provides the shading language, a C-like programming language that has new functions and data types designed to calculate colors based on geometric information. Programs that you write in the shading language are typically small (10 to 20 lines) and are loaded into the renderer at run time when they are requested by some part of the scene geometry.

These programs then replace the built-in shading equations. You can use this language to customize shading on a per-object basis. This new freedom gives you the power to model the appearance of objects as carefully as you model their shapes.

The shading language supports three basic data types, the `float`, the `point`, and the `color`. `Point` and `color` are abstract data types that are actually vectors of length three (`color` can have more than three components when spectral color is enabled). The standard C arithmetic operators (*, +, /, etc.) work on these data types. In addition, there are some new operators for vector dot and cross product.

The familiar C conditional and looping constructs are available (except `switch`), as are subroutine definitions and calls. There is a rich library of mathematical functions, as well as a library of functions that implement common shading operations such as normalizing vectors, transforming points between coordinate systems, calculating diffuse and specular lighting, interpolating colors, splining, and calculating pseudorandom numbers.

The renderer calls the appropriate shading language program (the shader) every time it requires a light intensity, surface color, and so on. When a shader is called, it has access to a large number of global variables that the renderer provides. These variables include all the geometric information that the renderer knows about the surface being shaded, such as the position `P`; the surface normal `N`; the color `Cs` and opacity `Os` that you specified; the texture coordinates `s,t`; and others. The variables that you applied to the vertices of your primitives are also available inside the shaders.

Each type of shader accomplishes its specified task by calculating and modifying a specific part of this global state. For example, a surface shader is responsible for calculating and setting `Ci`, the color that the eye sees. A light shader is responsible for setting `Cl`, the light color.

Listing 1 is an example of a basic surface shader. Using a simple equation, this shader calculates the reflectivity of a metallic object. It uses the standard library functions `ambient`, `diffuse`, and `specular` to determine the amount of light arriving on the surface from the light sources.

Listing 1: *A simple shader that simulates the reflection of light off metallic objects.*

```
surface metallic (float Ka=.4, Kd=.4, Ks=.6,
          roughness=.25;){
      N=faceforward(normalize(N));
      Ci=Cs * (Ka*ambient() + Kd*diffuse(N)
          + Ks*specular(N, -normalize(I), roughness) );
}
```

Listing 2: *A shader that simulates wire mesh by varying the opacity of the object.*

```
surface wire_mesh (float Ka=.4, Kd=.4, Ks=.6,
                  roughness=.25, nu=2.0, nv=2.0;)
{
  if (mod(u*nu, 1.0) > 0.5 && mod(v*nv, 1.0) > 0.5){
    Oi=0.0;                     /* Transparent! */
       } else {
    Oi=1.0;                     /* Opaque metal! */
    N=faceforward(normalize(N));
    Ci=Cs * (Ka*ambient() + Kd*diffuse(N)
          + Ks*specular(N, -normalize(I), roughness));
    }
}
```

Listing 3: *A shader that simulates dents by moving the surface a small amount. This adds visual complexity that is very difficult to model convincingly using standard geometric modeling techniques.*

```
displacement dent (float scale=1.0;)
{
    float size=1.0, displace=0.0;
      for (i=0; i<6; i+=1.0) {
      /* Calculate a simple fractal 1/f noise function */
      displace += abs(.5 - noise(P * size)) / size;
    size *= 2.0;
    }
    /* Displace the surface and recalculate surface normals */
    P += N * pow(displace,3.0) * scale;
    N = calculatenormals(P);
}
```

These functions implement three customary equations based on the direction and strength of the incoming light. If those functions are not appropriate, the surface shader has access to the lights and can calculate whatever values it pleases from them.

The shader then calculates a weighted average of the incoming light intensities and multiplies by the color of the object. Notice also that the shading language automatically takes care of the multiplication of float values by color vectors, freeing you from having to write the ugly loops that would be necessary in most other languages.

The type of shader (in this example, `surface`) indicates its intended function. Parameters to the shader are specified using a syntax similar to that of ANSI C. This shader demonstrates another unique feature of the shading language, the presence of default values in the parameter list. When a modeler requests this shader, it specifies by name the parameters it wants to override. Any parameter not mentioned is left with the default value.

You can examine a slightly more complicated shading language function in listing 2. Every geometric primitive has a two-dimensional surface, and renderers typically define a simple coordinate system (known as *parametric coordinates*) on this surface in order to apply texture maps and do similar functions that need to vary along the surface. This shading language function uses a bit of tricky arithmetic on the parametric coor-

continued

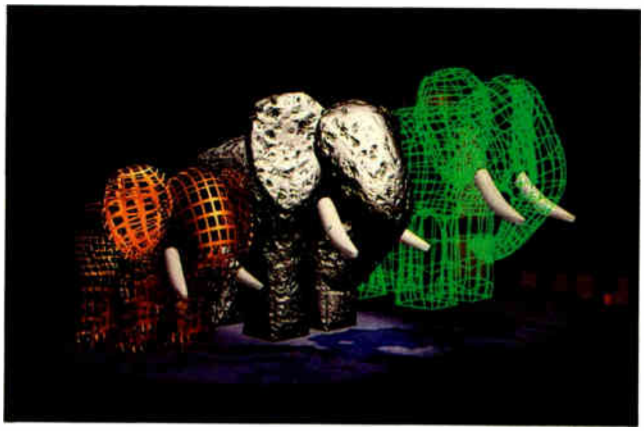

Photo 3: *The lead toy demonstrates the metallic and dent shaders, the golden toy demonstrates the wire-mesh shader, and the large toy uses a similar shader reminiscent of wire-frame drawings.*

dinates u and v to decide whether to make the surface opaque or transparent at each point. This has the visual appearance of cutting a hole in the object.

Listing 3 illustrates a displacement shader. A displacement shader moves the position of the surface around a little bit to simulate tiny fillets, dents, and other minor surface perturbations. This adds to the visual interest of an object and makes it look more realistic. This particular shader calculates a fractal dentedness using several iterations of *noise*, a function that produces a semirandom value that changes slowly over the surface of the object (using a purely random value would distort the surface beyond recognition, since adjacent points would have no relationship to each other).

Photo 3 shows the effect of using these shaders on a simple model of a toy. Achieving the same effect by trying to model the individual metal bands or the intricate surface dents would be extremely difficult.

You can see the value of complex shading using texture maps and nontrivial shading-language shaders by comparing photos 4 and 5. Photo 4 is a model of the inside of an office, generated using the AutoCAD modeling system and a simple renderer.

Photo 5 is exactly the same geometry file, but the objects were given custom material characteristics using the shading language: There are shadows; several objects have texture maps; there are reflections in the waxed floor and bookshelf; there are subtle displacements implementing fillets on the chair and books. Each of these shaders is a few lines long, and the difference in image quality is astounding.

Photo 4: *An office floor-plan model with simple shading. (Data courtesy of AutoDesk, Inc.)*

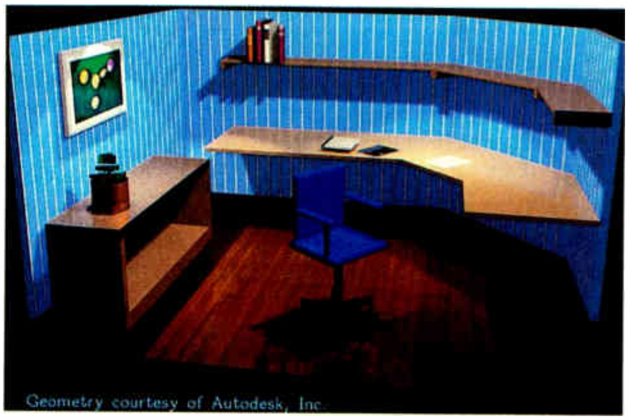

Photo 5: *The same office floor-plan model with custom shaders, textures, and shadows.*

Wrapping It Up

RenderMan is a powerful interface between three-dimensional modeling systems and photorealistic rendering systems. It represents the first graphics interface to deal with issues in high-quality synthetic-image generation such as antialiasing, texture mapping, motion blur, shadows, spectral color models, and programmable shading languages.

These advanced features are not available on most of the rendering software and hardware currently available. Thus, RenderMan represents a goal for sophisticated new graphics hardware and rendering software to shoot for.

Users of graphics workstations and personal computers will be the biggest winners, as photorealism becomes inexpensive, commonplace, and compatible across a wide range of platforms.

Pixar publicly announced the RenderMan interface last May after over six months of industry review by computer graphics workstation manufacturers, software vendors, and sophisticated end users. At that time, 19 companies endorsed the specification as a common interface to high-quality computer graphics.

Many of these companies are working on products that adhere to the specification, and these products will probably be announced in the next six to 12 months. Once these products appear, it will only be a matter of time before everyone has photorealism on the desktop. ■

Editor's note: *Copies of the RenderMan Interface version 3.0 are available from Pixar, Inc., 3240 Kerner Blvd., San Rafael, CA, 94901. Please enclose $15 to cover the cost of printing and mailing.*

Tony Apodaca, a software engineer at Pixar, Inc. (San Rafael, CA), has a master's degree in computer and systems engineering from RPI. He can be contacted on BIX c/o "editors."

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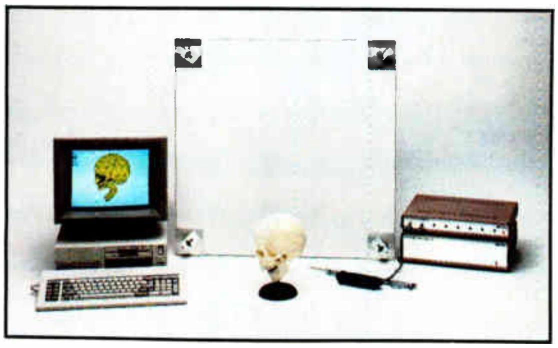

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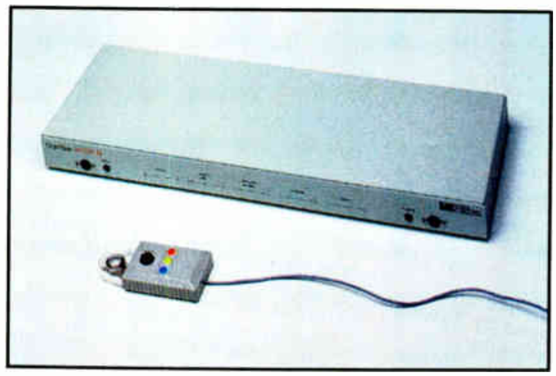

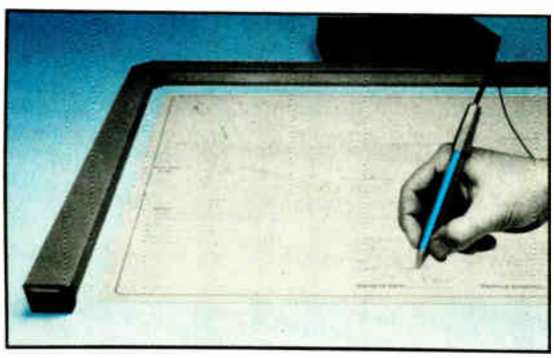

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Interacting with the Tiny and the Immense

Craig Mundie

Scientific visualization lets researchers see the unseeable

An important part of scientists' work is to think of ways to represent information so that they and others can understand what's going on. Visual images help scientists develop and share their insights and theories with colleagues, by providing an effective way to pass along the mental images that are the basis of their thoughts. Computer graphics can facilitate the transfer of research results from one scientist to another by generating precise graphical representations to illustrate important points. When the relatively new capability of animation is applied to graphical representations, more insight into the data can be attained.

In this article, I'll describe some of the philosophy and techniques behind scientific visualization and discuss where it's heading. I have included photos that demonstrate the results of scientists' work in several areas.

The Importance of Visualization

Scientists and engineers have long understood the importance of pictures in perceiving and communicating the functional relationships in quantitative data. Graphical devices, such as contour plots, structural diagrams, or ball-and-stick models, have long been familiar tools in sciences such as physics, chemistry, and cartography. And since the early 1960s, scientists have used computers to perform calculations and manipulate the resulting data for output in graphical form on plotters and display terminals. Computers greatly expanded the volume of data that could be handled, and graphical displays eased the chore of understanding data by presenting relationships visually.

The human being's ability to comprehend pictures stems from the fact that over half the brain's neurons are used to process and understand visual input and that the brain's ability to take in this information is enhanced by a visual input data channel with a bandwidth estimated to be about 2 gigabits per second.

Higher-resolution graphics display terminals now let scientists view data in three dimensions rather than two, extending their capability to understand the data. Three-dimensional color representation provides a global picture of the data—one displayed image can represent 10 million numbers. This gives scientists the ability to see simultaneously all the information that otherwise might have to be printed on reams of paper. This substantially cuts the amount of time required to discover whether or not the data represents a sensible result.

But maximum individual productivity will not be attained until researchers and scientists are able to control not only the display of the images generated but also the computation of data that is used to generate the pictures. The goal of scientific visualization is to give researchers the ability not only to view pictorial representations of the data as they are generated but to promote insight into the meaning of the data by allowing researchers to modify the computational process as it is occurring.

Extending the Capabilities

Scientific visualization extends the capabilities of computer graphics technology by giving researchers better control of the process by providing added levels of functionality. Scientific visualization, the conversion of massive amounts of data into pictures that show the results of computations, moves beyond ordinary simulation techniques. It can be an animated, interactive process in which scientists can manipulate data and, in real time, see the results of their changes. Under the umbrella of scientific visualization, the previously discrete disciplines of computer graphics, image processing, computer vision, CAD, signal processing, and user interface technology are applied to scientific research.

While the term scientific visualization simply refers to the use of images to interpret scientific data, its most important function is to provide researchers with the means to *interact* with the graphics in such a way that real-time interpretation of data is possible. True interactive visualization offers scienctists impressive advantages that include better analysis, improved productivity, and cost savings.

Full-function scientific visualization is now available primarily to groups of researchers who work at supercomputer installations. (See the text box "PCs and Scientific Visualization" on page 280 for a look at the personal computer's future in visualization.) To make the technology more widely available throughout the scientific community, the National Science Foundation is urging implementation of a federally funded initiative in the area. An NSF Panel on Graphics Image Processing and Workstations report released in late 1987 on "Visualization in Scientific Computing" encourages the formation of interdisciplinary teams with a mix of skills to produce visualization tools that can be shared among diverse research areas.

The NSF panel and its recommended initiative are the results of many factors. Spurring the effort is increased recognition that the volume of scientific data being produced is overwhelming the ability of researchers to comprehend it. Coupled with this backlog of uninterpreted data is the recent introduction of advanced graphics technologies that promise to better handle vast amounts of visual data as well as the recognition that even more

continued

PCs and Scientific Visualization

The evolution of computing sheds light on the role that personal computers will take in scientific research in the future. These advances portend a day when an intimate partnership will exist between desktop computers and high-capacity machines. This partnership will extend the benefits of scientific visualization to more and more researchers by allowing personal computers to serve as viewports into the data generated by supercomputers. The large and growing numbers of personal computers within organizations have given individuals necessary computing resources, but the organizations themselves often no longer can access the peak computational capacity that they require.

This situation is analogous to equipping 100 astronomers charged with studying a distant star with 100 pairs of binoculars instead of providing them with a single large-aperture telescope. Just as even the most powerful binoculars cannot gather much useful data on the star, the most powerful desktop computer is fundamentally the wrong instrument for some applications.

The Right Resource at the Right Time

To escape this dilemma, all computer users must be provided access to whatever level of resource they need on a demand basis, with the personal computer serving as the access point. The ability to bring this model to reality hinges on the evolution of a human-to-machine interface that is adequate to support transparent escalation of computing capability. Until it arrives, users are stuck with either big computers that have poor human interfaces or user-friendly personal computers that lack the capacity to solve really big problems.

A harbinger of what is to come for personal computers with future technology is today's multimedia workstation, which has the ability to interface to more extensive sources of data and information than can be generated locally or outside the scope of traditional alphanumeric or simple graphics displays. The impact of multimedia workstations is now seen particularly in training areas where it is possible to interact with the pictures accessed from compact disks or VCRs and displayed on the screens of personal computers. It is also seen in the image management systems that are able to store, manipulate, and interact with image data.

To make visualization more accessible, the capability of both the server computer that generates the pictures and the desktop client computer that displays them must be extended. Also, today's networking technology must evolve to complementary networks that are better suited to dealing with images and pictures.

The ability to interact with pictures across heterogeneous machines is necessary not only in the network, but more importantly in the hierarchy of the computing environment. It will extend into the server and potentially into other special-purpose machines, be they larger supercomputers or special-purpose architectures that provide other particular services in the environment.

massive amounts of data will be generated as new supercomputers emerge.

The problems of examining or assimilating the vast and growing quantities of numerical data result from increasing use of computer-based data acquisition techniques in fields like medicine. Medical diagnostic imaging, for example, generates massive amounts of data. Until recently this data was captured and used in photographic form, but it is becoming increasingly common to acquire it in digital form and to process it for review on visual display terminals.

Supercomputers are producing vast amounts of data for researchers in natural sciences like geology and astronomy and in physical sciences like chemistry and physics. As each new generation of supercomputer provides the capacity to handle larger and larger problems at far greater speeds, and as powerful "mini-supercomputers" act as computing resources to increasing numbers of researchers, more and more experimental data will be produced in each of the scientific disciplines. And orbiting satellites continue to send streams of data from space to earth, vastly increasing the amount of geophysical and astrophysical data available to researchers and analysts. But facilities for its examination and use are being tasked to the limit by the vast data sets.

As the volumes of available data expand, researchers are increasingly hampered in fully understanding it. For example, much of the data being acquired from satellites orbiting earth is merely stored on tapes and sent to government warehouses because facilities for interpreting and understanding the data are simply not available. To prevent further waste of acquired data, it has become essential that researchers and scientists find more efficient ways to process it.

Today's Visual Applications

Ideally, scientists want to be able to compute phenomena over a time span, create a series of images that illustrate the interrelationships of various parameters at specific time periods, and analyze these images at a workstation. Although this ideal has been achieved only at certain select locations, many scientists and researchers have put visualization techniques to use in their work.

Of all applications, molecular modeling makes the best and fullest use of the interactive three-dimensional technology available today. For years, research scientists have been drawing diagrams and building molecular models to gain insight into the way that molecules' structure influences their properties. But Styrofoam and wire only vaguely represented the real thing. The space that molecules occupy is not solid but consists of clouds of electrons moving at the speed of light. And molecules are not static; as the energy level of a molecule varies with time, its geometry changes.

Scientific visualization techniques have enabled researchers to analyze the forces generated by electron clouds and view animations of dynamic molecular sequences. The knowledge gained has been used to develop laundry soaps and shampoos; lighter, stronger materials for aircraft; and effective pharmaceuticals.

When used in designing drugs to combat specific diseases, visualization makes the process more rational than previous experimental methods. It allows researchers to analyze a compound's electronic charges and the three-dimensional shape of its molecular structure before the drug is fabricated in the laboratory to determine if it will be effective against a disease. Researchers can display the disease on a host molecule and pinpoint the active site, where the disease-causing agent attaches to healthy molecules. Having determined the shape

continued

end-user scientists and computer scientist computing center managers must be overcome to provide the scientific community with access to visualization technology. The teams being formed to implement visualization technology, such as the University of Illinois National Center for Supercomputing Applications group, are designed to accomplish this by bringing together scientists and graphics experts. A common understanding of the diverse problems faced on either side will provide the knowledge and sensitivity needed to overcome difficulties and will result in tools that can be effectively used by researchers in many disciplines.

Ideally, scientists want to be able to compute data, generate sequences of images, and analyze these images at a workstation. However, workstations that are capable of providing high-performance, high-resolution graphics often are priced outside the researchers' budgets. But many scientific problems can be analyzed interactively without high-quality three-dimensional graphics. Scientists don't always require dazzling presentations to benefit from visualization techniques. They need very high-quality visualization to present or publish known information, lesser quality to share information with colleagues, and a third type of visualization to study phenomena by themselves or with collaborators.

While presentation images require the use of powerful equipment and the assistance of visualization experts, when graphics are used to share scientific discoveries among peers, the primary requirement is for clear visual representation of information. This can be provided by equipment as simple and inexpensive as a video recorder in a graphics workstation.

Personal graphics for scientific analysis is the most important of the three types of visualization because it results in the greatest productivity. This is where scientists need to interact with their data, observe phenomena in close to real time, steer their computations, and have the effects displayed immediately on the screen in graphical format. The emphasis is on data representation and interactivity, not on quality. It is at this level that next-generation personal computers will take a more active part in the scientific process.

Already providing high-resolution color graphics capabilities, next-generation personal computers will also offer higher levels of computational performance, better communications capabilities to speed data transfer, and a better-designed human interface to encourage widespread use by researchers. The price levels at which these tools will be made available will enable even the most cost-conscious project director to put them on every researcher's desktop. ■

Photo 3: *Tornadic storm. This is the moment at which a storm becomes a tornado. (Research by Bob Wilhemson, research scientist at NASA; visualization by Stefan Fangmeier.)*

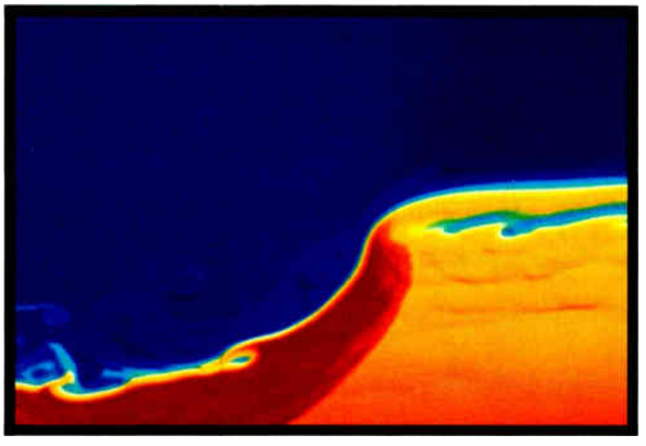

Photo 4: *Formation of Venus plasma clouds and streamers. (Solar wind research by Robert Wolf and Michael Norman, astronomers at the University of Illinois; visualization by Stefan Fangmeier and Matthew Arrot.)*

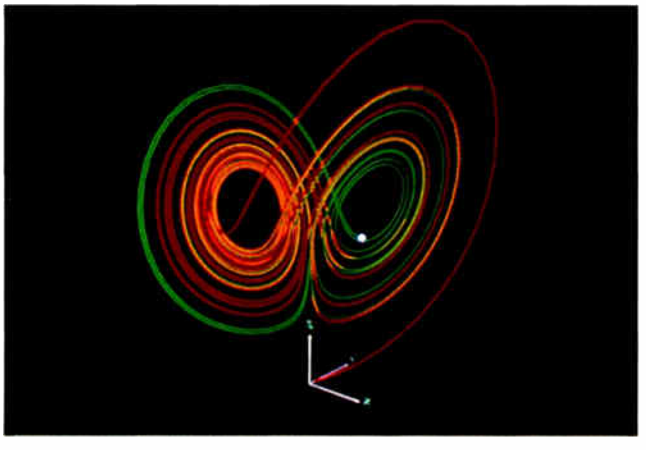

Photo 5: *A visualization of the first system of equations in which the "chaos" phenomenon was discovered. (Research: David Hobill, Dan Simkins, and Michael Welge, University of Illinois; visualization: Jeffery Yost.)*

ACKNOWLEDGMENTS

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Craig Mundie is vice president of R&D for Alliant Computer Systems Corp., Littleton, Massachusetts, a maker of supercomputer visualization systems. He can be reached on BIX c/o "editors."

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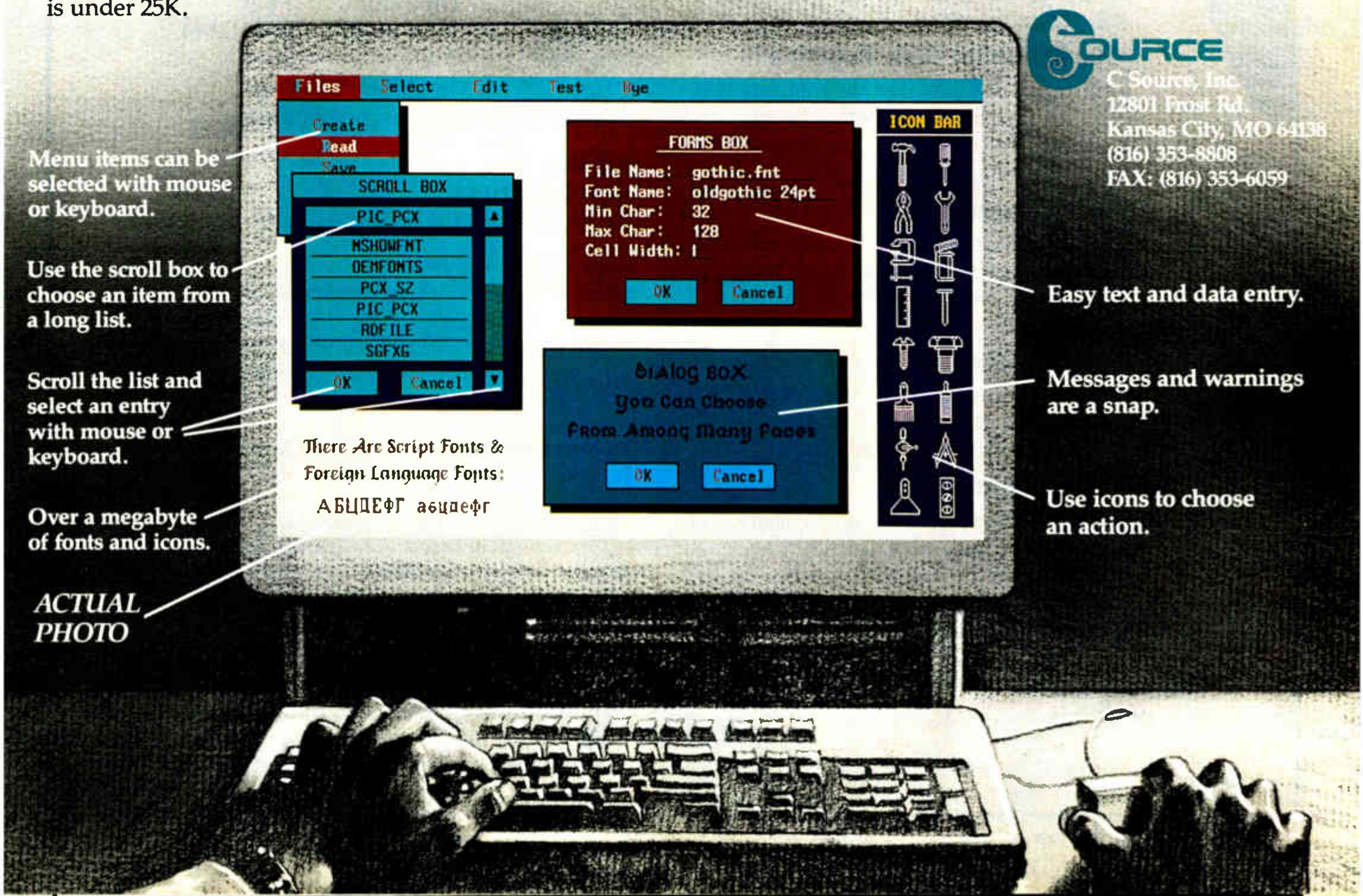

THE TRON PROJECT

An open architecture, a family of VLSI chips, and system software designed to revolutionize the way we use computers

Ken Sakamura and Richard Sprague

Computers are now used in every product imaginable, from household appliances to automobiles to calculators, yet there are no standards for letting those computers communicate with each other. A standardization effort called TRON (The Real-Time Operating System Nucleus) aims to establish a set of common data-interchange standards that will make it easy for all computers to communicate in real time.

The central philosophy of the TRON project is that computers will become more distributed and their uses more varied, and that the overall power of computers will increase if they can work together. There are few standards for communications between PCs and mainframes and among multiple character sets.

Another issue that has been largely ignored involves standards for real-time, as well as multiprocessing, communications between devices. The user feels these problems, too: Incompatible interfaces between systems make it difficult to imagine different computers as part of a single world network.

TRON is an ambitious, broad-based response to these issues and has already received the participation of many of the major Japanese computer companies and leading semiconductor makers, as well as a number of U.S. and European software and hardware companies. The name TRON applies to the development of the whole concept, not a specific product. TRON encompasses the development of an open architecture, a family of VLSI chips, and system software.

TRON's designers consider an open-architecture philosophy essential and believe it is the most important factor in its receiving wide adoption by manufacturers. For this reason, all TRON specifications are available for adoption by anyone, free of charge. TRON standardization is coordinated by a nonprofit organization, the TRON Association, in cooperation with the University of Tokyo.

The three most important aspects of the TRON project are the concept of highly functional distributed systems, operating system software on the TRON PC, and the TRON CPU.

Highly Functional Distributed Systems

Until the popularization of LANs and other means for easy exchange of data among desktop computers, PCs were largely kept independent of one another. A primitive means of PC-to-PC communication existed—writing data to a floppy disk, and hand-carrying that disk to another computer. But in general, documents developed on one machine were not typically used by other machines.

Exactly the same situation now exists for computers used in embedded systems, such as the microprocessors that are now standard components of everything from microwave ovens to VCRs. Primitive means of data-sharing sometimes exist, but only on a case-by-case basis using specialized products. For example, some advanced calculators offer add-on interfaces that let you exchange ASCII data with the IBM PC, and crude devices exist for communication with VCRs. But there is nothing comparable to the easy connectivity among desktop computers now available with LANs.

TRON's essential goal is to bring the concept of networking to all computers, including those used internally in consumer products. IBM's Systems Application Architecture (SAA) has a similar purpose, but its scope is limited to traditional computer systems. The TRON operating system, however, defines multiple application-specific architectures covering every domain in which computers are used. These areas are

- ITRON, for embedded industrial systems;
- BTRON, for business-oriented workstations;
- CTRON (central TRON), for large file servers in networking environments;
- MTRON (macro TRON), for interconnecting "intelligent objects" and super personal computers or workstations.

Each of the TRON subfamilies is designed to be compatible with the others (see figure 1). A good analogy of how these families work together is the open system interconnection, or

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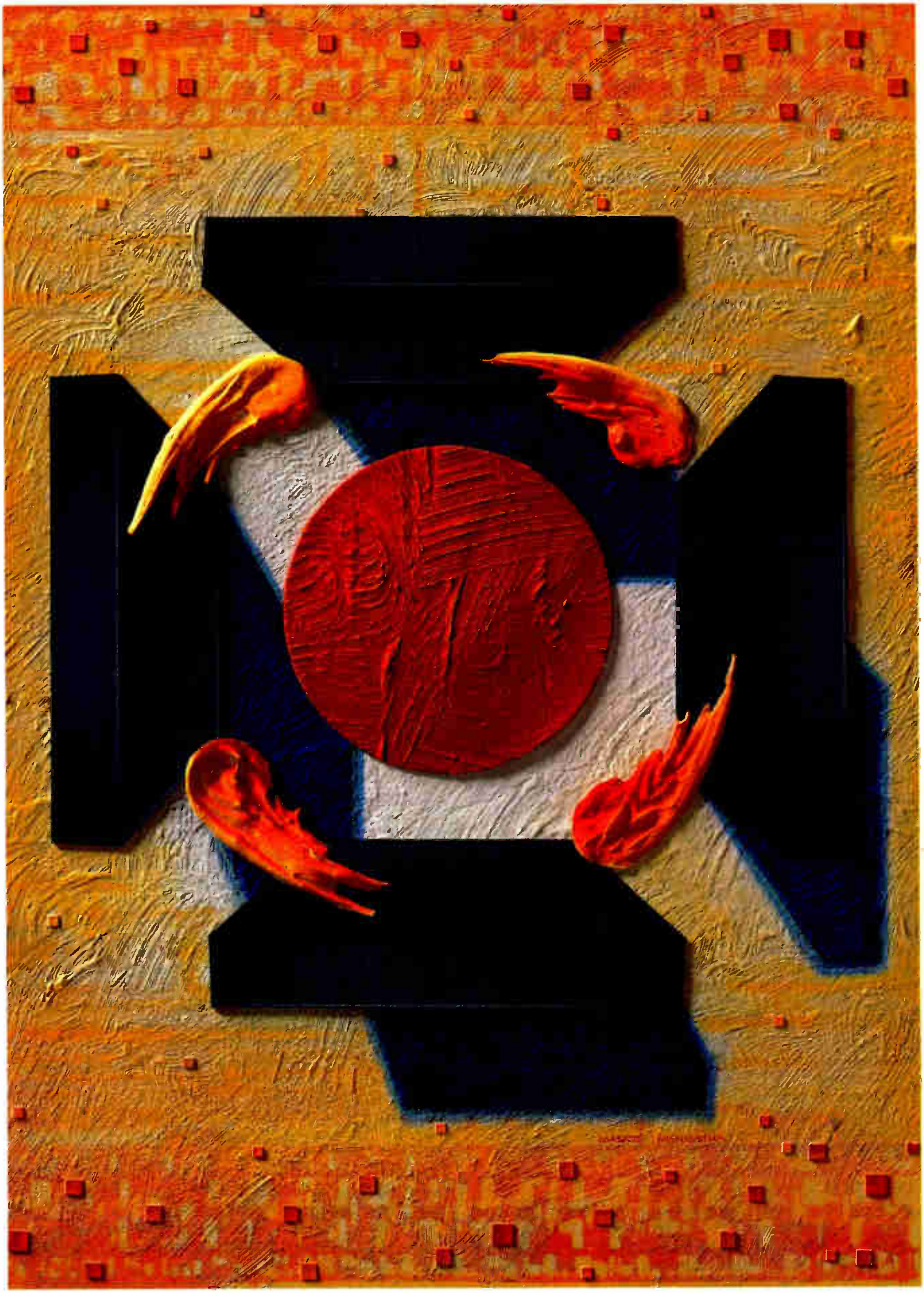

ILLUSTRATION: MASATO NISHIMURA © 1989

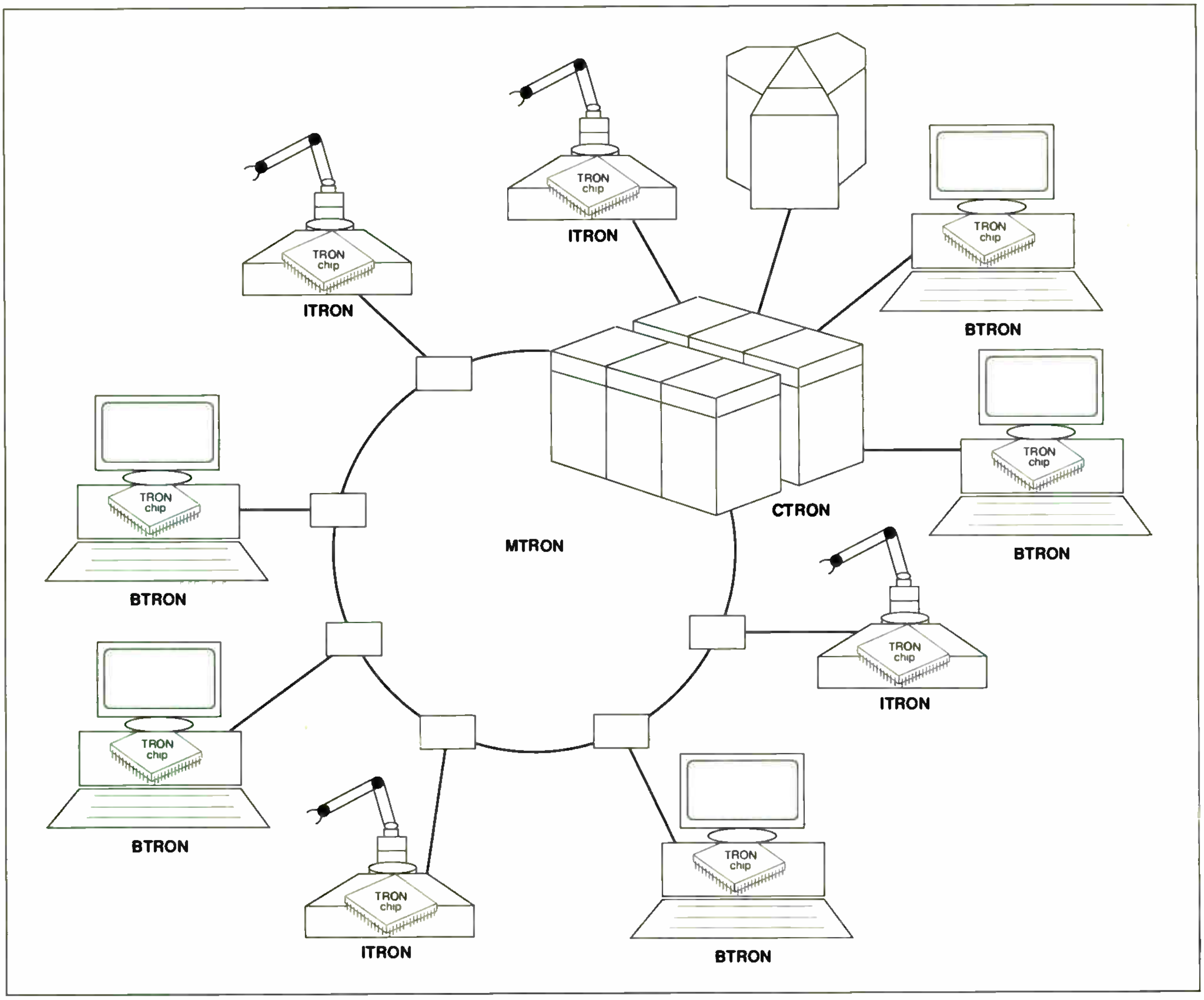

Figure 1: *In the TRON world, BTRON workstations can communicate with ITRON industrial computers and share data with a mainframe through a CTRON file server. The network linking these and other "intelligent objects" together is MTRON.*

OSI model, well-known in the data communications industry.

Like the OSI model, the TRON project is divided into layers: an instruction-set-processor layer, an operating-system kernel layer (which is separated into the I/B/C/MTRON architectures), the operating-system shell layer, and an application and human/machine interface layer. Also like the OSI model, work on the individual TRON layers can proceed independently of work on the others, with confidence that the resulting total TRON architecture will fit together.

The four separate operating-system kernels are designed to satisfy the broad categories of applications that will need interconnection by computers of the future. The four are used independently, but they are designed to easily exchange data.

The ITRON and CTRON Kernels

In the industrial field, microprocessors use many real-time operating systems. None of these operating systems has distinguished itself as a standard in the way Unix has become a de facto standard in the field of software development. ITRON is an attempt to create such a real-time standard.

Industrial applications require fast response time to external signals, and the ITRON specification has been designed to permit it. Using a two-level approach to standardization, ITRON tries to achieve rapid response time with a very low overhead.

On the upper level, the TRON project defines a machine-independent logical interface, which includes universal aspects of real-time systems such as intertask communication. The second level is a CPU- or architecture-dependent part, which determines the performance of real-time systems.

The ITRON specification includes a wide variety of system calls, many of which can be removed from the kernel to improve the performance of a specific application. For example, although ITRON specifies task synchronization primitives for semaphores, event flags, mailboxes, and others, an application that uses only semaphores may legally remove all the other synchronization primitives from the kernel.

CTRON is the specification for a multiuser operating system that works with machines and networks linked with ITRON and BTRON. It is designed for applications that require very large data bases and memory storage or extremely fast processing. CTRON is also oriented toward high-speed, high-quality printing, graphics, and voice processing.

The MTRON Kernel

In the future, microprocessors will be found in an ever greater variety of products. Walls of TRON houses will have processors that sense temperature and pressure variations and send the information to microprocessor-controlled doors and windows.

Massive numbers of processors, called *intelligent objects,* will require real-time control by, and compatibility with, other TRON computers. The smart network that links them together is called MTRON. An underlying programmable specification language called the TRON Universal Language System (TULS) makes it possible to design a set of standard communication protocols to coordinate all these intelligent objects.

The ultimate goal of the TRON project is to build a highly functional distributed system in which billions of intelligent objects can be connected to work cooperatively.

The BTRON Kernel

The TRON family that will ultimately be most important to the PC and workstation industry is BTRON, an open-architecture specification of computers meant for use as personal workstations by businesses and homes. People will want future business- and home-oriented computers to be easy to use, so the BTRON design pays careful attention to its user interface.

Users of window-based systems, like the Macintosh and Microsoft Windows, will be familiar with many of BTRON's user-interface design techniques. Another BTRON feature that will be especially appealing to the international market is the ability to deal with large character sets. BTRON imposes no constraints on the size of its character set.

Since BTRON machines require much more interaction with users than other TRON families, the user-interface specification is the most critical part of the design. BTRON relies heavily on bit-mapped graphics, and input can be done with either an ergonomically designed keyboard or a pointing device, a stylus that the TRON project designers consider superior to the mouse for most tasks (see photo 1).

New applications for computers based on the BTRON concept were created for users of individual computers networked with other computers and used as mediums for information exchange and presentation. BTRON defines several functions to make this condition possible.

The BTRON specification supports the processing of large numbers of different characters in as many languages as possible (if possible, all the characters used in the world). Unlike the 8-bit character-based operating systems of the past designed to handle the Roman alphabet only, the BTRON specification begins with a more generalized approach to language processing.

The BTRON specification allows both 1-byte and 2-byte codes to coexist. A 1-byte code permits the use of 2^8 (or 256) characters; a 2-byte code permits the use of 2^{16} (or 65,536) characters. This feature permits the storage efficiency of 8-bit codes for Roman-like languages, while enabling the elegant representation of large character sets for languages like Japanese. The operating system allows each language to choose its

continued

Photo 1: *The BTRON standard PC includes a flat-panel display, an ergonomic keyboard, and a stylus pointing device. Everything from the CPU instruction set to the user interface was designed as a unit.*

own algorithms for direction of writing, formatting rules, and input methods.

BTRON provides a common format for the representation of graphical data. The operating system includes hypertext-like features such as an outline processor and the ability to link documents in network fashion.

The BTRON Real-Object/Virtual-Object Model

A file system is the part of an operating system that provides for the storage, representation, and management of data. Most file systems consist of static ASCII data stored in files, which are grouped into directories. In Unix and MS-DOS, for example, directories are themselves files that can be created, deleted, and moved. BTRON's file system uses a very different model called the real-object/virtual-object model. This system is a set of specifications designed to efficiently handle data in the BTRON operating system.

BTRON stores a collection of data in a *real object* referenced by multiple tags called *virtual objects*. A real object, like a conventional document, can contain text and figures in any combination. Real objects can also contain virtual objects (i.e., pointers to other real objects), a feature that provides a hypertext-like ability to structure data into its semantic components.

Virtual objects ordinarily appear as rectangles on a bit-mapped display (see figure 2a). These rectangles can be manipulated by selecting them with a pointing device. When a virtual object is opened (see figure 2b), it displays the contents of the real object it points to (in this case, a bit-mapped graphical figure of a clock).

In the real-object/virtual-object model, a real object is made up of ordered variable-length records of data, called segments. There are four standard types: text, figures, virtual objects, and *fusen* (a Japanese word pronounced "foo-sen" meaning "a stick-on label").

Text and figures are primitive segments for information transfer, and BTRON requires that the main text of an object be readable. This fact, plus the ability to nest virtual objects inside real objects, provides a simple hypertext-like feature as a basic component of the operating system.

There are two different types of real objects, with the difference lying in how the segments are stored within the real object. In one type, called the one-dimensional real object (or "text" real object), segments are stored sequentially (see figure 3a). In the other type, called the two-dimensional real object (or "figure" real object), segments are arranged in a two-dimensional overlapped manner (see figure 3b).

Figure 4 summarizes the basic features of the real-object/virtual object model:

- Real-object data can be displayed in one or two dimensions.
- Viewports onto the real-object data can be nested.
- Real-object relationships are handled by a linked network of pointers.
- Objects are linked together by either virtual objects or opened virtual objects.

As with the Macintosh operating system, you can start a BTRON application by specifying a real object to be processed and letting the system determine the pertinent application. In this sense, the BTRON environment resembles the object-oriented systems that are being used on PCs and workstations.

In reality, however, the BTRON data model more closely resembles the conventional method of separating data and program because it also allows the opposite relationship: Different applications can be used for the same real object. This relationship is possible because a real object knows which applications can be invoked on itself. The knowledge is stored in the *function fusen* segment, which contains the parameters required for linkage to the application program.

With the basic editor, users can modify fusen and gain a variety of benefits, including the ability to specify default parameters and automatically invoke programs.

Efficient and free data compatibility between all computers is an important part of the TRON project. BTRON uses a model called the TAD (TRON application data bus) concept to provide for this data exchange. The purpose of the BTRON TAD protocol is to define the record structure corresponding to each segment in a real object. TRON applications should conform to the TAD protocol as much as possible. Therefore, a real object created by spreadsheet programs can be read by word processors as numeric text data, or a real object created by graphical chart programs can be read by a graphics editor.

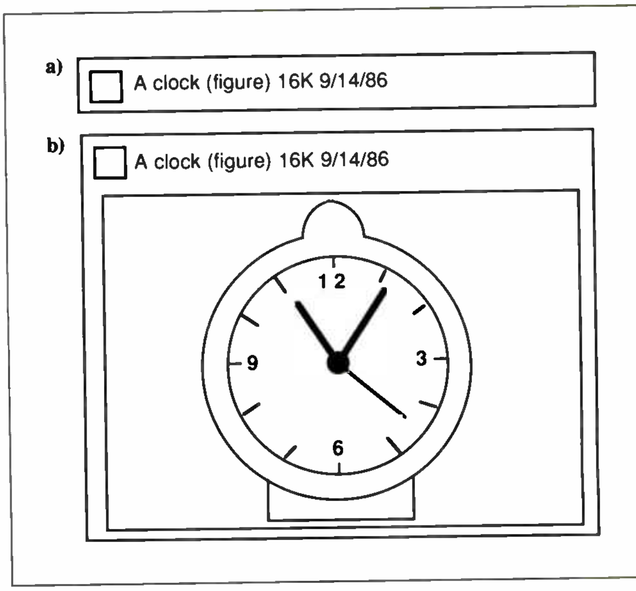

Figure 2: *A BTRON virtual object is normally displayed as a rectangle* **(a)**, *which, when opened* **(b)**, *displays the contents of the real object it points to.*

In conventional computers, general data compatibility is typically guaranteed only on the level of ASCII text. Spreadsheet data from Lotus 1-2-3, for example, can be loaded into most word processors or communications programs only after being stripped down into raw text, losing its essential numeric content. Graphical charts generated from that data become meaningless when converted into text this way.

Under BTRON, however, all data is divided into two parts: one that can be shared and one that is application-specific; the

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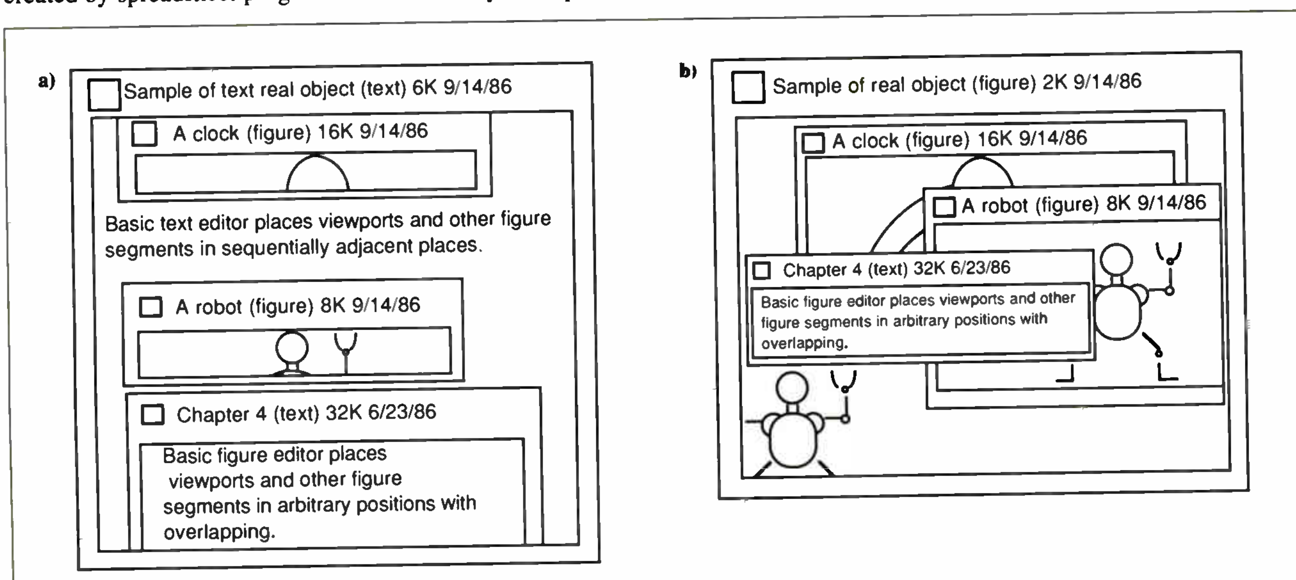


Figure 3: *In BTRON, one-dimensional real objects (or text real objects) are stored sequentially* **(a)**, *while two-dimensional real objects (or figure real objects) are arranged in a two-dimensional overlapped manner* **(b)**.

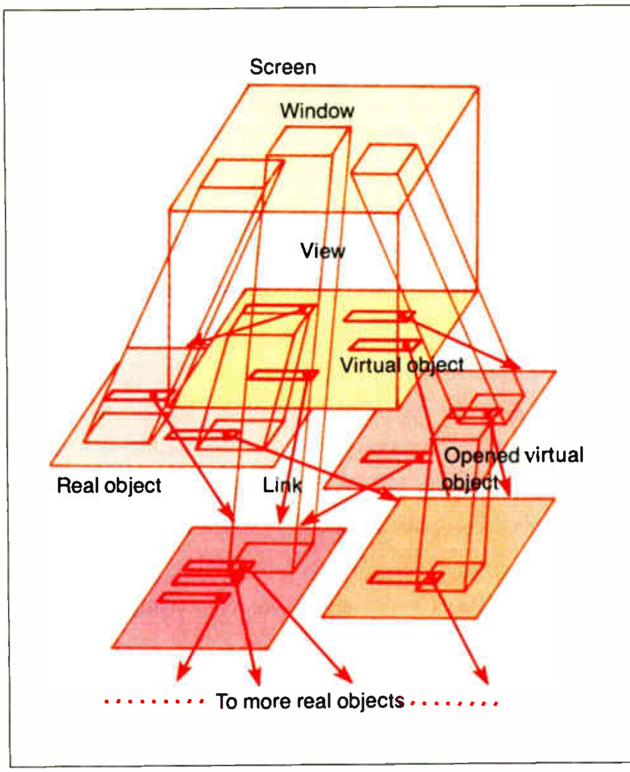


Figure 4: *All BTRON data is stored in a real-object/virtual-object hierarchical structure. Real objects can be arbitrarily linked, and the virtual objects they contain can be nested to any depth.*

shared data is standardized with TAD. Text and figures can always be shared among applications, so these are stored in distinct segments of the real object. Application-specific data, such as information describing the application and parameters necessary to read the data, are stored in fusen.

The Standard TRON CPU

One reason why there are efficient implementations of programming languages such as C and Pascal is that compiler companies build their systems according to common specifications and must therefore compete with one another. Multivendor implementations also encourage the wide development of programs written in the common language.

Exactly the opposite situation exists with current CPU architectures, where instruction sets typically have been held proprietary to the particular CPU manufacturer. This situation tends to discourage innovation in future CPU generations because the manufacturer must design its new CPUs to run with its customers' currently operating software.

The TRON project seeks to change this situation and has proposed a standard public domain CPU instruction set that is designed for efficient implementation of the TRON operating system. The current CPU architecture, called CHIP32, provides 32-bit addressing that is upwardly compatible with the 48-bit and 64-bit addressing modes for future CHIP48 and CHIP64 versions. Hitachi has already produced a first-generation version of CHIP32 called Gmicro/200 (see figure 5 and figure 6).

It is important to note that TRON bucks the recent trend toward RISC, headed by the SPARC from Sun Microsystems, and

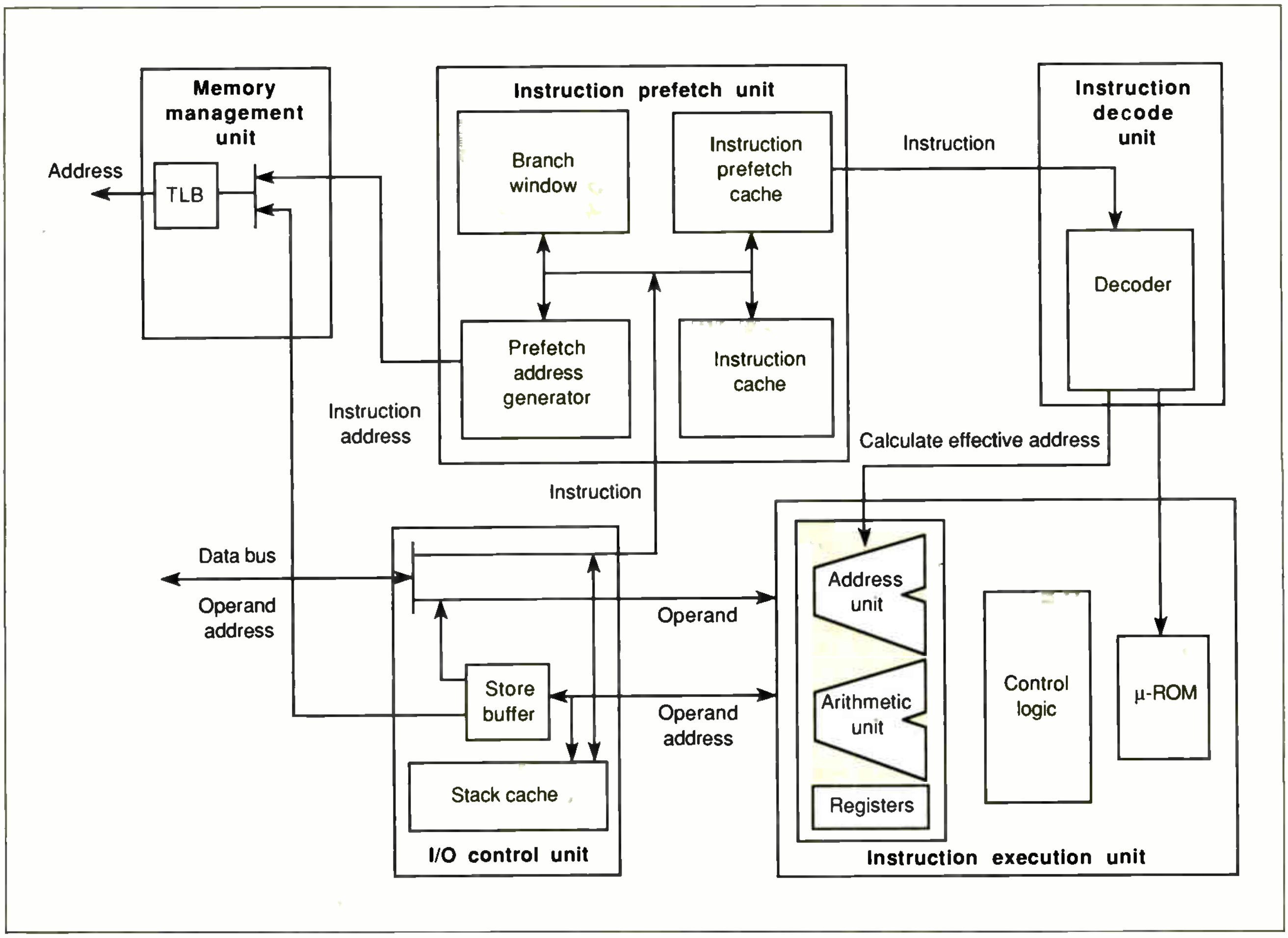


Figure 5: *The Gmicro/200, the first generation of TRON CPUs, provides 32-bit addressing that is upwardly compatible with future 48-bit and 64-bit addressing modes. The internal structure of the Gmicro/200 provides for a very orthogonal instruction set. With the symmetry of operands, you can combine operands in any order.*

similar systems from Hewlett-Packard and IBM. TRON instructions are very high-level, and CPU implementations of the TRON instruction set are among the most complex chips ever designed.

This is not to say, however, that TRON is a traditional CISC (complex-instruction-set computer). On one hand, it includes very high-level instructions designed to be useful for a compiler or an operating system. On the other hand, it has also shaved the length and speed of many of the most common instructions to make it compete favorably with RISC. In other words, it tries to combine the high-speed simplicity of RISC with the programming ease of CISC.

The TRON microprocessor is designed to be a general-purpose processor that is as suitable for high-level workstations as it is for small-scale embedded computers. Excellent performance (as compared to other processors, including RISC) is an important goal, but TRON's general-purpose role allows it to also benefit from easy-to-write and widely available development tools.

Compiler-Oriented Instructions

TRON's instruction set is designed to make it easier to develop high-level language compilers. Wherever possible, the format for operands is kept the same among all instructions, with a minimum of special cases. Memory is treated as one contiguous address space, without segment registers. There are no distinctions between address and data registers, as there are in some processors.

To enable compilers to generate efficient object code, all registers are general registers having the same functions and the same length. Symmetry among instructions makes it much easier to allocate variables and programming work spaces, so TRON's instructions have been made as symmetrical as possible. All 16 general-purpose registers have the same functions, and many restrictions on sizes and available addressing modes have been removed.

In addition, there are two compiler-oriented instruction types not found on other processors: *chained addressing mode* and *arithmetic operations on different data sizes.*

The chained-addressing-mode function generates a complex addressing mode by combining a number of addressing primitives. For example, a sequence of instructions like

```
mov @(8,fp),r1
mov @(r1),r1
mov @(r1),r1
mov @(r1),r1
```

continued

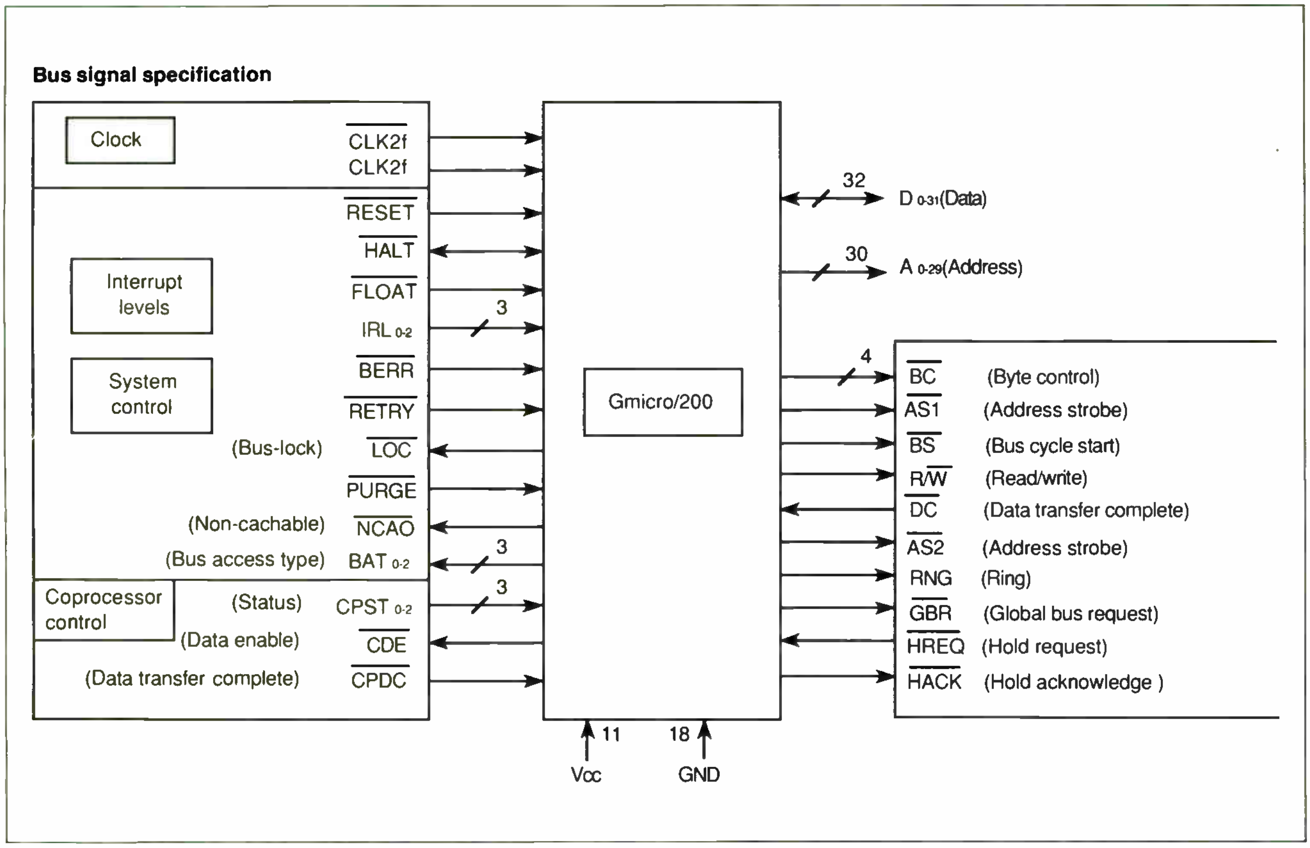


Figure 6: *The bus signal specification for the Gmicro/200. The instruction set permits an elegant way of handling interrupts including error-handling and virtual memory.*

can be replaced by the single instruction

```
mov @(@(@(8,fp))),r1.
```

This feature can be especially effective for referencing between modules.

The arithmetic-operations-on-different-data-sizes function makes operand data symmetrical in size. With this facility, for example, 32-bit data can be multiplied by 8-bit data in a single instruction. This feature can be very useful in compiling a language like C, which frequently converts between data types. A processor without this function needs an additional instruction to expand smaller data to the size of larger data, which, in turn, requires a temporary register and further complicates register allocation in the compiler.

Operating System Support

Typically, CPU instruction sets are designed independently from any operating system. TRON's CPU, however, has been carefully designed with specialized instructions to support both the embedded computer requirements of ITRON and the high-performance workstation requirements of BTRON. There are high-level instructions for context switching (LDCTX and STCTX), for handling queues (QSCH, QINS, and QDEL), bit-map manipulation (BVPAT, BVMAP, and BVCPY), and string instructions (SSCH, SMOV, SCPY, and SSTR).

In other CPU architectures, many of these functions are handled by coprocessors. For example, bit-map functions are often handled by graphics coprocessors. But communication between the main processor and the coprocessor requires a very wide data path, and the resulting overhead can cause performance problems. Since the TRON CPU was designed with its operating system software, there is no need to isolate graphics processing from other functions required by the chip. This makes bit-mapping functions a natural part of the instruction set.

The Implications of TRON

The huge American hardware and software markets have always been big enough that most developers have not considered the international market to be a potential starting point for new hardware and software standards. Japanese domination of the consumer electronics market, and a desire to provide highly functional distributed-systems capability to household products not limited to PCs, has created a standardization vacuum that led to TRON's Japanese origins.

U.S. software companies have already begun to explore TRON's potential as a way to export software to Japan, whose market still lags behind that of the U.S. by several years. Until BTRON, Japanese PC software was mostly limited to programs for MS-DOS-based personal computers (Apple's Macintosh has only 1 percent of the Japanese market). The possibility of a Japanese Macintosh-like competitor should open lucrative opportunities for American companies that are already old hands at designing efficient software that uses windows and pointing devices.

TRON is perhaps the world's most language-independent computer architecture, a situation that should make it much easier to port English-oriented software into Japanese.

Will TRON Succeed?

Janet J. Barron

The TRON concept sounds great: an open architecture that lets users freely shop for chips, components, and peripherals and includes common data-interchange standards that will make everything compatible with everything else. But how will the concept fare in the U.S., and what effect will it have on the computer industry?

John Roach, chairman of Tandy Corp., says that TRON's chances of overtaking the U.S.'s computer industry are very uncertain, although he admits that it's too soon to tell.

"There have been other efforts to develop more viable players, such as the MSX," says Roach. The MSX project was intended to make diverse software programs compatible with low-end home computers from different manufacturers. "Companies were going to make a set of compatible machines. That was the way they were going to attack the world market at the low end," Roach recalls. "That's pretty well gone today."

Besides, anybody who wants to try to build a new operating system is welcome to do so, says Roach. "The development of a new operating system is quite a challenge and takes a very long time. OS/2 is still trying to achieve that critical mass. Unix, I think, is still a relatively important competitor. I doubt that most of us appreciate its full potential. There's a tremendous amount of resources behind U.S. operating systems such as Unix."

About TRON's open architecture, Roach says, "Basically, MS-DOS and Unix are open architectures. Even if TRON becomes successful, it, too, will be limited by the constraints of its past." Since the TRON concept encompasses an open architecture, many domestic companies are deciding whether or not to implement it or to run TRON applications.

Ron Waters, director of Streamlined Instruction Processors at Advanced Micro Devices, suggests that because of the growth of Japanese markets, TRON should be a major success there. With a ready-made educational market of 10 million computers alone, TRON's chances for success in Japan are assured. But, says Waters, "I don't think it will be a success in the U.S."

"Today we have capabilities for embedded processing that are competitive and offer excellent performance. Our innovative microprocessors and software continue to provide superior performance over any TRON chip that could be offered, and we have the capability to run TRON applications without the chips themselves," says Waters.

However, the fact that TRON encompasses VLSI chips, system software, and an open architecture prompted Larry Woodson, a strategist in Texas Instruments's semiconductor group, to say, "It has all the necessary elements for success: Complete software and tools are in development to allow the architecture to be marketed on a worldwide basis. In addition, the Japanese are developing an infrastructure by implementing TRON into some key end products. There is already working silicon and software available, so this is real."

Michael Dell, chairman and founder of Dell Computers, says his firm has no plans to use any of the TRON technology in its next generation of computers. "I don't think TRON will have much impact on microcomputers at this time. In this country, we have an intensely competitive market for microprocessors that drive computers. That market is based around standards that have already been set. As a processor engine, TRON doesn't offer any specific advantages vis-à-vis processors from several of the other major U.S. companies." He added, "There's no basis to suggest the tariff situation will change to make it competitive for Japanese machines."

The Compatibility Question

Meanwhile, Japanese companies such as Mitsubishi, Fujitsu, and Hitachi have already implemented the TRON architecture, a task made easier by the TRON project's specification and standardization of the registers, the I/O, the instruction set, and addressing modes.

According to Charles Glenn, product-line manager for Fujitsu's 32-bit TRON Gmicro microprocessors and peripherals, the TRON project will certify the compatibility of the various implemented architectures with the TRON architecture. In essence, this certification will mean that the architecture can run all the TRON software. Many large Japanese silicon manufacturers endorse this architecture, said Glenn, and thus, "they will make it successful. The merits of the architecture are good."

But according to Jeff Nutt, technical marketing manager for CPUs at Motorola, TRON will not play an important role for at least one reason. "There will be different, and possibly incompatible, implementations by individual companies," says Nutt. "To make its products different from other companies' goods, a firm will do the same thing it does now—it will add its own special features or enhancements. These beefed-up products may be as incompatible as some competitive company's products are right now," Nutt says.

Janet J. Barron is a technical editor for BYTE. She can be reached on BIX as "neural."

TRON's presence is certain to be felt in the emerging educational market created by the Japanese ministry of education's recent announcement that all public schools must use computers. This order creates a market of up to 10 million machines by the mid-1990s. The lack of an easy-to-use Japanese counterpart to MS-DOS should further help its adoption by businesses, in the same way that, in its nascence, Apple succeeded with the Macintosh by creating new markets.

TRON's direct impact on the U.S. PC industry probably won't be felt for several years, although MTRON- and ITRON-compatible home appliances and consumer electronics will be widely available in Japanese-made products much sooner. The fact that it will be easy to connect TRON PCs and TRON products could be an important step in giving TRON a growing share of the 1990s U.S. PC market.

Ken Sakamura, Ph.D., is the founder and director of the TRON project and is an associate professor of information sciences at the University of Tokyo. Richard Sprague has a B.S. in computer science from Stanford University and consults on issues related to the Japanese PC hardware and software markets. He can be reached on BIX c/o "editors" and "rsprague," respectively.

Circle 65 on Reader Service Card

THE IBM PC BIOS

Specialized ROM routines form the heart of a standard

Recently a user on BIX asked some tough questions about IBM PC compatibles. "How can you tell if a clone of the IBM PC is really compatible?" he wanted to know. And, "How do you choose a BIOS for your machine to make it as compatible as possible?"

Alas, there are no easy answers to these questions. But this article—which takes a look inside the IBM PC BIOS—will give you an idea of what goes on inside a typical BIOS. I'll also present some ideas on how to test for compatibility and how to attain compatibility with protected-mode operating systems (which can't use the BIOS).

Perhaps no other single piece of commercial software has been more painstakingly studied, reverse-engineered, and cloned than the IBM PC's BIOS, or basic I/O system. This small set of control programs, which rarely consumes more than 64K bytes even in its most elaborate incarnations, is the key to making a system compatible with the IBM PC family—and to adding new features while retaining compatibility.

Roots: The CP/M BIOS

What is a BIOS? To understand the answer to this question, you must return to the ancient days B.P.C. (before the IBM PC) and look at what was perhaps the first commercially successful microcomputer operating system: CP/M.

CP/M (sometimes called CP/M-80 to distinguish it from other implementations) ran on the Intel 8080 and Z80 microprocessors and had two parts: the BDOS (basic disk operating system) and the BIOS. The BDOS, which was the exclusive property of Digital Research, was the same in every implementation of CP/M, regardless of what hardware it was running on. The BIOS, by contrast, was tailored to the individual machine. The BDOS used the BIOS to access the terminal screen, read and write to the disk, and control the printer; the BIOS, in turn, gave machine independence to the BDOS.

The BDOS source code, as you might expect, was proprietary and was not published. However, the source code for each unique machine's BIOS was frequently available (in manuals, on BBSes, on the Internet network, and so on) so that users could understand and modify it as needed. Any weekend programmer could port CP/M to a new machine simply by creating a set of 17 short BIOS routines (typically only a few K bytes of code) and concatenating them with the BDOS on the disk. Because the relative locations of the BIOS and BDOS were exactly the same in memory as on the disk (one right after the other), they could be loaded together as one large "chunk" when the system was booted. Table 1 lists the original CP/M BIOS functions, and figure 1 shows how the BIOS and BDOS were laid out in RAM. The BIOS functions were entered through a *jump table* (a list of jump instructions).

The IBM PC, PC-DOS, and the ROM BIOS

One of the few problems with concatenating CP/M's BIOS and BDOS on the same disk was that you couldn't start one brand of machine from a disk intended for another (even if the disks were in the same format). When IBM adopted Microsoft's MS-DOS (derived from a CP/M look-alike called 86-DOS) as the primary operating system for its early PCs, it avoided this problem by moving the BIOS to a ROM within each machine, creating the new memory arrangement shown in figure 2. The result—at least in theory—was a system in which disks could be freely exchanged between radically different types of hardware.

This approach might have been completely successful had early versions of the BIOS been highly optimized and fully featured in all areas. The IBM PC BIOS's disk functions, for instance, were quite fast, and programmers saw little need to bypass them. However, the BIOS screen functions in the original PC were slow, and some calls, such as those that controlled serial communications, were barely useful at all. In a few areas (e.g., sound), BIOS support was even lacking on the original PC. In these areas, programmers were forced to write code that manipulated the hardware directly.

Also, many programmers made use of memory locations used internally by the BIOS—locations that IBM was forced to maintain to keep new hardware compatible with the established software base. The result was a de facto "hardware/software" standard—consisting of BIOS calls, directly mapped storage locations, and peripheral hardware addresses—that remains prevalent to this day. While not as hardware-independent as IBM might have wished, this standard *has* been successful, and it has been implemented on a wide variety of machines, including laptops, diskless workstations, and even IBM's Micro Channel computers, which among compatibles represent some of the greatest departures from the original IBM PC architecture.

The ROM BIOS Services

The functions of the IBM PC ROM BIOS fall into several categories, each handled by its own software interrupt (see the text box "Software Interrupts and the 8086" on page 307). These categories include video, machine configuration, memory size determination, disk operations, serial communications, miscellaneous functions, keyboard control, the printer, invocation of BASIC in ROM (if present), bootstrapping the system, and system

continued

timer functions. (Other services, such as expanded memory and NetBIOS, are also activated via software interrupts but are not, strictly speaking, part of the main BIOS.)

Once the software interrupt for a particular category of functions has been invoked, the AH register—a byte-wide register within the 8086 CPU—is examined to determine the specific task to be performed. The other CPU registers hold additional information to be passed to the BIOS function. Since registers, rather than the stack, are used to pass information to the BIOS routines, the information transfer is especially quick.

Table 2 gives a summary of the major BIOS services, grouped by function and software interrupt.

Video—INT 10h

The first BIOS software interrupt, INT 10h, controls the PC's video display. It allows you to put the display into text or graphics mode; select the number of characters, pixels, and colors that appear on the screen; move the cursor; scroll the screen; plot pixels; and read or write characters. The EGA, PGA, VGA, and 8514 display adapters contain their own ROMs, which enhance and supersede the functions of the original BIOS ROMs; the enhanced functions allow application programs to select fonts, print the screen on the printer, change the color palette, and perform other graphics functions.

IBM later streamlined the video BIOS and added more functions (e.g., the AT has a function that can write more than one character to the screen at a time). But programmers couldn't wait for these enhancements to come along; they needed to have their programs run on *every* system, and they wanted more speed than they could get through the BIOS routines. They therefore adopted the practice of bypassing the BIOS and writing directly to the PC's memory-mapped screen. Because it is so common, this approach will work on virtually all compatible machines. However, in multitasking and windowing environments such as DESQview, programs that perform direct screen writes—often called "ill-behaved" programs—may not be able to share the screen gracefully with others. The Intel 80386's "virtual 8086" mode may help to contain such programs and force them to be "cooperative" in a multitasking environment, but the 80286 has no such mode. This is why the OS/2 compatibility box can currently run only one DOS application at a time.

Equipment Status—INT 11h

This software interrupt has only one function: to return information on the equipment attached to the PC. It returns a 16-bit word whose bits contain this information, as shown in figure 3.

Memory Size—INT 12h

This software interrupt also has only one function: to return the size of the computer's RAM (in K bytes) in the AX register. Extended memory (the memory above 1 megabyte) isn't included in this number.

Disk Operations—INT 13h

The INT 13h interrupt controls disk operations. Unlike the original CP/M BIOS, the IBM PC BIOS has a set of disk functions complete enough to eliminate the need for hardware-specific utilities. This has allowed the development of a wide variety of add-on disk controllers for the IBM PC; had there been a need to bypass the BIOS, it wouldn't be possible to add a new type of disk simply by adding a BIOS extension ROM.

Table 1: *CP/M BIOS functions. These 17 routines, shown as they were arranged in the BIOS jump table, were all that were needed to adapt the CP/M-80 operating system to a new machine.*

Jump table location	Name	Function
0	BOOT	Cold bootstrap.
3	WBOOT	Warm bootstrap.
6	CONST	Console (terminal) status.
9	CONIN	Console input.
12	CONOUT	Console output.
15	LIST	List device (printer) output.
18	PUNCH	"Punch" (paper tape) output.
21	READER	"Reader" (paper tape) input.
24	HOME	Move disk head to track 0.
27	SELDSK	Select a disk to read/write.
30	SETTRK	Set the disk track to read/write.
33	SETSEC	Set the sector to read/write.
36	SETDMA	Set memory location for disk data.
39	READ	Read a sector.
42	WRITE	Write a sector.
45	LISTST	Status of List device.
48	SECTRAN	Translate logical sector number to physical.

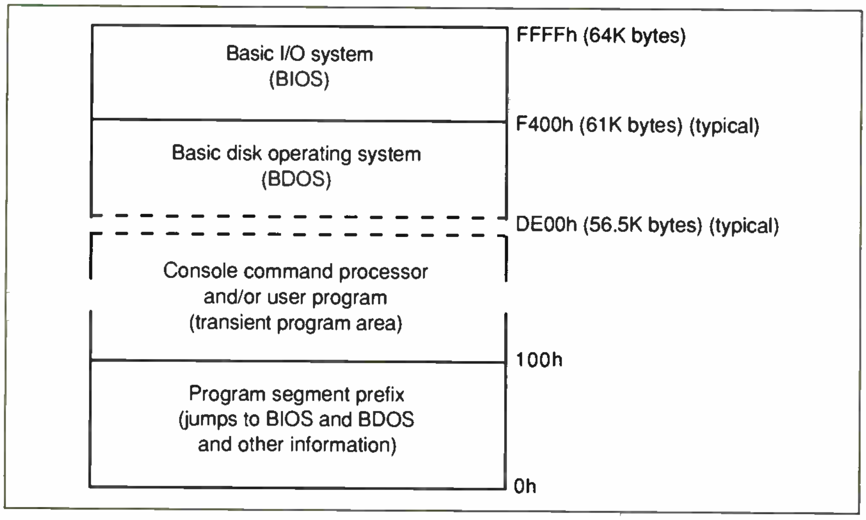


Figure 1: *In CP/M, the BIOS loaded from disk together with the BDOS in a single chunk. Both resided in high memory above the user program.*

Interestingly, the AT disk BIOS did not support some functions listed in the documentation for the XT; these may have been intended for use with SCSI drives (which IBM has never sold for the PC line).

Serial Communications—INT 14h

The serial communications functions of the IBM PC BIOS are, alas, among its weakest points. Unlike all but the most basic communications programs, the BIOS serial routines do not use hardware interrupts to receive characters from external serial devices. This means that characters are likely to be "dropped" if a program on the PC does not constantly watch for their arrival.

As a result, virtually every program that uses the serial port (with the possible exception of print spoolers, which rarely receive characters) takes over the communications hardware and controls it directly.

Cassette/Joystick/Multitasking—INT 15h

Software interrupt 15h controlled the cassette tape interface and joystick port on the original IBM PC. However, since this interrupt was rarely used (the cassette interface was dropped entirely on the AT), IBM added new INT 15 functions, including support for multitasking, peripheral sharing, switching to protected mode, and detecting the use of the System Request key on the AT keyboard. The multitasking support functions are used only by some network programs, and OS/2 implements a faster mode switch than the standard BIOS does, so many of these functions are rarely invoked.

Keyboard—INT 16h

Software interrupt 16h manages the IBM PC's keyboard. Keystrokes are returned as both ASCII characters and "scan codes"—codes directly related to the position of each key on the keyboard. Scan codes can help a program distinguish between two keys that send the same ASCII characters—for instance, the + key on the main keyboard and the + key on the numeric pad.

BIOS keyboard functions are sometimes bypassed by utilities, like SuperKey, that remap the keyboard or enable and disable specific key sequences. Most application programs, however, use the keyboard as is.

Printer—INT 17h

This software interrupt provides three simple printer functions. It can initialize the printer port, check the printer status, or output a character to the printer.

BASIC—INT 18h

Most PC compatibles don't implement a BASIC in ROM, as IBM PCs do. However, on machines that *do* have a BASIC, software interrupt 18h starts it running.

Bootstrap—INT 19h

Software interrupt 19h reboots the PC. Since this interrupt causes a "warm" boot, the BIOS doesn't test memory; however, it does scan for ROMs containing extensions to the BIOS, as I'll explain shortly.

System Timer—INT 1Ah

Software interrupt 1Ah allows a program to set and get the system time (measured in *ticks*). A tick occurs roughly 18.2 times per second, so there are exactly 65,536 ticks in an hour. On the AT, the

continued

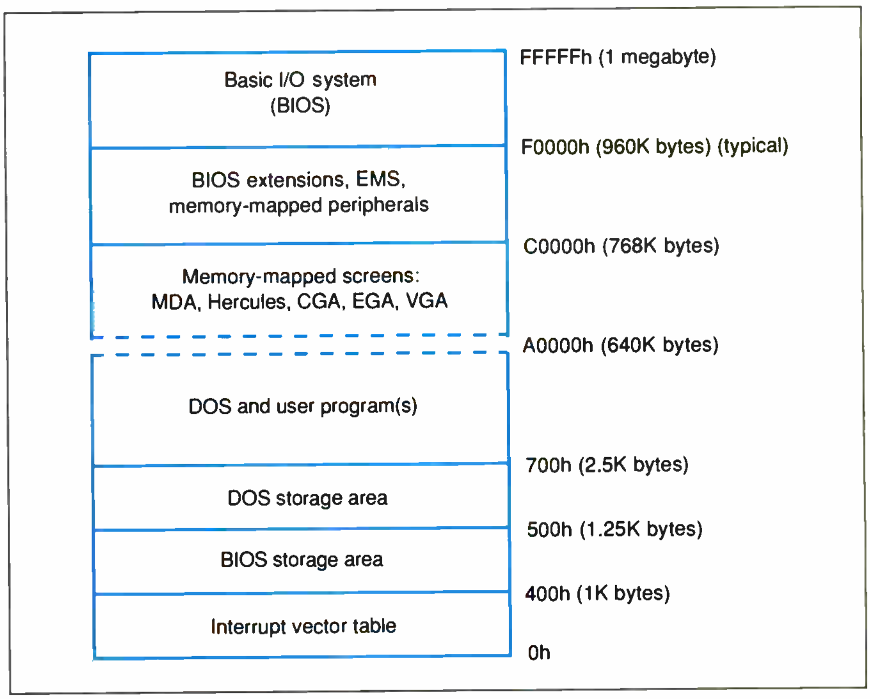


Figure 2: *In the IBM PC, the high end of the memory-address space is reserved for the ROM BIOS, memory-mapped video, BIOS extension ROMs, memory-mapped peripherals, and expanded memory. The interrupt vector tables, BIOS storage area, DOS storage area, and DOS itself occupy the low end of memory. The user program loads between DOS and the memory-mapped screen area. Note that this memory layout gives rise to the infamous "640K barrier" that EMS, OS/2, and 80386 memory managers attempt to circumvent.*

15–14	13	12	11–9	8	7–6	5–4	3–2	1	0
Printer	0	Game port? (0/1)	Serial ports (0-7)	0	Floppy drives (0-3)	Video mode	System board memory	8087	Floppy boot? (0/1)

Figure 3: *The equipment configuration word, returned by the equipment status BIOS call, gives basic information on how the PC is equipped.*

Table 2: *Major IBM PC BIOS services.*

INT 10h BIOS video functions

AH value	Function
0	Set video mode (0-6 for CGA, 7 for monochrome).
1	Set cursor shape (starting and ending lines).
2	Move the cursor.
3	Read cursor location and shape.
4	Read position of a light pen (seldom used).
5	Switch "pages" of display in text mode.
6	Scroll up.
7	Scroll down.
8	Read character and attribute at cursor.
9	Write character and attribute at cursor.
10	Write character at cursor.
11	Set color for medium-resolution graphics modes.
12	Write a pixel.
13	Read a pixel.
14	Write character to screen with rudimentary terminal emulation.
15	Read video mode and page.

INT 13h BIOS disk functions

AH value	Function
0	Reset the disk system.
1	Get status from last disk operation.
2	Read sector(s).
3	Write sector(s).
4	Verify sector(s) against memory.
5	Format a track.
6	Format a track and flag bad sectors (not on AT).
7	Format drive starting at a specified sector (not on AT).
8	Return parameters of current drive (heads, cylinders, sectors).
9	Initialize drive characteristics.
10	"Read long" (read with ECC).
11	"Write long" (write with ECC).
12	Seek to track.
13	"Alternate" disk reset.
14	Read sector buffer (not on AT).
15	Write sector buffer (not on AT).
16	Test if drive is ready.
17	Recalibrate the drive.
18	Controller RAM diagnostic (not on AT).
19	Drive diagnostic (not on AT).
20	Controller diagnostic.
21	Read drive type (fixed, floppy).

INT 14h BIOS serial communications functions

AH value	Function
0	Initialize a serial port.
1	Send a character.
2	Receive a character.
3	Read serial port status.

INT 15h cassette and multitasking functions

AH value	Function
0-3	Cassette functions.
4-127	Not used.
128	Open a shared device.
129	Close a shared device.
130	Terminate a task/program.
131	Wait for an event.
132	Read joystick position/switches.
133	System Request key.
134	Multitasking "wait" function.
135	Return size of AT extended memory.
136	Enter protected mode.
137-138	Concurrent I/O for multitasking systems.

INT 16h keyboard functions

AH value	Function
0	Get a keystroke.
1	Check for available keystroke.
2	Return status of Shift keys.

INT 17h printer functions

AH value	Function
0	Print a character.
1	Initialize the printer port.
2	Return printer status.

INT 1Ah time functions

AH value	Function
0	Read the system time.
1	Set the system time.
2	Read the CMOS clock.
3	Set the CMOS clock.
4	Read the date from the CMOS clock.
5	Set the date in the CMOS clock.
6	Set the "alarm" in the CMOS clock.
7	Reset the "alarm" in the CMOS clock.

time and date in the CMOS clock chip can also be written and read via this interrupt, and an "alarm" can be set to go off at a specific time.

Most current software does not use INT 1Ah to access the PC's real-time clock but inserts a service routine into a chain of routines called by interrupt 1Ch each time the timer ticks. Programs must be careful to remove themselves from this chain before terminating to avoid system crashes.

Bringing Up the System: POST and ROM Scan

Besides providing user-callable services, the ROM BIOS also performs general maintenance functions on the PC. When the system is first powered up, the BIOS performs a thorough check of the CPU, RAM, and ROM; it also looks for stuck keys on the keyboard and disk functions. Few parts of the PC escape scrutiny; if the power-on self test (or POST) is successful, it's likely that the machine is ready to boot.

After the POST, the ROM BIOS performs a function known as ROM Scan. The beginning of each 2K-byte block of memory between locations C8000 and E0000 is checked for a special signature; if the signature is found, the BIOS performs a checksum and executes a ROM at that location. This is the way peripheral board makers create "BIOS extensions" to accommodate new peripherals—a ROM on the board can initialize the peripheral, make it accessible via preexisting BIOS calls, and/or even interact with the user. This simple feature is one of the touches that make the IBM PC architecture truly "open" to peripheral vendors.

BIOS Variables

The area of PC memory from location 40h to location 50h contains storage locations that are reserved for use by the BIOS. While this area was not originally intended for use by application programmers, certain locations were so frequently accessed by user programs that IBM was forced to "freeze" their implementation to ensure compatibility with future machines. The information contained in the BIOS storage area includes:

- The number and locations of serial and parallel ports
- Equipment information (also accessible via INT 11h but frequently accessed directly)
- The keyboard queue
- Disk status information
- The system clock (often read directly instead of through INT 1Ah)
- Information on the current video mode and cursor position

Many reference books now describe these locations in detail (see the bibliography).

Compatibility

One of the most common questions asked by prospective buyers of IBM compatibles is "How compatible *is* it?" (See the text box "The 'Clean Room' Approach" on page 308.) Alas, because the "standard" for compatibility is at best an ad hoc one, there is no easy answer to this question. Because it can, in many cases, hide hardware differences, the compatibility of the BIOS is one of the most important factors in determining the compatibility of the entire machine. But since software often bypasses the BIOS and accesses the hardware directly, a well-coded BIOS is not a 100 percent guarantee of compatibility.

How do you test for compatibility? Traditionally, the standard measures of IBM compatibility have been badly behaved application programs. According to Samuel Adams Yorko, a BIOS compatibility expert, these programs include Microsoft's Flight Simulator, Lotus 1-2-3, XenoCopy, Microsoft Windows, Microsoft Word, Microsoft QuickC, DESQview, and early versions of Novell NetWare. TSR programs are excellent tests of compatibility. Copy-protected software, in its zeal not to be copied, tends to be quite finicky, and games in particular are good benchmarks because they cut corners to push the hardware and software to their performance limits.

Interestingly, as BIOSes have become more compatible, programs have also be-

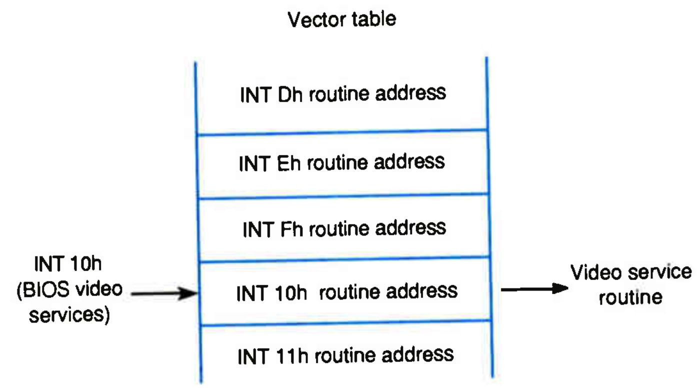
continued

Software Interrupts and the 8086

The IBM PC BIOS routines are called via 8086 instructions called *software interrupts*. Each instruction, represented by the mnemonic INT, carries a 1-byte interrupt number from 0 to 255 (FF hexadecimal). The interrupt number is an index into a *vector table*, a table in memory containing the 4-byte address of the service routine for each interrupt. In figure A, an INT 10h instruction is used to call a BIOS video routine.

Most of the INT instructions are 2 bytes long, but one of them—INT 3—also contains a special single-byte encoding. You can take advantage of this variant when debugging or as a quick 1-byte subroutine call.

Figure A: *The number in an INT (software interrupt) instruction is used to find the address of an interrupt service routine in the 8086's interrupt vector table. In the IBM PC, this table normally starts at the very bottom of memory, with the INT 0 vector at location 0, INT 1 at location 4, and so on.*



The "Clean Room" Approach

Makers of IBM PC compatibles have long feared lawsuits from Big Blue—especially after IBM was said to have threatened legal action against fledgling Eagle Computer for copying parts of the IBM PC BIOS. Amid the uncertainty and confusion that prevailed in 1983, Phoenix Technologies consulted lawyers and came up with a technique for creating a "legal" IBM-compatible BIOS: the so-called "clean room" approach.

First, Phoenix assembled two teams of programmers. The first became thoroughly familiar with the IBM BIOS and wrote a detailed specification explaining what the BIOS had to do to be compatible. A second team, consisting of programmers who swore they had never seen a copy of the IBM PC BIOS, created a BIOS from that specification and sent it back to the first team. The first team checked the BIOS for compatibility and then modified the specification to cover areas that were missed on the first pass. All information that was exchanged by the two teams was monitored and recorded in the event of legal action.

After many cycles, the "clean" team (the programmers who had never seen the actual IBM BIOS) was able to produce a set of ROMs that was nearly 100 percent compatible with IBM's. Because these programmers had never seen the original IBM code, but only a description of how it worked, Phoenix was confident that IBM would not be able to sue for copyright infringement.

To bolster this claim, Phoenix persuaded an insurance company to underwrite a "BIOS insurance policy" that would protect BIOS licensees from legal costs incurred in the event of an IBM lawsuit. While no policy was ever written, Phoenix's customers were apparently convinced that the approach was sound: There are more compatibles with Phoenix BIOSes than there are genuine IBM PCs in use today. Furthermore, despite questions regarding the originality of "clone" hardware designs, the originality of Phoenix's software has never been challenged in court.

come more well behaved. Newer versions of the programs mentioned above now run on more machines than ever, and the demise of copy protection has removed one of the major barriers to compatibility. Nowadays, it's relatively safe to assume that if a system runs a reasonable selection of TSRs, multitasking environments (e.g., OS/2, DESQview, Windows), and applications, it will be compatible with most other software. This convergence is due, in part, to economic necessity; because there are far more clones than there are "genuine" IBM PCs in the marketplace, it's in every software manufacturer's best interest to test products with the major compatible BIOSes (e.g., Phoenix, Compaq, AMI, and Award) before release.

Building Your Own BIOS

At one time, the art of building a compatible BIOS was shrouded in so much mystery that few dared to attempt it. At long last, though, those days appear to be over; dozens of books have now been published giving detailed information on the "innards" of the IBM PC BIOS. In fact, one—which arrived on my desk last week—gives the complete source code for an AT BIOS, largely written in C!

This book is worthy of special mention because of its novelty. *XT and AT BiosKits* from Annabooks (see bibliography) lets you create and customize a BIOS using only a C compiler, an assembler, and an EPROM programmer. Within an hour of assembling the required tools, I was able to burn a set of EPROMs and install them in my AT clone. As far as I could tell, the BIOS was fully compatible, and I was able to patch it to understand a new type of hard disk (not included in the standard tables) in only a few minutes. Other forms of "surgery" were equally straightforward. The Annabooks BIOS includes a resident monitor called SysVue that lets you peek at what's going on inside the system at any time—very handy for debugging the BIOS itself as well as other programs.

The Annabooks code is reasonably well commented, and it comes with a little gem that's very useful for PC-family programmers: the *XT-AT Handbook*. This pocket-size reference book is packed with information on DOS commands, the PC character set, I/O port addresses, interrupt vectors, RS-232C pinouts, and the PC hardware in general.

The literature that comes with the BIOS kit says that the authors will license the BIOS for use by any compatible manufacturer for $4 a copy. At that price, they may get a lot of takers.

Exploring an Existing BIOS

If your hardware is close to 100 percent compatible with the IBM PC or AT, it's likely that many manufacturers' BIOSes (including IBM's, if you are able to get hold of some original ROMs) will run on it with no modifications. However, if your computer's architecture differs significantly from that of the IBM PC and AT, and you want to make BIOS modifications (or just see how it works), you may need to disassemble the existing code. Also, because IBM no longer provides BIOS source code in its *Technical Reference* manuals, disassembly may be the only way you'll be able to get a look at what's going on inside the BIOS.

One painful but time-honored method of peeking into a BIOS is to trace through it with a debugger. DOS's DEBUG command will work for some of the less esoteric sections of the code; I've successfully used it to follow and understand the BIOS serial port services, for instance. But to trace through interrupt service routines and nonreentrant parts of the code, you'll need an in-circuit emulator—or a board like Atron's PC Probe, which performs a similar function.

Another less expensive approach is to purchase a disassembler explicitly designed to handle BIOSes. I recently used a product called Sourcer, made by V Communications of San Jose, California, to examine the BIOS of a PC-compatible coprocessor board with reasonably good results.

Sourcer, which can also disassemble standard .EXE and .COM files, comes with a very clever preprocessor that captures the BIOS *as it's running* on a machine. Besides looking at the code in the ROMs, the BIOS preprocessor looks in low memory for software and hardware interrupt vectors that point into the BIOS—identifying key entry points that might not be found otherwise. The preprocessor also takes snapshots of key variables within the BIOS's data area, so you can see the same values that the BIOS code might see while running. Everything the preprocessor identifies is neatly labeled in the disassembly listing.

No disassembler is perfect, though, and you'll probably find places where the disassembler mistook code for data (or the other way around). You can put the disassembler back on track by modifying a control file to give explicit information about that area of the ROM.

At $140, Sourcer with the BIOS preprocessor is a reasonably priced learning tool as well as a programmer's lifesaver. Just looking at the disassembled BIOS code is a real education and is highly recommended for the inquisitive.

ABIOS

The IBM PC BIOS is able, in many cases, to isolate programs that run in the "real" mode of the 8086 family of processors from the physical hardware—especially in certain areas such as disk I/O. However, it has few provisions for handling memory sizes greater than 1 megabyte and little support for the protected modes of the 80286 and 80386 processors. Thus, an environment that uses protected mode to multitask or to break the 640K-byte barrier on a standard AT compatible is forced to take one of two approaches: It can switch the processor back to real mode (slowly!) to use the IBM PC BIOS calls, or it can go directly to the hardware in protected mode, sacrificing the hardware independence provided by the BIOS.

To avoid this problem, IBM has come up with a new kind of BIOS: the Advanced BIOS, or ABIOS. (IBM sometimes calls the older, real-mode BIOS by the name CBIOS, or Compatibility BIOS, to distinguish it from the ABIOS.) ABIOS, which is currently implemented only on IBM's PS/2 series and compatibles, can be used by operating systems running in either real mode or protected mode—or by "bimodal" environments, like OS/2, that switch back and forth. ABIOS provides even greater isolation from the physical hardware than the original CBIOS and contains complete support for concurrent device access by many processes in a multitasking system.

Types of ABIOS Requests

ABIOS can support three types of requests made by an operating system or a user program: *single-staged*, *discrete multistaged*, and *continuous multistaged*. Each type of request is diagrammed in figure 4.

In a single-staged request, the ABIOS performs the requested function right away; it's done by the time the program regains control. A discrete multistaged request happens in two or more stages, with delays between the stages; the calling program regains control during the delays so that other processing can be done. A continuous multistaged request starts a staged operation that never ends.

Calling ABIOS

The ABIOS calling conventions reflect the needs of protected-mode and bimodal operating systems. ABIOS, unlike CBIOS, is entered by far calls rather than by software interrupts and is completely reentrant even between modes. That is, a real-mode function X can be interrupted and a protected-mode function Y started, even if X is identical to Y.

When a program or operating system calls ABIOS, it must pass pointers to two structures: a request block and a common data area (see table 3). The request block specifies the function to be performed—just as the interrupt number, the AH register, and the other CPU registers tell the CBIOS what needs to be done. The common data area, ABIOS's "master switchboard," is a table that contains pointers to all of ABIOS's other tables and data areas.

The internal data structures linked together by the common data area include function transfer tables—which, like CP/M's jump tables, list the addresses of routines that perform certain functions—and device blocks, which contain public and private information used by devices. The details of these complex structures can be found in IBM's *Personal System/2 Seminar Proceedings* (see bibliography).

When a program calls ABIOS, it must provide the segment or selector of the common data area and a pointer to a request block. Two other parameters may or may not be furnished by the caller, depending on the convention used. In the ABIOS Transfer Convention, ABIOS accepts just the addresses of the request block and the common data area and finds a function transfer table and a device block to use when handling the request. In the Operating System Transfer Convention, the caller furnishes the locations of these data structures. Note that since the parameters are not removed from the stack upon return to the caller, an operating system can save the function transfer table and device block addresses after they have been furnished by ABIOS during a call.

Interrupt Handling in ABIOS

Unlike CBIOS, ABIOS expects system devices to share interrupts. In a machine running under ABIOS, the operating system—not ABIOS—gets control when a hardware interrupt occurs. It is the responsibility of the operating system to call ABIOS to have the interrupt serviced, and the operating system must also issue the End of Interrupt command to the interrupt controller.

continued

Table 3: *ABIOS stack frame and calling conventions.*

Bytes	Stack contents
2	Common data area pointer (segment/selector only)—required.
4	Request block pointer—required.
4	Function transfer table pointer—furnished by ABIOS or caller.
4	Device block pointer—furnished by ABIOS or caller.
4	(Return address.)

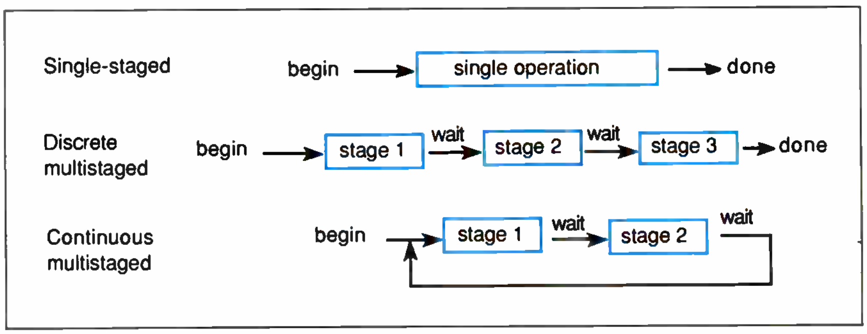


Figure 4: *ABIOS request types. A single-staged ABIOS request completes immediately before returning to the caller. A discrete multistaged request is processed in stages with delays between the stages; control is returned to the caller until a hardware interrupt signals that a stage has completed. A continuous multistaged request starts a process—sometimes called a daemon—that loops forever between one or more stages.*

If more than one device is sharing a hardware interrupt, the operating system must invoke the ABIOS interrupt handling routine for each of these devices in turn. If the current interrupt does not affect a device, the operating system receives a return code from ABIOS that says, "This is not my interrupt; please try another device." The operating system continues to query devices until one responds to the interrupt request.

Future Compatibility

ABIOS is an important step toward maintaining machine independence in the Intel protected-mode world. Unfortunately, the majority of PC compatibles available today—and likely to be available in the near future—do not have ABIOS, and users of multitasking operating systems such as OS/2 and Unix must buy machine-specific versions of the operating system and/or install device drivers to adapt the operating system to each machine.

There is no compelling technical reason, however, why ABIOS cannot be added to existing systems—as an add-in ROM or even as a TSR. (In fact, IBM has stated publicly that "ABIOS is contained in ROM but does not preclude a RAM implementation.") It is to be hoped that, in the long term, compatible manufacturers (and/or enterprising software designers) will provide ABIOS implementations for existing 80286 and 80386 machines. If this does happen, you can anticipate an even greater level of standardization and machine independence than that engendered by the original IBM PC BIOS.

In the meantime, the venerable IBM PC BIOS—now well understood and readily implemented—has fostered the development of a strong, consistent industry standard. While far from perfect, this standard will doubtless persist for another decade or more and will allow manufacturers to produce quality products at commodity prices. Hopefully, newer standards, such as ABIOS, will eventually replace the standard CBIOS as the basis for compatible machines with multitasking and more than 640K bytes of directly addressable memory. Only time will tell if these new standards will ever be as pervasive as the standard that lies at the heart of the IBM PC. ■

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FLOATING-POINT REVISITED

This variation on a theme yields a floating-point package particularly suited to business applications

Floating-point is a topic of sufficient significance for me to cover it more than once, particularly when there's some ground that I didn't cover last time (see my two-part "Floating-Point without a Coprocessor" in the September and October 1988 BYTE). This month's package is fundamentally different from the previous one—it has strengths where the other has weaknesses, making it particularly suited to business and accounting applications.

The floating-point package that I described in "Floating-Point without a Coprocessor" manipulated floating-point numbers in base 2; such numbers are called binary floating-point numbers. This time, I'm presenting a binary-coded-decimal (BCD) floating-point package. As you'll see, BCD floating-point computing has some advantages over binary foating-point computing. (Borland includes a BCD option in its Turbo Pascal programming language, and Zedcor uses a BCD representation for floating-point numbers in its ZBasic compiler.)

But it's not all roses. There are some disadvantages to using BCD floating-point numbers. That's part of life in the computer world. This column should help you decide which form—BCD or binary—is best for your applications.

Inside

I can describe the format of a BCD floating-point number easily. (You'll catch on even quicker if you've read my earlier article on floating-point.) Take a look at figure 1 and see if you recognize the structure. FAC1_EXP holds the exponent, FAC1_SIGN contains the sign of the number, and FAC1_MAN holds the mantissa.

The main difference between a binary floating-point number and a BCD floating-point number is that the binary number is in base-2 representation while the BCD number is still in base 10. A binary floating-point number looks like this:

$\pm a \times 2^b$

where a is the mantissa and b is the exponent. In the case of most floating-point formats in use today (e.g., the IEEE formats and the one I presented in "Floating-Point without a Coprocessor"), a is interpreted as a binary fraction, and b is an integer.

In contrast, a BCD floating-point number has the following form:

$\pm a \times 10^b$

Now, a is a decimal fraction in BCD notation; b is still an integer, but this time it indicates a base-10 exponent. But how is a number stored in BCD notation? The process is simple: Take each decimal digit in the number, convert that digit into its 4-bit binary equivalent, and store each 4-bit packet into successive nibbles of the BCD number. So, 32 base 10 would be 00110010 base 2; 458 base 10 would be 010001011000 base 2.

Actually, I'm being needlessly difficult. If you convert a base-10 number into its BCD equivalent and display the results in hexadecimal, it looks just like the original number. So, 458 base 10 converted to a BCD number and displayed in base 16 is just 458. Each decimal digit is stored in 4 bits—exactly the number of bits required to store a hexadecimal digit. (This is particularly nice when you're in a debugging session: You can determine a BCD floating-point number's value on sight. Try that with a binary floating-point number!)

In my September column, I introduced the idea of a *bias* in regard to the exponent. You can think of the bias as an offset that allows the exponent to represent both positive and negative values. This BCD floating-point package uses a bias of 16384; the true value of the exponent is FAC1_EXP − 16384. This translates to a range of ±16383 for the exponent. An exponent of 0 is a special case that indicates true zero, allowing routines to test for zero by looking at a single word rather than having to scan the entire contents of the mantissa.

So what do all these attributes buy you? First, since the BCD floating-point number is in base 10, you don't lose any precision when converting numbers between their external form (ASCII strings on the display) and their internal representation. As a contrasting example, the binary equivalent of 0.1 is a repeating binary fraction, just as the decimal number for ⅓ is 0.333333.... Consequently, 0.1 cannot be precisely converted into a binary floating-point number. Happily, BCD floating-point numbers do not suffer from such rounding errors: 0.1 is easily stored as a BCD floating-point number with no precision loss. On the other hand, BCD floating-point numbers suffer from truncation errors: The number is simply snipped off at the least-significant digit.

Another drawback to using BCD floating-point numbers is that the four basic arithmetic routines are slower than their binary counterparts. This is because you can't use the base number system native to the machine (base 2). Designers of the 8088 (and other popular CPUs) have incorporated special instructions for working with BCD numbers, but these instructions tend to be, shall we say, less robust than their binary counterparts. BCD instructions operate on byte-size quantities only, so you can-

continued

Listing 1: *Pseudocode for BCD floating-point addition and subtraction.*

```
{ Add the contents of FAC1 to the contents of FAC2. The result is
{ returned in FAC1.
FADD:
  IF FAC2_EXP > FAC1_EXP THEN
   Swap FAC1 and FAC2;
  N := FAC1_EXP - FAC2_EXP;
  { N is the difference between the two exponents; i.e., the number
  { of times we have to multiply FAC2 by 10. This multiplication is done
  { by shifting, and if we have to shift more times than there are
  { digits, the number is already in FAC1.
  IF N > NUMBER_OF_DIGITS THEN
     RETURN no error;
  REPEAT N TIMES
   SHIFT FAC2_MAN right 1 nibble;
  IF FAC1_SIGN = FAC2_SIGN
   { The following is a BCD addition.
   FAC1_MAN := FAC1_MAN + FAC2_MAN;
  ELSE
   BEGIN
     { The following is a BCD subtraction.
     FAC1_MAN := FAC1_MAN - FAC2_MAN;
     IF borrow occurs THEN
      BEGIN
        FAC1_SIGN := FAC1_SIGN XOR 80H;
        { The following step may only be necessary on the 80x86 family-
        { thanks to the way it performs BCD operations. To negate the
        { mantissa, set the carry and perform a BCD subtraction from 0.
        Negate FAC1_MAN;
   END
  GOTO NORM_FAC1;
{ Subtract contents of FAC2 from contents of FAC1. Result in FAC1.
FSUB:
  FAC2_SIGN := FAC2_SIGN XOR 80H;
  GOTO FADD;
```

not take advantage of a 16- or 32-bit register. In contrast, a binary floating-point package written specifically for an 80386 or 68000 would perform approximately half the register-to-memory fetches than would an equivalent BCD package on, say, an 8088.

There is a final saving grace for BCD. As I've mentioned, I/O of BCD floating-point numbers is tons simpler and quicker than I/O of binary floating-point numbers. If you review "Floating-Point without a Coprocessor," you'll see that the input and output routines have to perform floating-point multiplication and division by 10; they're time-consuming operations. With BCD floating-point numbers, I/O isn't much more than a series of shift and mask operations. No multiplications are involved.

Addition

You may recognize the similarity between these algorithms and the ones that appeared in my previous discussion of floating-point; if not, you should still have no trouble following the discussion, thanks to the fact that this BCD package lets you work in base 10. These routines use a set of floating-point accumulators (memory locations, actually) to hold the factors of operations taking place. FAC1 and FAC2 are the primary accumulators; FAC3 operates as a temporary holding location. The results always appear in FAC1.

The first step in the BCD floating-point addition routine (see listing 1) is to align exponents (equivalent to aligning decimal points when adding numbers on paper). FADD moves the factor with the largest exponent into FAC1 and then subtracts exponents to determine how many times it is necessary to increment FAC2's exponent to align it with FAC1's. Incrementing FAC2's exponent is offset by dividing FAC2's mantissa by 10; actually, the exponent of FAC2 is never incremented since all that is needed is FAC2's adjusted mantissa. Once the two factors are aligned, FAC1 can provide the exponent, because both exponents are now equal.

This is where BCD floating-point numbers and binary floating-point numbers show their differences: A single shift of a binary floating-point mantissa multiplies or divides (depending on the direction of the shift) the number by 2. Shifting a BCD floating-point mantissa left (or right) by a nibble multiplies (or divides) that number by 10. See the symmetry? The operations are the same; only the bases have changed.

There is the possibility that, in order to align exponents, FADD might have to shift FAC2's mantissa to the right more times than FAC2 has significant digits. In other words, FAC2's magnitude may be negligible relative to FAC1's magnitude—at least within the accuracy of this BCD package. In this case, FADD simply exits without changing the contents of FAC1.

Once the exponents are equal (i.e., the decimal points are aligned), the routine can do the addition. Actually, it may not always be addition; the routine will need to do subtraction if the mantissas' signs are different. If the routine ends up subtracting the numbers, then, on the way out the door, it makes a final check to see if a borrow (subtraction's counterpart to addition's carry) has been generated. A borrow indicates that the sign of the result is opposite FAC1's sign; in other words, FAC1 was a positive number, and FAC2 is a negative number with an absolute value greater than FAC1. Since FAC1 now holds the result, the routine flips its sign accordingly. Finally, the FADD exits through the normalization routine.

Normalization

Normalization preserves a high accuracy for floating-point numbers. Simply put, when you normalize a floating-point number, you move its most-significant digit just to the right of the decimal point. Instead of storing 270 as $.00027 \times 10^6$ (and wasting space on all those zeros), you normalize the number and store it as $.27 \times 10^3$. So normalization simply boils down to multiplying or dividing the mantissa by powers of 10 while incrementing or decrementing the exponent, thereby leaving the value of the number unchanged. You've already seen that multiplication and division of the mantissa can be accomplished by shift operations. The pseudocode for normalization uses the same concepts and should therefore be pretty easy to understand (see listing 2).

Keep in mind that two memory representations of numbers are at play in this package: the internal representation (how the number is stored in the floating-point accumulators) and the external representation (how the number is stored in the local memory of an application). These representations are different, and for good reason. When numbers are being processed by the floating-point package, you are concerned primarily with maintaining accuracy. Space conservation is no real issue; at most, only two numbers are being manipulated at a time. Outside, in the application program that is calling the floating-point package, memory space may be a serious consideration.

For example, the number 0.4332198, normalized and stored in FAC1, would look like this:

0433 2198 0000 0000 0000 0000

Notice that when the number is in internal form, it is shifted to the right one digit. This leaves an empty "overflow" slot that catches any carries (or borrows) out of the most-significant-digit position. Also, the internal representation carries an extra word at the number's least-significant digits (see figure 1). This padding becomes important as numbers are shifted to the right to align exponents for the addition operation; some of the least-significant digits are retained, rather than being shifted off into never-never land.

Subtraction

This is as easy as ever: Flip the sign of FAC2 and call the FADD routine.

Multiplication

In multiplication, the speed advantage of a binary package over a BCD package begins to show through. Multiplication is actually a series of shift-and-add operations. In the binary floating-point package, an addition takes place for each 1 bit in the multiplier. But in this BCD package, the number of additions that takes place is equal to the sum of the multiplier's digits.

Let me illustrate. When the BCD package goes to multiply 15 and 34, it first sets up a product memory location, initialized to 0. Then, it looks at the rightmost digit of the multiplier (4 in this case), which indicates the number of times to add the multiplicand into the product. So the partial product in step 1 is 15+15+15+15=60. Then the routine examines the multiplier's next digit to the left (3), which is the number of times to add the multiplicand *times 10* into the partial product. So the result in this example is 60 + (150+150+150), or 510. For a bigger multiplier, this process would repeat with the multiplicand times 100, times 1000, and so on.

Put another way, the routine is repeatedly multiplying the multiplicand by 10

continued

Listing 2: *Normalizing a BCD floating-point number.*

```
{ Normalize the floating-point number in FAC1.
NORM_FAC1:
  IF FAC1_MAN = 0 THEN
    BEGIN
      FAC1_EXP := 0;
      FAC1_SGN := 0;
      RETURN no error;
    END
  IF highest nibble of FAC1_MAN <> 0 THEN
    BEGIN
      Shift FAC1_MAN right 1 nibble;
      FAC1_EXP := FAC1_EXP + 1;
      IF high bit of FAC1_EXP <> 0 THEN
        RETURN exponent overflow;
    END
  WHILE highest nibble of FAC1_MAN = 0
    BEGIN
      Shift FAC1_MAN left 1 nibble;
      FAC1_EXP := FAC1_EXP - 1;
    END
  IF high bit of FAC1_EXP <> 0 THEN
    RETURN exponent underflow;
  ELSE
    RETURN no error;
```

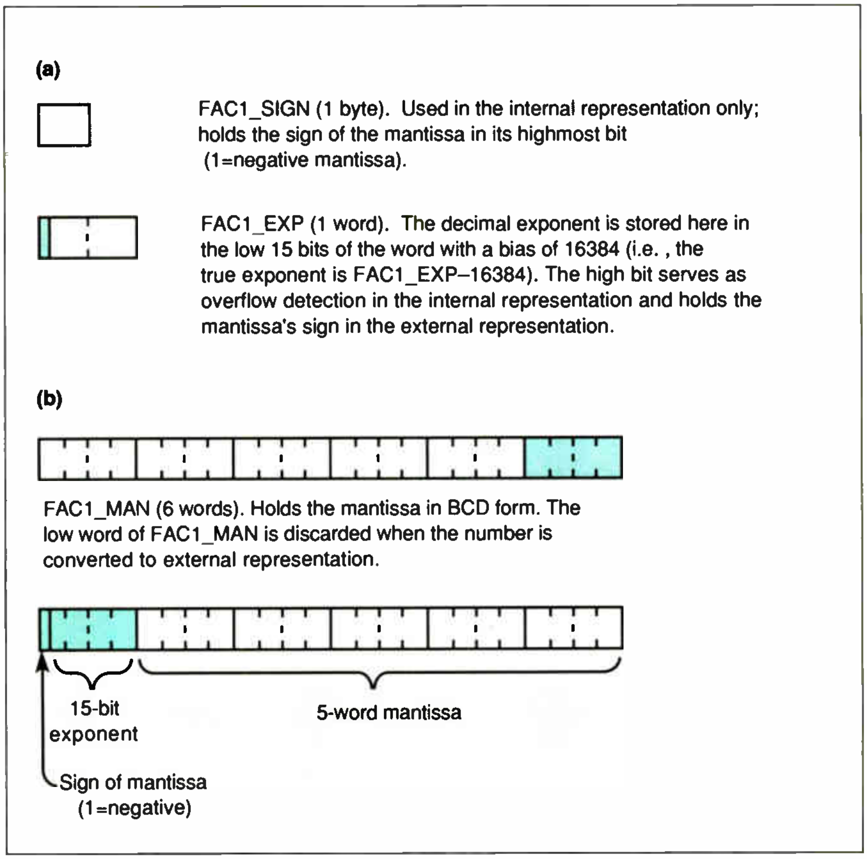

Figure 1: (a) *A BCD floating-point number's internal storage (i.e., when the floating-point package is operating on the number), and* **(b)** *its external storage, as seen by the calling application. This format can handle numbers in the range* $\pm n \times 10^{\pm 16383}$*, where* n *is a decimal fraction with 20 digits of precision.*

Listing 3: *Pseudocode for the multiplication algorithm.*

```
{ Multiply contents of FAC1 and FAC2. Result in FAC1.
FMUL:
  IF (FAC1_EXP = 0) OR (FAC2_EXP = 0) THEN
    BEGIN
      FAC1_MAN := 0;  { Clear the mantissa
      GOTO NORM_FAC1;
    END
  { Calculate exponent of result.
  FAC1_EXP := FAC2_EXP - BIAS + FAC1_EXP;
  IF high bit of FAC1_EXP = 1 THEN
     RETURN exponent overflow error;
  { Determine sign of the result.
  FAC1_SIGN := FAC2_SIGN XOR FAC1_SIGN;
  { Copy FAC1's mantissa to FAC3 and clear FAC1's mantissa in
  { preparation.
  Copy FAC1_MAN into FAC3_MAN;
  FAC1_MAN := 0;
  REPEAT number_of_digits times
    BEGIN
      N := lowest nibble of FAC3;
      REPEAT N times
        { NOTE: The following statement adds two BCD numbers.
        FAC1_MAN := FAC2_MAN + FAC1_MAN;
      IF this is not last time through REPEAT loop THEN
        BEGIN
          Shift FAC1_MAN right 1 nibble;
          Shift FAC3_MAN right 1 nibble;
        END
    END
  GOTO NORM_FAC1;
```

while scanning through the digits of the multiplier. Multiplying by 10 is easy with BCD numbers: You can just shift them 1 nibble to the left. But wait—this package uses BCD fractions, not whole numbers, and shifting to the left might roll the most-significant digits "off the top." Instead, then, the package shifts the product right by 1 nibble for the same effect, and you sacrifice least-significant digits on the right rather than most-significant digits on the left.

So the maximum number of additions that can take place is $9 \times d$, where d is the number of digits in a BCD floating-point mantissa. Now, remember that a BCD digit is stored in a 4-bit nibble, and that, in a binary package, an addition takes place for every 1 bit. You can see that, *per byte*, a BCD floating-point multiply routine may have to perform up to 18 additions (9 per nibble), while a binary floating-point multiply would have to perform 8 at most.

By now you should understand the mechanics of the multiply routine, so I won't go into great detail concerning its pseudocode in listing 3. About the only thing I've left out is how the routine determines the sign of the result. And that's easy: You do an EXCLUSIVE-OR on the signs of the two factors.

Two of the more important functions provided with floating-point libraries are conversion to and from integer.

Division

Multiplication is shift-and-add; division is subtract-and-shift. If you think about what's going on when you perform long division, you know that most of your time is spent calculating how many times a number *a* can go into *b*. For a human, this process is a set of heuristics that usually involve eying the number and taking educated guesses. For a computer, the process is cruder: Keep subtracting *a* from *b* until *b* is less than or equal to 0, and keep track of how many times you perform the subtraction. There—now you know the core of the division algorithm (see listing 4).

It is easy to dissect the other parts of the routine. To determine the sign of the quotient, you use the same technique as in the multiplication routine: You do an EXCLUSIVE-OR on the sign bytes of the factors. Make sure you're watching for a 0 in the denominator (FAC2 in this package), and bail out with an error condition if you see one. Finally, if the numerator (in FAC1) is 0, the routine can exit without having to do any work since the result is already in place.

Conversions

Two of the more important functions provided with floating-point libraries are conversion to and from integer. In the interest of completeness, I include them in this package.

Listing 5 shows the pseudocode for converting a 16-bit integer to a BCD floating-point number. This routine is primarily a process that pulls decimal digits out of the integer number one at a time, shifting each digit into the mantissa and incrementing the exponent for each digit shifted in. You can extract digits from an integer by repeatedly dividing that number by 10 and examining the remainder after each division.

Since a 16-bit number can hold, at most, five decimal digits, the routine need not concern itself with overflow checks. The only extra test is for the sign, at the start of the routine.

Getting a number back to integer from the floating-point format is another matter. There are two possible situations in which the conversion will fail. In case 1, the number is too large to fit into a 16-bit signed integer (whose range, by the way, is ±32767); in case 2, the number has no integer part—that is, it's something like 0.001.

Strictly speaking, case 2 doesn't cause a failure; the routine simply returns a 0 for the result. Handling case 1 is trickier. You can get a quick idea of whether or not the floating-point number will fit by examining its exponent. When FAC1_EXP −BIAS is a positive number, the result is the number of digits to the left of the decimal point. Consequently, if FAC1_EXP −BIAS is greater than 5, the routine can exit immediately: The number is surely too big. Otherwise, checking for overflow has to proceed as the number is converted to integer.

The routine multiplies an accumulator by 10, shifts the topmost digit out of the mantissa, and adds this digit to the accumulator. (The number of digits to shift

continued

Listing 4: *BCD floating-point division.*

```
{ Divide number in FAC1 by number in FAC2.
{ Leave the result in FAC1.
FDIV:
  IF FAC2_EXP = 0 THEN
    RETURN divide by zero error;
  { If FAC1 = 0, the result is zero.
  IF FAC1_EXP = 0 THEN
    RETURN no error;
  { Calculate the exponent of the quotient.
  FAC1_EXP := FAC1_EXP - FAC2_EXP + BIAS;
  { Determine the sign of the quotient.
  FAC1_SIGN := FAC1_SIGN XOR FAC2_SIGN;
  { Clear FAC3 to hold the result.
  FAC3_MAN := 0;
  REPEAT number_of_digits times
    BEGIN
      DO
      { NOTE: The following expression subtracts BCD numbers.
        FAC1_MAN := FAC1_MAN - FAC2_MAN;
        BORROW := TRUE if borrow is generated
               else FALSE;
        IF BORROW=FALSE THEN
          Increment lowest nibble in FAC3_MAN;
      WHILE BORROW=FALSE;
      BEGIN
        { Following is a BCD addition.
        FAC1_MAN := FAC1_MAN + FAC2_MAN;
        IF this is not last time through REPEAT loop THEN
        BEGIN
          Shift FAC1_MAN left 1 nibble;
          Shift FAC3_MAN left 1 nibble;
        END
      END
    END
  FAC1_MAN := FAC3_MAN;
  GOTO NORM_FAC1;
```

Listing 5: *Pseudocode for converting an integer to a floating-point number.*

```
{ Convert integer in IREG to a BCD floating-point number stored in
{ FAC1. NOTE: In this and the floating-to-integer routine, IREG is
{ assumed to hold a 16-bit signed integer.
INT_TO_FLOAT:
  IF IREG < 0 THEN
    FAC1_SIGN := 80H;
  ELSE
    FAC1_SIGN := 0;
  { Clear FAC1 to receive number.
  FAC1_EXP := 0;
  FAC1_MAN := 0;
  { Repeatedly divide IREG by 10. Capture each remainder into top
  { nibble of FAC1_MAN.
  WHILE IREG <> 0 DO
    BEGIN
      IREG := IREG / 10;
      High nibble of FAC1_MAN := remainder from
       previous division;
      Shift FAC1_MAN right 1 nibble;
      FAC1_EXP := FAC1_EXP + 1;
    END
  IF FAC1_EXP <> 0 THEN
    FAC1_EXP := FAC1_EXP + BIAS;
  GOTO NORM_FAC1;
```

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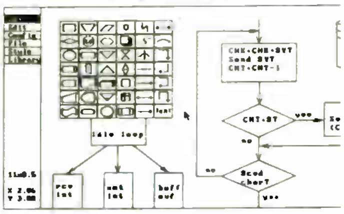

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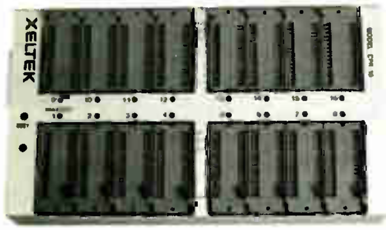

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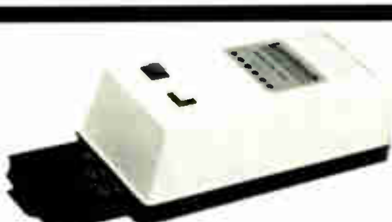

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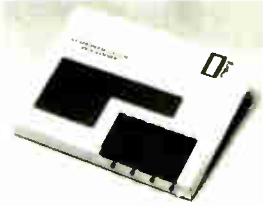

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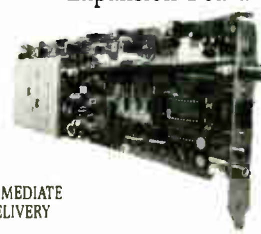

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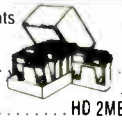

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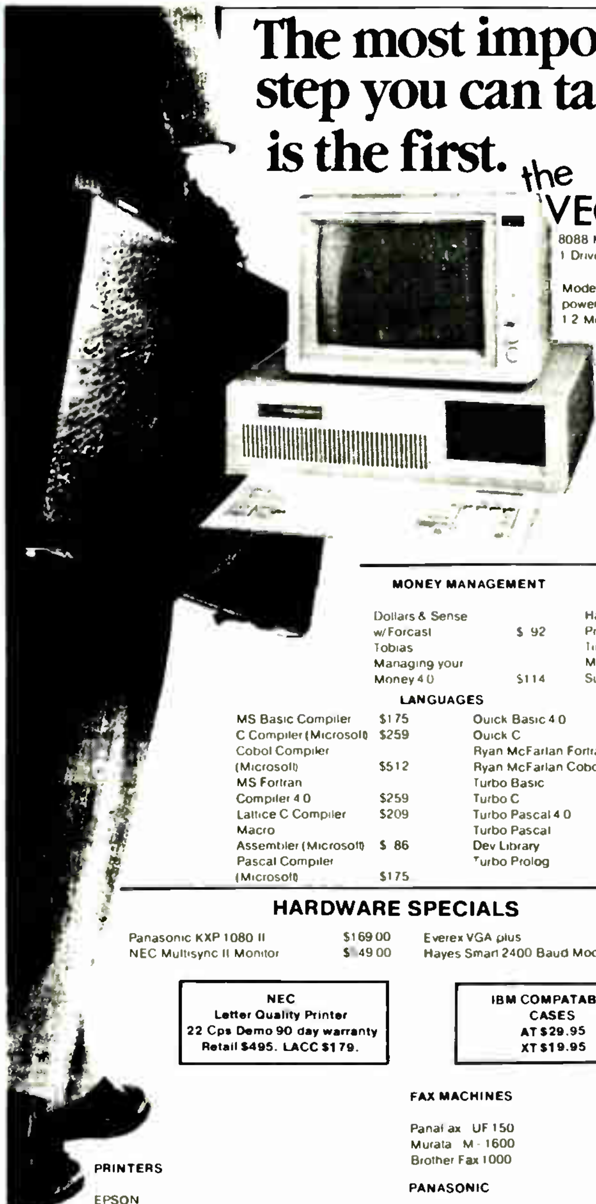

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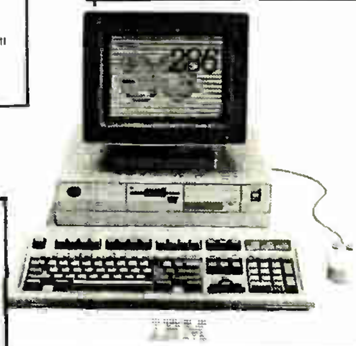

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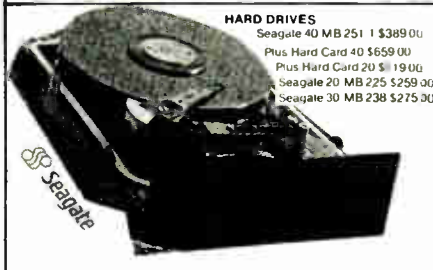

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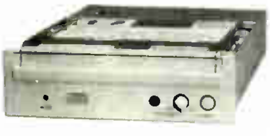

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7476	.45	.35	74367	SALE	.35

74LS

Part No.	1-9	10+	Part No.	1-9	10+
74LS00	SALE	.15	74LS165	.75	.65
74LS02	SALE	.15	74LS166	SALE	.69
74LS04	SALE	.16	74LS173	SALE	.25
74LS05	SALE	.16	74LS174	SALE	.25
74LS06	.59	.49	74LS175	SALE	.25
74LS07	.59	.49	74LS189	SALE	2.95
74LS08	.28	.18	74LS191	SALE	.39
74LS10	SALE	.15	74LS193	.69	.59
74LS14	SALE	.29	74LS221	SALE	.49
74LS27	SALE	.19	74LS240	SALE	.45
74LS30	SALE	.15	74LS243	SALE	.45
74LS32	SALE	.19	74LS244	SALE	.49
74LS42	.49	.39	74LS245	SALE	.59
74LS47	.89	.79	74LS259	.99	.89
74LS73	SALE	.25	74LS273	.89	.79
74LS74	SALE	.19	74LS279	.49	.39
74LS75	SALE	.25	74LS322	3.49	3.39
74LS76	.39	.29	74LS365	SALE	.35
74LS85	.59	.49	74LS366	SALE	.35
74LS86	.29	.19	74LS367	SALE	.29
74LS90	SALE	.29	74LS368	SALE	.35
74LS93	SALE	.29	74LS373	SALE	.59
74LS123	SALE	.35	74LS374	SALE	.49
74LS125	.49	.39	74LS393	SALE	.69
74LS138	.49	.39	74LS590	5.95	5.85
74LS139	SALE	.29	74LS624	1.95	1.85
74LS154	1.19	1.09	74LS629	SALE	1.95
74LS157	.45	.35	74LS640	SALE	.89
74LS158	SALE	.25	74LS645	SALE	.89
74LS163	SALE	.35	74LS670	SALE	.79
74LS164	SALE	.35	74LS688	2.39	2.29

74S/PROMS*

Part No.			Part No.		
74S00	SALE	.19	74S188*		1.49
74S04	SALE	.19	74S189		1.49
74S08	SALE	.19	74S196	SALE	.99
74S10	SALE	.19	74S240		1.39
74S32	SALE	.19	74S244	SALE	.75
74S74	SALE	.19	74S253	SALE	.29
74S85	SALE	.49	74S287*		1.49
74S86	SALE	.19	74S288*		1.49
74S124	SALE	1.25	74S373	SALE	.99
74S174	SALE	.25	74S374	SALE	.99
74S175	SALE	.25	74S472*	SALE	2.49

CD—CMOS

Part No.	Price	Part No.	Price
CD4001	.19	CD4076	.59
CD4008	.59	CD4081	.22
CD4011	.19	CD4082	.22
CD4013	.29	CD4093	.35
CD4016	.29	CD4094	.89
CD4017	.49	CD40103	1.49
CD4018	.59	CD40107	.49
CD4020	.59	CD4510	.69
CD4024	.45	CD4511	.69
CD4027	.35	CD4520	.75
CD4030	.35	CD4522	.79
CD4040	.65	CD4538	.79
CD4049	.29	CD4541	.89
CD4050	.29	CD4543	.79
CD4051	.59	CD4553	3.95
CD4052	.59	CD4555	.79
CD4053	.59	CD4559	7.95
CD4063	1.49	CD4566	1.95
CD4066	.29	CD4583	.59
CD4067	1.49	CD4584	.49
CD4069	.19	CD4585	.69
CD4070	.29	MC14411P	7.95
CD4071	.22	MC14490P	4.49
CD4072	.22		

EEPROMS

Part No.			Price
2816A	2048x8	350ns (9V-15V) 5V Read/Write	5.25
2816A-25	2048x8	250ns (9V-15V) 5V Read/Write	5.49
2817A	2048x8	350ns 5V Read/Write	6.95
2864A	8192x8	250ns 5V Read/Write (Pin 1, No R/B)	13.95
2864A-30	8192x8	300ns 5V Read/Write (Pin 1, No R/B)	12.95
2865A-30	8192x8	300ns 5V Read/Write	9.95
52B13	2048x8	350ns (21V) 5V Read Only	1.49

MICROPROCESSOR COMPONENTS

MISCELLANEOUS CHIPS

Part No.	Price
D765AC	~~3.95~~ 2.95
WD9216	3.95

Z80, Z80A, Z80B SERIES

Part No.	Price
Z80	1.19
Z80-CTC	~~1.29~~ .99
Z80-PIO	~~1.29~~ .99
Z80A	1.29
Z80A-CTC	1.65
Z80A-DART	~~4.95~~ 3.95
Z80A-PIO	~~1.89~~ 1.49
Z80A-SIO/0	~~3.95~~ 2.95
Z80B	2.75
Z80B-CTC	~~3.95~~ 3.25
Z80B-PIO	3.95
Z8530	4.95
Z8671	7.95
Z8681B1	8.95

8000 SERIES

Part No.	Price
8031	~~3.95~~ 3.49
80C31	~~9.95~~ 8.95
8035	~~1.49~~ 1.25
8039	~~1.95~~ 1.59
8052AHBASIC	24.95
8080A	~~2.25~~ 1.49
8085A	2.49
8086	3.95
8086-2	~~6.95~~ 5.49

8000 SERIES Continued

Part No.	Price
8087 (5MHz)	99.95
8087-1 (10MHz)	194.95
8087-2 (8MHz)	139.95
8088 (5MHz)	~~4.95~~ 3.49
8088-2 (8MHz)	~~6.95~~ 5.95
8116	~~4.95~~ 3.95
8155	2.49
8155-2	3.49
8156	2.95
8212	2.29
8224	~~2.25~~ 1.95
8228	~~1.95~~ 1.49
8237-5	~~4.25~~ 3.95
8243	1.75
8250A	~~4.95~~ 3.95
8250B (For IBM)	~~5.95~~ 4.95
8251A	1.69
8253-5	1.95
8254	3.95
8255A-5	2.95
8259-5	~~2.25~~ 1.75
8272	~~3.95~~ 2.95
8279-5	~~2.95~~ 2.75
8741	9.95
8742	~~19.95~~ 17.95
8748 (25V)	7.95

8000 SERIES Continued

Part No.	Price
8748H (HMOS) (21V)	9.95
8749	9.95
8751 (3.5-8MHz)	37.95
8751H (3.5-12MHz)	39.95
8755	~~13.95~~ 12.95
80286-10 LCC	59.95
80287 (6MHz)	159.95
80287-8 (8MHz)	229.95
80287-10 (10MHz)	279.95
80386-16 PGA	309.95
80387-16 (16MHz)	439.95
80387-20 (20MHz)	499.95
80387-25 (25MHz)	599.95
80387-SX (16MHz)	399.95
82284 (8MHz)	9.49
82288 (8MHz)	9.95

DATA ACQUISITION

Part No.	Price
ADC0804LCN	2.79
ADC0808CCN	~~5.95~~ 5.49
ADC0809CCN	~~3.69~~ 3.29
ADC1205CCJ-1	19.95
DAC0808LCN	~~1.75~~ 1.49
DAC1008LCN	5.95
AY-3-1015D	~~4.95~~ 3.95
AY-5-1013A	1.95

STATIC RAMS

Part No.	Function		Price
2016-12	2048x8	120ns	~~4.95~~ 3.75
2018-45	2048x8	45ns 300MIL	6.95
2102	1024x1	350ns	.89
2114N	1024x4	450ns	~~.99~~ .79
2114N-2L	1024x4	200ns Low Power	1.49
21C14	1024x4	200ns (CMOS)	.49
5101	256x4	450ns (CMOS)	~~2.95~~ 2.49
6116P-1	2048x8	100ns (16K) CMOS	~~6.25~~ 3.95
6116P-3	2048x8	150ns (16K) CMOS	~~4.95~~ 3.49
6116LP-1	2048x8	100ns (16K) LP CMOS	~~6.49~~ 4.39
6116LP-3	2048x8	150ns (16K) LP CMOS	~~5.99~~ 3.95
6264P-10	8192x8	100ns (64K) CMOS	~~10.49~~ 9.95
6264P-15	8192x8	150ns (64K) CMOS	9.25
6264LP-10	8192x8	100ns (64K) LP CMOS	10.95
6264LP-12	8192x8	120ns (64K) LP CMOS	10.49
6264LP-15	8192x8	150ns (64K) LP CMOS	10.25
6514	1024x4	350ns (CMOS)	~~3.75~~ 3.49
43256-10L	32,768x8	100ns (256K) Low Power	20.95
43256-15L	32,768x8	150ns (256K) Low Power	18.95
62256LP-85	32,768x8	85ns (256K) LP CMOS	22.95
62256LP-10	32,768x8	100ns (256K) LP CMOS	21.95
62256LP-12	32,768x8	120ns (256K) LP CMOS	20.95

DYNAMIC RAMS

Part No.	Function		Price
THM91000L-10	1,048,576x9	100ns 1Meg x9 SIP	~~549.95~~ 399.95
THM91000S-10	1,048,576x9	100ns 1Meg x9 SIM	399.95
THM91000L-80	1,048,576x9	80ns 1Meg x9 SIP	419.95
THM91000S-80	1,048,576x9	80ns 1Meg x9 SIM	419.95
TMS4416-12	16,384x4	120ns	~~7.75~~ 6.75
TMS4416-15	16,384x4	150ns	~~7.25~~ 6.25
4116-15	16,384x1	150ns (MM5290N-2)	~~1.39~~ 1.25
4128-15	131,072x1	150ns (Piggyback)	5.29
4164-100	65,536x1	100ns	3.49
4164-120	65,536x1	120ns	2.95
4164-150	65,536x1	150ns	2.59
41256-60	262,144x1	60ns	14.49
41256-80	262,144x1	80ns	13.49
41256-100	262,144x1	100ns	12.49
41256-120	262,144x1	120ns	11.95
41256-150	262,144x1	150ns	11.49
41264-12	64Kx4	120ns Video RAM	19.95
41464-10	65,536x4	100ns	~~15.95~~ 14.49
41464-12	65,536x4	120ns	~~14.49~~ 11.95
41464-15	65,536x4	150ns	~~13.95~~ 10.95
51258-10	262,144x1	100ns Static Column	13.95
85227-10PL	262,144x9	100ns 256Kx9 SIP	149.95
85227-10PS	262,144x9	100ns 256Kx9 SIM	149.95
511000P-10	1,048,576x1	100ns (1 Meg)	~~39.95~~ 29.95
511000P-80	1,048,576x1	80ns (1 Meg)	42.95
511000P-85	1,048,576x1	85ns (1 Meg)	~~44.95~~ 41.95
514256P-10	262,144x4	100ns (1 Meg)	~~44.95~~ 34.95

EPROMS

Part No.			Price
TMS2516	2048x8	450ns (25V)	~~6.95~~ 5.95
TMS2532	4096x8	450ns (25V)	~~5.95~~ 4.95
TMS2532A	4096x8	450ns (12.5V)	~~4.49~~ 3.25
TMS2564	8192x8	450ns (25V)	~~6.95~~ 4.95
TMS2716	2048x8	450ns (−5V, +5, +12V)	~~6.95~~ 6.49
1702A	256x8	(1μs)	4.95
2708	1024x8	450ns	6.95
2716	2048x8	450ns (25V)	3.75
2716-1	2048x8	350ns (25V)	~~4.25~~ 3.95
27C16	2048x8	450ns (25V) CMOS	~~4.25~~ 3.75
2732	4096x8	450ns (25V)	3.95
2732A-20	4096x8	200ns (21V)	4.25
2732A-25	4096x8	250ns (21V)	3.95
27C32	4096x8	450ns (25V) CMOS	~~4.95~~ 4.25
2764-20	8192x8	200ns (21V)	4.25
2764-25	8192x8	250ns (21V)	3.59
2764-45	8192x8	450ns (21V)	3.39
2764A-25	8192x8	250ns (12.5V)	3.69
27C64-15	8192x8	150ns (12.5V) CMOS	5.95
27128-20	16,384x8	200ns (21V)	~~6.95~~ 6.49
27128-25	16,384x8	250ns (21V)	5.95
27128A-15	16,384x8	150ns (12.5V)	7.75
27128A-20	16,384x8	200ns (12.5V)	~~5.75~~ 5.25
27128A-25	16,384x8	250ns (12.5V)	~~5.25~~ 4.95
27C128-25	16,384x8	250ns (21V) CMOS	5.95
27256-15	32,768x8	150ns (12.5V)	8.95
27256-20	32,768x8	200ns (12.5V)	~~6.95~~ 6.25
27256-25	32,768x8	250ns (12.5V)	5.49
27C256-15	32,768x8	150ns (12.5V) CMOS	7.95
27C256-25	32,768x8	250ns (12.5V) CMOS	~~6.25~~ 5.49
27512-20	65,536x8	200ns (12.5V)	10.95
27512-25	65,536x8	250ns (12.5V)	9.95
27C512-25	65,536x8	250ns (12.5V) CMOS	~~10.25~~ 9.49
27C1024	131,072x8	200ns (12.5V) CMOS (1 Meg)	27.95

6500/6800 68000 Series

Part No.	Price
6502	2.65
65C02 (CMOS)	7.75
6520	1.95
6522	2.95
6532	~~5.49~~ 4.95
6551	2.95
65C802 (CMOS)	~~15.95~~ 14.75
6800	~~1.95~~ 1.49
6802	2.95
6810	~~1.25~~ .99
6821	1.75
6840	~~3.49~~ 2.95
6845	~~2.75~~ 2.49
6850	~~1.95~~ 1.49
6852	~~.75~~ .59
6854	~~1.19~~ .99
MC68000L8	9.95
MC68000L10	~~11.95~~ 10.95
MC68010L10	~~49.95~~ 39.95
MC68020RC12B	99.95
MC68701	14.95
MC68705P3S	~~9.95~~ 8.95
MC68705U3L	10.95
MC68705U3S	10.95
MC68450L10	49.95
MC68881RC12A	129.95

Commodore

Part No.	Price
LAG570	9.95
WD1770	~~8.95~~ 7.49
S13052P	~~1.25~~ .99
6504A	1.19
6507	2.95
6510	12.95
6522	2.95
6525	~~4.95~~ 3.95
6526	~~14.95~~ 13.95
6532	~~5.49~~ 4.95
6545-1	3.95
6560	~~10.95~~ 8.95
6567	24.95
6569	~~15.95~~ 13.95
6572	~~10.95~~ 7.95
6581 (12V)	~~12.95~~ 10.49
6582 (9V)	~~14.95~~ 12.95
8502	7.95
8564	~~4.95~~ 2.95
8566	~~9.95~~ 7.95
8701	9.95
8721	~~14.95~~ 9.95
8722	~~13.95~~ 10.95
310654-05	9.95
318018-03	~~12.95~~ 10.95
318019-03	~~12.95~~ 10.95
*82S100PLA**	15.95
901225-01	~~15.95~~ 13.95
901226-01	~~15.95~~ 13.95
901227-03	15.95
901229-05	15.95

*No specs. available
**Note: 82S100PLA U17 (C-64)

74C/CMOS

Part No.		Price	Part No.		Price
74C00	Sale	.19	74C174	Sale	.39
74C02	Sale	.19	74C175	Sale	.59
74C04	Sale	.19	74C192	Sale	.99
74C08	Sale	.19	74C194	Sale	.49
74C10	Sale	.15	74C221		1.79
74C14		.49	74C240	Sale	.75
74C32	Sale	.19	74C244		1.79
74C74		.49	74C373	Sale	1.49
74C85		1.49	74C374	Sale	1.49
74C86	Sale	.19	74C911		6.95
74C89		2.95	74C912	Sale	7.95
74C90		.99	74C915	Sale	1.19
74C151	Sale	1.49	74C917	Sale	3.95
74C154	Sale	2.75	74C920	Sale	3.95
74C157	Sale	1.25	74C921	Sale	2.95
74C160	Sale	.49	74C922		3.95
74C161	Sale	.49	74C923		3.95
74C162	Sale	.49	74C925		5.49
74C173		.59	74C926		5.95

MISC. COMPONENTS

TANTALUM CAPACITORS

TM 1/35	1μf @ 35V	.19	TM4.7/35	4.7μf @ 35V	.45
TM1/35	1μf @ 35V	.19	TM6.8/35	6.8μf @ 35V	.49
TM2.2/35	2.2μf @ 35V	.25	TM10/35	10μf @ 35V	.59

POTENTIOMETERS

Values available (insert ohms into space marked "XX"): 500Ω, 1K, 2K, 5K, 10K, 20K, 50K, 100K, 200K, 1MEG

43PXX	¾ Watt, 15 Turn	.99	63PXX	½ Watt, 1 Turn	.89

TRANSISTORS AND DIODES

PN2222	.13	PN2907	.13	1N4004	.12
2N2222A	.29	2N4401	.12	1N4148	.07
2N3055	.65	1N270	.25	1N4735	.29
2N3904	.12	1N751	.15	C106B1	.49

SWITCHES

JMT123	SPDT, On-On	1.19	206-8	SPDT, 16 pin DIP	1.25
MPC121	SPDT, On-Off-On	1.19	MS102	SPST, Momentary	.39

D-SUB CONNECTORS

DB25P	Male, 25-pin	.69	DB25S	Female, 25-pin	.75

LEDS

XC556R T1¾, Red	.13	XC556Y T1¾, Yellow	.17
XC556G T1¾, Green	.17	XC556C T1¾, Clear/Red	.17

IC SOCKETS

Low Profile		Wire Wrap (Gold) Level #2	
8LP	.11	8WW	.59
14LP	.12	14WW	.65
16LP	.13	16WW	.69
24LP	.25	24WW	1.19
28LP	.27	28WW	1.39
40LP	.29	40WW	1.89

Soldertail Standard (Gold & Tin) & Header Plug Sockets Also Available

74HC HI-SPEED CMOS

Part No.		Price	Part No.		Price
74HC00	SALE	.17	74HC175		.59
74HC02	SALE	.17	74HC221	SALE	.69
74HC04	SALE	.17	74HC240	SALE	.59
74HC08	SALE	.17	74HC244		.79
74HC10	SALE	.19	74HC245	SALE	.69
74HC14		.29	74HC253	SALE	.39
74HC30	SALE	.19	74HC259		.49
74HC32	SALE	.25	74HC273	SALE	.49
74HC74	SALE	.29	74HC373		.69
74HC75	SALE	.29	74HC374	SALE	.59
74HC76	SALE	.29	74HC595		1.29
74HC85		.55	74HC688	SALE	1.25
74HC86	SALE	.25	74HC943	SALE	7.95
74HC123	SALE	.49	74HC4040	SALE	.79
74HC125		.49	74HC4049	SALE	.25
74HC132	SALE	.39	74HC4050	SALE	.25
74HC138		.45	74HC4060	SALE	.69
74HC139	SALE	.35	74HC4511	SALE	.99
74HC154		1.49	74HC4514		1.79
74HC163	SALE	.39	74HC4538	SALE	.99
74HC174		.59	74HC4543	SALE	.99

74HCT — CMOS TTL

Part No.		Price	Part No.		Price
74HCT00	SALE	.15	74HCT139	SALE	.29
74HCT02	SALE	.15	74HCT157	SALE	.19
74HCT04	SALE	.17	74HCT174	SALE	.25
74HCT08	SALE	.15	74HCT175	SALE	.25
74HCT10	SALE	.15	74HCT240	SALE	.59
74HCT32	SALE	.17	74HCT244	SALE	.49
74HCT74	SALE	.25	74HCT245	SALE	.49
74HCT86	SALE	.15	74HCT373	SALE	.45
74HCT138		.39	74HCT374	SALE	.45

LINEAR

Part No.	Price	Part No.	Price
DS0026CN	1.95	LM1458N	.35
TL074CN	.99	LM1488N	.45
TL084CN	.89	DS14C88N (CMOS)	1.19
AF100-1CN	8.95	LM1489N	.45
LM307N	.39	DS14C89N (CMOS)	1.19
LM309K	1.25	LM1496N	.69
LM311N	.39	MC1648P	2.95
LM317T	.65	LM1871N	1.95
LM318N	.99	LM1872N	1.95
LM319N	1.29	LM1896N-1	1.49
LM323K	3.49	ULN2003A	.75
LM324N	.35	XR2206	3.95
LM338K	4.49	XR2211	2.95
LM339N	.39	XR2243	1.95
LF347N	1.49	26LS29	2.95
LM348N	.69	26LS31	.99
LM350T	2.95	26LS32	.99
LF351N	.39	26LS33	1.49
LF353N	.49	LM2901N	.25
LF355N	.79	LM2907N	1.29
LF356N	.79	LM2917N (8 pin)	1.79
LF357N	.89	MC3446N	.99
LM358N	.49	MC3450P	.49
LM360N	1.95	MC3470P	1.19
LM361N	1.49	MC3471P	.99
LM380N-8	.99	MC3479P	3.95
LM386N-3	.89	MC3486P	1.19
LM387N	1.09	MC3487P	.99
LM393N	.39	LM3900N	.49
LM399H	3.49	LM3905N	1.19
LF411CN	.79	LM3909N	.89
TL497ACN	1.49	LM3914N	1.79
NE540H (C540H)	.99	LM3916N	1.49
NE555V	.29	NE5532	.69
XRL555	.59	NE5534	.69
LM556N	.45	7805K (LM340K-5)	1.29
NE558N	.79	7812K (LM340K-12)	1.29
LM565N	.89	7815K (LM340K-15)	1.29
LM567V	.75	7805T (LM340T-5)	.45
NE592N	.75	7812T (LM340T-12)	.45
LM741CN	.29	7815T (LM340T-15)	.45
LM747CN	.49	7905K (LM320K-5)	1.35
MC1350P	.59	7905T (LM320T-5)	.49
MC1377P	2.29	75472	.49
MC1398P	4.95	75477	.89
LM1414N	.99	MC145106P	1.95
		MC145406P	2.95

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TEST EQUIPMENT

Metex Digital Multimeters

M4650 Pictured

Metex General Specs:
- Handheld, high accuracy
- AC/DC Voltage, AC/DC Current, Resistance, Diodes, Continuity, Transistor hFE • Manual ranging w/overload protection

M3650/B & M4650 only:
- Also measures frequency and capacitance

M4650 only:
- Data Hold Switch
- 4.5 Digit

M3610	3.5 Digit Multimeter	$49.95
M3650	3.5 Digit w/Freq. & Capacitance	$69.95
M3650B	Same as M3650 w/Bargraph	$79.95
M4650	4.5 Dig. w/Freq., Capacitance and Data Hold Switch	$99.95

Metex Autoranging Jumbo Readout DMM

- AC/DC Voltage, AC/DC Current, Resistance, Diodes, Continuity and Frequency
- 3.75 Digit (.8" High)
- Ruggedized, Water-resistant case
- Easy-to-use pushbutton switches

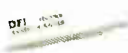
M80....................$59.95

COMPUTER ACCESSORIES

DFI Handy Scanner and 3 Button Mice for IBM PC/XT/AT

FREE DPE Software with HS3000!

The HS3000 offers a full 4 inch window at 400dpi resolution. Scan photos, logos, drawings, etc. Can be used with today's most popular applications.



HS3000 Pictured

HS3000	Handy Scanner	$199.95
DMS200	200DPI 3-Button Ser. Mouse	$49.95
DMS200S	200DPI 3-Button Ser. Mouse with Dr. Halo Software	$59.95

Additional IBM Compatible Accessories

JE2010 Pictured

JE1030 Pictured

JE1010	Flip-Top Standard PC/XT Case	$34.95
JE1014	Flip-Top Baby XT Turbo Case	$69.95
JE1017	Flip-Top Baby AT Case ~~$69.95~~	$54.95
JE1018	Slide Baby AT Case ~~$69.95~~	$59.95
JE1030	150 watt PC/XT Power Supply	$59.95
JE1032	200 watt Baby AT Power Supply	$89.95
JE2010	Tower Case w/250 watt Pwr. Supply	$299.95

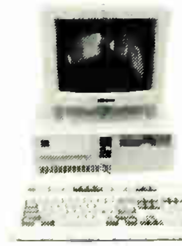
2400/1200/300 Baud Modems *Datatronics*

- Hayes command compatible
- Bell 103/212A compatible
- Auto dial/auto answer • FCC approved • 1 year warranty • Includes MaxiMite Communication Software (except 1200P)

1200P	1200/300 Baud Pocket Size Modem	$99.95
1200H	1200/300 Baud Internal Modem	$69.95
2400H	2400/1200/300 Baud Internal Modem	$129.95
1200C	1200/300 Baud External Modem	$99.95
2400C	2400/1200/300 External Modem	$169.95

Jameco 12MHz IBM AT Compatible Kit With 512K RAM

- *Free! QAPLUS Diagnostic Software Included!*
- *Free! PC Write Word Processing Software Included!*
- *512K RAM Included, Expandable to 4MB*
- *AMI BIOS ROMs Included*
- *8 or 12MHz Operation*
- *Regular $934.52 value for only $799.95!*
- *Flip-Top Case w/200 Watt Power Supply*
- *1.2MB Disk Drive*
- *13.7 Norton SI Rating*
- *101-Key (Enhanced) Keyboard*



Shown with EGA Option (not included) JE1059 Monitor and Adapter Card $519.95 (See Below)

JE3008	12MHz IBM AT Compatible Kit	$799.95
EZDOS	Digital Research MS/PC-DOS Comp. Operating Sys.	$49.95
EZDOSP	Same as above with TrueBASIC	$69.95

IBM COMPATIBLE DISPLAY MONITORS

AMBER	12" Amber Mono	$99.95
CTX2410	14" RGB 640x200	$279.95
TM5154	EGA 14" 720x350	$399.95
JE1059	EGA Monitor & Card	$519.95
TM5155	14" Multiscan 800x560	$499.95
QC1478	14" VGA 720x480	$449.95
JE2055	VGA Monitor & Card	$649.95

NEW

QC1478 Pictured

JAMECO IBM PC/XT/AT COMPATIBLE CARDS

JE1041	20/40MB Hard Disk Controller Card (PC/XT)	$79.95
JE1043	360K/720K/1.2MB/1.44MB Floppy Disk Controller Card (PC/XT/AT)	$49.95
JE1044	360K Floppy/Hard Disk Controller Card (PC/XT)	$129.95
JE1045	360K/720K/1.2MB/1.44MB Floppy/Hard Disk Controller Card (AT)	$149.95
JE1050	Monochrome Graphics Card w/Parallel Printer Port (PC/XT/AT)	$59.95
JE1052	Color Graphics Card w/Parallel Printer Port (PC/XT/AT)	$49.95
JE1055	EGA Card w/256K Video RAM (PC/XT/AT)	$159.95
GC1500	Orchid 8-Bit VGA Card w/256K Video RAM (PC/XT/AT)	$269.95
GC1501	Orchid 8/16 Bit VGA Card w/256K Video RAM (PC/XT/AT)	$349.95
JE1060	I/O Card w/Serial, Game, Printer Port & Real Time Clock (PC/XT)	$59.95
JE1061	RS232 Serial Half Card (PC/XT)	$29.95
JE1062	RS232 Serial Half Card (AT)	$34.95
JE1065	I/O Card w/Serial, Game and Parallel Printer Port (AT)	$59.95
JE1071	Multi I/O Card w/Controller & Monochrome Graphics (PC/XT)	$119.95
JE1081	2MB Expanded or Extended Memory Card (zero-K on-board) (AT)	$119.95

SEAGATE HALF-HEIGHT HARD DISK DRIVES

ST225	20MB Drive only (PC/XT/AT)	$224.95
ST225XT	20MB w/Controller (PC/XT)	$269.95
ST225AT	20MB w/Controller (AT)	$339.95
ST238	30MB Drive only (PC/XT/AT)	$249.95
ST238XT	30MB w/Controller (PC/XT)	$299.95
ST238AT	30MB w/Controller (AT)	$389.95
ST251	40MB Drive only (PC/XT/AT)	$379.95
ST251XT	40MB w/Controller (PC/XT)	$419.95
ST251AT	40MB w/Controller (AT)	$489.95
ST251-1	40MB Fast 28ms (Drive only)	$469.95

Your One-Stop Center for Hard Disk Drive Needs!

ST225XT Pictured

Seagate 60MB Hard Disk Drives Also Available!

IBM PC/XT/AT COMPATIBLE MOTHERBOARDS

JE1001	4.77/8MHz (PC/XT)	$89.95
JE1002	4.77/10MHz (PC/XT)	$109.95
JE3005	Baby 8/12MHz (AT)	$329.95
JE3010	Baby 8/16MHz *NEAT* (AT)	$469.95
JE3020	Baby 16MHz 80386 (AT)	$1199.95
JE3025	Baby 20MHz 80386 (AT)	$1499.95
JE3026	Full Size 25MHz 80386 (AT)	$2299.95

JE3025 Pictured

IBM PC/XT/AT COMPATIBLE 3.5"/5.25" DISK DRIVES

352KU	3.5" 720KB (PC/XT/AT)	$109.95
356KU	3.5" 1.44MB (PC/XT/AT)	$129.95
JE1020	5.25" 360KB (PC/XT/AT) Black	$89.95
JE1021	5.25" 360KB (PC/XT/AT) Beige	$89.95
JE1022	5.25" 1.2MB (PC/XT/AT) Beige	$99.95

JE1022 Pictured

SUPER SONY SALE

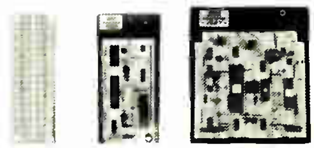
720KB 3.5" Floppy Drive

- For use with IBM PC/XT/AT and compatible computers • Double sided, double density • 135TPI • 160 tracks • Rotation speed: 300rpm • Size: 4"W x 6"D x 1"H

MPF11........................$79.95

PROTOTYPING PRODUCTS

Jameco Solderless Breadboards



JE23 JE24 JE27

Part No.	Dim. L" x W"	Contact Points	Binding Posts	Price
JE21	3.25x2.125	400	0	$4.95
JE23	6.5x2.125	830	0	$7.95
JE24	6.5x3.125	1,360	2	$12.95
JE25	6.5x4.25	1,660	3	$19.95
JE26	6.875x5.75	2,390	4	$24.95
JE27	7.25x7.5	3,220	4	$34.95

COMPUTER ACCESSORIES

Jameco IBM PC/XT/AT Compatible Keyboards

JE2016 Pictured

JE1015	Standard AT Layout (PC/XT/AT)	$59.95
JE1016	Enhanced Layout (PC/XT/AT)	$69.95
JE2016	Enh. w/Solar Calculator (PC/XT/AT)	$79.95

Jameco Switch Boxes

- Female Connectors
- All Pins Switched

JE1170 Pictured

Part No.	Description	Price
JE1170	DB25 pin A/B Switch	$22.95
JE1171	DB25 pin A/B/C Switch	$27.95
JE1172	DB25 pin A/B/C/D Switch	$29.95
JE1173	Centronics36 pin A/B Switch	$24.95
JE1174	Centronics36 pin A/B/C Switch	$27.95

30MB Hard Disk Card for IBM PC/XT

SA30.....................................$379.95

40MB Tape Back-Up for IBM PC/XT/AT

DJ10	40MB Tape Back-Up and Tape	$349.95
TB40	40MB Tape Cartridge	$24.95

ENGINEERING/DATA BOOKS

21035	Sams TTL Cookbook (88)	$14.95
21398	Sams CMOS Cookbook (88)	$19.95
22453	Sams Op Amp Cookbook (88)	$21.95
270645	Intel 8 bit Controller Hndbk (89)	$19.95
270646	Intel 16 bit Controller Hndbk (89)	$19.95
270647	Intel 32 bit Controller Hndbk (89)	$19.95
400041	NSC Linear Data Book Vol 1 (88)	$14.95
400042	NSC Linear Data Book Vol 2 (88)	$9.95
400043	NSC Linear Data Book Vol 3 (88)	$9.95
ICM89	1989 IC Master (3 Volume Set)	$129.95

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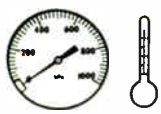
Circle 140 on Reader Service Card

A-BUS™ NEWS

REMOTE DATA ACQUISITION AND CONTROL



Although affordable, powerful and easy to use, the A-BUS I/O system until recently had a major limitation: it had to be located close to the controlling computer. Now two new serial adapters from Alpha Products have removed this restriction. Any computer with an RS232 port can control the A-BUS line of data acquisition and control cards.

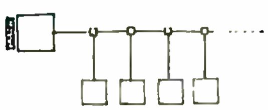
Using standard telephone type cable, the A-BUS system can be located up to 500 feet away from the computer. With the addition of a Modem the A-BUS system can be controlled from anywhere. As with all A-BUS cards, the adapters are easily installed and are programmed using standard commands.

NEW SERIAL PROCESSOR HAS BRAIN

Besides implementining a full A-BUS on a serial port, the low cost SP-127 A-BUS Serial Processor fills a great need in remote data acquisition. It includes a complete BASIC interpreter and can run programs independently of the host computer. This distributed processing relieves the host of housekeeping chores and low level decision making. The SP-127 can read and log data at set intervals for later reviewing or recalling at the host's convenience.

The Serial Processor, which communicates with any computer through an RS232 port, includes a complete BASIC interpreter and 32K of memory. Adding a Modem turns the SP-127 into a automated remote data and control station.

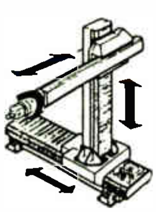
THE A-BUS ON NETWORK



Unique features such as the new "Serial Nodes" greatly expand the usefulness of the A-BUS. These inexpensive ($49) devices provide the ability to connect up to 16 complete A-BUS systems to a single serial port on any computer. The node also functions as a repeater to increase the reach of the adapter beyond the 500 foot limit

The nodes work in conjunction with the company's SA-129 Serial A-BUS Adapter. Plant-wide data collection and control will become widespread thanks to the system's low cost, outstanding capabilities, and ease of use.

ADVANCE IN MOTION CONTROL



Seeking new heights in motion control and robotics, Alpha's Smart Quad Stepper Controller outperforms systems costing 5-10 times more. This $299 board includes a multitasking microprocessor capable of controlling 4 stepper motors simultaneously at speeds up to 1000 steps per second. Four Axis positioning is perfect for robot arms, positioners, pick and place, etc.

Commands are intuitive; plain English words and a forgiving syntax make it easy to write (and edit) command sequences. Scaling factors allow for meaningful units of your choice, and 32 bit floating point arithmetic ensures accurate calculations. The "learn" feature involves storing a series of movements so that even a complex sequence can be repeated easily. Alpha's engineers thoughtfully included direct drivers for small motors, and a variety of inputs (limit switches, remote keypad, panic button, etc.).

An SC-149 can be set up quickly and easily, minimizing development time and allowing more effort to be devoted to the rest of the robotic project.

ALPHA Products
a Sigma Industries Company
(203) 656-1806 Darien, CT 06820

Circle 19 on Reader Service Card

World Radio History

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• COMPLETE CUSTOMER SATISFACTION • SUPERIOR SERVICE • FRIENDLY, KNOWLEDGEABLE SALES STAFF

EPROMS

PART	SIZE	SPEED	Vpp	PRICE
2708	1024x8	450ns	25V	4.95
2716	2048x8	450ns	25V	3.49
2716-1	2048x8	350ns	25V	3.95
2732	4096x8	450ns	25V	3.95
2732A	4096x8	250ns	21V	3.95
27C64	8192x8	250ns	12.5V	4.95
2764	8192x8	450ns	12.5V	3.49
2764-250	8192x8	250ns	12.5V	3.69
2764-200	8192x8	200ns	12.5V	4.25
27128	16384x8	250ns	12.5V	4.95
27128A-200	16384x8	200ns	12.5V	5.95
27C256	32768x8	250ns	12.5V	7.95
27256	32768x8	250ns	12.5V	5.95
27256-200	32768x8	200ns	12.5V	7.95
27512	65536x8	250ns	12.5V	11.95
27C512	65536x8	250ns	12.5V	12.95
27C101-20	131072x8	200ns	12.5V	34.95

■ CALL TO CONFIRM CURRENT PRICES ■

STATIC RAMS

PART	SIZE	SPEED	PRICE
2114	1024x4	450ns	.99
2114L-2	1024x4	200ns	1.49
TC5516	2048x8	250ns	3.95
TMM2016-200	2048x8	200ns	3.25
TMM2016-150	2048x8	150ns	3.29
TMM2016-100	2048x8	100ns	4.29
HM6116-4	2048x8	200ns	4.95
HM6116-3	2048x8	150ns	5.95
HM6116-2	2048x8	120ns	6.45
HM6116LP-4	2048x8	200ns	5.95
HM6116LP-3	2048x8	150ns	6.45
HM6116LP-2	2048x8	120ns	6.95
HM6264LP-15	8192x8	150ns	9.95
HM6264LP-12	8192x8	120ns	10.95
HM43256LP-15	32768x8	150ns	17.95
HM43256LP-12	32768x8	120ns	19.95
HM43256LP-10	32768x8	100ns	24.95

■ CALL TO CONFIRM CURRENT PRICES ■

DYNAMIC RAMS

PART	SIZE	SPEED	PRICE
4116-200	16384x1	200ns	.89
4116-150	16384x1	150ns	.99
4164-150	65536x1	150ns	2.89
4164-120	65536x1	120ns	3.19
4164-100	65536x1	100ns	3.95
TMS4416	16384x4	150ns	8.95
41128-150	131072x1	150ns	5.95
TMS4464-15	65536x4	150ns	14.95
TMS4464-12	65536x4	120ns	15.95
41256-150	262144x1	150ns	12.45
41256-120	262144x1	120ns	12.95
41256-100	262144x1	100ns	13.45
41256-80	262144x1	80ns	13.95
MK4332	32768x1	200ns	6.95
HM51258-100	262144x1	100ns	13.95
1 MB-120	1048576x1	120ns	34.95
1 MB-100	1048576x1	100ns	37.95

■ CALL TO CONFIRM CURRENT PRICES ■

CO-PROCESSORS

8087	5 MHz	99.95
8087-2	8 MHz	139.95
8087-1	10 MHz	194.95
80287	6 MHz	159.95
80287-8	8 MHz	229.95
80287-10	10 MHz	289.95
80387-16	16 MHz	449.95
80387-SX	16 MHz	599.95
80387-20	20 MHz	599.95
80387-25	25 MHz	699.95

intel 5 YEAR WARRANTY

INCLUDES MANUAL & SOFTWARE GUIDE

EPROM ERASERS

DATARASE II $39.95

■ SHIRT POCKET SIZE!
■ ERASES MOST EPROMS/EPLD'S IN 3 MINUTES ■ ALL SIZES UP TO 4 AT A TIME

DATARASE II

SPECTRONICS CORPORATION

Model	Timer	# of Chips	Intensity (uW/Cm²)	Unit Cost
PE-140	NO	9	8,000	$ 89
PE-140T	YES	9	8,000	$139
PE-240T	YES	12	9,600	$189

HIGH-TECH SPOTLIGHT

MAXTOR DISK DRIVES

THESE DRIVES ARE BIG, FAST AND BUILT TO LAST!

AN EXCELLENT VALUE! GIVE YOUR SYSTEM EXTRA SPEED AND STORAGE SPACE. THESE HIGH-QUALITY MAXTOR DRIVES ARE BUILT FOR YEARS OF DEPENDABLE SERVICE– THEY HAVE A FIELD-PROVEN MTBF (MEAN TIME BETWEEN FAILURES) OF NEARLY 8 YEARS.

MB	MODEL	AVG. SPEED	INTERFACE	PRICE
120MB	XT-1140	27MS	ST-506	$1595
160MS	XT-2190	29MS	ST-506	$1695
340MB	XT-4380S	16MS	SCSI	$2395

PARTIAL LISTINGS ONLY! CALL FOR COMPLETE CATALOG

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6500

6502	2.25
6502A	2.69
6502B	4.25
65C02*	7.95
6522	2.95
6526	13.95
6532	5.95
6551	2.95
6581	14.95

* CMOS

6800

6800	1.95
6802	2.95
68B02	3.95
6803	3.95
6809	2.95
68B09	5.99
6809E	2.95
68B09E	5.49
6808	2.49
6810	1.95
6821	1.25
68B21	1.85
6840	3.95
6845	2.75
6847	4.75
6850	1.95
68B50	1.75
6852	4.95
6883	22.95
68000	9.95
68020	189.95

8000

8031	3.95
8035	1.49
8039	1.95
8052AH BASIC	34.95
8080	2.49
8085	1.95
8085A-2	3.75
8088	5.99
8088-2	7.95
8155	2.49
8155-2	3.95
8156	2.95
8741	9.95
8748	7.95
8749	9.95
8755	14.95

8200

8205	3.29
8212	1.49
8216	1.49
8224	2.25
8228	2.25
8237	3.95
8237-5	4.75
8238	4.49
8243	1.95
8250	6.95
8251	1.29
8251A	1.69
8253	1.59
8253-5	1.95

8200

8254	2.79
8255	1.99
8255-5	2.49
8256	15.95
8259	1.95
8259-5	2.29
8272	4.39
8274	4.95
8275	16.95
8279	2.49
8279-5	2.95
8282	3.95
8283	3.95
8284	2.25
8286	3.95
8287	3.95
8288	4.95

Z-80

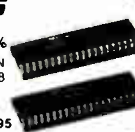
Z80-CPU	1.25
Z80A-CPU	1.29
Z80B-CPU	2.75
Z80A-CTC	1.69
Z80B-CTC	4.25
Z80A-DART	5.95
Z80B-DART	6.95
Z80A-DMA	5.95
Z80A-PIO	1.89
Z80B-PIO	4.25
Z80A-SIO/0	5.95
Z80A-SIO/1	5.95
Z80A-SIO/2	5.95
Z8671BASIC	9.95

SIMM MODULES

41256A8B-15	256K x 8-BIT	FOR MAC	150ns	$89.00
41256A8B-12	256K x 8-BIT	FOR MAC	120ns	$99.00
41256A8B-10	256K x 8-BIT	FOR MAC	100ns	$129.00
41256A9B-15	256K x 9-BIT	FOR PC	150ns	$119.00
41256A9B-12	256K x 9-BIT	FOR PC	120ns	$129.00
41256A9B-10	256K x 9-BIT	FOR PC	100ns	$139.00
41256A9B-80	256K x 9-BIT	FOR PC	80ns	$159.00
42100A9B-10	1MB x 9-BIT	FOR PC	100ns	$529.00
42100A9B-80	1MB x 9-BIT	FOR PC	80ns	$599.00

V-20 SERIES

SPEED UP YOUR PC BY 10% TO 40%



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V20* 5 MHz	8.95	V20* 8 MHz	10.95
V20* 10 MHz	12.95	V30 8 MHz	13.95

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16L8	$2.95	20R4	$4.95
16R8	2.95	20R6	4.95
16R6	2.95	20R8	4.95
16R4	2.95	20X8	4.95
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7400

7400	.19
7402	.19
7404	.19
7405	.25
7406	.29
7407	.29
7408	.24
7410	.19
7411	.25
7414	.49
7416	.25
7417	.25
7420	.19
7432	.29
7447	.89
7473	.34
7474	.33
7475	.45
7476	.35
7586	.35
7489	2.15
7490	.39
7493	.35
74121	.29
74123	.49
74151	.55
74154	1.49
74157	.55
74166	1.00

74LS00

74LS00	.16
74LS02	.17
74LS03	.18
74LS04	.16
74LS05	.18
74LS08	.18
74LS10	.16
74LS11	.22
74LS14	.39
74LS20	.17
74LS21	.22
74LS27	.23
74LS30	.17
74LS32	.18
74LS38	.26
74LS42	.39
74LS47	.75
74LS51	.17
74LS73	.29
74LS74	.24
74LS75	.29
74LS76	.29
74LS83	.49
74LS85	.49
74LS86	.22
74LS90	.39
74LS92	.49
74LS93	.33
74LS109	.36
74LS112	.29
74LS123	.49
74LS125	.39
74LS132	.39
74LS138	.39
74LS139	.39
74LS151	.39
74LS153	.39
74LS154	1.49
74LS155	.59
74LS156	.49
74LS157	.35
74LS158	.29
74LS161	.39
74LS163	.39
74LS164	.49
74LS165	.65
74LS166	.95
74LS175	.39
74LS192	.69
74LS193	.69
74LS197	.59
74LS221	.59
74LS240	.69
74LS241	.69
74LS244	.69
74LS245	.79
74LS251	.49
74LS257	.39
74LS258	.49
74LS259	1.29
74LS260	.49
74LS273	.79
74LS279	.39
74LS322	3.95
74LS323	2.49
74LS367	.39
74LS373	.79
74LS374	.79
74LS377	.79
74LS393	.79
74LS541	1.49
74LS670	.89
74LS682	3.20
74LS688	2.40

74S

74S00	.29
74S02	.29
74S04	.29
74S74	.49
74S86	.35
74S112	.50
74S287	1.69
74S288	1.69
74S374	1.69
S32	.35
S138	.79
S240	1.49
S244	1.49
S373	1.69

74F

74F00	.35
74F04	.35
74F08	.35
74F32	.35
74F74	.39
74F138	.79
74F244	1.29

74HC

74HC00	.21
74HC04	.25
74HC08	.25
74HC14	.35
74HC32	.35
74HC74	.35
74HC138	.45
74HC139	.45
74HC154	1.09
74HC157	.55
74HC244	.85
74HC245	.85
74HC373	.69
74HC374	.69

74HCT

74HCT00	.25
74HCT04	.27
74HCT08	.25
74HCT32	.27
74HCT138	.35
74HCT240	.89
74HCT244	.89
74HCT245	.99
74HCT373	.99
74HCT374	.99

LINEAR COMPONENTS

TL071	.69	TL497	3.25	MC3487	2.95
TL072	1.09	NE555	.29	LM3900	.49
TL084	1.49	NE556	.49	LM3909	.98
LM301	.34	NE558	.79	LM3914	1.89
LM309K	1.25	LM565	.95	IC7760	1.99
LM311	.59	LM567	.79	7805T	.49
LM311K	3.49	NE570	2.95	7808T	.49
LM317T	.69	NE590	2.50	7812T	.49
LM318	1.49	LM723	.49	7815T	.49
LM319	1.25	LM741	.29	7905T	.59
LM323K	3.49	MC1330	1.69	7912T	.59
LM324	.34	MC1350	1.19	7915T	.59
LM334	1.19	LM1458	.35	MAX232	7.95
LM336	1.75	LM1488	.49	ICL8038	3.85
LM338K	4.49	LM1489	.49	ICM7207A	5.95
LM339	.59	LM1496	.85	ICM7208	15.95
LF353	.59	ULN2003	.79	75150	1.95
LF356	.99	XR2206	3.95	75154	1.95
LM358	.59	XR2211	2.95	75188	1.25
LM380	.89	CA3046	.89	75189	1.25
LM386	.89	CA3146	1.29	75451	.39
LM393	.45	MC3470	1.95	75452	.39

MISC.

ADC0804	2.99
ADC0809	3.85
DAC0800	3.29
DAC0808	1.95
1793	9.95
8272	4.39
COM8116	8.95
UPD765	4.39
1691	6.95
2143	6.95
AY5-3600 PRO	11.95
AY3-1015	4.95
AY5-1013	3.95
MC146818	5.95
MM58167	9.95
MM58174	9.95
MSM5832	2.95
IM6402	3.95
IM6403	9.95
INS8250	6.95
NS16450	10.95

CRYSTALS

32.768 KHz	.95
1.0 MHz	2.95
1.8432	2.95
2.0	1.95
2.4576	1.95
3.579545	1.95
4.0	1.95
5.0688	1.95
6.0	1.95
8.0	1.95
10.0	1.95
12.0	1.95
14.31818	1.95
16.0	1.95
18.0	1.95
20.0	1.95
22.1184	1.95
24.0	1.95

CRYSTAL OSCILL.

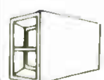
1.0MHz	5.95
1.8432	5.95
2.0	5.95
2.4576	5.95
4.0	4.95
5.0688	4.95
8.0	4.95
10.0	4.95
14.31818	1.95
16.0	4.95
18.432	4.95
20.0	4.95
24.0	4.95

STD. CMOS LOGIC

4001	.19	4050	.29
4011	.19	4051	.69
4013	.35	4060	.69
4015	.29	4066	.29
4016	.29	4069	.19
4017	.49	4093	.49
4024	.49	14411	9.95
4040	.69	4511	.69
4046	.69	4538	.95
4049	.29	4702	9.95

SHORTING BLOCKS

5/$1.00



DISCRETE

1N751	.49	2N3904	.10
IN4004	10/1.00	2N3906	.10
IN4148	25/1.00	2N4401	.25
KBP02	.55	4N26	.69
2N2222	.10	4N35	.89
2N2907	.25	JMBO RED	.10
2N3055	.79	JMBO GRN	.14

BYPASS CAPS

CERAMIC DISC:

.01UF	100/5.00
.1UF	100/6.50

MONOLITHIC:

.01UF	100/10.00
.1UF	100/12.50

Circle 6 on Reader Service Card (DEALERS: 7)

POWER SUPPLIES

135 WATT POWER SUPPLY
- UL APPROVED
- +5V @15A, +12V @ 4.2A, -5V @ .5A, -12V @ .5A

PS-135 $59.95
PS-150 150W SUPPLY $69.95

200 WATT POWER SUPPLY
- UL APPROVED
- +5V @ 20A, +12V @ 7A, -5V @ .5A, -12V @ .5A

PS-200 $89.95

APPLE TYPE SUPPLY
- WITH APPLE CONNECTOR
- +5V @ 6A, +12V @ 3A, -5V @ 1A, -12V @ 1A

PS-A $49.95

36 WATT POWER SUPPLY
- +5V @2.5A, +12V @1.5A
- 3 PIN INPUT, 6 PIN OUTPUT
- SELECTABLE 110V-220V

PS-3045 $12.95

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SOLDER/DESOLDER STATION
- OIL-FREE VACUUM PUMP
- TEMP ADJUSTS (212° F-900° F) & VACUUM (0-60 CM/HG)
- WITH GUN REST, COOLING TRAY, WIRE BRUSH & TIP CLEANER ROD

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XY999D DESOLDER ONLY $349.99

DELUXE SOLDER STATION
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- LED TEMPERATURE READOUTS
- INCLUDES COOLING TRAY

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- TIP TEMPERATURE READOUT
- REPLACEMENT TIPS @ $2.95

168-3C $59.95

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JDR-PR1 WITH +5V AND GROUND PLANE $27.95
JDR-PR2 ABOVE WITH I/C DECODING LAYOUT $29.95

FOR AT
JDR-PR10 16 BIT WITH I/O DECODING LAYOUT $34.95
JDR-PR10PK PARTS KIT FOR JDR-PR10 ABOVE $12.95

FOR PS/2
JDR-PR32 32 BIT PROTOTYPE CARD $69.95
JDR-PR16 16 BIT WITH I/O DECODING LAYOUT $49.95
JDR-PR16PK PARTS KIT FOR JDR-PR16 ABOVE $15.95
JDR-PR16V 16 BIT FOR VIDEO APPLICATIONS $39.95

JDR INSTRUMENTS—2 YEAR WARRANTY!

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- WIDE BAND WIDTH
- VARIABLE HOLDOFF

MODEL-3500 (SHOWN) $499.95

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- COMPONENTS TESTER

MODEL 2000 $389.95

3.5 DIGIT PROBE TYPE DMM
- AUTORANGING
- AC/DC 2V - 500V, RESIS. 2K-2M

DPM-1000 (SHOWN) $54.95

3.5 DIGIT POCKET SIZE DMM
- BASIC DC ACCURACY ±0.5%
- 14 RANGES

DMM-100 $29.95

3.5 DIGIT FULL FUNCTION DMM
- BASIC DC ACCURACY ±0.25%
- 22 RANGES

DMM-200 $49.95

THE ULTIMATE 3.5 DIGIT DMM
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- 34 RANGES
- TEMP, TRANSISTOR & RESISTANCE FEATURES

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RELIABLE, HIGH QUALITY TEST EQUIPMENT, REASONABLY PRICED

DATA SWITCH BOXES

TYPE	# OF POS	PARALL	SERIAL	PRICE
PUSHBTTN	2 WAY	AB-P	AB-S	39.95
ROTARY	2 WAY	RSP-2	RSS-2	24.95
ROTARY	3-WAY	RSP-3	RSS-3	27.95
ROTARY	4-WAY	RSP-4	RSS-4	29.95

EXTENDER CARDS

FOR PROTOTYPE DEBUGGING, TESTING AND TROUBLESHOOTING

EXT-8088 XT COMPATIBLE $29.95
EXT-80286 AT COMPATIBLE $39.95
EXT-16 MICROCHANNEL 16 BIT $69.95
EXT-32 MICROCHANNEL 32 BIT $99.95

COMPUTER CABLES

GOLD-PLATED CONTACTS; 100% SHIELDED

CBL-PRINTER PC PRINTER CABLE 9.95
CBL-PRINTER-25 AS ABOVE-25 FT. 15.95
CBL-PRINTER-RA RT ANGL. PRINTER 15.95
CBL-DB25-MM DB25 MALE-MALE 9.95
CBL-DB25-MF DB25 MALE FEMALE 9.95
CBL-9-SERIAL 9 PIN 25 PIN SERIAL 6.95
CBL-KBD-EXT KEYBOARD EXTEN 7.95
CBL-CNT-MM 36 PIN CENTRON -M/M 14.95
CBL-HD-20 20 PIN HARD DISK 3.95
CBL-HD-34D 34 PIN DUAL HARD DISK 6.95
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CBL-MNT-9 9 PIN MONITOR EXTEN. 6.95
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NEW LOW PRICES!

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PDS-200 1660 TIE PTS $19.95
PDS-300 2390 TIE PTS $24.95
PDS-400 3220 TIE PTS $34.95

RS-232 BREAKOUT BOX

FOR TROUBLESHOOTING SERIAL COMMUNICATIONS

- OPEN/CLOSE INDIVIDUAL CIRCUITS
- 20 JUMPERS CROSS-CONNECT ANY TWO CIRCUITS
- 10 CIRCUIT ACTIVITY LEDS

GENDER-BO $34.95

IDC CONNECTORS/RIBBON CABLE

DESCRIPTION	ORDER BY	CONTACTS 10	20	26	34	40	50
SOLDER HEADER	IDHxxS	.82	1.29	1.68	2.20	2.58	3 24
RIGHT ANGLE SOLDER HEADER	IDHxxSR	.85	1.35	1.76	2 31	2.72	3.39
WIREWRAP HEADER	IDHxxW	1.86	2.98	3.84	4 50	5.28	6.63
RIGHT ANGLE WIREWRAP HEADER	IDHxxWR	2.05	3.28	4.22	4 45	4.80	7.30
RIBBON HEADER SOCKET	IDSxx	.63	.89	.95	1.29	1.49	1.69
RIBBON HEADER	IDMxx	--	5.50	6.25	7.00	7.50	8.50
RIBBON EDGE CARD	IDExx	.85	1.25	1.35	1.75	2.05	2.45
10' RIBBON CABLE	RCxx	1.60	3.20	4.10	5 40	6.40	7.50

FOR ORDERING INSTRUCTIONS, SEE D-SUBMINIATURE CONNECTORS BELOW

D-SUBMINIATURE CONNECTORS

DESCRIPTION		ORDER BY	CONTACTS 9	15	19	25	37	50
SOLDER CUP	MALE	DBxxP	.45	.59	.69	.69	1.35	1.85
	FEMALE	DBxxS	.49	.69	.75	.75	1.39	2.29
RIGHT ANGLE PC SOLDER	MALE	DBxxPR	.49	.69	--	.79	2.27	--
	FEMALE	DBxxSR	.55	.75	--	.85	2.49	--
WIREWRAP	MALE	DBxxPWW	1.69	2.56	--	3.89	5.60	--
	FEMALE	DBxxSWW	2.76	4.27	--	6.84	9.95	--
IDC RIBBON CABLE	MALE	IDBxxP	1.39	1.99	--	2.25	4.25	--
	FEMALE	IDBxxS	1.45	2.05	--	2.35	4.49	--
HOODS	METAL	MHOODxx	1.05	1.15	1.25	1.25	--	--
	PLASTIC	HOODxx	.39	.39	--	.39	.69	.75

ORDERING INSTRUCTIONS: INSERT THE NUMBER OF CONTACTS IN THE POSITION MARKED "xx" OF THE "ORDER BY" PART NUMBER LISTED. EXAMPLE : A 15 PIN RIGHT ANGLE MALE PC SOLDER WOULD BE DB15PR

MOUNTING HARDWARE .59

IC SOCKETS/DIP CONNECTORS

DESCRIPTION	ORDER BY	CONTACTS 8	14	16	18	20	22	24	28	40
SOLDERTAIL SOCKETS	xxST	.11	.11	.12	.15	.18	.15	.20	.22	.30
WIREWRAP SOCKETS	xxWW	.59	.69	.69	.99	1.09	1 39	1.49	1.69	1.99
ZIF SOCKETS	ZIFxx	--	4.95	4.95	--	5.95	--	5.95	6.95	9.95
TOOLED SOCKETS	AUGATxxST	.62	.79	.89	1.09	1.29	1 39	1.49	1.69	2.49
TOOLED WW SOCKETS	AUGATxxWW	1.30	1.80	2.10	2.40	2.50	2.90	3.15	3.70	5.40
COMPONENT CARRIERS	ICCxx	.49	.59	.69	.99	.99	99	.99	1.09	1.49
DIP PLUGS (IDC)	IDPxx	.95	.49	.59	1.29	1.49	--	.85	1.49	1.59

FOR ORDERING INSTRUCTIONS SEE D-SUBMINIATURE CONNECTORS ABOVE

PC BREADBOARD

- 62 CLEARLY LABELLED BUS LINES
- ACCEPTS UP TO 24 14-PIN ICS
- 1940 TIE POINTS
- EXT FEMALE DB25 D-SUB CONNECT.

PDS-604 $49.95

SURGE SUPPRESSOR

PROTECTS YOUR EQUIPMENT!

- SIX 3-PRONG AC OUTLETS
- 15 AMP CIRCUIT BREAKER
- 6' HEAVY DUTY CORD
- 15 AMP MAX LOAD, 1875 WATTS
- UL APPROVED

POWER-SURGE $12.95
POWER-STRIP W/O SURGE $9.95

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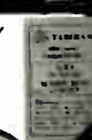
GENDER-FF FEMALE-FEMALE $7.95
GENDER-MM MALE MALE $7.95
GENDER-MF MALE-FEMALE $7.95
GENDER-NM NULL MODEM $8.95
GENDER-JB JUMPER BOX $8.95
GENDER-MT MINITESTER $14.95
GENDER-VGA DB9-DB15 $19.95
GENDER-9-25 DB9 DB25 $4.95

LITHIUM BATTERY



- 6V FOR 286 AND 386 PCS
- MOTHERBOARD CONNECTOR
- ADHESIVE VELCRO MOUNTING STRIP

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COIN TYPE BATTERY
LONG-LASTING 3V LITHIUM

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FLOPPY DRIVE FOR APPLE II SERIES
AP-150 *(W/CABLE, CONNECTOR)* $99.95

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RP-525 *(NO POWER SUPPLY REQ.)* $89.95

16K RAM CARD BOOSTS 48K APPLE TO 64K
RAM-CARD (2 YEAR WARRANTY) $39.95

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SNAP APART TO MAKE ANY SIZE HEADER, WITH .1" CENTERS

1 x 40 STRAIGHT LEAD .99
1 X 40 RIGHT ANGLE LEAD $1.49
2 X 40 STRAIGHT LEAD $2.49
2 X 40 RIGHT ANGLE LEAD $2.99

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400 DPI!

HS-3000

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- SERIAL/PARALLEL PORTS, CLOCK/CALENDAR & GAME ADAPTOR
- RUNS COLOR GRAPHICS SOFTWARE ON ITS MONOCHROME MONITOR
- MOTHERBOARD
- 256K RAM MEMORY
- 135W POWER SUPPLY
- FLIPTOP CASE
- 84 KEY KEY-BOARD
- 360K FLOPPY DRIVE
- MONOGRAPHICS I/O CARD

12 MHz MINI-286 SYSTEM $1152.70

- 12 MHZ MINI-286 MOTHERBOARD
- 512K RAM MEMORY
- MINI-AT CASE W/POWER SUPPLY
- 84 KEY KEYBOARD
- MONOCHROME MONITOR
- 1.2 MB FLOPPY DRIVE
- FLOPPY/HARD CONTROL
- MONOGRAPHICS CARD

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- 24-PIN PRINT HEAD, 8K MEMORY BUFFER
- 400 CPS DRAFT, 133 CPS LQ
- PRINTS UPTO 7 COLORS
- SEVEN FONTS
- PARALLEL & SERIAL INTERFACES

EPSON LQ-950 CALL FOR THE PRICE!

- 24-PIN PRINT HEAD; 6K MEMORY BUFFER
- 110-COL. CARRIAGE
- 264 CPS DRAFT, 88 CPS LQ
- 360 X 360 DPI GRAPHICS RESOL.
- PARALLEL AND SERIAL INTERFACES

EPSON LQ-850 CALL FOR THE PRICE!

- 24-PIN PRINT HEAD; 6K MEMORY BUFFER
- 80-COL CARRIAGE
- 264 CPS DRAFT, 88 CPS LQ
- BIDIRECTIONAL TEXT & GRAPHICS
- PARALLEL AND SERIAL INTERFACES

EPSON LQ-500 CALL FOR THE PRICE!

- 24-PIN PRINT HEAD; 8K MEMORY BUFFER
- 80-COL CARRIAGE
- 180 CPS DRAFT, 60 CPS LQ
- BIDIRECTIONAL TEXT & GRAPHICS
- PARALLEL INTERFACE (SERIAL OPT.)

EPSON FX-850 CALL FOR THE PRICE!

FOR FORMS, ENVELOPES, FANFOLDS!
- 80-COL. CARRIAGE
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- 9 PIN PRINT HEAD, 8K MEMORY BUFFER
- AUTO LOAD
- PARALLEL INTERFACE

FX-1050 WITH 136-COLUMN CARRIAGE $CALL!

EPSON EX-800 CALL FOR THE PRICE!

- 9-PIN PRINT HEAD; 8K MEMORY BUFFER
- 80-COLUMN CARRIAGE
- 300 CPS DRAFT, 50 CPS NLQ
- BIDIRECTIONAL TEXT MODE
- PARALLEL & SERIAL INTERFACES

EPSON LX-800 CALL FOR THE PRICE!

- 9-PIN DOT MATRIX PRINT HEAD; 3K MEMORY BUFFER
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- COMPONENT TESTER
- BRIGHT 5-INCH CRT
- X-Y OPERATION 110/120V
- 2 YEAR WARRANTY

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MODEL 2000 OFFER EXPIRES 5/31/89

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MODULAR CIRCUIT TECHNOLOGY

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- PARALLEL PRINTER PORT CONFIG. AS LPT1 OR 2

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- HOOK UP A MONOCHROME MONITOR, PARALLEL PRINTER, MODEM AND JOYSTICK ALL AT ONCE!
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- AT COMPATIBLE
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- PARALLEL, SERIAL AND GAME PORTS
- HERCULES COMPATIBLE MONOGRAPHICS
- WITH SOFTWARE TO RUN COLOR GRAPHICS ON A MONOCHROME MONITOR

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- FREES 1 SLOT IN YOUR XT SYSTEM
- INTERFACES 2 FDD'S AND 2 HDD'S; CABLING FOR 2 FDD, 1 HDD

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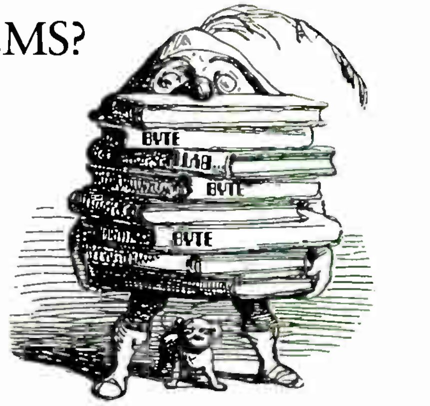

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EDITORIAL INDEX BY COMPANY

Index of companies covered in articles, columns, or news stories in this issue
Each reference is to the first page of the article or section in which the company name appears

COMING UP IN BYTE

PRODUCTS IN PERSPECTIVE:

In **the front** of the May BYTE will be Microbytes, What's New, and Short Takes. May's Short Takes will include MultiBoot, an OS/2 booting utility from Bolt Systems; Compaq's external hard disk drive subsystem; Connectix's Virtual, a program that provides 8 megabytes of virtual RAM for the Macintosh II; Lotus's Magellan; and Disk Technician Advanced, an upgraded hard disk drive test and analysis program from Prime Solutions.

May's **Product Focus** will be on three-dimensional CAD software. These ever-more-capable programs on microcomputers are getting more popular all the time. Find out the state of the art in this article.

System reviews will consider two hand-held computers, Sharp's Wizard and Psion's Organiser II. Also on the list is a comparison of the Tandon and FiveStar systems featuring removable disk cartridges.

A couple of **hardware reviews** are a comparison of add-in facsimile cards for the Mac and a comparison of TrueScan and OmniPage scanners.

Software reviews concentrate on JPI's TopSpeed Modula-2 and Logic Gem from Sterling Castle; **application reviews** deal with Samna's Ami, a plethora of statistical analysis packages for the Mac, and Crowninshield Software's MediaBase.

IN DEPTH:

Unix, once obscure and often difficult, is enjoying something of a flowering of popular appreciation. With increased attention to easily absorbed user interfaces, more people are coming to know its speed and power. Our May In Depth section looks at Unix shells, Unix communications, Unix security, new incarnations of Unix, and making the changeover from DOS.

FEATURES:

Our May lineup of **columnists** begins with the Expert Advice group in the front of the book: Jerry Pournelle's Computing at Chaos Manor, Ezra Shapiro's Applications Plus, Wayne Rash's Down to Business, Brock N. Meek's COM1:, Don Crabb's Macinations, and Mark Minasi's OS/2 Notebook. Back in the Features section, under the heading Hands On, our two columns are Brett Glass's Under the Hood and Rick Grehan's Some Assembly Required.

In addition, we'll have a feature that analyzes the latest advancement in microcomputing science, **quantum technology**. While standard microcomputing manufacturing techniques are starting to reach physical size and speed limits imposed by the physics of electron flow, quantum technology offers improvements that are measured in orders of magnitude. If we've been waiting for the emergence of the next "breakthrough" technology, this may well be it.

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6) When TIPS says "Enter magazine code & issue code"
Enter 1 # 4 9 # #

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7a) When TIPS says "Enter (next) Inquiry Number" Enter one inquiry selection from below (ignore blank boxes)

b) Repeat 7a as needed (maximum 17 inquiry numbers)

1. ☐ ☐ ☐ ☐ ☐ # #   6. ☐ ☐ ☐ ☐ ☐ # #   10. ☐ ☐ ☐ ☐ ☐ # #   14. ☐ ☐ ☐ ☐ ☐ # #
2. ☐ ☐ ☐ ☐ ☐ # #   7. ☐ ☐ ☐ ☐ ☐ # #   11. ☐ ☐ ☐ ☐ ☐ # #   15. ☐ ☐ ☐ ☐ ☐ # #
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4. ☐ ☐ ☐ ☐ ☐ # #   9. ☐ ☐ ☐ ☐ ☐ # #   13. ☐ ☐ ☐ ☐ ☐ # #   17. ☐ ☐ ☐ ☐ ☐ # #
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8) End session by entering # #

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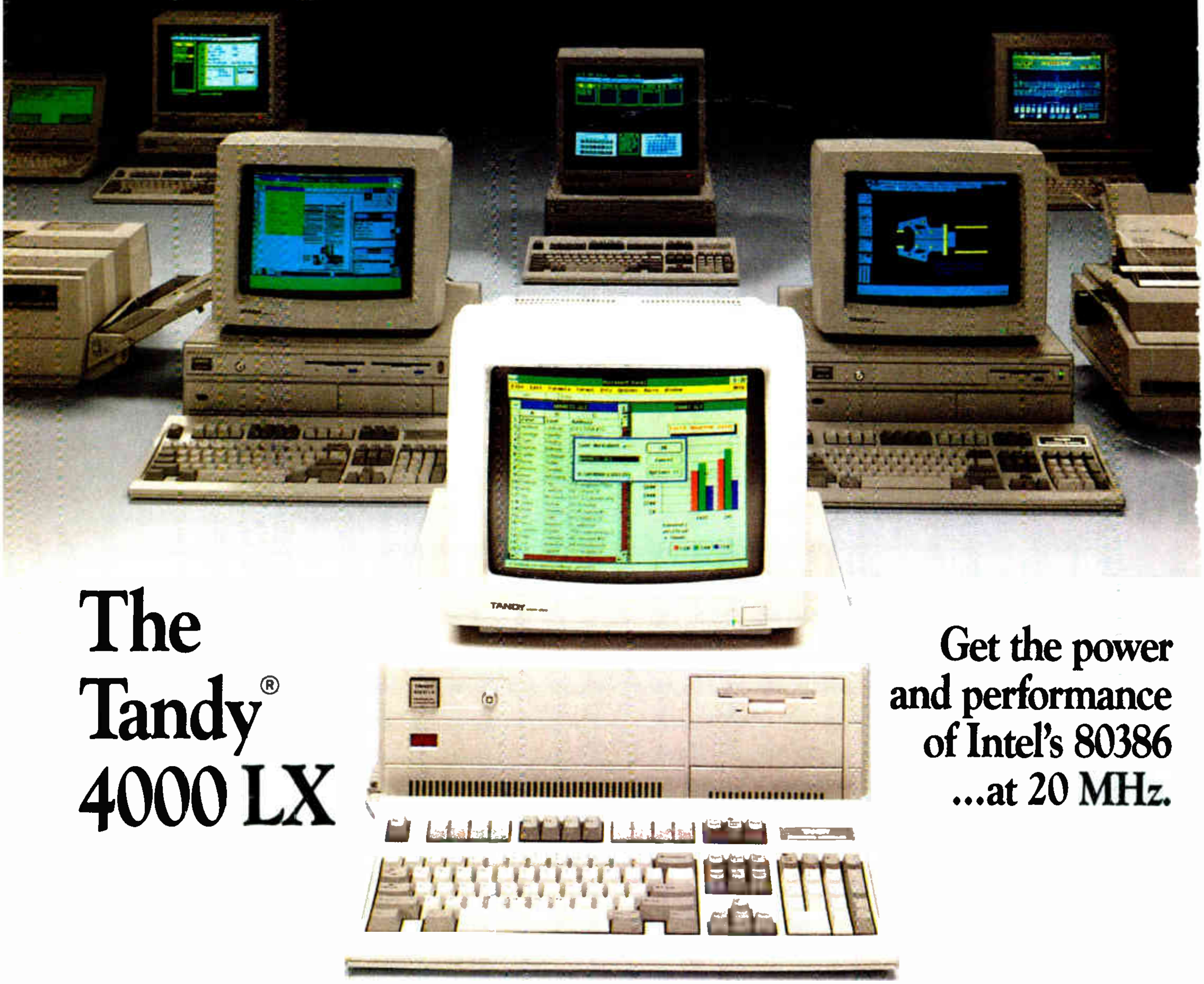

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