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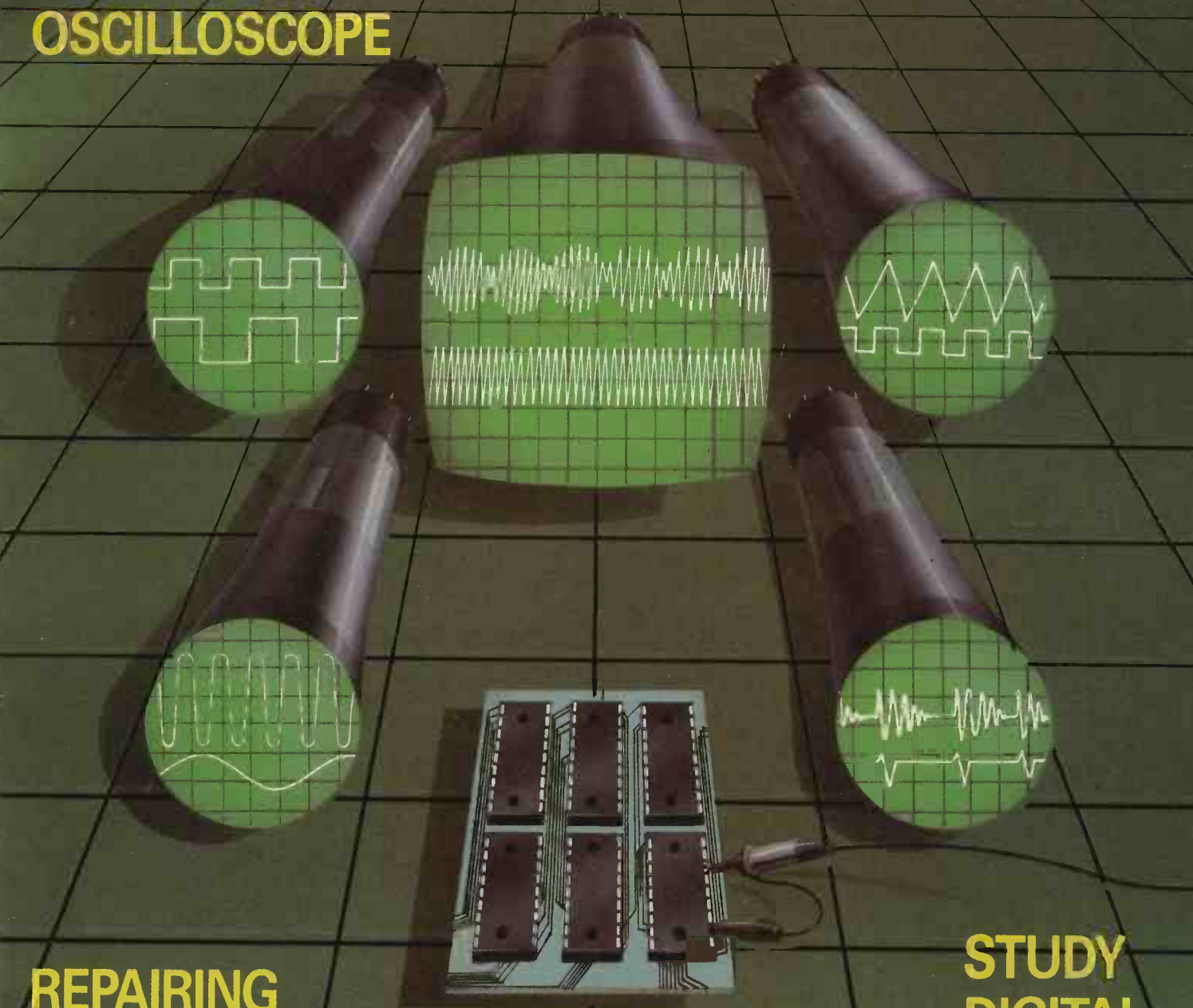

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PRACTICAL ELECTRONICS

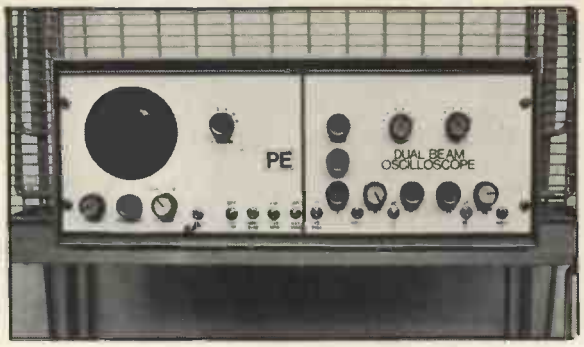
PE VOL 24 NO 11 NOVEMBER 1988

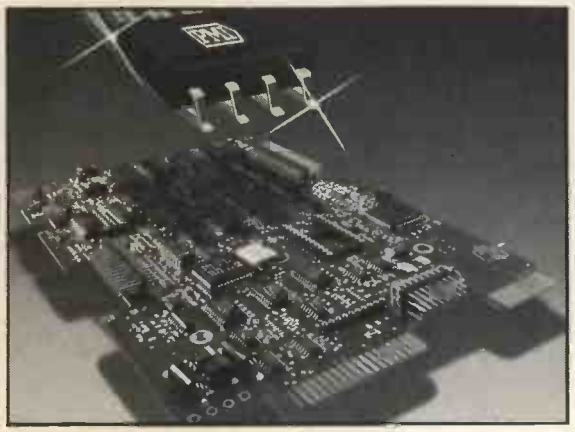
CONSTRUCTIONAL PROJECTS

DUAL BEAM OSCILLOSCOPE by John Becker 12
Tracing the route to getting the tube home and facing up to electronics with a doubly beaming smile.

DIGITAL ELECTRONICS – PART THREE by Owen Bishop 35
Multivibrators don't need springs or motors if you know how to logically open and close the astable gate before the force has gone.

PLD PROGRAMMER by Chris Kelly & Steve Pattinson 49
Programming your own logic devices minimises design time and board space – building your own programmer saves pounds by the hundred.





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Join the growing band of diy initiates on the wings of a gull and learn about handling surface mounted component maintenance.

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CMOS 4000 completes the circuits that other chips cannot reach.

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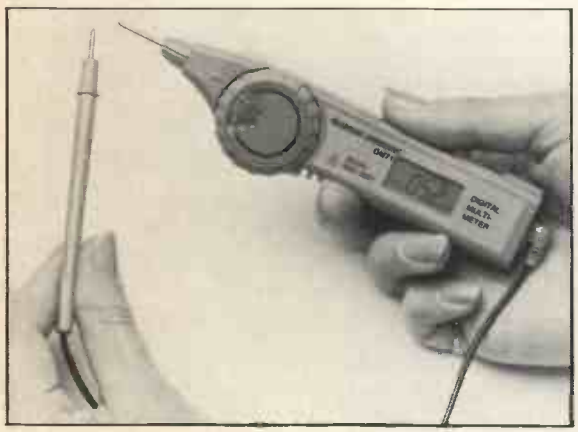

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NEXT MONTH . . .

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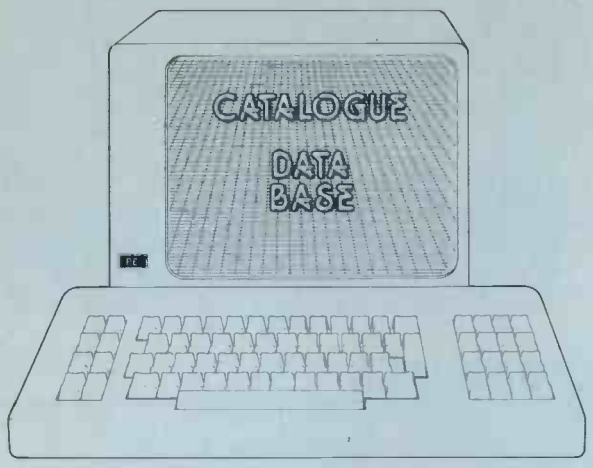


We have recently received the following literature:

Schlumberger have issued a comprehensive 22-page brochure on their new SI-1220 multichannel spectrum analyser. The instrument offers **exciting new measurement opportunities** in speech and music research, environmental and structural engineering etc. They have also sent an illustrated brochure on their 7061 and 7062 systems voltmeters for high speed precision measurement. **Schlumberger-Solartron,** Victoria Road, Farnborough, Hants, GU14 7PW, tel 0252 544433.

Klippon have produced a new full colour catalogue on their **extensive range of high-quality tools** for cable cutting, stripping, crimping, terminating and testing. **Klippon,** Power Station Road, Sheerness, Kent, ME12 3AB, tel 0795 580999.

Hitachi have a 16-page brochure available on their **range of cmos and bicmos gate arrays.** It also discusses the major issues involved in gate array selection and use. **Hitachi Europe Ltd,** 21 Upton Road, Watford, Herts, WD1 7TB, tel 0923 246488.

Bruel and Kjaer have sent an informative brochure describing the company's type 2231 modular **precision sound level meter.** The leaflet defines the many application areas for this versatile instrument in an easily understood format. **Bruel and Kjaer (UK) Ltd,** 92 Uxbridge Road, Harrow, Middx, HA3 6BZ, tel 01-954 2366.

STC Electronic Services have sent information about their new 7-page full-colour product selection guide for their **entire range of PMI opamps** and conversion times. Information is provided on various opamps including low offset voltage, low power, low bias current, low noise, high slew rate, wide bandwidth, 8-pin dual, 14-pin quad, plus 14 and 16-pin dual types. A-D and d-a products are also covered. The Integrated Circuits Group, **STC Electronic Services,** Edinburgh, Harlow, Essex, CM20 2DF, tel 0279 626777.

Global Specialities have produced their most comprehensive catalogue ever available. The booklet is entitled 'The Album' and features the **company's full range of products, including oscilloscopes,** signal sources, psus, safety testers, logic analysers, plus voltage and wattage meters etc. **Global Specialities,** 2-10 St John's Street, Bedford, MK42 0DH, tel 0234 217856.

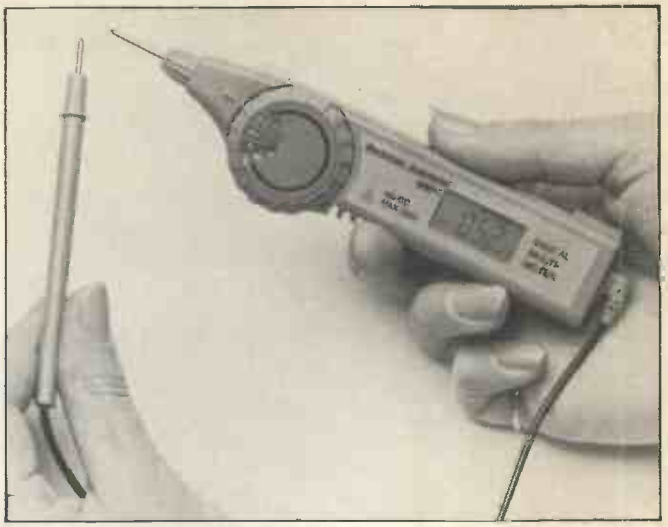
Penfriend

Master Heat Tools have added a new pen size pocket unit to their range of butane powered three-in-one heat tools.

The model UT-50 is a truly portable soldering iron, heat shrink tool with flameless heat and butane torch. Powered by ordinary lighter fuel it can be carried in "pen clip" style for instant soldering or heating wherever necessary. What you might call the write approach to soldering.

For further details contact: Master Heat Tools, Unit M, Portway Industrial Estate, Andover SP10 3LU, Hants, England. Tel: 0264 51347/8.

WHAT'S NEW



Fingertip DMM

A highly portable pen-type autoranging digital multimeter with 17 ranges/functions is the most recent addition to the Circuitmate series of handheld instruments from Beckman Industrial. Using dual slope integration, the DM71 measures dc and ac voltages, resistance, and includes a continuity and diode test function.

Incorporating a rotary function selector, a touch hold function which freezes the display and an audible indication of continuity and range changing makes the meter ideal for use in difficult situtations where close attention is required to taking measurements.

The 3½ digit lcd display indicates value, polarity, measurement unit, overrange, data hold and low battery. The dc voltages measured are in five ranges between 200mV and 250V with accuracies between ±0.7% plus four digits and ±2% plus four digits, dependent on range. The four ac voltage ranges are between 2V and 250V and have an accuracy of ±2.3% plus four digits.

Resistance has six ranges with 20M the maximum, an accuracy of ±2% plus four digits on all but the highest range, and an open circuit-voltage of less than 0.45V. The continuity/diode test has a threshold level of 1.5KΩ at 1.5V battery level. Input impedence is 100MΩ on the 200mV range, around 12MΩ on the 2V range and about 11MΩ on all other ranges.

The DM71 is supplied complete with batteries, test leads, operators manual and a rigid plastic shirt pocket carrying case. A padded vinyl carrying case is available as an accessory.

Contact: Beckman Industrial Ltd., 43-48 New Street, Birmingham B2 4LJ. Tel: 021-643 8899.

Metering percentages

For the truly addicted enthusiast, as well as for professionals, there is a new higher accuracy version of Thurlby's industry standard 1503 multimeter, which features a basic dc voltage accuracy of 0.03%.

The 1503HA has full multimeter functions of dc and ac voltage and resistance, together with the added advantage of a frequency measurement range. This measures frequency up to 4MHz with a resolution of 0.1 kHz (up to 7MHz with display overlfow) and an accuracy of 0.005%.

A scale length of 4¾ digits (15 bits/32768 counts) gives the 1503HA much greater resolutions, typically of 10μV, 10mΩ and 1nA.

Use of a crisp liquid crystal display with low power circuitry gives a long battery life, though the meter can also be powered from the standard ac line adaptor. Optionally, it can be factory-fitted with an internal mains unit or a rechargeable battery pack.

The meter comes in a tough, knock-proof and scuff-proof case with a multiangle tilt stand that doubles as a carrying handle. It weighs 1.2kg, and costs £185.

Contact: Thurlby Electronics Ltd., New Road, St. Ives, Huntingdon, Cambs. PE17 4BG. Tel: 0480 63570

Piezo house

STC Mercator has introduced a new range of high quality piezo audio transducers. The low-cost devices can be directly driven by ics or lsi circuits and feature a no-contact design which provides a very reliable piezo-electric element whilst eliminating noise problems.

Other features, such as lower power consumption, a slim light-weight construction, and the facility to create a variety of tones makes the range suitable for several applications including telephone handsets (tone ringer, receivers, transmitters) as well as other telecommunications equipment; electronic medical systems; office equipment; detector/alarm facilities; plus various domestic and consumer products.

For further details, contact: Malcolm Green, STC Mercator, South Denes, Great Yarmouth, Norfolk NR30 3PX. Tel: 0493 844911.

VME backplanes

BICC VERO have released the new J1 Series Enhanced VME (an ICL proprietory operating system) backplanes for on-board or off-board termination.

Several unique features have been designed-in including facilities for connecting of de-coupling capacitors at each daughter board – allowing users to individually 'tune' their own configuration of products.

In addition, to improve under-shoot/overshoot values Schottky diodes have been added to the critical on-board terminators, and the design of the board itself enables three alternative power connections to be made using a single standard layout.

The on-board termination system is a multilayer pth backplane which conforms to the VME specification Rev B. It offers pick-up positions of ac fail and systems fail, as well as system reset, and comes fully loaded with wire-wrap tail, press-fit DIN 41612 connectors, and a mating male power connector.

For further details, contact: The Enclosure Group, STC Electronic Services, Edinburgh Way, Harlow, Essex CM20 2DF. Tel: 0279 626777.

Eprom emulator

An advanced eprom emulator has been designed by JPD for use with an RS232 serial communications link. The pcb has an onboard microprocessor so that common file formats can be directly transferred from the development system to the emulator using binary, Intel hex or Motorola 'S' formats.

The unit also offers a direct/terminal operatinal mode which allows you direct access to the emulation memory. It is supplied with a 28 pin JEDEC type ribbon cable header for insertion into the target socket. This method of software development is many times faster than the traditional practice of blowing eproms.

The emulator comes complete with full user notes and circuit diagram at £99.95 plus VAT.

For more details contact: J.P. Designs, The Old School, Prickwillow, Ely, Cambridgeshire CB7 4UN. Tel: 035 388 325/455.

COUNTDOWN

If you are organising any event to do with electronics, big or small, drop us a line – we shall be glad to include it here. **Please note:** Some events listed here may be trade or restricted category only. Also, we cannot guarantee information accuracy, so check details with the organisers before setting out.

1988–89 season of Radio Amateur's Examination courses at Paddington College, London. You may find that there are late places still available for this course if you hurriedly ring David Hunt (G6MFR) on 01-402 6221 or David Peace (G4KKM) on 01-892 7585.

Oct 11–13. British Laboratory Week. Grand Hall, Olympia. 0799 26699.

Oct 18–20. Brighton Electronics Show. Metropole Hotel, Brighton (filling the slot vacated by Internepcon). 0822 614671.

Nov 1–3. Custom Electronics & Design Techniques Show. Heathrow Penta. 0799 26699.

Nov 8–12. Electronics 88, Munich. World's largest trade fair for electronic components and assemblies. 01-948 5166.

Nov 29–Dec 1. DMC-PC. Drives, motors, programmable controllers etc. National Exhibition Centre, Birmingham. 0799 26699.

Dec 11. Satro Annual Computer and Technology Show. Music Hall, Aberdeen. 0224 273161. Satro, the Science and Technology Regional Organisation is a non-profit making organisation dedicated to supporting and enhancing science and technology education. Profits from the show will be devoted to developing computer and electronics clubs. **We hope it will be well supported.**

1989

Apr 5–6. Laboratory Science and Technology Show, Kelsey Kerridge, Cambridge. 0799 26699.

Apr 25–27. British Electronics Week. Olympia. 0799 26699.

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88MHz			
PT4177A	12.5V	1.1W	£1.90
PT4236A	12.5V	1.5W	£2.00
PT8750	12.5V	50W	£10.00
PT8766	24V	1.6W	£2.25
TP210	7.5V	50mW	£0.50
175MHz 12.5V			
2N4428	1W		£0.89
2N5702	1.5W		£1.10
2N5704	12W		£2.80
2N5705	25W		£5.80
2N5706	40W		£5.00
2N6081	15W		£4.00
PT4544	10W		£2.50
PT8711A	40W		£5.00
PT8726	27W		£9.80
PT8828F	9W		£7.85
TP2310	2.5W		£1.49
TP2313	20W		£5.60
TP2320	17W		£8.25
TP2320F	17W		£9.10
1GHz			
2N4429	1W	28V	£10.78
2N5764	3W	28V	£11.50
BFR96TRW	700mW	10V	£0.46
TP3094	1Vo/p	15V	£5.40
TP3095	1Vo/p	15V	£2.20
TP390	100mVo/p	5V	£0.60
30MHz			
PT9795	12.5V	15W	£10.80
PT9796	12.5V	30W	£11.20
PT9788AA	28V	20W	£12.10
2N5070AL	28V	25W	£9.00
2N5070A	28V	25W	£9.00
175MHz 28V			
PT8706	7W		£2.50
TP2405	90W		£19.80
TP8706	7W		£2.50
TP8706D	7W		£2.50
175MHz 13.6V			
2N5589	3W		£2.50
175MHz 25V			
PT4566	40W		£10.00
400-500MHz			
2N3866/2	1W	28V	£1.20
2N5016	15W	28V	£8.50
BFX89TRW.	300mW	5V	£0.40
TP2016	1W	12.5V	£4.50
TP250	10mW	7.5W	£1.20
TP252	1.5W	7.5V	£5.00

Miscellaneous

CA2350B	Module 40-300MHz 22dB			
	24V			£27.00
MX12-4	Module 470-512MHz 12W			
	12V			£22.00
LNA501 TO8	Module 10-500MHz 5dBm			
	12V			£10.00
2N5071	76MHz	24W	24V	£7.50
PT4555	50MHz	25W	12.5V	£8.00
PT4561	150MHz	4W	26V	£3.00
TPV386	225MHz	30W	28V	£25.00

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Picking SMDS

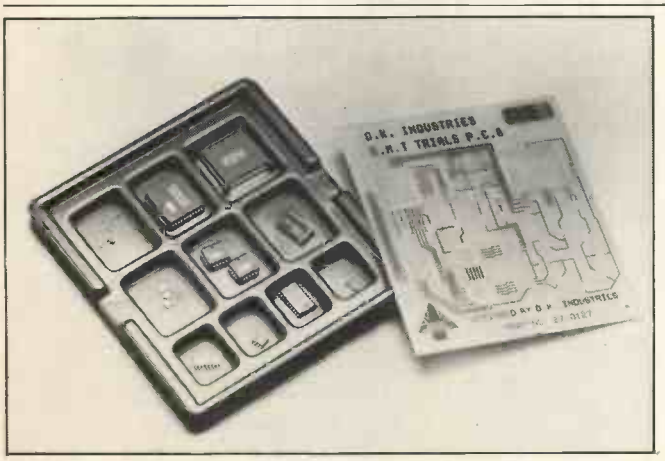
An smd test tweezer accessory for the AVO B184 mini-sized lcr component tester will be a boost for repair organisations seeking a simple solution to surface mount device identification and value measuring.

At around £25 the smd test tweezer will also spare quality assurance and development engineers the problems arising from making circuit diagram assumptions. It is the latest in an expanding range of accessories for the AVO B184 and plugs into an existing socket.

The tester is the first compact, microprocessor controlled lcr analyser to combine accuracy to 0.5 per cent, a comprehensive range of automatic routines and a rechargeable battery/mains alternative at a price as low as £375 plus VAT. As the instrument's full range of automatic facilities cover component discrimination, test frequency and ranging, the user can identify the type and value of a component simply by picking it up with the smd tweezers.

There are inductance ranges of 1mH to 1kH, capacitance ranges from 200pF to 1mF and resistance ranges from 10 ohms to 1 Mohm. An alpha-numeric display shows the major parameter of the component under test, while four keys on the tactile board may be used to disable automatic selection and manually choose the minor component, serial/parallel function, display Q and D factor or change the test frequency (100 Hz to 1 kHz).

Contact: Kate Grenshaw, Megger Instruments Limited, Archcliffe, Road, Dover, Kent CT17 9EN. Tel: 0304 202620.



Try diy smt

Anyone interested in trying out smt (surface mounted technology could take a look at OK's lates pcb trials kit, the 27-0300. This has been designed to enable the production engineer or smt student to carry out qualified evaluation of production and rework techniques.

Included is a trials board which has been produced with the manufacturing experience of British Aerospace, using the latest technology in dry-film masking between lead centres of 0.813mm. Gull wing 100 pin flat packs are supplied together with other components in dummy form, and all parts are in a reusable conductive tray to prevent static introduction to work areas.

Contact: OK Industries UK Ltd., Barton Farm Industrial Estate, Chickenhall Lane, Eastleigh, Hants SO5 5RR. Tel: 0703 619841.

SMT jet set

A hot air jet system introduced for manual soldering and desoldering smt components can operate with factory compressed air or to be supplied with a compact electric pump. It has a dc controlled ceramic heating element and produces a temperature range at the handpiece of 75° to 500°C when used with a six-bar external compressed air supply.

An automatic safety device switches the unit off if the air supply is cut or the jet outlet gets blocked. The low consumption units operate from 220/240V.

Contact: OK Industries UK Ltd., Barton Farm Industrial Estate, Chickenhall Lane, Eastleigh, Hants SO5 5RR. Tel: 0703 619841.

Ess is for service

There is a new mail order service for the amateur and hobbyist electronics markets. Ess is based in Southampton and offers a wide range of components, tools and equipment which are detailed in a new catalogue available on request. There is no minimum order quantity, and all major credit cards are accepted for telephone and written orders.

Included in the product range are cutters, pliers, solder and etching kits, reamers, multimeters, drills and drill bits. A full range of enclosures and accessories such as knobs, scales, front panel switches are available off the shelf.

Customised sheet metal work for electronics applications is another speciality of the company. Anything from a simple bracket to a custom designed enclosure.

For further information contact David Sully, ESS Ltd, Harrison House, Harrison Road, Swaythling, Southampton SO2 3TL. Tel: (0703) 671166.

CHIP COUNT!

This month's list of new component details received.

HG62E family. Autodiagnostic gate arrays having a wider choice of array sizes and a guaranteed speed of up to 80MHz. The family now comprises 12 devices offering from 770 to 24020 gates, with 68 to 272 i-o pads **(HT)**

IMS A110. The latest digital signal processor from Inmos. Employs parallel processing architecture and is capable of 400 million operations per second on video data clocked at speeds up to 20MHz. **(IN)**

PLS159A. A new 20-pin FPLS device that is 27% faster than the basic type and has an improved programming algorithm. **(PL)**

PLH159A. A new PAL device with a two-level logic element consisting of 64 AND gates and 8 OR gates with fusible connections for programming i-o polarity and direction **(PL)**

SC87C51B. Cmos eprom microcontroller featuring a 4K x 8-bit eprom instead of a similar capacity conventional prom, and a ram capacity of 128 x 8 bits. It is the latest addition to the 8031-8051 8-bit family **(PL)**

SC8XC451, PCB8XC652, PCB8XC552 series. Three additions to the 80C51 range of microcontrollers and which improve the flexibility, memory capacity and interface options for the associated family. **(PL)**

TEA1081. A peripheral supply circuit for use in line powered telephones, and having applications where relatively high supply currents are required for facilities such as listening-in, lcd display and extended dialling. **(PL)**

(HT) Hitachi, 21 Upton Road, Watford, Herts, WD1 7TB, 0923 246488. **(IN)** Inmos, 1000 Aztec West, Almondsbury, Bristol, BS12 4SQ, 0454 616616. **(PL)** Philips Components, Mullard House, Torrington Place, London, WC1E 7HD, 01-580 6633.

Maplin chat-back

No linkage wiring is required for Maplin's new fm wireless intercom. Simply plug the units into the mains wiring and you, and your property, are wired for sound.

The unit transmits or receives on any one of three switch-selectable channels. Any number of units can be connected – in a typical installation, the "master" can call each unit where a buzzer will sound.

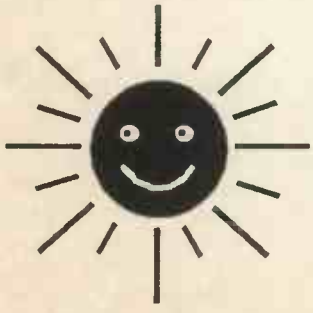
The intercoms are ideal for a baby room, where any sound can be monitored anywhere in the house, and reassurances broadcast. It may also be possible to communicate with neighbours, or local offices, up to about half a mile in distance, depending on whether both buildings are on the same mains phase and share the same local sub station transfer outlet.

The channel carrier frequencies are 110kHz, 125kHz, 140kHz, the overall size is 165 x 125 x 48mm high, and the power consumption 2W. Known as the YP36P intercom pair the price is £29.95, and may be bought from any of Maplin's shops.

Solar selling

Obviously commenting on the weather, TK now stock giant solar panels, 12ins x 12ins, which will deliver 12V 200mA in bright sunlight (chance would be a fine thing!) and 11.5V 60 mA on a typical British summer day (dull and overcast).

There are hundreds of uses for these cells, in the home, boat or caravan. Connect one to your car or boat battery to keep it in a charged state, or what about using one to charge nicad cells to provide a light in the garden shed or along the garden path without running cables? Or have a solar powered door bell or burglar alarm. The list is endless. Coupled to a lead acid battery and a simple inverter you could build yourself a self contained mains supply for the boat or caravan for low power appliances. With a few dozen cells and car batteries you could almost do without mains power altogether. They you could really tell the CEGB to go and get privatised! – well perhaps not, even though the price is only £14.50 plus VAT.



These solar cells are deposited on a sheet of glass and are well protected against the elements, but are rather fragile to send in the post. TK, therefore, have to make a charge of £1.75 per order to cover the cost of packaging units sent by post.

TK also stock low-cost smoke detectors – as low as £8.50 each, plus VAT.

For more information contact: TK Electronics, 13 Boston Road, London W7 3SJ. Tel: 01-567 88910.

Marco poly

There's even more to Marco Trading now that they have opened their third retail shop, in Birmingham.

It's called Supertronics and has over 1000 sq.ft of sales area offering not only a wide selection of components, including 100s of transistors, ics, resistors, cable, opto, but also speakers from 4W to 200W, test equipment both new and secondhand, alarm equipment, video surveillance cameras, and also, a full on site audio and video repair service by resident engineers.

They have designed this shop to display nearly all the items they sell and you can browse at your leisure during opening hours of Monday to Saturday, 9.00 to 6.00 pm (closed Wednesday).

Pay them a visit, not only will you receive a warm welcome but you will find many bargains.

The address is: Supertronics, 65 Hurst Street, Birmingham B5. Tel: 021-666 6504.

Servicing an Era

There can be few readers of an older generation living around London who will not remember, with affection, the Mecca that Lisle Street and Little Newport Street used to be.

Sadly, I learn from Gerry Cohen of the Service Trading Company, that the era of this constructor's paradise is now finally drawing to a close. After over forty years in Little Newport Street, Service Trading are closing their premises there in mid-November. With their departure, the last of the component shops along that stretch will have gone.

I am sure I am not alone in regretting the end of a diy electronics era. When, nearly thirty years ago, I bagan tentative explorations of electronics practicalities, much of my experimental stock came from these two streets. For many years the premises of G.W. Smith, Gee Bros, Southern Radio, West End Radio, Universal Radio, etc, as well as Service Trading, were my frequent haunts. Indeed, I half-recall that my original scope tube actually came from Service Trading, as well as my first piece of Veroboard.

The Service Trading Company was registered in 1947 by the partnership of Gerald Cohen, ex Sgt Intelligence Corp, and Ted Gilby, ex Capt RASC. After some years in the City, the business was transferred to Little Newport Street, joining those other memorable names. The company has been a frequent advertiser in PE right since the first issue in November 1964.

Gerry Cohen comments that thousands of young men, like myself, gained their first entry into the world of electronics through Lisle Street and Little Newport Street. He knows that many thriving companies owed their start to the help and advice given by "Uncle Ted" who, incidentally, designed the first stroboscope especially for the then-developing disco market.

Over the last few years, the area has been gradually taken over by the Chinese, and is now colloquially known as China Town. Bowing to the inevitable, Service Trading are abandoning the Orient, and retreating West, to West London in fact. Here they will be carrying on business from their warehouse premises at 57 Bridgman Road, Chiswick, London W4 5BB, telephone 01-995 1560. Gerry says that callers will continue to be welcome, and that the parking is easy!

A London era may be passing, but at least not all participants are mortally wounded. I, for one, shall remember those days with gratitude. **Ed.**

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Ferguson, market leader in video for over a decade, is aiming to establish a firm foothold in the new "digital" segment of the market with a state-of-the-art video recorder offering the latest in digital technology.

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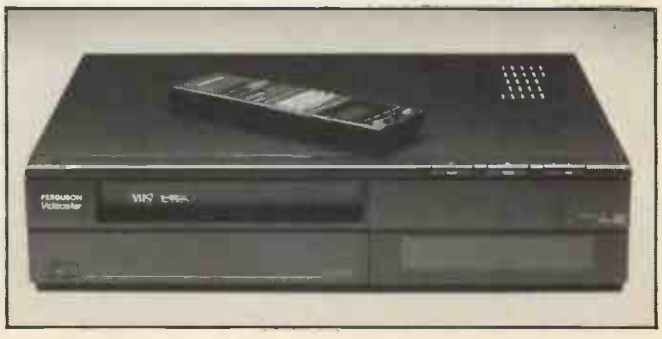
There are numerous other features which stockists will be pleased to demonstrate, aided by a five-minute video demo-tape.

For further information contact: Anne Waterman, Ferguson Ltd., Cambridge House, Great Cambridge Road, Enfield, Middx. EN1 1ND. Tel: 01-363 5353.



OPERATIONAL WAR GAMES

By Barry Fox
Winner of the 1987 UK Technology Press Award

MS DOS WON'T JUST GO AWAY

IBM is in the process of unveiling its new operating system, OS/2, but it will need a good argument to out flank the established MS DOS and the growing attractions of Unix.

The personal computer world is now gearing up for a major battle over operating systems – the software which must be loaded into a computer when it is first switched on, to control the running of all other programs, for instance a database, spreadsheet or word processor.

When IBM decided, in 1981, to start selling personal computers as well as mainframes, the company backed MS-DOS, (Disc Operating System) written by US company Microsoft. This made the system, now usually known simply as DOS, a de facto standard for small business computers.

Before that the de facto standard had been CP/M (Control Program for Microcomputers) developed by Digital Research. There are any number of stories which tell why IBM chose DOS rather than CP/M; the most colourful is that the CP/M people were out playing golf when Big Blue called.

Since last year IBM has been talking about OS/2 and PS/2 being the future standard; others say that they prefer to stay with DOS; and others see the future standard as Unix, an operating system developed by Bell Laboratories in America and now licensed by AT and T.

In an effort to get clearer in my mind what was going on, I recently went to two seminars, one run by IBM and another by Oracle, the world's fourth largest software company which is now marketing Case programs. This software (computer aided software engineering) helps professional computer programmers write better programs by letting them map out basic requirements on a computer – and then try out all possible options before finalising programs for sale to computer users. The software for Star Wars, the Strategic Defence Initiative, could for instance never be written without the aid of Case software.

As always with IBM, the message was one of Big Blue knows best complacancy. IBM's corporate line is smugly unswerving, and awkward questions are just brushed aside. But it all helps the jigsaw fit together.

MS DOS suffers from one major drawback. It cannot handle multitasking. In plain words, the computeer cannot do more than one thing at a time. If control software like Microsoft's Windows is used with DOS, to let several programs sit in a computer's memory at the same time, it must freeze work on one task when starting work on another. Clearly this is a nonsensical state of affairs, which bears no relationship to real business.

Quite why IBM should have worked with Microsoft to make a de facto standard out of such a limited system as DOS remains one of life's mysteries – unless it was intended to create future obsolescence. As far back as the late 1960s, IBM was making large main frame computers which would multitask, handling several programs at the same time.

Now, years overdue, IBM has worked with Microsoft to produce a multitasking operating system, known as Operating System/2. This works with the new generation of 32-bit computer chips (eg the Intel 80386) to handle more than one job at the same time. In reality, the computer chip is switching between different tasks so rapidly that the user does not realise there is any interruption.

This is an extension of the well known facility, available on most computers, called print spooling. The computer briefly takes time off from running an ordinary program, like a word processor and sets the printer running to print out a finished document. Then, while the printer chugs away, the computer goes back to word processing. Communication programs can also work in the same way, downloading files from a telephone line while the user is doing something else. In the future programs specially written for OS/2 and the 386 chip will run together so that the user can set a spread sheet calculating and a data base searching while carrying on with text editing in a word processor.

To preserve a degree of backwards compatibility OS/2 can be switched to mimic MS DOS. So a computer running OS/2 can still handle old programs written for DOS as well as new programs written for OS/2. Alternatively the hard disk in a computer can be partitioned so that part of it runs old programs under DOS and part of it runs new programs under OS/2.

The first version of OS/2, which is now available as V1.0, is quite basic and requires a fair degree of knowledge on the part of the user. At the end of this year Version 1.1, called Presentation Manager, will be much easier to use; it is more like an Apple Mac. The industry sees the release of Presentation Manager as a crucial watershed. IBM glibly assumes that OS/2 will then become the new standard.

Like a soap powder manufacturer pushing the latest whiter-than-white recipe, IBM is now surprisingly dismissive of existing IBM PCs and MS DOS. Many business users, who have invested in IBM equipment and programs for MS DOS, may see this as a breach of faith. So IBM's enthusiasm for OS/2 may well backfire.

The company has a hidden and very good reason for wanting OS/2 to become the new standard. IBM has developed a new range of computers, called PS/2 (Personal System/2) which are designed to run OS/2 software. They do this with a new technology called Micro Channel Architecture or MCA which was designed to work with the 386 computer chip. And IBM claims legal monopoloy on MCA.

Although IBM describes the Micro Channel as hardware, there is a considerable amount of confusion over exactly what it is and does. IBM usually describes MCA by making bland analogies with busy offices, hourglass bottlenecks and crowded bank queues. The nearest to nitty-gritty forthcoming from IBM is the explanation that MCA eliminates the old bottleneck of a single data bus by using multiple input and output channels for data signals going to and from the computer precessor chip. This lets the data run slower, creating less risk of radio frequency interference. Also the data is transferred in blocks of 256 bytes instead of one byte at a time. On the face of things, MCA sounds an obvious logical approach to a persistent problem.

IBM wants to emphasise the value of MCA, because it hopes to use the new PS/2-OS/2-MCA approach as a way of squeezing royalties out of other firms

Continued on page 30.

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DOMINATING BRITAIN

Crises in the British enconomy are nothing new and, despite media excitement each time another one occurs, we seem to overcome them. It was with this in mind that I read yet another crisis report, about the strength of the UK electronics industry.

NEDO, the National Economic Development Office, issued the report and in it they spotlight the fact that UK electronics companies have been losing business to overseas competitors. The report warns that by the early 1990s, British companies will be supplying only half the available home market compared to 15 years ago. This will be despite the growth of the market by close to 10% in the last year. Failure to meet the needs of this expanding market is blamed on management's lack of visionary leadership.

While criticism of this nature comes as no surprise, the report's attitude towards British electronic's involvement in defence and telecommunications is unexpected. There is too heavy an emphasis, it says, on these two areas which it considers to be currently stagnant.

We cannot disagree with the need for Britain to take a more aggressive and visionary approach, but to criticise British success in defence and telecoms, seems contradictory. Other reports which we periodically receive appear to indicate that both areas are growing and that we are winning overseas orders in the face of stiff foreign competition.

We recognise that in some areas, British technology, although by no means outclassed on quality, is frequently outflanked by low price tags on foreign equivalents. This is particularly true of consumer goods, as exemplified by the proliferation of imports in the high street shops.

Aggression may well be needed in making the home market more aware that to buy British means to buy quality, but when the average person is buying sophisticated equipment, cost is an important factor. To discriminating purchasers, no amount of aggressive promotion will convince them that of two technically equivalent products, the more expensive will necessarily be the best. Most people would prefer to support British industry, but they cannot be expected to subsidise it by choice.

There is no question that management must continue to streamline production costs. An area that is debatable is the way in which stock market activities often play an unpredictable part in management's attempt at achieving competitive costs. The report rejects the premise that British electronics is hindered by the fickleness of City investors, but it concedes that industry can do more to communicate long term strategies to the financial markets. It also calls for a better component infrastructure and more emphasis on human resources.

Although the report is negative in some aspects, it should be regarded as a source of workable recommendations for achieving future success. We are reassured in our belief that British electronics can dominate the global technology market.

THE EDITOR

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7417	0.40
7420	0.30
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7422	0.36
7423	0.36
7425	0.40
7426	0.40
7427	0.32
7428	0.43
7430	0.30
7432	0.36
7433	0.30
7437	0.30
7438	0.40
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7447A	1.00
7448	1.00
7450	0.36
7451	0.35
7453	0.38
7454	0.38
7460	0.55
7470	0.50
7472	0.45
7473	0.45
7474	0.50
7475	0.60
7476	0.45
7480	0.65
7481	1.80
7483A	1.05
7484A	1.25
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7486	0.42
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7491	0.70
7492A	0.70
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7494	1.10
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7497	2.90
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74107	0.50
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74111	0.55
74116	1.70
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74119	1.70
74120	1.00
74121	0.55
74122	0.70
74123	0.80
74125	0.65
74126	0.55
74128	0.55
74132	0.75
74136	0.70
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74142	2.50
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74144	2.70
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74147	1.70
74148	1.40
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74151A	0.70
74153	0.80
74154	1.40
74155	0.80
74156	0.90
74159	2.25
74160	1.10
74161	0.80
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74163	1.10
74164	1.20
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74170	2.00
74172	4.20
74173	1.40
74174	1.10
74175	1.05
74176	1.00
74178	1.50
74179	1.50
74180	1.00
74181	3.40
74182	1.40
74184	1.80
74185A	1.80
74190	1.30
74191	1.30
74192	1.10
74193	1.15
74194	1.10
74195	0.80
74196	1.30
74197	1.10
74198	2.20
74199	2.20
74221	1.10
74251	1.00
74259	1.50
74265	0.80
74273	2.00
74276	1.40
74278	1.70
74279	0.90
74283	1.05
74742	3.20
74290	0.90
74293	0.90
74298	1.80
74351	2.00
74365A	0.80
74366A	0.80
74367A	0.80
74376	1.60
74390	1.10
74393	1.20
74490	1.40

74LS SERIES

74LS00	0.24
74LS01	0.24
74LS02	0.24
74LS03	0.24
74LS04	0.24
74LS05	0.24
74LS08	0.24
74LS09	0.24
74LS10	0.24
74LS11	0.24
74LS13	0.34
74LS14	0.50
74LS15	0.24
74LS20	0.24
74LS21	0.24
74LS22	0.24
74LS24	0.50
74LS26	0.26
74LS27	0.24
74LS28	0.24
74LS30	0.24
74LS32	0.24
74LS33	0.24
74LS37	0.24
74LS38	0.24
74LS40	0.24
74LS42	0.50
74LS43	1.50
74LS48	0.90
74LS49	1.00
74LS51	0.24
74LS54	0.24
74LS55	0.24
74LS73A	0.30
74LS74A	0.35
74LS75	0.45
74LS76A	0.36
74LS78	0.42
74LS83A	0.70
74LS85	0.75
74LS86	0.35
74LS90	0.48
74LS92	0.95
74LS93	0.54
74LS95B	0.75
74LS96	0.90
74LS107	0.40
74LS109	0.40
74LS112	0.45
74LS113	0.45
74LS114	0.45
74LS122	0.70
74LS123	0.80
74LS125	0.50
74LS126	0.50
74LS132	0.65
74LS133	0.55
74LS136	0.45
74LS138	0.55
74LS139	0.55
74LS145	0.95
74LS147	1.75
74LS148	1.40
74LS151	0.65
74LS152	2.00
74LS153	0.65
74LS154	1.80
74LS155	0.65
74LS156	0.65
74LS157	0.50
74LS158	0.65
74LS160A	0.65
74LS161A	0.75
74LS162A	0.75
74LS163A	0.75
74LS164	0.75
74LS165A	1.10
74LS166A	1.50
74LS168	1.30
74LS169	1.00
74LS170	1.40
74LS173A	1.00
74LS174	0.75
74LS175	0.75
74LS181	2.00
74LS183	1.90
74LS190	0.75
74LS191	0.75
74LS192	0.80
74LS194A	0.75
74LS195A	0.75
74LS196	0.80
74LS197	0.80
74LS221	0.90
74LS240	0.80
74LS241	0.80
74LS242	0.90
74LS243	0.90
74LS244	0.70
74LS245	0.90
74LS247	1.10
74LS248	1.10
74LS249	1.10
74LS251	0.75
74LS253	0.75
74LS256	0.90
74LS257A	0.70
74LS258A	0.70
74LS259	1.20
74LS260	0.75
74LS266	0.80
74LS273	1.25
74LS279	0.70
74LS280	1.90
74LS283	0.80
74LS290	0.80
74LS292	14.00
74LS293	0.80
74LS295	1.40
74LS297	14.00
74LS298	1.00
74LS299	2.20
74LS321	3.70
74LS322A	3.90
74LS323	3.00
74LS324	3.20
74LS348	2.00
74LS352	1.20
74LS353	1.20
74LS356	2.10
74LS363	1.80
74LS364	1.80
74LS365	0.50
74LS366	0.50
74LS367	0.52
74LS368	0.50
74LS373	0.70
74LS374	0.70
74LS375	0.75
74LS377	1.30
74LS378	0.95
74LS379	1.30
74LS381	4.50
74LS385	3.25
74LS390	0.60
74LS393	1.00
74LS395A	1.00
74LS399	1.40
74LS445	1.80
74LS465	1.20
74LS467	1.20
74LS490	1.50
74LS540	1.00
74LS541	1.00
74LS608	7.00
74LS610	25.00
74LS612	25.00
74LS624	3.50
74LS626	2.25
74LS628	2.25
74LS629	1.25
74LS640	2.00
74LS640-1	3.00
74LS641	1.50
74LS642	2.50
74LS642-1	3.00
74LS643	2.50
74LS643-1	3.00
74LS644	3.50
74LS645	2.00
74LS645-1	4.00
74LS668	0.90
74LS669	0.90
74LS670	1.70
74LS682	2.50
74LS683	3.00
74LS684	3.50
74LS687	3.50
74LS688	3.50
74LS783	16.00

74S SERIES

74S00	0.50
74S02	0.50
74S04	0.50
74S05	0.50
74S08	0.50
74S10	0.50
74S20	0.50
74S22	0.50
74S30	0.50
74S32	0.60
74S37	0.60
74S38	0.80
74S40	0.50
74S51	0.60
74S64	0.45
74S74	0.70
74S85	5.50
74S86	1.00
74S112	0.90
74S113	1.20
74S114	1.20
74S124	3.00
74S132	1.00
74S133	0.80
74S138	1.80
74S139	1.80
74S140	1.00
74S151	1.50
74S153	1.50
74S157	2.00
74S158	2.00
74S163	3.00
74S169	5.50
74S174	3.00
74S175	3.20
74S188	1.80
74S189	1.80
74S194	3.00
74S195	3.00
74S196	3.50
74S200	4.50
74S201	3.20
74S225	5.20
74S240	4.00
74S241	4.00
74S244	4.00
74S251	2.50
74S257	2.50
74S258	2.50
74S260	1.00
74S261	3.00
74S283	2.70
74S287	2.25
74S288	2.00
74S289	2.25
74S299	4.50
74S373	4.00
74S374	4.00
74S387	2.25

Special offer to PE readers only – 10% off on all TTLS and CMOS

74ALS SERIES

74ALS00	0.45
74ALS02	0.45
74ALS04	0.50
74ALS08	0.50
74ALS10	0.45
74ALS20	0.45
74ALS32	0.45
74ALS74	0.70
74ALS138	1.50
74ALS139	1.50
74ALS244	4.00
74ALS245	4.75
74ALS573	2.60
74ALS574	4.50
74ALS580	2.60

4000 SERIES

4000	0.20
4001	0.24
4002	0.25
4006	0.70
4007	0.25
4008	0.60
4009	0.45
4010	0.60
4011	0.24
4012	0.25
4013	0.36
4014	0.60
4015	0.70
4016	0.36
4017	0.55
4018	0.60
4019	0.60
4020	0.80
4021	0.60
4022	0.70
4023	0.30
4024	0.48
4025	0.24
4026	0.90
4027	0.40
4028	0.60
4029	0.75
4030	0.35
4031	1.25
4032	1.00
4033	1.25
4034	2.50
4035	0.70
4036	2.50
4037	1.10
4038	1.00
4040	0.60
4041	0.55
4042	0.50
4043	0.60
4044	0.60
4045	1.00
4046	0.60
4047	0.60
4048	0.55
4049	0.36
4050	0.35
4051	0.65
4052	0.60
4053	0.60
4054	0.80
4055	0.80
4056	0.85
4060	0.70
4063	0.85
4066	0.40
4067	2.30
4068	0.25
4069	0.24
4070	0.24
4071	0.24
4072	0.24
4073	0.24
4075	0.24
4076	0.65
4077	0.25
4078	0.25
4081	0.24
4082	0.25
4085	0.60
4086	0.75
4089	1.20
4093	0.35
4094	0.90
4095	0.95
4096	0.90
4097	2.70
4098	0.75
4099	0.99
4501	0.36
4502	0.55
4503	0.36
4504	0.95
4505	3.60
4506	0.90
4507	0.35
4508	1.20
4510	0.55
4511	0.55
4512	0.55
4513	1.50
4514	1.10
4515	1.10
4516	0.55
4517	2.20
4518	0.48
4519	0.32
4520	0.60
4521	1.15
4522	0.80
4526	0.70
4527	0.80
4528	0.65
4529	1.00
4531	0.75
4532	0.65
4534	3.80
4536	2.50
4538	0.75
4539	0.75
4541	0.90
4543	0.70
4551	1.00
4553	2.40
4555	0.36
4556	0.50
4557	2.40
4560	1.40
4566	1.40
4568	2.40
4569	1.70
4572	0.45
4583	0.90
4584	0.48
4585	0.80
4724	1.50
14411	7.50
14412	7.50
14416	3.00
14419	2.60
14490	4.20
14495	4.50
145000	6.50
14599	2.00
22100	3.50
22101	7.00
22102	7.00
40014	0.48
40085	1.20
40097	0.36
40098	0.40
40100	1.50
40101	1.25
40102	1.30
40103	2.00
40104	1.20
40105	1.50
40106	0.48
40107	0.55
40108	3.20
40109	0.80
40110	2.25
40114	2.25
40107	2.80
40163	1.00
40173	1.20
40174	1.00
40175	1.00
40192	1.00
40193	1.00
40194	1.00
40244	1.50
40245	1.50
40257	1.80
40373	1.80
40374	1.80
80C95	0.75
80C97	0.75
80C98	0.75

LINEAR ICs

ADC0808	11.90
AM7910DC	12.00
AN103	2.00
AN-1-5050	1.00
AY-3-1350	5.00
AY-3-8910	4.90
AY-3-8912	5.00
CA3019A	1.00
CA3020	3.50
CA3028A	1.10
CA3046	0.70
CA3059	3.25
CA3060	3.50
CA3080E	0.70
CA3085	1.50
CA3086	0.60
CA3089E	2.50
CA3090AQ	3.75
CA3130E	0.90
CA3130T	1.30
CA3140E	0.45
CA3140T	1.00
CA3146	2.25
CA3160E	1.50
CA3161E	2.00
CA3162E	6.00
CA3189E	2.70
CA3240E	1.50
CA3280G	3.00
D7002	8.00
DAC1408-8	3.00
DAC0800	3.00
DAC0808	3.00
DG308	3.00
HA1366	1.90
ICL7106	6.75
ICL7611	0.95
ICL8038	4.00
ICM7555	0.90
ICM7556	1.40
LC7120	3.00
LC7130	3.00
LC7131	3.50
LC7137	3.50
LF347	1.20
LF351	0.60
LF353	0.90
LF355	0.90
LF356N	1.10
LF357	1.00
LF398	4.00
LM10CLH	4.50
LM301A	0.30
LM307	0.45
LM308CN	0.75
LM310	2.25
LM311	0.60
LM318	1.50
LM319	1.80
LM324	0.45
LM334Z	1.15
LM335Z	1.30
LM336	1.60
LM339	0.40
LM348	0.60
LM358P	0.50
LM377	3.00
LM380N-8	1.50
LM380N	1.50
LM383	3.25
LM384	2.20
LM386N-1	1.00
LM387	2.70
LM391	1.80
LM392N	1.10
LM393	0.85
LM394CH	4.00
LM709	0.35
LM710	0.48
LM711	1.00
LM723	0.80
LM725CN	3.00
LM733	0.65
LM741	0.22
LM747	0.70
LM748	0.30
LM1011	4.80
LM1014	1.50
LM1801	3.00
LM1830	2.50
LM1871	3.00
LM1872	3.00
LM1886	6.00
LM1889	4.50
LM2917	3.00
LM3302	0.90
LM3900	0.80
LM3909	1.40
LM3911	1.80
LM3914	3.50
LM3915	3.40
LM3916	3.40
LM13600	1.50
MS1513L	2.30
MB1516L	4.50
MB3712	2.00
MC1310P	1.50
1413	0.75
MC1458	0.45
MC1495	3.00
MC1496	0.70
MC3340P	2.00
MC3401	0.70
MC3404	0.65
MF10CN	4.10
MK50240	9.00
ML902	5.00
ML922	4.00
MM6221A	3.00
NE529	2.20
NE531	1.20
NE544	1.90
NE555	0.22
NE556	0.60
NE564	4.00
NE565	1.20
NE566	1.50
NE567	1.25
NE570	4.00
NE571	3.00
NE592	0.90
NE5532P	1.50
NE5533P	1.60
NE5534P	1.20
NE5534AP	1.50
OP-07EP	3.50
PLL02A	5.00
RC4136	0.55
RC4151	2.00
RC4195	1.50
RC4558	0.55
S50240	9.00
SN76013N	5.00
SN76033N	5.00
SPO256AL2	7.00
SP8515	7.50
TA7120	1.20
TA7130	1.40
TA7204	1.50
TA7205	0.90
TA7222	1.50
TA7310	1.50
TBA231	1.20
TBA800	0.80
TBA810	0.90
TBA20	0.80
TBA820M	0.75
TBA920	2.00
TBA950	2.25
TC9109	5.00
TCA270	3.50
TCA940	1.75
TDA1010	2.25
TDA1024	1.10
TDA1170S	3.00
TDA1170N	3.00
TDA2002	3.25
TDA2003	1.90
TDA2004	2.40
TDA2006	3.20
TDA2030	2.50
TDA2593	5.00
TDA2653	7.00
TDA3560	7.50
TDA3810	7.50
TDA7000	3.50
TEA1002	7.00
TL061CP	0.40
TL062	0.60
TL064	0.90
TL071	0.40
TL072	0.70
TL074	1.10
TL081	0.35
TL082	0.55
TL083	0.75
TL084	1.00
TL094	2.00
TL430C	1.20
UA759	3.20
UA2240	1.20
UCN5801A	8.00
ULN2001A	0.75
ULN2002A	0.75
ULN2003A	0.75
ULN2004A	0.75
ULN2068	2.90
ULN2802	1.90
ULN2803	1.80
ULN2804	1.90
UPC575	2.75
UPC592H	2.00
UPC1156H	3.00
UPC1185H	5.00
XR210	4.00
XR2206	4.50
XR2207	3.75
XR2211	5.75
XR2216	6.75
XR2240	1.20
ZN404	1.00
ZN414	0.80
ZN419P	1.75
ZN423E	1.30
ZN424E	1.30
ZN425E8	3.50
ZN426E8	3.00
ZN428E8	6.00
ZN428E8	4.50
ZN429E8	2.25
ZN447E	9.00
ZN448I	7.50
ZN449E	3.00
ZN450E	7.50
ZN459CP	3.00
ZN1034E	2.00
ZNA1040	6.60
ZNA134H	23.00
ZNA234E	9.50

VOLTAGE REGULATORS

1A FIXED VOLTAGE PLASTIC TP220

+VE		−VE	
5V 7805	0.45	7905	0.50
6V 7806	0.50	7906	0.50
8V 7808	0.50	7908	0.50
12V 7812	0.45	7912	0.50
15V 7815	0.50	7915	0.50
18V 7818	0.50	7918	0.50
24V 7824	0.50	7924	0.50

1A FIXED VOLTAGE PLASTIC TO92

5V 78L05	0.30	5V 79L05	0.45
6V 78L06	0.30	12V 79L12	0.50
8V 78L08	0.30	15V 79L15	0.50
12V 78L12	0.30		
15V 78L15	0.30		

OTHER REGULATORS

FIXED REGULATORS

LM309K	1A 5V	1.40
LM323K	3A 5V	3.50
L78H05KC	5A 5V	7.50

VARIABLE REGULATORS

LM305AH		2.50
LM317T	TO-220	1.20
LM317K	TO3	2.40
LM337T		2.25
LM350T		4.00
LM723N		0.50

SWITCHING REGULATORS

ICL7660	2.50
SG3524	3.00
TL494	3.00
TL497	2.25
78S40	2.50
RC4195	1.50

COMPUTER COMPONENTS

CPUs

1802CE	6.50
2650A	10.50
6502	4.50
65C02-2MHz	12.00
6502A	6.50
6502B	8.00
6800	3.50
6802	3.50
6809	6.50
6809E	10.00
68B09	10.00
68B09E	12.00
68000-L8	36.00
8035	3.50
80C35	6.00
8039	4.20
80C39	7.00
8080A	9.50
8085A	3.00
80C85A	7.50
8086	22.00
8087-5	£120
8087-8	£160
8088	17.50
8741	15.00
8748	10.00
TMS1601	12.00
TMS9980	14.50
TMS9995	18.00
V20-8	12.00
V30-8	12.00
Z80	2.50
Z80A	2.90
Z80B	5.50
Z80H	7.50

SUPPORT DEVICES

2651	12.00
3242	8.00
3245	4.50
6520	3.00
6522	3.50
6522A	5.50
6532	4.80
6551A	6.00
6821	1.60
68B21	2.50
68040	3.75
68B40	8.00
6850	1.80
68B50	2.50
6852	2.50
6854	6.50
68B54	8.00
8154	8.50
8155	3.80
8156	3.80
8205	2.25
8212	2.00
8216	1.60
8224	POA
8226	4.25
8228	5.50
8237	6.50
8243	2.60
8250	12.00
8251A	3.25
8253C-5	3.50
8255AC-5	3.20
8256	18.00
8257C-5	54.00
8259C-5	4.00
8275	29.00
8279C-5	4.80
8282	4.00
8284	4.60
8287	3.80
8288	6.50
8755A	16.00

MEMORIES

2114-3	1.00
4116-2	1.50
4164-15	2.50
41256-15	2.00
41464-15	9.50
4416-15	3.00
5101-3	5.00
6116LP-15	6.50
62256	12.00
6810	2.50

PROMS

28L22	4.00
74S188	1.80
74S287	2.25
74S288	1.80
74S387	2.25
82S23	1.50
82S123	1.50
82S129	1.75

IBM AMSTRAD UPGRADES

8087-6	£75
8087-8	£120
8087-10	£175
80287-5	£160
80287-8	£190
8087-10	£225

EPROMs

2716-45	4.50
2732-45	4.50
2764-25	4.00
27C64	4.50
27128-25	4.80
27C256	5.50
27512	9.00
27513	18.00
27C128	5.50
27256	5.50

RS232

MAX232	7.50

CRT CONTROLLER

CRT5037	12.00
CRT6545	9.00
EF9365	32.00
EF9366	32.00
EF9367	36.00
EF9369	12.00
MC6845	6.50
MC6845SP	6.50
MC6847	6.50
SFF96364	8.00

INTERFACE ICs

ADC0808	10.00
AD561J	20.00
AM25S10	3.50
AM25LS2521	3.50
AM25LS2538	3.50
AM26LS31	1.20
AM26LS32	1.20
AM7910DC	25.00
DM8131	8.00
DP8304	4.50
DS3691	4.50
DS8830	1.40
DS8831	1.50
DS8832	1.50
DS8833	2.25
DS8836	1.50
DS8838	2.25
D7002	8.00
MC1488	0.60
MC1489	0.60
MC3446	2.50
MC3459	4.50
MCS3470	4.75
MC3480	8.50
MC3486	2.25
MC3487	2.25
MC4024	5.50
MC4044	5.50
MC6852	16.00
MC14411	7.50
MC14412	7.50
75107	0.90
75108	0.90
75109	1.20
75110	0.90
75112	1.60
75113	1.20
75114	1.40
75115	1.40
75121	1.40
75122	1.40
75150P	1.20
75154	1.20
75159	2.20
75160	5.00
75161	6.50
75162	7.50
75172	4.00
75182	0.90
75188	0.80
75189	0.80
75365	1.50
75450	0.80
75451	0.50
75452	0.50
75453	0.70
75454	0.70
75480	1.50
75491	0.65
75492	0.65
8T26	1.20
8T28	1.20
8T95	1.20
8T96	1.20
8T97	1.20
8T98	1.20
81LS95	1.40
81LS96	1.40
81LS97	1.40
81LS97	1.40
81LS98	1.40
88LS120	6.50
9602	3.00
9636A	1.80
9637AP	1.80
E	9.50
9638	1.80
9639	2.50

DISC CONTROLLER ICs

765A	10.00
6843	8.00
8272	10.00
FD1771	20.00
FD1791	20.00
FD1793	20.00
FD1797	22.00
WD1770	24.00
WD1691	15.00
WD2143	12.00
WD2793	27.00
WD2797	27.00

CHARACTER GENERATORS

RO32513UC	7.50
RO32513LC	7.00

TELETEXT DECODER

SAA5020	6.00
SAA5030	7.00
SAA5041	18.00
SAA5050	9.00

KEYBOARD DECODERS

74C922	5.00
74C923	6.00

BAUD RATE GENERATORS

MC14411	7.50
COM8116	6.50
4702B	7.50

UARTs

AY3-1015	3.00
AY51013P	3.00
COM8017	3.00
IM6402	4.50

MODULATORS

6MHz	3.75
8MHz	4.50

SOUND & VISION

12MHz	12.00

CRYSTALS

32.768KHz	1.00
1.8432MHz	2.25
2.00MHz	2.25
2.45760MHz(L)	2.00
2.45760MHz(S)	2.50
2.5MHz	2.50
3.276MHz	1.50
3.5795MHz	1.00
4.00MHz	1.50
4.194MHz	2.00
4.43MHz	1.00
4.9152MHz	2.50
5.00MHz	1.50
5.068MHz	1.75
6.00MHz	1.40
6.144MHz	1.40
7.00MHz	1.50
7.16MHz	1.75
8.00MHz	1.50
8.876MHz	1.75
10.00MHz	1.75
10.50MHz	2.50
10.70MHz	1.50
11.00MHz	3.00
12.00MHz

DUAL BEAM OSCILLOSCOPE

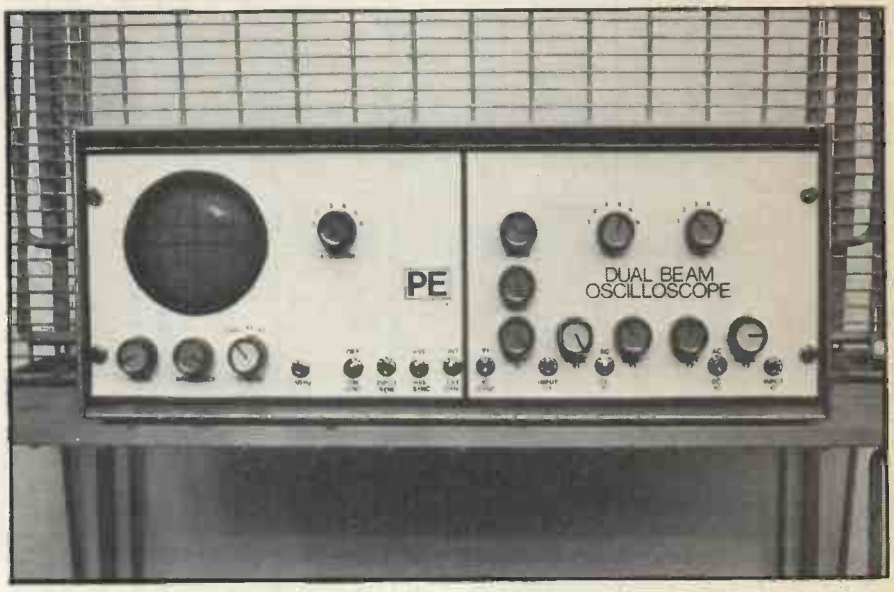
PART ONE BY JOHN BECKER

IT'S HERE AT LAST! THE DIY 'SCOPE

'It gave electronics a face – I could see what things were doing. It was like having a blindfold taken away', says your Editor of his first oscilloscope. Now – after many requests – PE has the tubes and the design for you to make your own.

Periodically, I am asked why PE does not publish an article on how to make your own oscilloscope. Well, we have in the past, but the last time was 1967. Recently, though, the basic problem has not been the designing of the electronic circuitry, but the inexpensive and reliable supply of the necessary cathode ray tube (crt). Since I last gave this answer as a published reply to one letter, Langrex Supplies have kindly offered to supply low cost tubes to interested readers.

I know that there are many of you who *are* interested in building your own scope, and others who would like to know the basic principles of how a scope works. Next to a multimeter, a scope is the most important piece of test gear that a serious electronics enthusiast should own. Indeed a scope used properly can in many instances be used instead of a multimeter.



Front panel of the dual beam oscilloscope

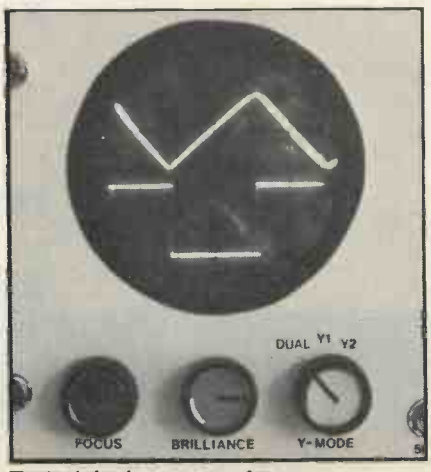
HISTORICAL CONTEXT

When, as an amateur, I first started serious electronics, a scope was the first major project I ever built. At that time, in the late 50s, ex-government crts were readily and cheaply available. Buying one, I built up a very simple single-trace scope from the details given in the 1957 edition of *The Oscilloscope Book* by E.N. Bradley. I don't suggest you try to find a copy as I expect the book is long out of print, and in any case I doubt you could still get the parts.

Historically, though, I am sure that this book was of considerable importance and many amateurs from that era must have had their enthusiasm reinforced by constructing its contents. For me it gave electronics a face – I could *see* what things were doing. Previously, I could only try to imagine what was happening to electronic signals – it was like being blindfolded. With that scope the blindfold was taken away and electronic experimentation ceased to be a mere fumbling through cloaks of mystery.

With hindsight, it's remarkable that although the transistor was invented in 1947, most circuits available to amateurs in the late 50s still used valves (that scope had seven of them). Printed circuit boards, too, had yet to arrive for amateurs and components were still soldered back and forth across tag boards. Even Veroboard did not appear until about 1964 (the year PE started). Whereas today we think of circuits drawing only a few milliamps or less, currents of several amps were still considered normal.

That diy scope served me well for several years, until I was given an old Solatron – still valve driven, still built on tag boards, and still current hungry – no need for separate central heating with that machine switched on. Incidentally, it still works.



Typical dual trace waveforms.

FURTHER SCOPE

More sophisticated machines now grace my workshop, but such are beyond the pockets of most amateurs. So, knowing how much can be gained from building and using one's own scope, I am pleased to offer you the opportunity to do both. Here then is a description of a simple low cost diy scope, designed around semiconductors, on pcbs, and which doesn't significantly raise room temperatures. And, moreover, it has *two* traces – effectively it's a dual-beam machine.

I shall keep the description of basic scope principles to just the essentials as they apply to simple equipment using tubes of an electrostatic nature. For those who would like to know greater detail about various types of scope, I recommend you read *Oscilloscopes (How to Use Them, How They Work)* by Ian Hickman, published by Newnes Technical Books and available through the PE book service.

CATHODE RAY TUBE

The cathode ray tube is the device which displays the picture of the electrical waveforms under examination. Fig. 1 shows a simple schematic of the internal functional construction.

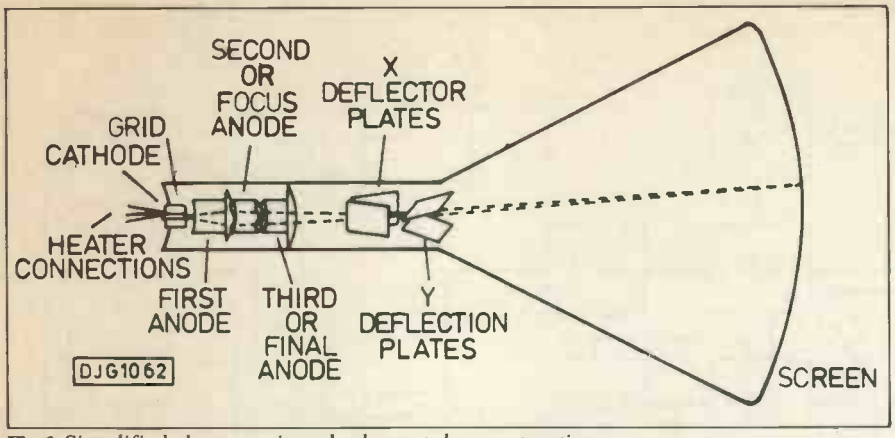


Fig.1 *Simplified electrostatic cathode ray tube construction.*

The crt consists of an evacuated glass tube with one end flared out into the screen area, and the other end having a ring of connecting pins. Within the tube are a series of plates which control the production, acceleration and deflection of an electron beam aimed at the screen. The screen is specially coated with a material which fluoresces under the impact of the electron beam.

In common with any thermionic valve, the crt has a heated cathode which emits a cloud of electrons. These flow towards a positively charged first anode, but can be impeded by the potential on a grid placed along their path, so allowing the picture brightness to be varied. The second anode also attracts the electrons, but its charge can be varied so that it behaves as a focusing control. The final anode is held at the same potential as the first anode, and further accelerates the electron flow.

Two pairs of additional plates, known as the X and Y plate pairs, are placed between the final anode and the screen and deflect the beam according to their applied potential. Positioned perpendicular to each other about the beam path, the X pair deflects horizontally, and the Y pair deflects vertically. Although the control potentials may differ for the two sets of plates, in most instances they can be treated interchangeably, the physical alignment of the tube determining the horizontal or vertical attributes. Having passed the deflection plates the beam continues on to hit the screen at the relevant position.

DEFLECTION

The way that the deflection plates function is shown in Fig. 2. The electron beam is negatively charged, and consequently it will be attracted by any plate that is positively charged, hence the way that the beam can be accelerated and focused by the anodes. Conversely, if a plate is negatively charged, the beam of electrons will be repelled by it. If the deflection plates have their charge balanced with respect to one another, the beam will not be deflected and will take a central course between the plates. Since the plates are in pairs, the beam can be moved to the left, for example, by positively charging the left hand plate, or by negatively charging the right hand plate. In a final design the plate control may be implemented by holding one plate of a pair at a constant potential while applying the signal charge to the other. Alternatively, both plates may be controlled by the signal in a push-pull mode.

Conventionally, the beam is repeatedly drawn across the screen face along the X axis, horizontally left to right. The voltage level to be analysed is displayed on the Y axis, upwards for positive-going potentials, and downwards for negative-going potentials. Fig. 3.

To produce and manipulate a beam across the screen we need three basic circuit blocks (Fig.4), a power supply, an

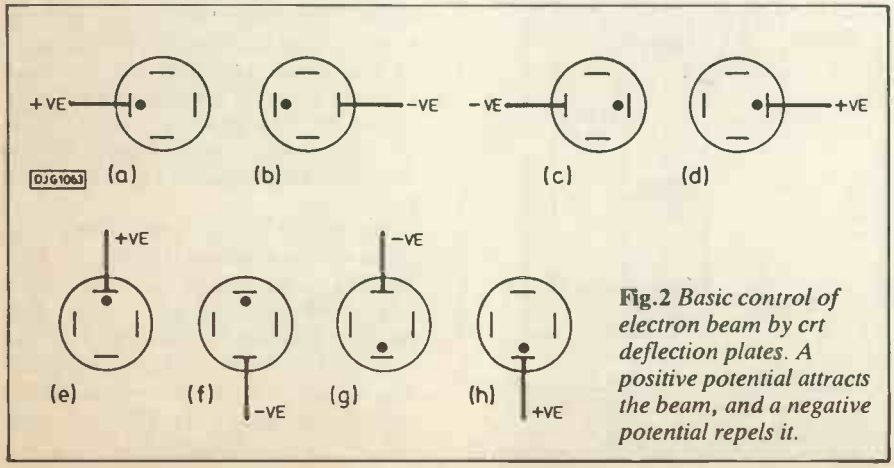


Fig.2 *Basic control of electron beam by crt deflection plates. A positive potential attracts the beam, and a negative potential repels it.*

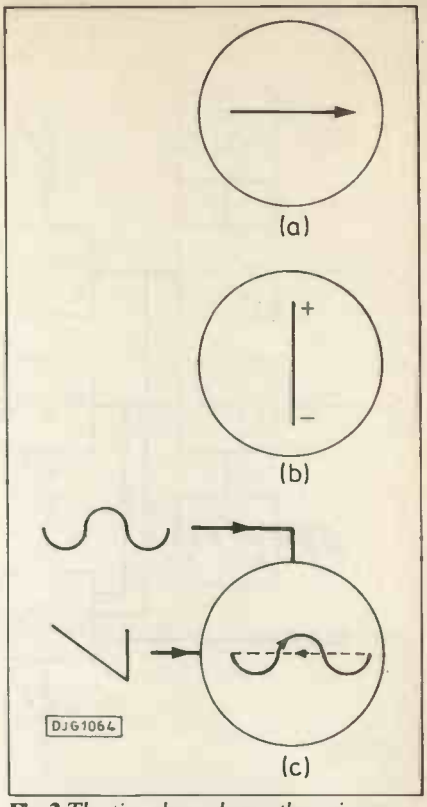


Fig.3 *The time base draws the primary beam deflection from left to right (A) the vertical deflection is controlled by the polarity of the applied potential (B). A ramp sets the horizontal deflection rate while the sampled signal modulates the vertical deflection (C).*

oscillator to repeatedly produce the horizontal trace, and a circuit to allow an input signal to be traced vertically. As we progress we shall see that we need to be able to do a few more things as well, such as vary the X-beam rate, modify the Y-beam amplitude, and to synchronise the start of the X-trace to particular points on the Y signal waveform. I shall also show how a single beam tube can be made to behave as though it has two beams so that two simultaneous input signals can be displayed.

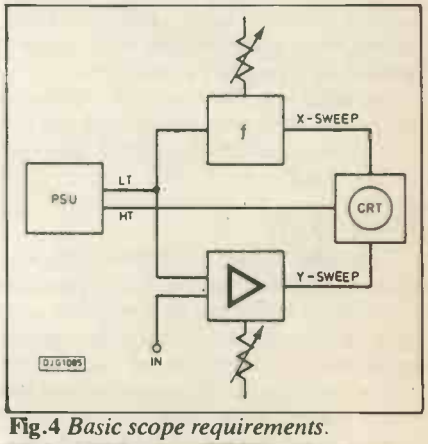


Fig.4 *Basic scope requirements.*

FACILITIES

The full facilities offered by this project are:

Two independent Y amplifiers with six switch-selectable level ranges providing

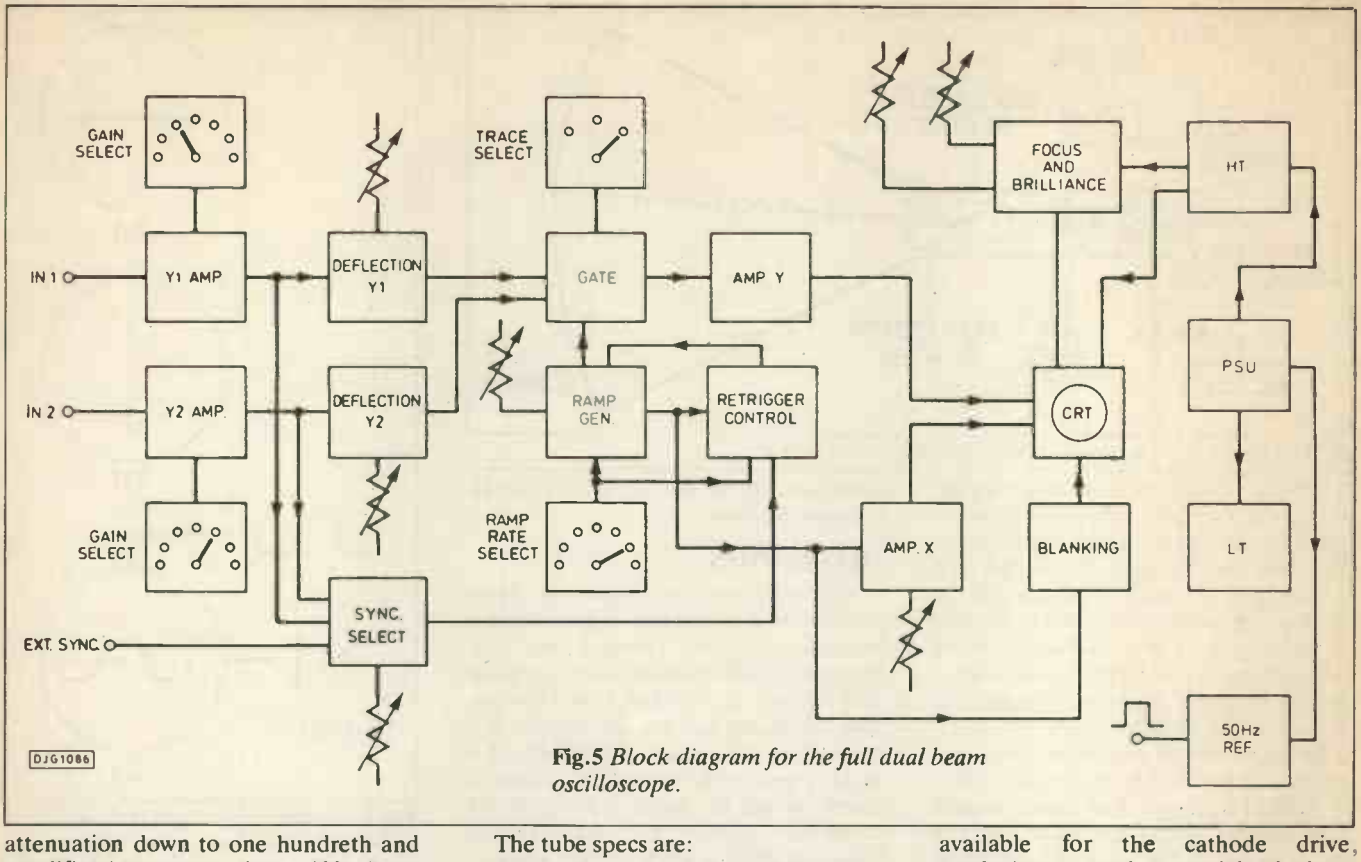


Fig.5 *Block diagram for the full dual beam oscilloscope.*

attenuation down to one hundreth and amplification up to times 100; intermediate fully variable control over level delivered to crt from zero to maximum; maximum display frequency greater than 1MHz; switchable ac–dc input selection; independent control of relative positions of both traces on the screen; switched selection between Y display modes of Y1 only, Y2 only, dual trace Y1 plus Y2, and Y2 to X.

X-amplifier with 6 switch-selectable rates from 0.05Hz to 20kHz, plus variable fine-tuning rate control, and a control for varying horizontal relative position of trace across the screen.

Switched synchronisation selection between both Y amplifiers, additionally switchable between positive and negatively triggered sync; variable control over sync trigger level; switchable sync 'bright-line' on-off; additional input for external sync.

Separate focus and brilliance controls; output for 50Hz 1Vp-p reference signal.

The basic full function block diagram is shown in Fig. 5.

TUBE TYPE

The tube around which this scope has been designed is the DG7-31. A similar tube, the DG7-32 is also suitable and may be substituted without circuit modification. There is no significant difference between the specifications for either tube. Both require the same control and psu voltages and the connections to the B12A valve base are identical. Note that when buying the tube you must also buy the base as a separate item. Fig. 6 shows the physical dimensions of the tube.

The tube specs are:

Screen diameter = 7cm (3in) with a full scan display area in both directions. Deflection factors = X 21V cm, Y 37V cm. Heater = 6.3Vac at 300mA.

Operating voltages with reference to the cathode = anodes A1 and A3 400V min to 800V max, focus electrode A2 0V to 200V max, control grid −200V to +2V max.

POWER SUPPLY

To keep the overall power supply requirements within a reasonable degree of simplicity, the high tension voltage needed by the tube is derived from two sources. With reference to the ground line, +250V is supplied to anodes A1 and A3, and approximately −350V is

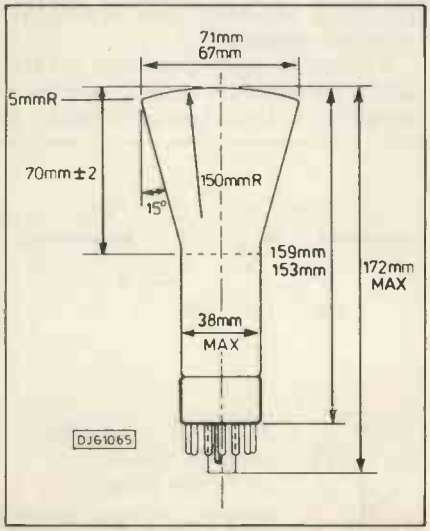


Fig.6 *Dimensions of the DG7-31 and DG7-32 oscilloscope tubes.*

available for the cathode drive, producing a total potential of about 600V across the tube.

In Fig. 7, transformer T1 has three secondary windings, one of 250Vac and two of 6.3Vac, (2 × 6V will also be ok). The 250Vac winding, which is used purely as a mains isolation winding, has its output rectified by D13 and smoothed by C26. TR7 is then biased by R52 and VR11, the latter adjusting the bias so that TR7 delivers +250Vdc at its emitter. C28 serves to minimise ripple on the bias voltage, and R53 provides a path for leakage to ground following power switch off. In addition to feeding anodes A1 and A3, the output of TR7 also supplies power to the deflection drive circuits discussed later.

The 250Vac output of T1 is additionally rectified by D14 to produce a negative supply of about −350Vdc smoothed by C27 and C29 in parallel. Leakage to ground is normally available through the chain of R55, VR13 and VR14. The latter varies the cathode potential and thus controls the brilliance of the screen trace. VR13 is the focus control, applying a variable voltage to anode A2. The grid is normally tied to the negative line via D17 and R54. The function of the input via C30 will be seen later.

The heater voltage is taken from one of the 6.3Vac windings. The other 6.3Vac winding is the source for producing a fully stabilised ±5V split supply, D15 provides positive rectification, C31 and C32 offer smoothing, and IC7 regulates the voltage at +5V. The −5V supply is similarly produced via D16, C34 and IC8.

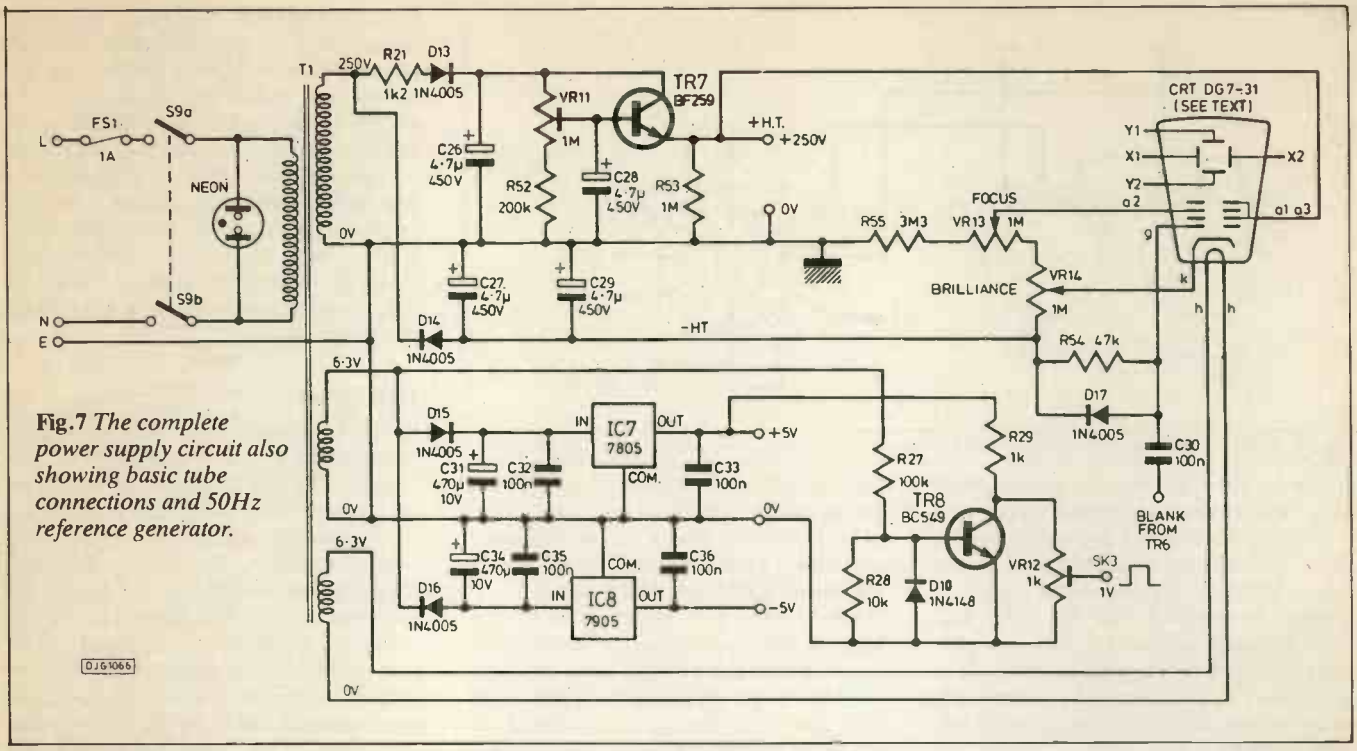


Fig.7 *The complete power supply circuit also showing basic tube connections and 50Hz reference generator.*

50Hz REFERENCE

The remaining circuitry associated with the latter transformer winding is used to produce a 50Hz squarewave reference signal. Taking the 50Hz ac sinewave from the winding via R27 it triggers TR8 on and off, with D9 inhibiting the negative portion of the signal. As TR8 opens and closes it generates a squarewave output at the junction of the collector load resistor R27, and the potential divider VR12. The wiper of VR12 can be adjusted to deliver a squarewave output variable up to a peak swing of 2.5V.

POWER SUPPLY PCB

AT ALL TIMES BEWARE OF MAINS AND OTHER HIGH VOLTAGES WHEN CHECKING AND SETTING-UP THE SCOPE CIRCUITS. REMEMBER THAT CAPACITORS MAY BRIEFLY RETAIN THEIR CHARGE FOLLOW-ING MAINS SWITCH-OFF.

The component layout for the full power supply circuit is shown in Fig. 8, and Fig. 9 shows the mains and transformer connections.

It is essential to have the psu working before any of the other circuits are attached. Assemble the components on the board and *fully* check the joins, the polarity of the electrolytic capacitors and the correct orientation of all the semiconductors.

Now temporarily connect R55 to the negative ht line (−350V). Set VR11 so that its wiper is at the end closest to R52. With a meter set on a range of about 400Vdc switch on the supply and check that about +350V is present at the junction of D13 and C26, and that about −350Vdc is at the junction of D14 and C27. As there is yet no significant load on

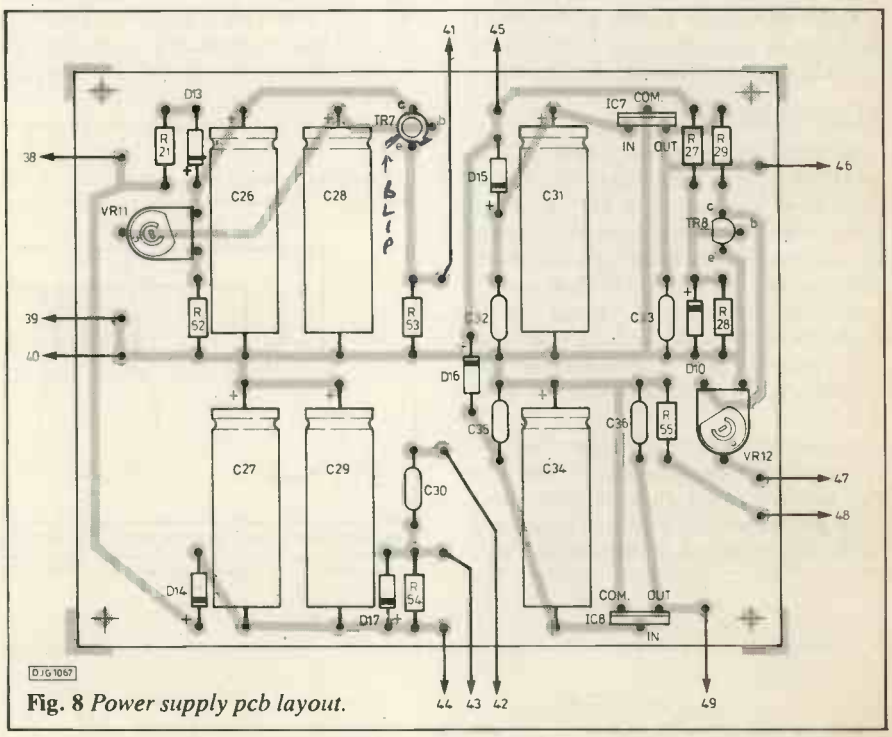


Fig. 8 *Power supply pcb layout.*

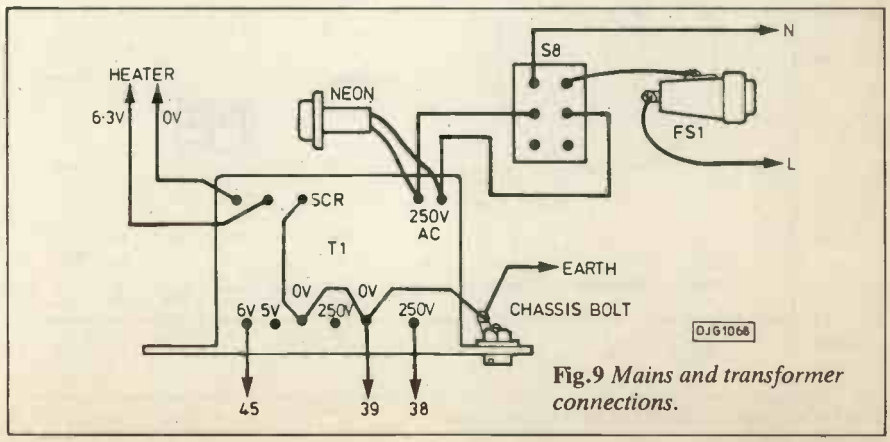


Fig.9 *Mains and transformer connections.*

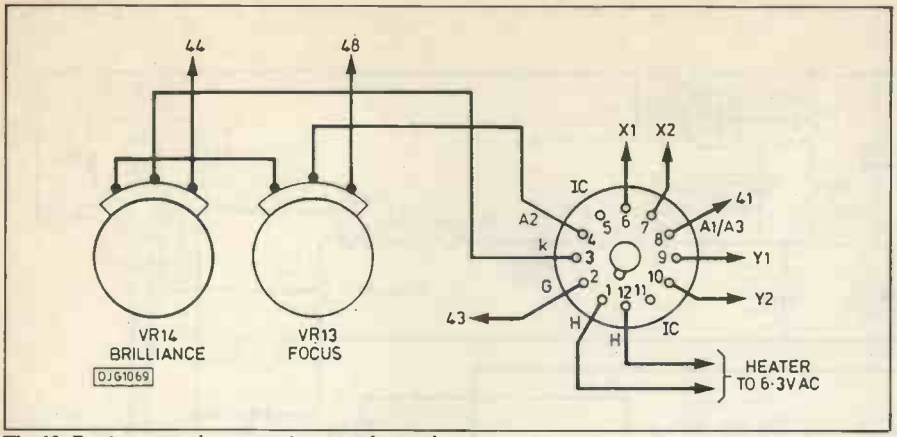


Fig.10 *Basic control connections to the crt base.*

the ht outputs, the actual voltages found may be somewhat higher than this.

Switch off, connect the meter across the +250Vdc output and 0V, then slowly adjust VR11 until a reading of +250V is shown. Now check that the +5V and −5V voltages are correct. You can also roughly check that the 50Hz reference is functional by switching the meter to a range of about 10Vac and monitoring the wiper of VR12.

Once the psu is known to be operational you can move straight on to checking out the functioning of the tube.

TUBE TESTING

Remove the temporary link to R55 and connect R55, VR13 and VR14 so that they form a chain as in Fig. 10. Then solder wires to the tube base (unattached to the tube) for the heater, grid, cathode and anodes, and connect to their correct control points. When all connections are made carefully push the tube base onto the pins at the end of the tube – not too hard and not too far, just enough to hold it there.

Switch on while watching the base end of the tube. At the moment of switch on the heater should glow brightly for a brief instant and then drop in brightness as it heats up. Look at the screen end of tube and after a few moments you should see a green spot glowing somewhere near the centre. Check that its brilliance and focus can be adjusted by the controls VR14 and VR13. Switch off again.

Now, take four rotary pots of the highest value you have, preferably 1M lin, and wire them to the tube base and the positive power supply as in Fig. 11. In this test role the pots are used to manually adjust the deflection voltages on the X and Y pairs of plates. Values as low as 100k lin should still be ok, though as they are wired in parallel, TR7 may get a bit warm under the load, (but don't touch it to find out while the power is on).

Switch on once more and look at the screen. Probably the spot will have disappeared as the deflection voltages from the pots could have shifted it off the screen area. Adjusting the pots in turn should soon bring it back onto screen, and also confirm how varying potentials on the plates will deflect the beam path. You will probably also see that a sharper, brighter spot can be produced when VR13 and VR14 are readjusted, especially when the wipers of the test pots are more towards the positive ends of their tracks.

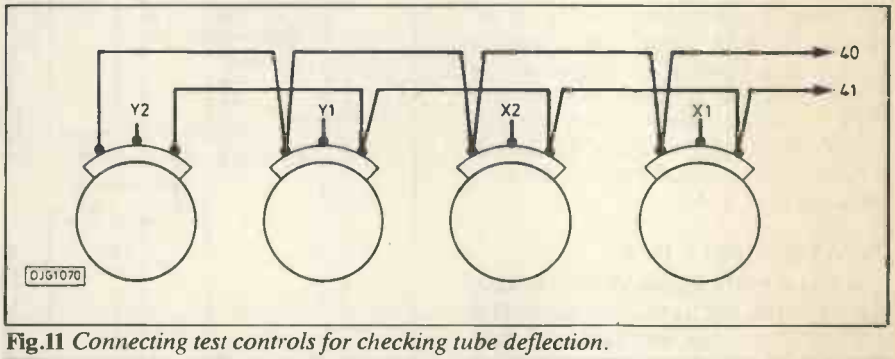


Fig.11 *Connecting test controls for checking tube deflection.*

HOUSING

CASE 406mm x 196 x 152

The prototype scope was assembled inside a metal case measuring 16 x 7.75 x 6 inches (l x w x h). Since the time base and y-amp pcbs are fixed behind the front panel by means of pcb-mounted control switches, the drilling measurements are determined by the size of the pcbs and the switch mounting positions.

Fig. 12 shows the front panel layout and positional measurements, all in inches – I'm sure you've got an inches ruler *somewhere!* Have an enlargement photocopy made of this layout and glue it down to the front panel, taking advantage of its control legends. A sheet of self-adhesive clear film, available from stationers, can be placed over the print to give protection.

Stationers may also sell a draftsman's transparent plastic grid (1 cm squares) to

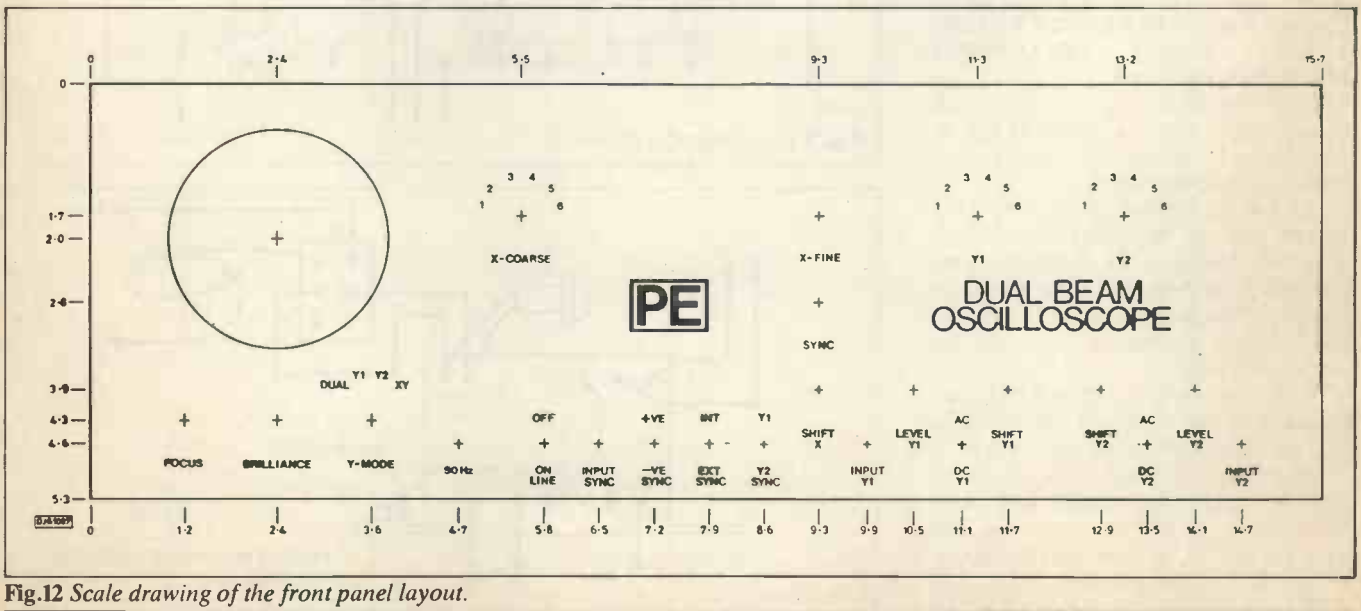


Fig.12 *Scale drawing of the front panel layout.*

COMPONENTS PSU BOARD

RESISTORS

R21	1k2 ½W
R27	100k
R28	10k
R29	1k
R52	200k
R53	1M
R54	47k
R55	3M3

All resistors ¼W 5% cf unless stated.

CAPACITORS

C26–C29	4μ7 450V electrolytic (4 off)
C30,C32,C33, C35,C36	100n polyester (5 off)
C31,C34	470μ 10V electrolytic (2 off)

POTENTIOMETERS

VR11	1M skeleton
VR12	1k skeleton
VR13,VR14	1M lin mono rotary (2 off)

SEMICONDUCTORS

D10	1N4148
D13–D17	1N4005 (5 off)
TR7	BF259
TR8	BC549
IC7	7805
IC8	7905

SWITCH

S9	mains dpdt

TRANSFORMER MAPLIN XP27E

T1	250Vac 50mA min, 6.3Vac 300mA min, 6.3Vac 50mA min.

MISCELLANEOUS

pcb clips (4 off), knobs (2 off), mains neon, Phonosonics pcb 290c, 3.5mm jack socket, 1A fuse and panel fuse holder, case 16 x 7.75 x 6 inches (see text), cable clamping grommet, scope tube type DG7-31 (see text).
A suitable transformer is available from Barrie Electronics, type Y1. (see advert)

place over the front of the tube. Otherwise, carefully rule thin black lines on a heavy-duty clear plastic film.

If you don't have a 2.8 inch diameter hole cutter for the tube face cut-out, drill out the perimeter with a series of small holes and then file down the edges. If possible, line the screen hole with a length of grommet strip, or use the sheathing from a plastic mains cable, gluing it in place.

The mains input lead, switch and fuse are secured to the rear panel. Be sure to use a cable-clamping grommet for the mains lead hole. I omitted to drill a hole for the mains neon, but I recommend that you use one, mounting it at the top right of the front panel.

Two 3.5mm jack sockets were used as the Y-amp input points, in the expectation that most readers will probably make their own probes. BNC

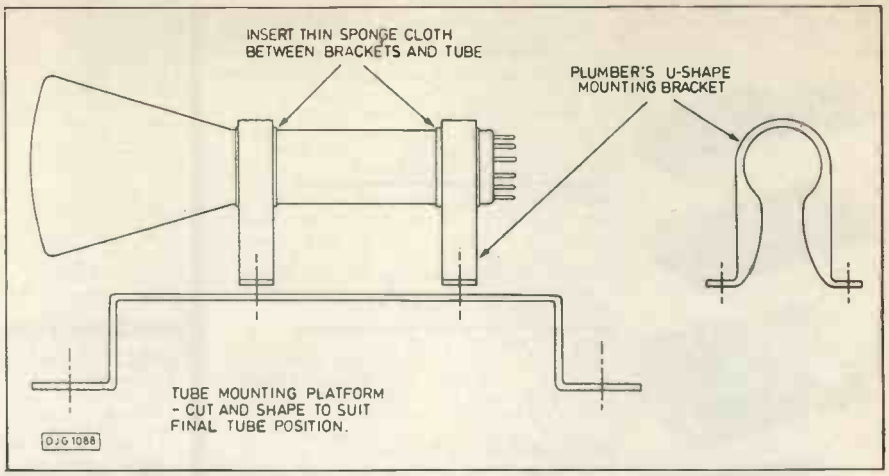


Fig.13 *Tube mounting details.*

type sockets may be substituted if commercially-made probes are to be used.

The tube is mounted on a raised platform, as in Fig. 13, using two flanged U-shaped plastic brackets. Only cut and bend the platform to shape after checking bracket dimensions and the tube hole position. The brackets were bought from a plumbing hardware store and are of the type used to secure drain pipes. Their diameter is just a little over the tube's body diameter of 1.5 inches. A very thin kitchen sponge, or similar, should be cut to size and inserted between the tube and the brackets to give good holding stability without excessive pressure.

DO NOT TIGHTEN DOWN THE BRACKETS TOO MUCH FOR FEAR OF BREAKING THE TUBE. ON NO ACCOUNT SHOULD THE TUBE SOCKET BE USED FOR SECURING THE TUBE IN PLACE.

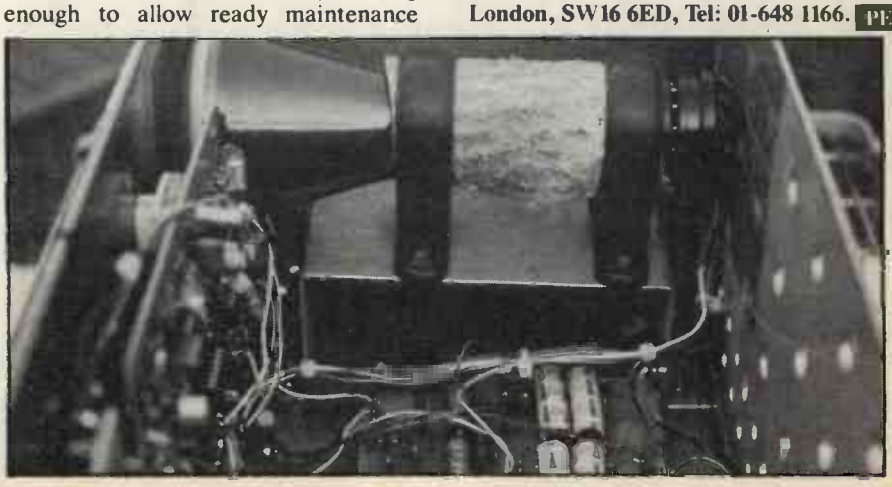
To prevent the tube base tags from shorting to the rear of the box, either use the cap supplied, or put insulating tape on the inside back panel behind the tube. Mount the transformer at the far right back corner, as far from the tube as possible to avoid magnetic fields interfering with the electron beam deflection. The power supply pcb should be mounted on the base of the box alongside the tube mount. Keep all leads as short as is reasonable (but long enough to allow ready maintenance access), and draw them into neat harnesses.

ALTERNATIVE HOUSING

A box that is more deep than wide may be substituted if a more compact front panel is preferred. In this situation, replace the pcb mounted switches with normal panel mounting types. Solder the associated components directly to the switches (to minimise interference that could be caused by many leads between switches and components). Then mount all pcbs on the base of the box. Place the transformer as far behind the tube as possible, rotating its position until minimum interference with the tube plates results. If significant interference still occurs in this alternative layout, the use of a mu-metal screen around the tube may be beneficial. Langrex Supplies, the tube suppliers, will advise on the availability of these screens. It may also be necessary to use screened leads for the signal routings in this altered layout. No screening of any sort was found necessary with the recommended layout as used in the prototype.

That concludes the psu and tube checking. We shall proceed next month to finding out how to draw a horizontal trace at varying rates.

The address from which to buy the tube and base is: Langrex Supplies Ltd, Climax House, Fallsbrook Road, London, SW16 6ED, Tel: 01-648 1166. PE



OMP POWER AMPLIFIER MODULES

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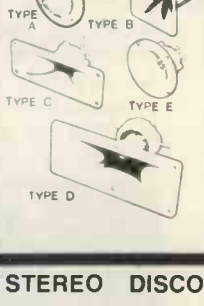

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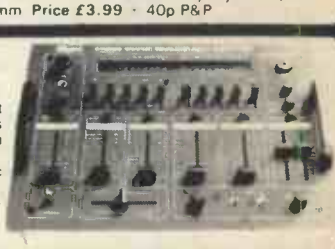

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SUBMARINE CABLES

PART TWO BY MIKE SANDERS

CANTATA FOR A GOLDEN AGE

The age of marine coax draws to a close, but the era of multi-megahertz optic cabling will rule the waves for future trans-ocean telecoms.

CANTAT 2, laid in 1974 is classed as an early system. The division is purely arbitrary. Among the 'newer' cables ANZCAN, SEA–ME–WE, TAT 7, TAT 8 and Belgium 5 will be described briefly. TAT 8 and Belgium 5 are optical fibre systems which end the golden age of coaxial cable. Future cables of any length are also likely to be optical fibre. The 14MHz and 45MHz systems will also be described.

CANTAT 2

This was laid by the cable ship Mercury, chartered by the Post Office from Cable and Wireless. Mercury returned to Southampton four times to reload with cable. The repeaters were spliced into the cables and a linear engine paid out both cable and the torpedo shaped repeater. The ship slowed to five knots while laying the repeater but before the linear engine was developed, the ship had to slow to 1.5 knots.

Mercury used a navigation system called Hydroplot which can locate the ship's position to an accuracy of 300 feet. Hydroplot is a navigation system using US Navy satellites.

The repeaters are spaced at six mile intervals and contain a total of 2,800 transistors which were manufactured under clinical conditions. Even the air to these rooms is filtered and operators wear hats, gloves and overalls. Gold plated surfaces are often used to give perfect mechanical contact and prevent surfact corrosion.

Only about 20% of the transistors manufactured are actually used. The rest are put on long term tests or tested to destruction. In this instance 18,000 additional transistors were tested. A typical ageing curve for a component is the bath tub curve of Fig. 12.

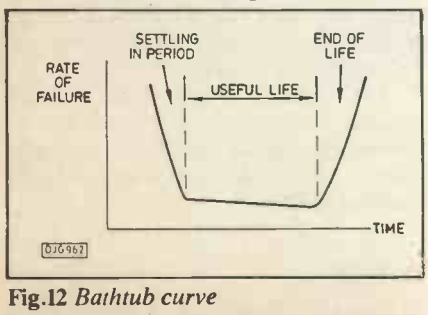


Fig.12 *Bathtub curve*

Submersible cable trencher "Sea-Dog" being lowered. Reproduced by kind permission of British Telecom.

The repeaters are sealed in a brass cylinder containing dry nitrogen which is an inert gas. The cylinder is then placed in a steel case which can stand the pressures of the deep. In the Atlantic Ocean the cable is sometimes at depths where the pressure is three tons per square inch.

The cable itself is light along the unarmoured sections, weighing only five tons a mile and less than two inches in diameter. The armoured sections from the shore to a depth of 300 fathoms is of a cross section shown in Fig. 13. Some shorelines and continental shelves drop steeply. Here the currents are strong, chafing the cable on the rocks.

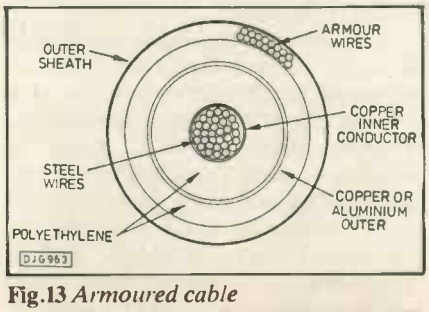


Fig.13 *Armoured cable*

The cable is manufactured by covering the steel wires in the centre with copper which will be the inner conductor. At high frequencies electrical signals travel only in the 'skin' of a conductor and because of this 'skin' effect a tube is sufficient.

The copper tube is then insulated with polyethylene which is shaved with cutters to give an accurate diameter. The outer conductor can then be aluminium or copper. This is followed by a sheath of polyethylene, a sheath of steel wires for external armour and a final cover of polyethylene.

The cost of polyethylene varies with the cost of oil and world copper prices are also variable. However, the price of aluminium is more stable and is about 6% cheaper than copper though the attenuation is about 5% higher than copper.

If aluminium is used as the outer conductor, then zinc is used as sacrificial anodes at the repeaters, to protect against corrosion of cable that might get exposed.

CANTAT 2 is a 14MHz system costing £30 million. The cost for each circuit in

TAT 1 was £294,000; for CANTAT 1 it was £100,000 and for CANTAT 2 it works out at £16,500. Technology has brought down the cost per circuit.

Before TAT 1 there were only telegraph cables across the Atlantic and telephone circuits were carried by radio with much fading and interference. In 1962 there were half a million telephone calls between Britain and the USA. This figure rose to 4.5 million in 1973 and 25 million in 1980. Today it is around 30 million.

14 MHZ SYSTEM

Many of the points relating to CANTAT 2 apply here. However, there are additional matters. The utilisation of the cable bandwidth of 14MHz is shown in Fig. 14.

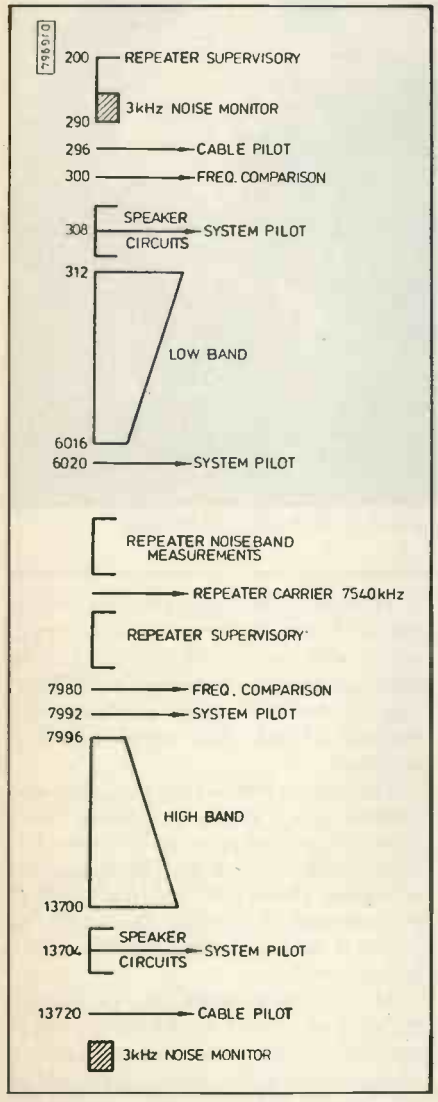


Fig.14 *14MHz Bandwith utilisation*

As mentioned before, the send and receive signals are on the same cable therefore the bandwidth is divided into a low frequency band and a high frequency band. If one terminal is designated A and the other B, the transmission from A to B, by convention, is over the low frequency band. It follows that transmission over the high frequency band will be in the B to A direction.

If a single amplifier were used in the repeater to amplify the whole band, overload instability could occur where power is transferred between bands and the amplifier starts 'singing'. Since separate amplifiers are used for each direction of transmission, then the requirements on transistor manufacture and amplifier design can be eased. A low frequency, wideband amplifier can be used in the A to B direction and a high frequency amplifier in the B to A direction.

An amplifier never has a characteristic that perfectly matches cable loss, therefore equalisers have to be inserted with every fifteenth repeater to 'mop' up any residual response. An equaliser is a circuit built out of basic passive components: capacitors, resistors and inductors. The effect of using an amplifier and equaliser on cable attenuation is shown in Fig. 15. CANTAT 2 has 31 equalisers at intervals of 90 miles.

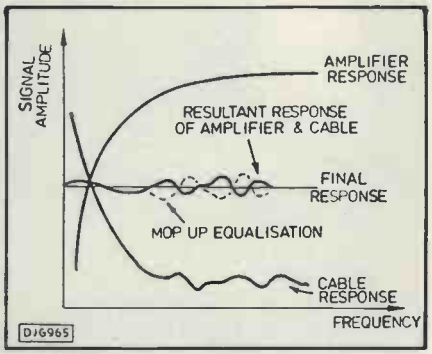


Fig.15 *Equalised cable response*

Each manufacturer offers a different equaliser arrangement. The British manufacturer builds the equaliser on the ship. This equaliser is then sealed into the repeater case before heaving it overboard.

The American manufacturer has equaliser sections in which presets are selected by control leads which are then cut and sealed before laying the repeater. The Japanese manufacturer has an equaliser which is magnetically controlled and also set on board ship. The French manufacturer uses equalisers which can be set from land. This is a good idea but the variation in cable response due to ageing needs to be predicted, so that the required limits of adjustment can be provided before laying the repeater.

The response of a cable needs to be matched, not only during laying but over its useful life. The cable response may change due to changes in temperature, pressure and of course, ageing. In shallow water particularly, the temperature may vary a lot, causing a gain variation as much as 32dB, and thermistors are provided in the amplifiers to change the gain automatically.

A component improvement in repeaters is the use of metal film resistors instead of wire wound resistors. At the high frequencies used wire wound resistors would exhibit an inductive effect. Sometimes components are subjected to accelerated ageing by applying higher than normal operating voltages or keeping them in an oven for a selected time. The accelerated ageing ensures that the component operates on the flat part of the bath tub characteristic Fig. 12, ie least likely to fail.

The 23 supergroups are transmitted from terminal A to terminal B in the low band and from terminal B to terminal A in the high band. The low band is from 312kHz to 6016kHz and the high band from 7996kHz to 13,794kHz. The spacing between supergroups is 8kHz or 12kHz depending on manufacturer, and each supergroup occupies the band 312kHz to 552kHz. Every supergroup is translated into its rightful place in the wideband.

PILOTS AND CARRIERS

There are two cable pilots, one in each direction that keep a check on the submerged equipment only. The other two pilots in each direction, the system pilots, keep a check on the traffic levels. Since these pilots are only 4kHz from the edges of the bands, it is possible to detect for instance if the wideband response suddenly develops a slope.

Stations which multiplex channels into supergroups are called carrier stations and usually contain a master oscillator which generates a 60kHz tone. This 60kHz is accurate to within 5 parts in 10^8. Sometimes it may be necessary to transmit this tone down a submarine cable to the distant terminal for comparison. This is achieved by multiplying the 60kHz up to 300kHz so that it fits in the low band or up to 7980 kHz so that it fits in the high band.

All these pilots are outside the speech band and therefore need not pass through any modulators or demodulators. Using multipliers and dividers, a whole range of frequencies like 12kHz and 124kHz are obtained from 60kHz. These frequencies are then used to derive carriers which are in turn used to modulate and demodulate the bands. If the master oscillator did not have the specified accuracy of 5 parts in 10^8 then the carriers would be even less accurate than they are. This would lead to the speech bands drifting up and down and overlapping with each other.

The system pilots pass through mercury-wetted reed relays. In the event of traffic failure and therefore pilot failure, the relays switch to a standby path within 10ms.

NOISE

There is a 3kHz channel at each end of

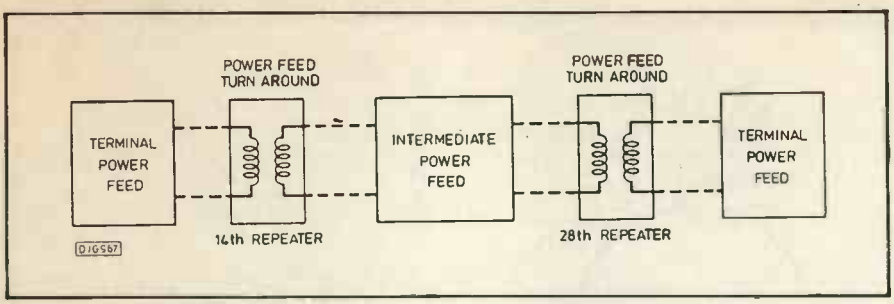


Fig.16 *. Power feed turn-around*

the cable bandwidth for measuring the noise performance in each direction, and this can be measured at all times. The specified noise level is not more than 1 picowatt in a 3kHz channel per kilometre of cable.

The cable has an attenuation of 6.5dB at 13.7 MHz and if the repeater amplifiers have a gain of 43dB at 13.7MHz, then the required signal to noise ratio is obtained.

POWER

A typical land based 12MHz system would have separate coaxial cables for each direction and power feeding would be at 49mA and +250V down one centre conductor and −250V down the other centre conductor. In effect this is a 500V supply but the hazard to maintenance engineers is reduced by making the supply 250–0–250V and limiting the current to 49mA.

At every fourteenth repeater the power is turned back on itself. This means that intermediate power feed stations must be found, Fig. 16.

In the middle of the ocean there is no intermediate power feed therefore the terminal voltage needs to be increased to 12kV and 500mA current. Over 500 repeaters this works out to about 20V per repeater. The voltage is fed down the centre conductor and since only one cable is used, the return path is via the sea.

In order to reduce the terminal voltage there is no reason why each terminal cannot feed half the system voltage in a series aiding mode, Fig. 17. One of the big advantages of reducing the system voltage is that the voltage rating of the power separating filter (psf) capacitor can be reduced.

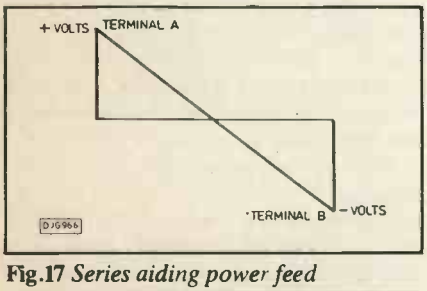


Fig.17 *Series aiding power feed*

No one wants 12kV down their earhole and since the power is fed down the same conductor that carries the signal, the power needs to be prevented from taking the signal path through the repeater. The psf capacitor is normally rated 12kV and is one of the components tested extensively for reliability.

To keep the repeater gain constant the line current needs to be regulated within 0.25%.

REPEATERS

A block diagram of a repeater is shown in Fig. 18. One useful feature of a terminal is its supervisory system and its ability to locate a break in the cable down to a section between two repeaters. This can be achieved if a repeater returns a distinct identification tone.

Using crystal filters, a bandwidth of 150Hz is sufficient for each tone. For 600 repeaters a total bandwidth of 150 x 600 = 90kHz is required. Therefore in the A to B direction the frequency range 200kHz to 290kHz is allocated and in the B to A direction 7740kHz to 7830kHz.

Associated with each repeater is a crystal oscillator and modulator for loop gain tests, ie if a single tone is sent in the low frequency direction, it must be translated to the high frequency band and returned by the repeater being interrogated.

The crystal oscillator is stable within ±10 parts per million at 7540kHz for a temperature range of 0°C to + 25°C. The components of a cable are designed for a life of 25 years and over this period, the oscillator is required not to drift more than ±12 parts per million.

One of the problems of connecting a cable to a repeater is that the strength member is in the centre of the cable and somehow this needs to be anchored to the repeater casing. One way of achieving this to overlay the point where the cable enters the casing, with steel wires in order to transfer the load from the cable core to the repeater casing.

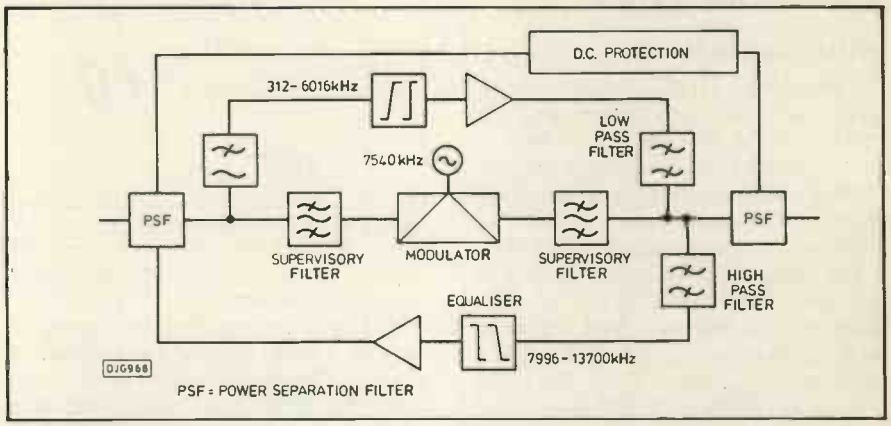


Fig.18 *Repeater*

TERMINAL EQUIPMENT

Fig. 19 is a block diagram of terminal equipment. It is an interface between the inland network and the 'wetbits'. It provides pilots, and power to the repeaters and supervisory facilities.

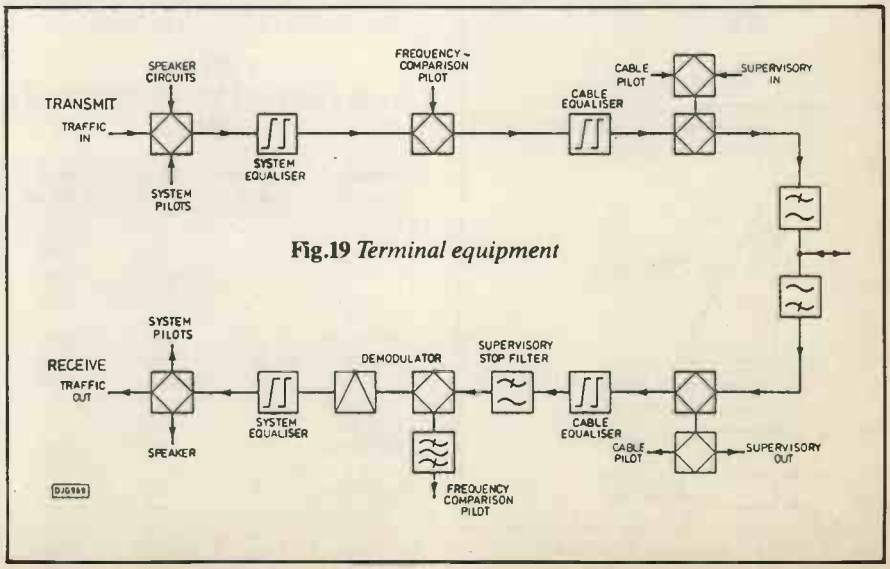


Fig.19 *Terminal equipment*

Included in the supervisory tests are loop gain, harmonic distortion and noise measurements.

Another facility available at the terminal is speaker circuits (engineer order wire, eow, if you are American). Four circuits each 2kHz are slotted between 304kHz and 312kHz. Alternately, if the cable pilots can be sacrificed and all the repeater supervisories are not required, three 4kHz telephone circuits can be fitted in between 288kHz and 300kHz.

45MHZ SYSTEM

Although the 14MHz system has been called the long haul system and the 45MHz the short haul, technology is always pushing the boundaries and the 45MHz is also available as a long haul version. Of course the division is arbitrary - how long is a piece of string?

The 45MHz, 69 supergroup system can carry 4140 4kHz circuits or 5520 3kHz circuits. The short haul version is used for distances up to 1000 nautical miles and requires 6.25kV. For long routes up to 3500 nautical miles a voltage of 12kV is required for repeaters every 3.5 nautical miles on a 1.7 inch cable.

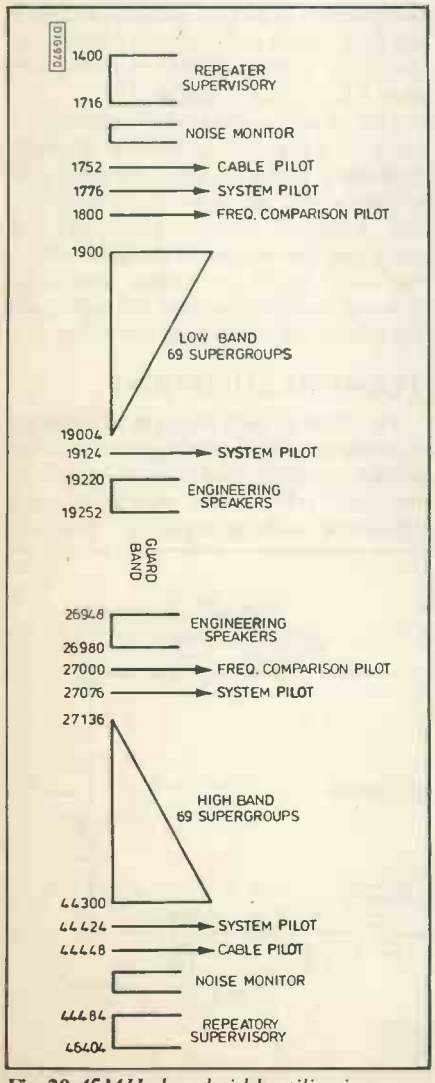


Fig.20 *45MHz bandwidth utilisation*

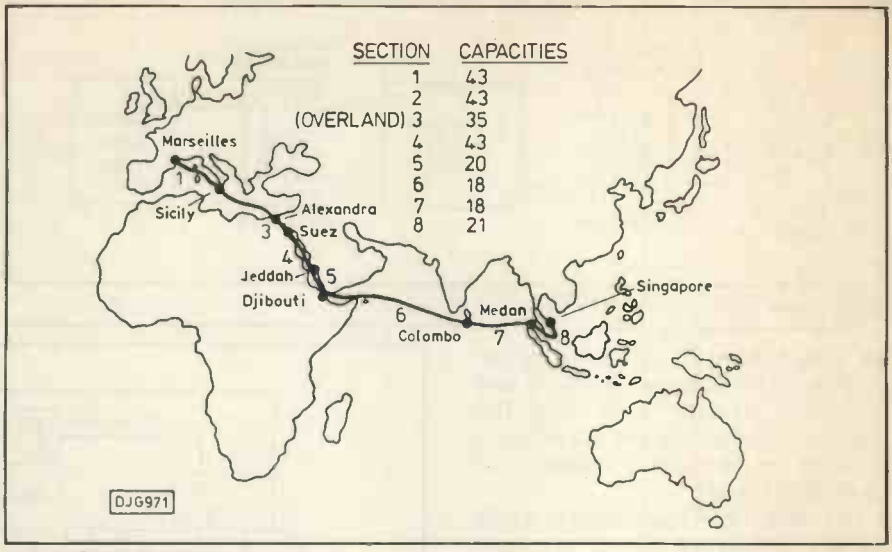


Fig.21 *SEA-ME-WE sections*

Equalisers are inserted at every twentieth repeater.

The bandwidth utilisation is similar to that for 14MHz systems and is shown in Fig. 20. Terminal equipment is cheap compared to the total cost of the cable therefore terminal equipment is duplicated in case of failure.

REPEATERS

At 500 mA current, the voltage across each repeater is about 15 volts. As before there are thermistors in shallow water sections. Quality components like mica capacitors and thin film resistors are used together with a transistor whose f_t is 4000MHz.

The low band is 1.9MHz to 19MHz and the high band is 27.2MHz to 44.3MHz. Most of the other arrangements are similar to that for 14MHz systems.

MODERN CABLES

Among the cables completed recently or nearing completion are ANZCAN, SEA–ME–WE, TAT 7, TAT 8, and Belgium 5. The last two will be optical fibre systems. Some manufacturers and what they provide in terms of analogue cable capacity is shown in Table 3.

TABLE 3: MANUFACTURERS OF ANALOGUE CABLES

Manufacturer	Bandwidth (MHz)	No. of 4kHz ccts	No.of 3kHz ccts
STC (Britain)	5	480	640
	9	900	1200
	14	1380	1840
	45	4140	5520
Submarcom (France)	5	480	640
	12	1260	1680
	25	2580	3440
AT&T (America)	6	633	845
	29.5	3150	4200
Fujtsu/NEC (Japan)	5	480	640
	10	900	1200
	12	1200	1600
	36	2700	3600

ANZCAN replaces the old COMPAC cable and has about 20 times the capacity of COMPAC. ANZCAN connects Australia, New Zealand, Fiji, Hawaii and Canada. It cost £250 million.

There are 1123 repeaters in the total distance of 8160 nautical miles and the line voltage is 406V. The sections and capacities are as follows:

Australia - Norfolk Island	14MHz
Norfolk Island - Fuju	14MHz
Fiji - Hawaii	14MHz
Hawaii - Canada	14MHz
Norfolk Island - New Zealand	5MHz

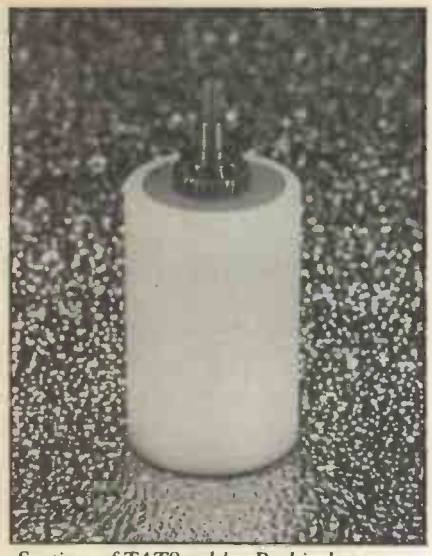
SEA–ME–WE stands for South–East Asia, Middle East, Western Europe and is 7330 nautical miles long. It was completed in 1986 and cost $ 400 million. The capacity of each section in supergroups is shown in Fig. 21.

TAT 7 was completed in 1983 and cost £100 million. It is a 45MHz cable and carries 4200 circuits. TAT 7 runs from Lands End in the UK to Tuckerton, New Jersey in the USA and will probably be the last analogue cable.

The cable is about 3800 miles long and 1.7 inches in diameter. Power feeding is 5275V from each end and 657mA. The repeaters are spaced at 5.1 nautical miles, and there are 12 equalisers which can be adjusted from Land's End.

In addition to handling telex, data and telephony, videoconferencing is also carried. This is a facility whereby a number of people in one country can talk simultaneously to business associates in another country, over a television link.

Instead of using the full 6MHz bandwidth required for broadcast television, which would require 68Mbit/s when converted to digital form, only 2Mbit/s are transmitted. This is achieved by making extensive use of bandwidth compression techniques and refreshing the picture store only occasionally since there is little movement in a conference studio.



Section of TAT8 cable. By kind permission of British Telecom

TAT 8 will be optical fibre operating monomode at 1300nm. It will be the first submarine cable to use a branching unit located on the ocean floor. This will enable the US traffic to be split between the UK and France.

A schematic of the branching unit is shown in Fig.22. Three fibre pairs will start off from Tuckerton, USA, and the branching unit, located off the European continental shelf will send two pairs to Widemouth in the UK and two pairs to Penmarch in France.

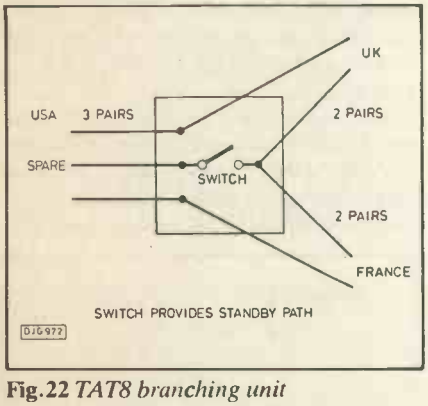


Fig.22 *TAT8 branching unit*

The total length of the cable will be 6657km and cost $335 million when completed in 1988. The repeater spacing will be about 45km and the fibres will operate at bit rate of 280 Mbit/s.

The basic capacity is 8000 circuits but sophisticated cme will give a 5:1 advantage bringing the total capacity up to 40,000 circuits. CME on its own gives an advantage of 2:1 or 2.5:1. In addition adaptive differential pulse code modulation (adpcm) will be used to give a further advantage of around 2.5:1 which brings the total advantage to 5:1.

Pulse code modulation uses eight bits to describe each speech sample. If four bits are used then an advantage of 2:1 is gained without loss of speech quality. Further reductions are possible but the speech quality may suffer.

Three manufacturers will be involved. AT&T of America will supply a 3625 mile cable from Tuckerton to the branching point. Submarcom of France will supply the 195 mile portion from the branching point to Pennarch, France and the 325 mile portion from the branching unit to Widemouth Bay, Cornwall will be manufactured by Standard Telephone and Cables.

Belgium 5 will cost about £7 million and was completed in 1987. It runs from Broadstairs in the south–east of England to Ostende in Belgium and has three repeaters spaced by about 35km. The 122km long cable was buried by the cable ship Alert to prevent damage in the busy English Channel.

There are three pairs of optical fibres operating at 280Mbit/s each. In addition to being optical fibres, TAT 8 and Belgium 5 will carry digital signals as opposed to analogue signals carried by coaxial cables in the past. Fig. 23 shows an analogue and a digital signal.

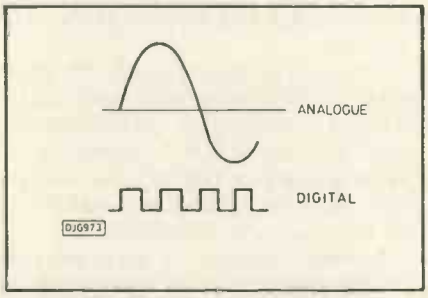


Fig.23 *Analogue and digital signals*

This does not imply that coaxial cable cannot carry digital signals. Many overland coaxial cables have been in operation carrying digital signals at various bit rates. In fact all that is required is for the repeater to be replaced by a regenerator. A repeater merely amplifies an analogue signal and passes it on. A regenerator actually replaces a ragged pulse with a new one - in fact for every pulse applied to the input, a new pulse is generated at the output.

The optical fibres of Belgium 5 will operate at a wavelength of 1300nm in monomode and 300 volts will be used to supply the diodes in the regenerators. A brief description of optical operation follows:

OPTICAL COMMUNICATIONS

The main difference between electrical communication and optical communication is that optical communication operates higher up the frequency spectrum Fig. 24.

Light emitting diodes (leds) and lasers (light amplification by simulated emission of radiation) are used as light sources. These are switched on and off rapidly to make them operate in the digital mode, ie light on = digital signal 1, light OFF = digital signal 0.

If we take the top frequency of a 45MHz cable system as 45MHz, this

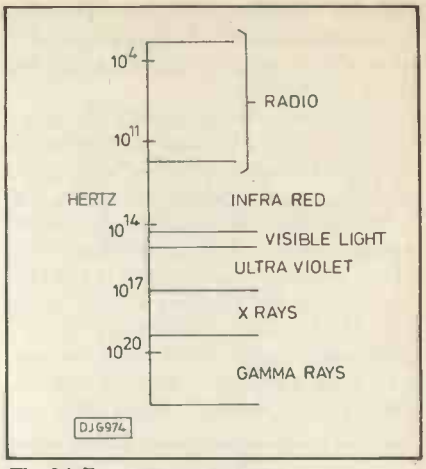


Fig.24 *Frequency spectrum*

produces a wavelength of 6.66 metres approximately. Compared to this, optical communication over optical fibres operates at 850 nanometres and 1300 nanometres (nm).

Note that we compare wavelengths rather than frequencies. Since digital systems operate in an on–off mode, there is no direct relationship to frequency as there would be in an electric transmission system using frequency division multiplex (fdm).

It is a well know fact that digital systems are hungry for bandwidth compared to analogue systems. For instance if a 4kHz speech band is amplitude modulated by a carrier it will still occupy 4kHz of bandwidth at the carrier frequency. However if the same 4kHz is converted to digital form using 7–bit pcm (pulse code modulation) it would occupy as much as 28kHz of bandwidth.

This is the inherent waste in digital systems. In return one gets excellent noise immunity. Various design rules are applied depending on what one is transmitting. For instance, a television picture of 6MHz nominal bandwidth when coverted to digital form could operate at 70Mbit/s and require at least 70MHz of bandwidth on the medium used to transmit it.

So although there is no direct relationship between a digital system and bandwidth, there is an indirect one. That is, the minimum frequency required to transmit at least the fundamental frequency is the same figure as the bits per second, 70MHz in this case. This is illustrated in Fig. 25.

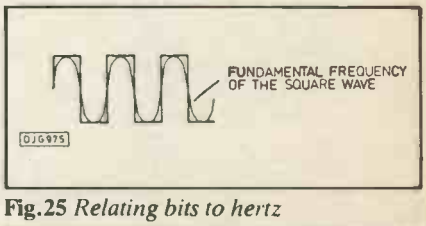


Fig.25 *Relating bits to hertz*

A block diagram of an optical fibre transmission system is shown in Fig. 26. The detector in a regenerator is an avalanche photodiode (apd) or positive

intrinsic negative (pin) diode. Here lies the noise immunity of a digital system. A new pulse is regenerated for every one received, Fig. 27.

Most noise is of an amplitude nature, therefore any electrical spikes on an amplitude modulated wave will remain after the signal is demodulated, Fig. 28.

The trouble with launching light down a glass fibre is that it is soon reflected out of the fibre and propagation is lost. The answer is to put cladding around it, Fig. 29a. The cladding is also glass but of a different refractive index. An alternative is to dope the glass so that the refrative index varies gradually instead of a step variation. The propagation mode is shown in Fig. 29b for graded index fibre.

If a pure wavelength can be obtained of as narrow a linewidth as possible then monomode operation can be achieved. Fig. 29c. This means that the light propagates straight down the core and a wider bandwidth is available. If a pure frequency is not available then other frequencies will be present and in modulating these carriers, care must be taken to ensure that sidebands do not overlap. This is the case with multimode propagation and the usable bandwidth is limited.

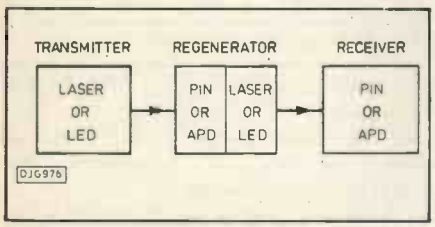


Fig.26 *Optical fibre transmission*

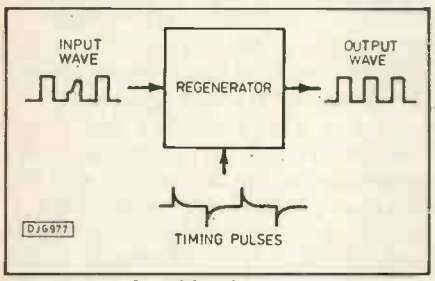


Fig.27 *New for old pulses*

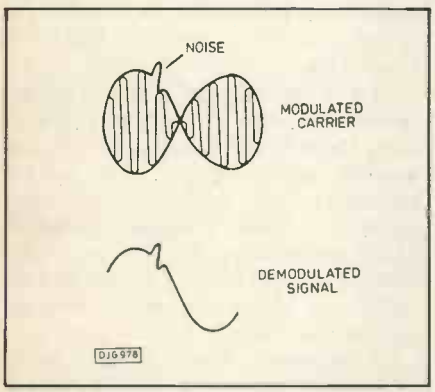


Fig.28 *Noise on modulated am wave*

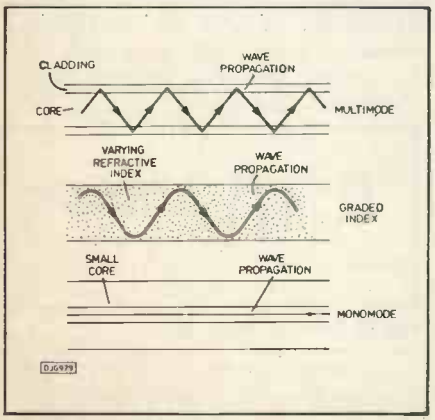


Fig.29 *Fibre types*

CHARGING POLICY

How do countries share the cost of a cable? What if a country did not wish to pay for it initially but wanted to use it later? It could get very complicated.

There are three solutions to this problem: ownership, indefeasible right of use (iru), and lease.

Ownership is a share in the total cost of a system which would include spare capacity.

IRUs are purchased for the life of the cable and the cost includes capital cost as well as a proportion of maintenance cost. The owner is still responsible for actually carrying out the maintenance. IRUs state the capacity in bandwidth or bit rate that has been purchased.

Leasing capacity is equivalent to renting supergroups or hypergroups or bit rates for a fixed period of time.

POWER CABLES

Power cables are much heavier than telephone cables and any ship laying or repairing power cables needs bow and stern skids. Power cables can be single core or three core and a diagram of a three core cable is shown in Fig. 30.

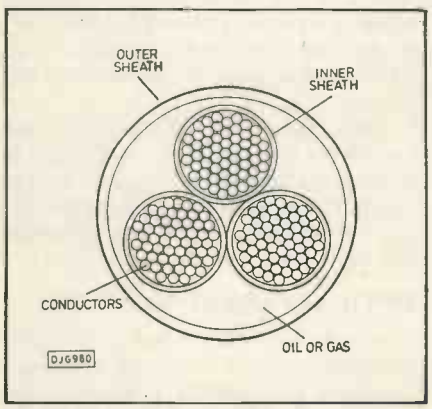


Fig.30 *3 core power cable*

Typical voltages and filling for power cables are:

33kV	solid type
138kV	gas pressured
420kV	oil filled

Spans of up to 16km are possible. There is a 50kV cable from Sweden to Denmark, then in 1951 a 132kV cable was installed between the two countries.

Submersible trenching plough. By kind permission of British Telecom

Nearer home, the Isle of Wight was supplied by a 33kV cable in 1942 followed by two more in 1947 and 1949. In 1964 and 1966 two 132kV cables were installed. These were three core and oil filled.

The Solent flows between the mainland and the Isle of Wight. There are sudden storms even in the summer and a strong tide of around four knots. This means that the cable is moved a lot and is therefore wrapped in heavy armour where it is routed over rocks.

The conductors are 0.3 square inches in cross-section and supply 550A of current. The distance across the Solent is about 4km.

LAYING AND MAINTENANCE

Before a cable can be laid, a survey is made of the seabed including wide scan sonar of shallow water sections. If cables already use the proposed route, then the history and reliability of these cables is taken into account.

Burying cable is expensive and so is the repair of buried cable. Although burying cable will protect it from trawlers, 0.6m of burial will not protect if from a heavy anchor dragged in a storm.

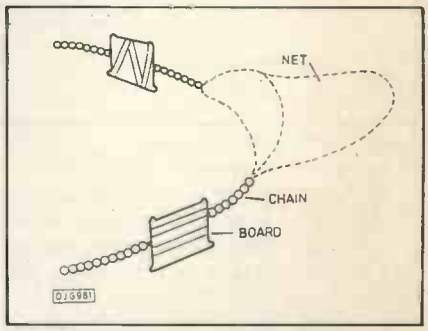


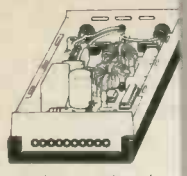
Fig.31 *Trawling*

Trawling for fish involves dragging heavy boards on the seabed, Fig. 31. These can weigh as much as four tons each and penetrate the seabed up to a depth of 0.4m. A far greater risk of damage is from trawlers rather than ship's anchors. In any case, burial to depths greater than 1m works out very expensive particularly if the cable has to be recovered for repair.

To be concluded next month. PE

POWER CONDITIONER

FEATURED IN ETI JANUARY 1988



The ultimate mains purifier. Intended mainly for lowering the noise floor and improving the analytical qualities of top-flight audio equipment.

The massive filter section contains thirteen capacitors and two current balanced inductors, together with a bank of six VDRs, to remove every last trace of impulsive and RF interference. A ten LED logarithmic display gives a second by second indication of the amount of interference removed

Our approved parts set consists of case, PCB, all components (including high permeability toroidal cores, ICs, transistors, class X and Y suppression capacitors, VDRs, etc.) and full instructions.

PARTS SET **£28.50** + VAT

Some parts are available separately. Please send SAE for lists, or SAE + £1 for lists, circuit, construction details and further information (free with parts set)

KNIGHT RAIDER

FEATURED IN ETI JULY 1987

The ultimate in lighting effects for your Lamborghini, Maserati, BMW (or any other car, for that matter). Picture this: eight powerful lights in line along the front and eight along the rear. You flick a switch on the dashboard control box and a point of light moves lazily from left to right leaving a comet's tail behind it. Flip the switch again and the point of light becomes a bar, bouncing backwards and forwards along the row. Press again and try one of the other six patterns. An LED display on the control box let's you see what the main lights are doing.

The Knight Raider can be fitted to any car (it makes an excellent fog light!) or with low powered bulbs it can turn any child's pedal car or bicycle into a spectacular TV-age toy!

The parts set consists of box, PCB and components for control. PCB and components for sequence board, and full instructions.

Lamps not included

PARTS SET **£19.90** + VAT

RAINY DAY PROJECTS

All can be built in an afternoon!

JUMPIN' JACK FLASH (ETI March 1988) Spectacular rock, stage and disco lighting effect!	£6.90 + VAT
CREDIT CARD CASINO (ETI March 1987) The wicked pocket gambling machine	£5.90 + VAT
MAINS CONTROLLER (ETI January 1987) Isolated logic to mains interface	£6.20 + VAT
MATCHBOX AMPLIFIERS (ETI April 1986) Listen. 50W of Hi-Fi power from an amp small enough to fit in a matchbox!	
Matchbox Amplifier (20W)	£6.50 + VAT
Matchbox Bridge Amplifier	£8.90 + VAT
L165V Power Amplifier IC, with data and circuits	£3.90 + VAT
TACHO/DWELL METER (ETI January 1987) Turn your Metro into a Porsche!	£16.40 + VAT
HI-FI POWER METER (ETI May 1987) Measures Hi-Fi output power up to 100W – includes PCB, components, meters	
Mono power meter	£3.90 + VAT
Stereo power meter	£7.20 + VAT

FEATURED IN ETI AUGUST 1988

There's nothing quite so encouraging as having a quantifiable result to show for your training efforts. If you are not particularly fit, your resting heart rate will be around 80 beats per minute. As your jogging, aerobics or sport strengthens your heart, the rate will drop dramatically – possibly to 60bpm or less. With the S101, you can watch your progress day by day.

Breathing is important too. How efficiently do you take up oxygen? How quickly do you recover from 'oxygen debt' after strenuous activity? The S101 will let you know.

The approved parts set consists of: case, 3 printed circuit boards, all components (including 17 ICs, quartz crystal, 75 transistors, resistors, diodes and capacitors), LCD, switches, plugs, sockets, electrodes, and full instructions for construction and use.

PARTS SET **£33.80** + VAT

Some parts are available separately. Please send SAE for lists, or SAE + £2 for lists, circuits, construction details and training plan (free with parts set)

THE DREAM MACHINE

FEATURED IN ETI DECEMBER 1987

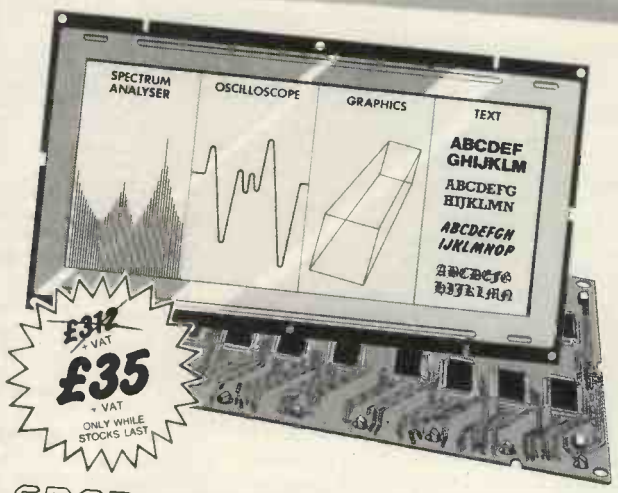
Adjust the controls to suit your mood and let the gentle, relaxing sound drift over you. At first you might hear soft rain, sea surf, or the wind through distant trees. Almost hypnotic, the sound draws you irresistably into a peaceful, refreshing sleep.

For many, the thought of waking refreshed and alert from perhaps the first truly restful sleep in years is exciting enough in itself. For more adventurous souls there are strange and mysterious dream experiences waiting. Take lucid dreams, for instance. Imagine being in control of your dreams and able to change them at will to act out your wishes and fantasies. With the Dream Machine it's easy!

The approved parts set consists of PCB, all components, controls, loudspeaker, knobs, lamp, fuseholders, fuse, mains power supply, prestige case and full instructions

PARTS SET **£16.50** + VAT

AVAILABLE WITHOUT CASE FOR ONLY **£11.90** + VAT



GRAPHICS DISPLAY OFFER

These beautiful dot matrix LCDs were originally ordered from Hitachi by a top flight instrumentation manufacturer. Unfortunately their new product – a portable 'scope – was ditched before they even had a chance to open the cartons!

But it's an ill wind that blows nobody any good. Because of their bad management, you now have the chance to own a high grade graphics display module at a tiny fraction of the normal price. Hitachi distributors will charge £312 each for these displays. From us, while stocks last, the price is £35!

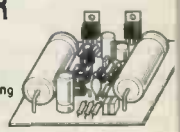
The LM236 display module has a 9½" x 4" display area, made up of 640 x 200 pixels. Since each pixel can be accessed individually, the display is equally at home as a 'scope screen, a spectrum analyser display, a graphics monitor or a text screen.

To help organise the display, mounted on the back is a control board with 20 LSI ICs. This keeps track of all the individual dots and allows the screen to be filled via a simple eight-bit-at-a-time interface.

To use the display, you will need to be fairly self-sufficient in logic design – you must know how to organise a frequency divider and serial data transfer. Apart from these basics, the data supplied with the module will tell all you need to know to get it up and running.

ARMSTRONG 75W AMPLIFIER

FEATURED IN PE JULY 1988



A.J. Armstrong's exciting new audio amplifier module is here at last!

Delivering a cool 75W (conservatively rated – you'll get nearer 100W), this MOSFET design embodies the finest minimalist design techniques, resulting in a clean, uncluttered circuit in which every component makes a precisely defined contribution to the overall sound.

You can read all about it in the July Issue of PE, but why bother with words when your ears will tell you so much more?

Parts set includes top grade PCB and all components.

SPECIAL INTRODUCTORY PRICE FOR FULLY UPGRADED MODULES.

SINGLE PARTS SET **£14.90** + VAT

STEREO PAIR **£25.90** + VAT

Please send SAE + £1 for data and circuits (free with parts set), including diagrams for matching pre-amp and power supply. This amplifier will not be available from your usual audio supplier – we produce the only designer approved parts set

MAINS CONDITIONER

FEATURED IN ETI SEPTEMBER 1986

Cleans up mains pollution easily and effectively. You'll hardly believe the difference in your Hi-Fi, TV, Video, and all other sensitive equipment.

PARTS SET **£4.90** + VAT

RUGGED PLASTIC CASE **£1.65** + VAT

BIO-FEEDBACK

FEATURED IN ETI DECEMBER 1986

Bio-feedback comes of age with this highly responsive, self-balancing skin response monitor! The powerful circuit has found application in clinical situations as well as on the bio-feedback scene. It will open your eyes to what GSR techniques are really all about.

The complete parts set includes case, PCB, all components, leads, electrodes, conductive gel, and full instructions.

PARTS SET **£13.95** + VAT

BIO-FEEDBACK BOOK **£3.95** (no VAT)

Please note: the book, by Stern and Ray, is an authorised guide to the potential of bio-feedback techniques. It is not a hobby book, and will only be of interest to intelligent adults.

POWERFUL AIR IONISERS

Ions, the miraculous *vitamins of the air*, have been credited with almost magical powers.

They are said to improve concentration, reduce blood pressure, help you sleep better, and even to raise you IQ! Although some of the claims may be exaggerated, there's no doubt that ionised air is cleaner, purer and more invigorating than dead air. Anyone who has owned an ioniser would never again want to be without one!

The Direct-Ion caused a sensation when it appeared as a project in ETI. Two years later, in October 1988, the Mistral was unveiled. Which will you go for – the compact, powerful, value for money Direct Ion or the sophisticated, no compromise Mistral? The choice is yours!

MISTRAL IONISER PARTS SET **£24.80** + VAT

DIRECT-ION PARTS SET (BLACK CASE) **£11.50** + VAT

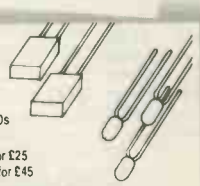
DIRECT-ION PARTS SET (WHITE CASE) **£11.80** + VAT

LM2917 EXPERIMENTER SET

Consists of LM2917 IC, special printed circuit board and detailed instructions with data and circuits for eight different projects to build. Can be used to experiment with the circuits in the 'Next Great Little IC' feature (ETI, December 1986).

LM2917 EXPERIMENTER SET **£5.80** + VAT

LEDs



Green rectangular LEDs for bar-graph displays.

50 for £3.50 500 for £25

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Access

SMT AND DIY

BY FRED THORNS

GLUE THAT RECTIFIER DOWN

Surface mounting components – after many predictions to the contrary – are increasingly coming within range of the home constructor. Here a solder manufacturer advises on the repair and construction of smt boards.

As far as the electronics professionals are concerned surface mount technology (smt) is here to stay and it is only a matter of time before enthusiasts develop smt skills of their own. Mixed technology pcbs – those which have both conventional lead components and surface mount devices (smds) are increasingly being incorporated into new equipment as smt takes hold.

This article investigates the practicalities of smt for those of us who are undoubtedly going to come face to face with it in the near future. Particular emphasis in the latter part of this feature is placed on two main areas of smt, removal and replacement, and new assemblies. Before that, however, a brief résumé of how the professionals manufacture smt equipment.

For the purpose of this article we will look at the two basic assembly methods used for discrete smds, whether they are manually or machine assembled. Knowledge of the assembly process will provide essential background.

A leadless surface mount component has to be adhered to the pcb or substrate so that it remains in position until it is soldered. This may be achieved by using a tiny blob of adhesive under the component, to hold it in position until soldering takes place. Alternatively a solder paste is used to stick the component's 'foot' to the pad. The paste is applied using a screening technique, or by syringe. The viscosity of the paste is sufficient to hold the component in position for soldering.

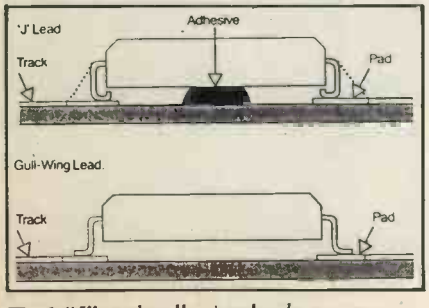


Fig.1 *"J" and gull-wing leads*

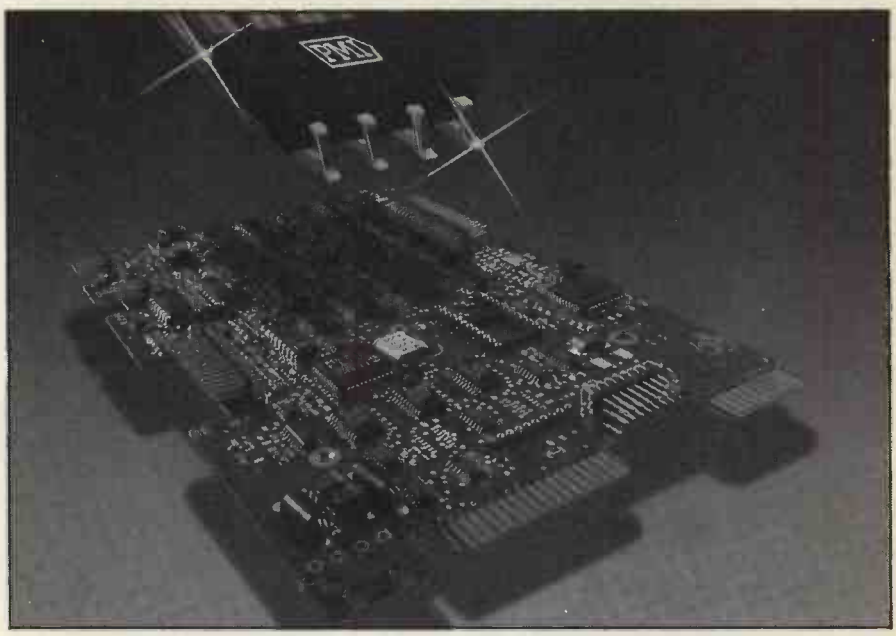
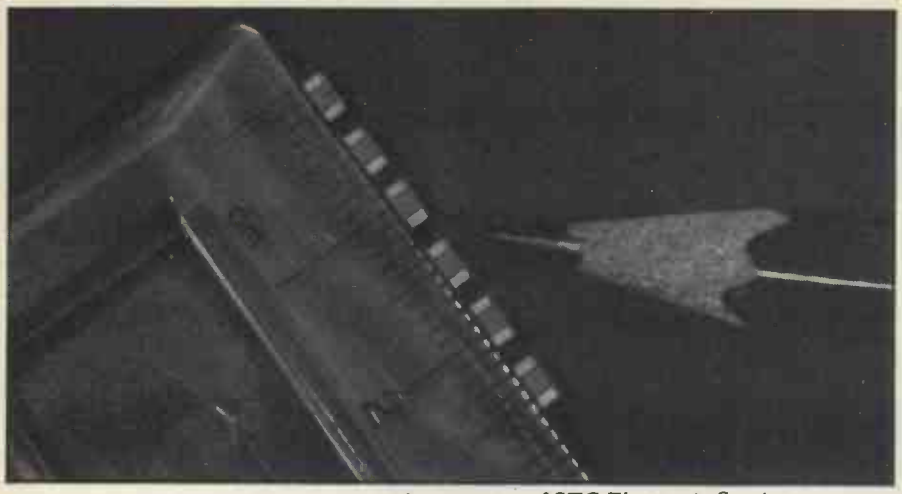


Illustration by courtesy of Precision Monolithics Inc.

In the case of the adhered component the method allows the pcb to be inverted for flow soldering. This technique permits smds to be secured on the track side of a pcb populated with conventional components ready for a single flow soldering operation. In the case of the solder-pasted component the solder paste itself forms the soldered joint after being subjected to a direct heat source – reflow soldering. Two lead shapes suited to these techniques are J-lead and gull-wing (Fig. 1).

As with all developing technologies, difficulties arise during manufacture. The flow soldering technique was developed originally for conventional components and when the method is applied to smd certain problems arise. Some mixed technology manufacturers are using 'old' wave soldering machines to solder mixed technology boards. These machines usually pass the pcb over a wave of flux followed by a single wave of solder. This was fine for conventional components but smds suffer from two main problems: shadowing and bridging. Shadowing is when the solder joint is not properly formed due to one component being in the shadow of another. Bridges occur when solder spans the gap between two components or tracks.

Double-wave machines have been developed to try to solve this problem but in doing so the components have had to run a temperature gauntlet, because it may take as long as 20 seconds to clear both waves. This is four times the recommended time that smds are supposed to be subject to temperatures around 240°C. Also, the first wave, designed to avoid shadowing by forcing solder between chip components, has the effect of washing away the active substance in the flux. This effect reduces solder drainage, creating heavy solder deposits and bridges. To overcome these



Kemet T491 SM tantalum chips. Photo by courtesy of STC Electronic Services

problems a suitable flux, such as Fry's Chipflow, must be used. Single and double wave machines have variable elements such as temperature and conveyor speed. For the best results when soldering smds it is necessary to vary these.

High degrees of success are realised using the reflow technique on smd assemblies, where solder pastes are fused by one of several available methods. The most popular methods are infra-red radiation and vapour-phase heat transfer. Solder paste deposited between the lead and the pad is melted by one of the fusing methods to form the joint. Advantages are that more complex and smaller circuits, such as hybrids, can be tackled effectively and efficiently. More recently highly-accurate laser soldering machines have been developed which heat only the lead, the pad and the paste, minimising thermal shock to components and pcbs.

Recent reports suggest that of the two techniques (wave and reflow) it is reflow that is providing the best results for soldering smds. More interestingly, the reports state that many smds are still soldered by hand for reasons such as low-cost, assured accuracy and joint quality, and convenience for small batch and prototype work. It seems the humble soldering iron will keep its place in the business until cost-effective automatic surface mount assembly is available to the majority of pcb manufacturers.

The information contained in these introductory paragraphs should be borne in mind when repairing or assembling pcbs containing surface mounted components.

SMT ASSEMBLY REPAIR

To begin with we will consider the various techniques for the removal and replacement of multi-lead quad packs.

Although it is not necessary to know the original assembly technique used it is advantageous to consider the three basic assembly methods when removing smds.

1 Wave soldering (adhesive under component).
2 Solder paste.
3 Standard soldering with cored solder.

ADHESIVES

When removing a component that has been secured to a pcb or substrate with adhesives, care must be taken to ensure that no damage is done to the substrate. In the case of a gull-wing multi-lead component, for example, the first step is to snip the leads with small cutters. The snipped leads should then be de-soldered and removed from the pads using snipe-nosed pliers. Use as little heat as possible when de-soldering to ensure that the track is not damaged or caused to lift from the pcb, and that future solderability is not affected. The track and pads need to be in good condition for re-soldering.

All that remains now is to physically remove the package from the board. This can be done by gently levering the device away from its adhesive bond using a small screwdriver for example. It is unlikely that a device will have tracks beneath it and so a clean removal should be possible, if care is taken.

For devices with J-leads the above technique may not be satisfactory because the leads cannot be seen when soldered. It may be necessary to remove the solder using a de-soldering wick or suction tool and then lift off the device as mentioned earlier with a small screwdriver.

In the case of devices that were mounted using cored solder or solder pastes it should be an easy task to remove the solder, using wick or suction. The device will then simply fall away, as no adhesive was used during assembly.

RE-SOLDERING

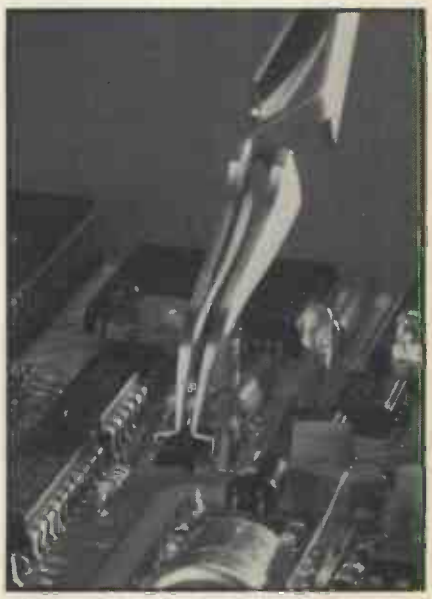
When re-soldering it is important to remember that some degradation of the pad's solderability may have occurred as a result of removal. Also bear in mind that some parts to be replaced may have a silver coating on soldering faces. To overcome the leaching effect of silver into the solder, due to silver's natural affinity for tin, the use of a two percent silver-loaded solder alloy is recommended.

Solderability is an important consideration when re-soldering. It is necessary to consider the level of activity of the flux (whether using cored solder or pastes). Mildly active (rosin-based) flux is preferred, if the solderability of substrates and components allows its use. If not, ensure that the activity is extended sufficiently to obtain a satisfactory result, but not into the range of superactive fluxes, where any entrapment might cause problems in use.

THE CHOICE

When using a solder paste for assembly one should use a material designed for the application. For example, it is extremely unlikely that enthusiasts will screen print new assemblies, therefore we must consider that application of the paste will be via a syringe. The solder powder used in the make-up of a solder paste is prepared from an alloy of tin/lead or tin/lead/silver. This powder is carefully selected with particles of a size that will eliminate the possibility of blocking by the paste in the syringe or needle. The powder is then blended with a flux carrier to form the solder paste. Consider carefully your choice of paste; advice from a top manufacturer should ideally be sought. Additionally it is advisable that only small quantities of paste are purchased at any one time, as pastes deteriorate with age.

To effectively produce the required result when replacing a component, ensure that the surfaces of the pads are free from any contamination. Also ensure that the pads and the solderable parts of the component are not touched by hand.



Self-gripping SMD tweezers manufactured by Ceka Works Ltd.

Sufficient paste should be applied to the pads to allow an adequate fillet to be formed when fused. In the case of flat-packs especially, excessive amounts of solder paste should be avoided as this may cause bridging. The design of the paste is so accurate that its properties encourage it to retract from non-metallic parts and migrate to metallic parts.

FUSION PASTE

Three methods may be employed when fusing solder paste.

1 Hot plate
2 Oven
3 Soldering iron

Do not use a naked flame.

When using the hot-plate technique it is extremely important to remove the pcb or substrate as soon as it is seen that fusion has occurred, otherwise other parts on the board may be subjected to secondary reflow.

The oven method suffers from the same problem in that its temperature has to be set at just above the melting point of the solder. Because it is not possible to see the job, it is even more difficult to ascertain when reflow has occurred.

Reflowing already soldered joints, as a result of using the hot-plate or oven technique, can create solderability problems because extended high temperatures can induce increased intermetallics.

A soldering iron then is the best choice. The bit should be chosen to suit the type of component and should ideally touch both surfaces bringing them to the correct temperature simultaneously. Solder paste can be dispensed for all the joints to be soldered and the component held in position by a non heat-transmitting material until all fusion has been implemented. (Flat-packs may not have to be secured. Because of their size they are unlikely to move.)

CORED SOLDER WIRES

Considerations for the choice of a suitable cored solder wire are basically the same as for pastes. Bear in mind, however, that the gauge must also suit the small assembly situation. As a rough guide, cored solder with diameters of more than 1.2mm should be avoided. It is not simply that too much solder might be inadvertently applied to the joint, but also that a potentially damaging amount of heat is required to melt the more substantial solders. The ideal size will be found in the 0.7mm to 1mm range. An obvious difficulty encountered when using wire solders is that only one joint can be soldered at a time.

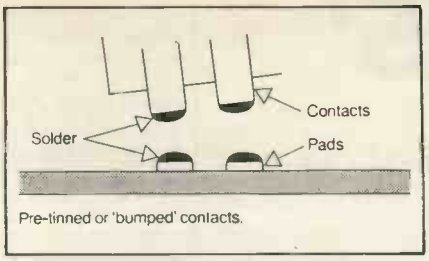


Fig.2 *SMT pre-tinned "bumping"*

NEW SM ASSEMBLIES

Enthusiasts, such as PE readers, are most likely to encounter one or more smds when working on kits for computers, musical and communications projects for example. Many of the pointers and techniques mentioned for repair and re-soldering are relevant for the new assembly. It should be borne in mind that mixed technology projects will contain sm components that need to be soldered at the same time as their conventional neighbours.

A combination of cored wire solder and solder paste may be used for mixed technology assemblies, particularly as the soldering is probably going to be on both sides of the pcb or substrate. The use of solder pastes is without doubt an advantage for the sm components on such assemblies.

Where wholly smt assemblies are concerned the use of pastes is again beneficial when compared to cored solder wire. The paste can be deposited for all components in a single operation.

The biggest problem, and probably the one that the enthusiast is most likely to have difficulty accepting, is that of 'tombstoning' following fusion. This is when chip components sit up on end, a problem that can easily occur when excessive paste is applied to one of the pads.

Other reasons for tombstoning include differing solderability levels at either end of a chip or on the pads. This is further evidence that the choice of a paste must ensure that its characteristics assist in overcoming these solderability problems, thus ensuring good joints. In the professional world, even distribution is achieved by screen printing the paste onto the pcb or substrate.

If difficulty is experienced with flat-packs they may be pre-tinned as can the pad areas to which they are to be soldered. A mild rosin flux may be used subsequently when fusing the device to the substrate. The pre-tinning of obstinate contact points may be carried out using a flux with a higher level of activity. In the smt world this pre-tinning is known as 'bumping' (see Fig. 2). It is important to remember that when such fluxes are used the area must be cleaned to remove any residue. Advice should be sought from a flux manufacturer as to a suitable flux and the cleaning requirement necessary for that flux.

RECOMMENDED FLUXES

Fry's Flowcream solder paste type 1024DG would be a good choice for the enthusiast. Similarly, for cored solder wire the Fryflow or Superspeed grade F270 (62% tin/2% silver/36% lead) are recommended. Diameters from 0.7mm to 1mm are available.

For further information contact the author, Fred Thorns, Fry's Metals Limited, Tandem Works, Merton Abbey, London, SW19 2PD. Telephone: 01-648 7020. PE

A diy SMT trials kit – see News and Market place.

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READERS' LETTERS

SKY WATCH

Dear Ed,

To continue the theme of Mr. Trice's letter in PE Aug, I believe that phototransistors can be used to monitor light from the sky by passing the signal through a narrow passband filter which looks for a 100Hz component due to ambient street lighting being reflected from clouds – no signal, no clouds.

I also believe it may be possible to use two temperature sensors, one exposed to the sky, the other shielded. The theory is that if a clear period develops the exposed sensor cools more than the shielded one.

Ken Wood, Ipswich.

Dear Ed,

Would a barometric pressure sensor help in detecting cloudless sky conditions?

R. Rockwell, Medmenham.

In some areas sky pollution and low cloud will permit the use of a filtered optodetector for monitoring visibility. John Mason of Astronomy Now magazine, tells me the Dutch use them a lot. However, he says that British pollution levels are not so universally bad as the Dutch and so such a method would only have limited appeal. He also says that these detectors will not monitor high level cloud which also interferes with viewing.

I suspect that the temperature sensing method would be too subject to local conditions for it to be usable for detecting far distant situations. Nice idea though.

The suggestion of barometric pressure sensing is also interesting, but it seems we can get clouds under both high and low pressure conditions. Perhaps readers who build my electronic barometer in the Sept. and Oct. 88 editions might care to let me have feedback on this one.

Ed.

EFFECTIVE

Dear Editor,

I'm new to electronics and am interested in making my own effects units. But I am worried about how diy projects compare to commercial ready-made units. How can I assess this?

Tom Greenwood, Stockport.

There is no simple answer to your question. It is not usually practical to physically compare commercial ready-built products with diy designs, largely because of the sheer number of them.

As general rule diy designs can offer you benefits on two main levels. It is quite possible to assemble a kit for considerably less outlay than for a ready made equivalent, and to achieve similar results. It is also possible that a kit may well offer you more facilities than a comparably priced commercial unit.

I suggest that you read the manufacturer's data on commercial units, and the published texts for diy projects. Also bear in mind that there can be more satisfaction from building your own project than simply installing a unit that someone else has made. If you are building a unit yourself, though, you must be prepared to takes pains to follow the assembly instructions fully and not rush the work. If you are inexperienced then start off by building a simple one. If you do not have the time or the expertise to devote to a large kit then a ready-made unit is your best answer.

Ed.

GLITCHED HERTZ

Dear Ed,

Barry Fox in his column of July 88 used the symbol Hz when he is actually talking about cycles. Hertz is of course a measure of frequency, the number of cycles per second, and so I assume "a 1Hz glitch" ought to mean, say, a change of frequency from 50Hz to 49Hz. But he means the loss of one cycle of energy which, at 50Hz, lasts 0.02 seconds.

This kind of error seemed quite common in the early days of the change from c/s to Hz, but I've not seen it recently. It would be a pity to start a new wave of misunderstanding.

L.W. Barclay, Chelmsford.

Good point. If memory serves me correctly, the original electricity industry reports make the same mistake. I think we have all been bullied for so long into converting feet and inches into metres and centimetres, even when (as in the case of tape technology) the original standards were set to imperial measurements, that we no longer dare use what would often be more accurate terms.

Barry Fox

IF YOU HAVE ANY COMMENT, CRITICISMS OR SUGGESTIONS, WRITE AND LET US KNOW. WE ARE INTERESTED IN WHAT YOU THINK AND SAY.

Is the Wizard of OS/2 being squeezed out by MS DOS and Unix in the operational system battle? (See Leading Edge).

LEADING EDGE

Continued from page 8.

who make similar "clone" PCs. The company is now asking for a 5% royalty on PS/2 clones, plus back payment of 1% royalty on all old DOS clones. Firms which do not pay up their back royalties will not get a PS/2 licence and IBM claims to have patents which give it the legal right to make this demand.

IBM files over 500 patents every year in Europe alone. Some of them are very long – I know of one IBM patent on Gene Amdahl's work which takes up three bound volumes of the library shelves. It is equivalent in length to 300 conventional patents! This creates a cobweb of confusion over what exactly IBM has covered, and doubtless this explains why the company is so vague about its MCA technology. It could take years, probably decades, for lawyers and courts to sort out what patents clone makers are or are not infringing.

By no menas everyone agrees with IBM that the computer world will ditch MS DOS in favour of OS/2. Many users will stick with MS DOS, just as video users are more likely to stick with VHS than upgrade to the new and only partially compatible Super VHS format.

Those who want something better than DOS may well decide to make the break and switch to Unix. In fact other divisions of IBM already support the Unix standard. Although less friendly to use, Unix is more powerful than OS/2. Oracle, for instance, clearly favours Unix. Ian Fisher, software product manager at Oracle, sums up the situation neatly.

"OS/2 is much better than MS DOS but many people favour Unix. OS/2 is in the space between DOS and Unix and it could be squeezed out".

If so, then IBM and Microsoft may rue the day when they decide to discard MS DOS, scorn Unix for Personal Computers and try to convince the world to switch to OS/2.

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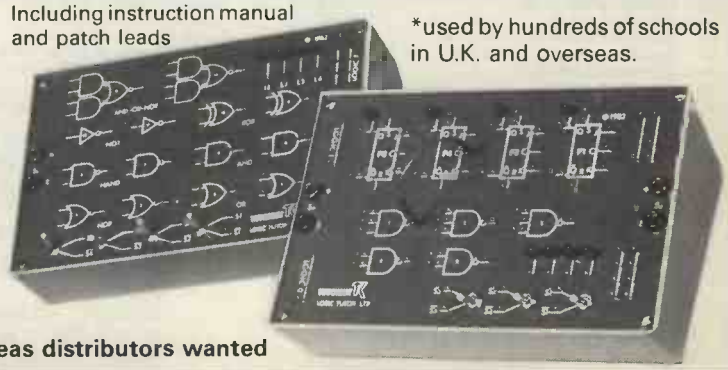

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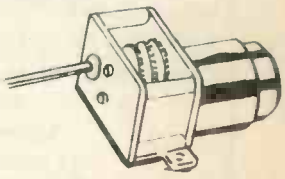

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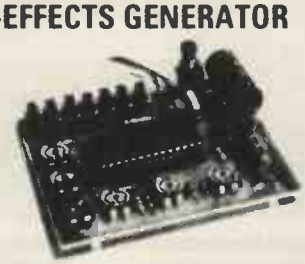

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XK125 Complete kit of parts **£24.00**

POWER STROBE KIT

Designed to produce a high intensity light pulse at a variable frequency of 1 to 15Hz, this kit also includes circuitry to trigger the light from an external voltage source (eg. a loudspeaker) via an opto isolator.

Instructions are also supplied on modifying the unit for manual triggering, as a slave flash in photographic applications or as a warning beacon in security applications. The kit includes a high quality pcb, components, connectors, 5Ws strobe tube and full assembly instructions. Supply: 240V ac. Size: 75x50x45.

XK124 Stroboscope Kit **£13.75**

VERSATILE REMOTE CONTROL KIT

This kit includes all components (+ transformer) to make a sensitive IR receiver with 16 logic outputs (0-15V) which with suitable interface circuitry (relays, triacs, etc – details supplied) can be used to switch up to 16 items of equipment on or off remotely. The outputs may be latched (to the last received code) or momentary (on during transmission) by specifying the decoder IC and a 15V stabilised supply is available to power external circuits.

Supply: 240V AC or 15–24V DC at 10mA. Size (excluding transformer) 9 x 4 x 2 cms. The companion transmitter is the MK18 which operates from a 9V PP3 battery and gives a range of up to 60ft. Two keyboards are available MK9 (4-way) and MK10 (16-way), depending on the number of outputs to be used.

MK12 IR Receiver (incl. transformer) **£16.30**
MK18 Transmitter **£7.50**
MK9 4-Way Keyboard **£2.20**
MK10 16-Way Keyboard **£6.55**
601 133 Box for Transmitter **£2.60**

SUPER-SENSITIVE FM MICROBUG

Highly sensitive FM transmitter measuring only 45 x 25 x 15mm, including the built-in microphone. Frequency 88-100MHz enabling reception on a standard domestic FM radio. Range approximately 300m depending on terrain. Powered by 9V PP3 (7mA). Its small size and highly sensitive electret microphone makes it ideal for surveillance, baby alarm, etc.

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CT6000K **£47.20**
XK114 Relay kit for CT6000 Includes PCB connectors and one relay. Will accept up to 4 relays 3A/240V c/o contacts **£4.30**
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DISCO LIGHTING KITS

DL1000K This value-for-money 4-way chaser features by-directional sequence and dimming. 1kW per channel **£19.25**
DLZ1000K — A lower cost uni-directional version of the above. Zero switching to reduce Interference **£10.80**
DLA/1 Optional opto input allowing audio 'beat'/light response **77p**
DL3000K — 3-channel sound to light kit features zero voltage switching, automatic level control and built-in microphone. 1kW per channel. **£15.60**

The DL8000K is an 8-way sequencer kit with built in opto-isolated sound to light input which comes complete with a pre-programmed EPROM containing EIGHTY — YES 80 different sequences including standard flashing and chase routines. The KIT includes full instructions and all components (even the PCB connectors) and requires only a box and a control knob to complete. Other features include manual sequence speed adjustment, zero voltage switching. LED mimic lamps and sound to light LED and a 300W output per channel. And the best thing about it is the price: **ONLY £31.50.**

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Superbly made, UK manufacture, PIL tube, all solid state colour monitors, complete with composite video and sound inputs, attractive teak style case, Ideal for a host of applications including Schools, Shops, Disco's, Clubs etc. Supplied in EXCELLENT little used condition with 90 day guarantee.
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MOTOROLA M1000-100 5" CRT black & white compact chassis monitor measuring only cm 11.6h ,12w, 22d, ideal for CCTV or computer applications. Accepts standard Composite video or individual H & V syncs. Operates from 12v DC at apprx 0.8a. Some units may have minor screen marks, but still in very usable condition. Fully tested with 30 day guarantee & full data **Only £29.00 (C)**
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TANDON TM101-4 FH 80 track double sided **Only £49.95 (B)**
JAPANESE Half Height double sided drives by **Canon, Tec,** Toshiba etc. Specify 40 or 80 track **Only £75.00 (B)**
TEAC FD55-F 40-80 track double sided Half Height **Brand New £115.00 (B)**

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DATEL 2412 2780/3780 4 wire modem unit EX BT fully tested. **£199.00 (E)**
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BOSHERT 13088 switch mode supply ideal disk drives or complete system. + 5v 6a. + 12 2.5a,-12 0 .5a, 5v 0.5a. Dim cm 5.6 x 21 x 10.8 **New £29.95 (B)**
BOSHERT 13090 same as above spec but outputs of + 5v 6a, + 24v 1.5a, + 12v 0.5a,-12v 0.5a **New £39.95 (B)**
GREENDALE 19AB0E 60 Watt switch mode outputs + 5v 6 a. + 12v 1a, -12v 1a, + 15v 1a.D. 11 x 20 x 5.5 **RFE Tested £24.95 (B)**
CONVER AC130-3001 High grade VDE spec compact 130 watt switch mode PSU. Outputs give + 5v 15a, -5v 1a, + &-12v 6a. Dim 6.5 x 27 x 12.5 Current list price £190. Our price **New £59.95.00 (C)**
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Made to the highest spec for **BT** this unit gives several fully protected DC outputs most suited to the **Electronics Hobbyist.** + 5v 2a, + &-12v 1a, - + 24v 1a and + 5v fully floating at 50ma. Ideal for school labs etc. Quantity discount available.
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Brand new high quality, fully cased, 7 channel UHF PAL TV tuner system. Unit simply connects to your TV aerial socket and video monitor turning same into a fabulous colour TV. Dont worry if your monitor doesn't have sound, the TELEBOX even has an integral audio amp for driving a speaker plus an auxillary output for Headphones or HI Fi system etc. Many other features: LED Status Indicator, Smart moulded case, Mains powered, Built to BS safety specs. Many other uses for TV sound or video etc. Supplied BRAND NEW with full 1 year guarantee. **Carriage code (B)**

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Colour when used with colour CRT. RGB version NOT suitable for IBM-CLONE type colour monitors . DATA sheet on request. PAL overseas versions CALL.

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4" 24v DC 8w 120 x 120 x 25 **£14.50 (B)**
BUHLER 12v DC 62 mm **£12.95 (A)**

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1.5 kw 115v 60 Hz power source **£950**
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WAYNE KERR RA200 Audio, real time frequency response analyzer **£3000**
TEKTRONIX 1411/R PAL TV test signal standard. **£6900**
TEKTRONIX R140 NTSC TV test signal standard. **£875**
HP 3271A Correlator system **£350**
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DISPLAY -ELECTRONICS-

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DIGITAL ELECTRONICS

BY OWEN BISHOP

PART 3 – MULTIVIBRATORS

Monostable and astable circuits provide pulses; pulses are essential to logic circuits where any kind of timing control is needed – including most computers. It follows that we should know how they work.

A multivibrator is a circuit that produces pulses. There are two types:

- Monostable multivibrator – the circuit has *one* stable state; it produces a single pulse each time it is triggered.
- Astable multivibrator – the circuit has *no* stable state; it produces a continuous series of pulses at a fixed frequency.

The two types are generally refered to as 'monostables' and 'astables' for short.

There are several ways of building monostables, using:

- a pair of transistors,
- a pair of logic gates
- or a timer ic.

In this series we consider only the latter two designs, as these are more suitable for use with logic circuits.

A MONOSTABLE USING LOGIC

Fig. 1 shows a monostable built from two NAND gates. The third gate, on the right, is not part of the monostable, but serves as a *buffer*. It connects the monostable to another logic circuit. The buffer takes only a small current from the monostable, but provides all the current that the other circuit needs. This means that the operation of the monostable is not affected by loss of current to the other circuit.

You may notice that the monostable circuit is similar to the SR flip-flop described last month. It is similar because both circuits are based on *cross-connected gates*. The monostable has only *one* stable state, because one of the cross-connections is by way of a capacitor.

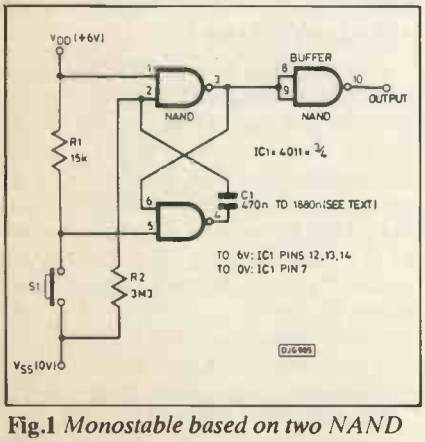


Fig.1 *Monostable based on two NAND gates.*

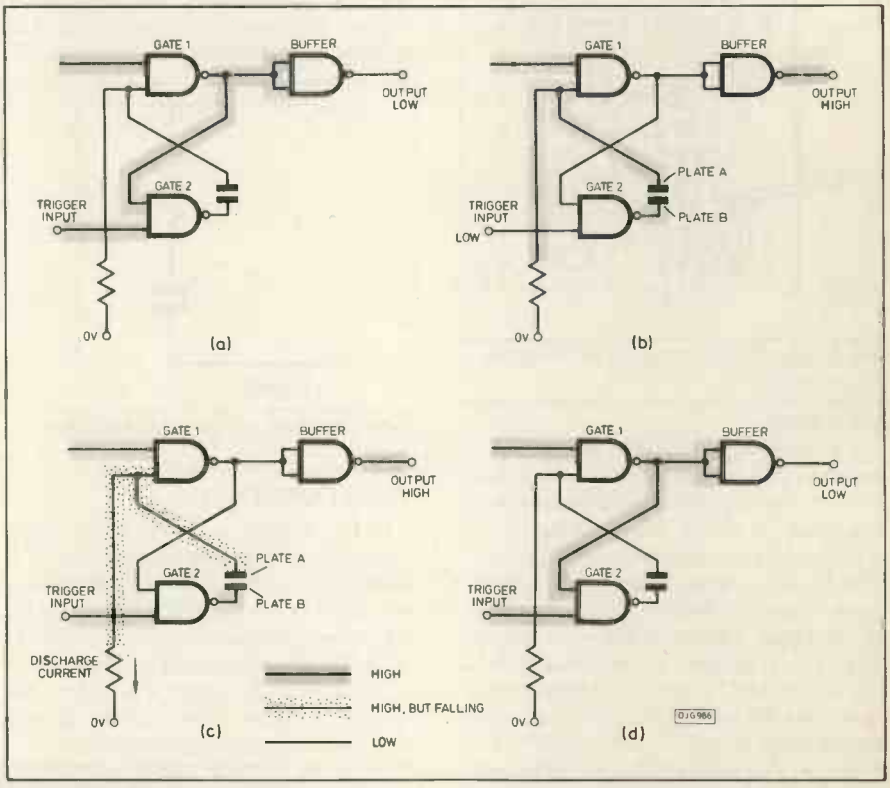


Fig.2 *How the Monostable works.*

Fig. 2 shows what happens when the monostable is triggered. At (a) the monostable is in its stable 'resting' state. The monostable is triggered by briefly pressing S1 (Fig. 1). This makes the input to gate 2 go 'low' for an instant. Fig. 2b shows that the low input causes the output of gate 2 to go high. The capacitor plate connected to gate 2 (plate A) is now at high potential. Plate B of the capacitor is also taken to high potential. This is because the *difference* in potential of the plates of a capacitor can not change immediately. If the potential on plate A goes up by, say, 2V, the potential on plate B also rises by 2V. The result is that both inputs to gate 1 are now high, its output is low, and the buffer output is high.

We can now let the trigger input go high again without affecting the monostable (c). The high potential on one side of R2 and the low potential (0V) on the other side of R2 causes a discharge current to flow to ground. The charge on plate B is gradually removed. The potential in that section of the circuit gradually falls. Obviously this is *not* a stable state as the high potential is falling steadily. Eventually the potential falls low enough to be counted as a 'low'. At this stage (Fig. 2d) the low potential at the input of gate 1 makes it change state. Its output becomes high. This causes gate 2 to change state. We are back to the stable state of Fig. 2a.

To sum up, the monostable is normally in its stable state and the buffer output is low. When triggered, it changes to its unstable state and the buffer output goes high. It stays like this while the capacitor is discharged through R1, and then

returns to its stable state. The length of time it is in its unstable state depends on how long the capacitor takes to discharge. This depends on the values of R2 and C1. To see what the effect is, try this investigation.

Investigation 1

Timing a monostable

In this investigation you find out what factors decide the length of the output pulse of a monostable. This monostable makes use of cmos logic.

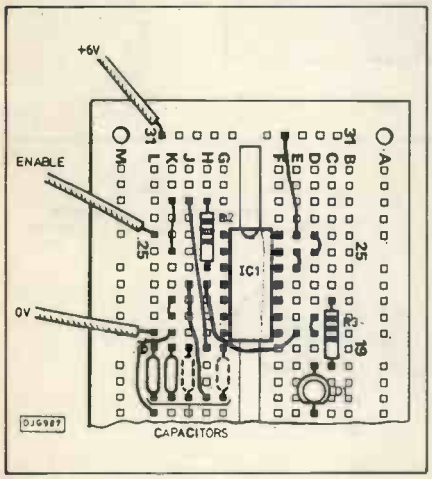


Fig.3 *Breadboard layout of monostable circuit in Fig.1.*

What to do

1. Connect a monostable circuit on a breadboard, as in Fig. 3. We have provided an area below the ic where you can plug in up to four capacitors in parallel. Begin with two capacitors, each 470nF as shown, giving a total capacitance of 940nF. R3 has a resistance of 180 ohms. Instead of the push-button S1 of Fig. 1, we have a flying lead, which can be touched to the 0V line as required. Note that the fourth gate of the ic has its inputs (pins 12 and 13) connected to 6V. This is so that we obey the rule stated last month, that cmos inputs must never be left unconnected. Instead of using D1 and R3 to indicate the output, you can use the logic probe described last month. Remember the other rules about handling cmos, given last month.
2. Switch on the power. The led should be out, indicating a low output.
3. Trigger the monostable by *briefly* touching the flying lead to 0V. Either plug it into a socket in the bottom row and take it out again *immediately,* or briefly touch it against a bare wire lead of one of the components that are plugged into the bottom row.
4. The led comes on while the monostable is in its unstable state, then goes out again. How long is the led alight? Use a watch to measure the output pulse length as precisely as you can, and write down the result. Do this five times and work out the average of your measurements.
5. Try the effect of altering the *capacitance* of C1. Remove one of the capacitors. Repeat step 4. The capacitance is half what it was before. What is the effect on the length of the pulse (the length of the unstable state)?
6. Replace the capacitor and plug two more into the vacant sockets. This gives a total capacitance of 1880nF, double that at step 3. Repeat step 4. What is the effect on the pulse length of doubling the original capacitance?
7. Now try the effect of altering the *resistance*. Have two capacitors plugged in, as at the start in Fig. 3. Change the resistance for a 1M5 resistor. Repeat step 4. The resistance is about half the original value. What is the effect on pulse length of halving the resistance?
8. Change the resistor for a 10M resistor. The resistance is about three times the original resistance. What is the effect on pulse length of trebling the resistance?

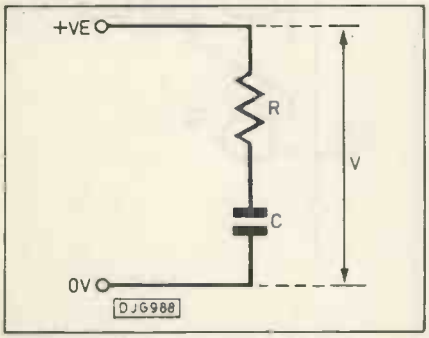


Fig.4 *Charging a capacitor through a resistor.*

TIME CONSTANT

In Fig. 4 there is a voltage V across a capacitor of C farads and resistor of R ohms. Current flows through the resistor and charges the capacitor. To start with, the capacitor is uncharged. The voltage across the capacitor is zero and the *whole* of the charging voltage V is across the resistor. Current flows rapidly through the resistor, charging the capacitor.

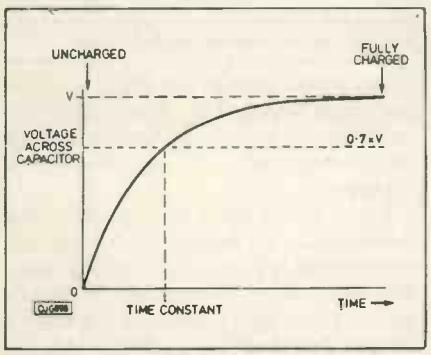


Fig.5 *Change of voltage across a capacitor as it is charged.*

Fig. 5 shows what happens as the capacitor becomes charged. The voltage across the capacitor gradually increases, which means that the voltage across the resistor must gradually decrease. The current therefore decreases gradually too and the rate of charging decreases.

In Fig. 5 the curve is steep to begin with, indicating rapid charging, but becomes less steep as time passes. Finally, when the capacitor has become fully charged, the voltage across it is V. This means that the voltage across the resistor has fallen to zero and no more current flows. Charging ceases and the curve in Fig. 5 levels out.

The rate at which the capacitor charges depends on the value of R and C. We express this in an equation:

$$t = RC$$

In this equation t is the *time constant* of the resistor-capacitor combination. The equation shows that the greater the resistance and/or capacitance, the greater the time constant. The greater the time constant, the longer the time taken to charge the capacitor to a given voltage. This is what was found in Investigation 1. The time taken to charge the capacitor to a level at which the monostable resets depends directly on the time constant of the circuit – the values of the resistance and capacitance involved.

The final question is 'What time do we mean by t?' (No – not tea-time!) Without going into the mathematics, t is the time taken to charge the capacitor to 0.7 of the charging voltage, V. In Fig. 5 it is the time taken for the voltage across the capacitor to reach the dashed line at $0.7 \times V$. In the cmos monostable working on a 6V supply, it is the time taken to charge the capacitor to $0.7 \times 6V = 4.2V$. Since the gates change state when the capacitor is charged to 3V, the unstable period lasts rather less than t. With R=3M3 and C=940nF, for example, $t = 3.3 \times 10^6 \times 940 \times 10^9 = 3.102s$. Did you get a pulse length a little less than 3 seconds?

SIMPLE PULSE GENERATORS

Fig. 6 shows four pulse generators each based on two ttl gates. The graphs illustrate their action. In each case, a pulse is generated following a change in input level – either high-going or low-going. These circuits are useful building-blocks for any logical circuit in which timing is required. We leave it to the reader to find out how they work, either by using truth tables or by practical investigation. The working of the first one is explained on P.41.

555 MONOSTABLE

In this section we introduce what must be one of the most useful ics ever devised (Fig. 7). Although it is essentially a timer ic, it is so easy to use and has so many applications that one wonders how we ever managed to get by in those days before the 555 arrived. Fig. 8 shows how to use it as the basis of a monostable circuit. Its action depends upon the time taken to charge a capacitor, C1. The current for charging C1 flows through R1 and, as we might expect, the time taken is proportional to the values of R1 and C1.

Treating the 555 as a black box, without going into any details of its *internal* circuitry, the action of the ic is as

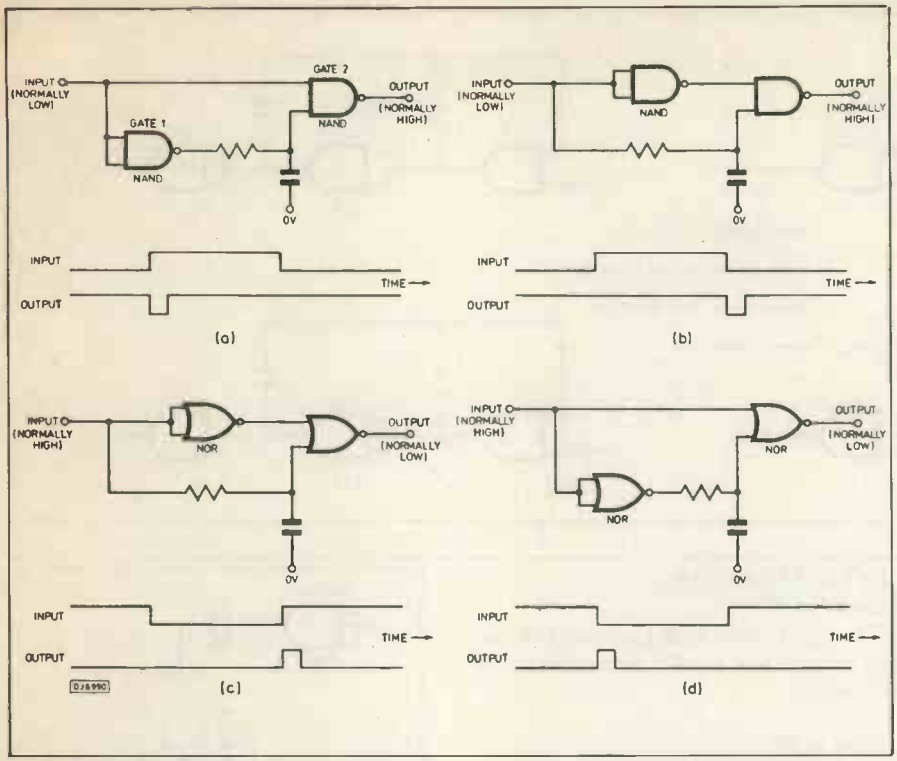


Fig.6 *Pulse generation.*

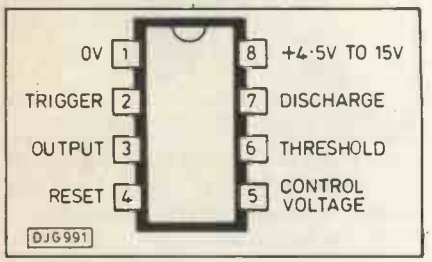


Fig.7 *The 555 timer ic.*

follows. Its output is normally low (0V). When it is triggered, by a low pulse to the trigger input, the output rises to high (+V). At this instant, the capacitor begins to charge. When charging is complete, the output falls to 0V again.

We must look a little more closely at the charging process.

Normally, the capacitor is held charged to a level that is precisely V/3 (one third of the supply voltage). Current flows continuously through R1 but, instead of flowing on and increasing the charge on C1, it is diverted into the ic through pin 7, and conducted away through pin 1 to the 0V rail. When the ic is triggered, the current is no longer diverted. Instead of flowing into pin 7, it flows on toward the capacitor. The capacitor charges and the voltage across it gradually rises. The output at pin 3 is high while this is happening. The rising voltage across the capacitor is sensed by a circuit connected internally to pin 6. After a period of time (depending on the values of R1 and C1) the voltage across the capacitor reaches 2V/3 (two-thirds of the supply voltage). Pin 6 senses when this voltage has been reached and, as soon as this happens, the output (pin 3) falls sharply to 0V. At the same time, pin 7 once more allows current to pass into the ic. The capacitor is discharged very rapidly, until pin 6 senses that the voltage has fallen to V/3 again.

The length of time (in seconds) for which the output is high is the time constant multiplied by 1.1:

$$\text{Pulse length} = 1.1RC$$

An advantage of the 555 is that, given high values of R1 and C1, a very long pulse length can be obtained. For example, with a resistance of 1M and capacitance of 1000μF, the period is 1100 seconds, or just over 18 minutes. Longer periods are possible, but there is a upper limit because the current flowing to the capacitor is very small. For high capacitance it is generally necessary to use electrolytic capacitors and these have a high leakage current. The charging current leaks away through the capacitor, which fails to charge.

Another advantage of the 555 is that the times are independent of the level of the supply voltage. For example, if the supply voltage is 6V, the capacitor is charged from 2V (=V/3) to 4V (=2V/3). The voltage increases by 2V. If the supply voltage is 12V, the capacitor is charged from 4V to 8V, a rise of 4V. The rise in voltage with 12V supply is twice as great, currents are twice as great, the capacitor charges twice as quickly and the *time is the same.*

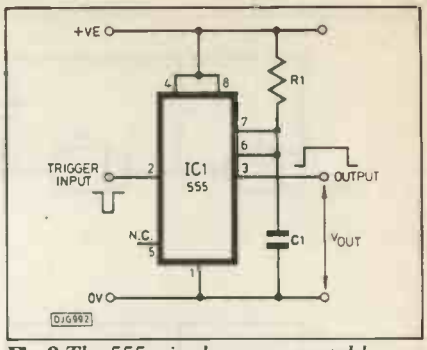


Fig.8 *The 555 wired as a monostable N.C.; no connection is made in this circuit.*

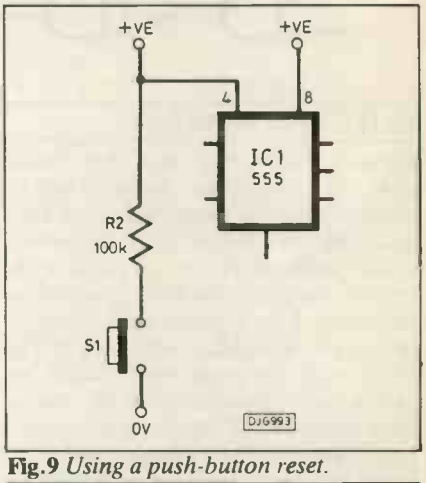


Fig.9 *Using a push-button reset.*

As already mentioned, timing is triggered by a low-going pulse to the input pin. Triggering is very sensitive; if the pin is unconnected the ic can be triggered by touching a finger against the input pin. This feature makes the ic useful in touch-switches. If the ic is triggered again while the output is high, there is no effect on the timing period. The reset pin is similarly very sensitive. In the circuit of Fig. 8 it is shown connected to the supply line, so that the ic can not be reset. If it is connected as in Fig. 9, closing S1 while output is high grounds the reset input and the output immediately goes low. At the same instant the capacitor is discharged rapidly to V/3, ready for the next triggering.

ASTABLE USING LOGIC

An astable circuit has *no* stable states. It alternates (or oscillates) between two

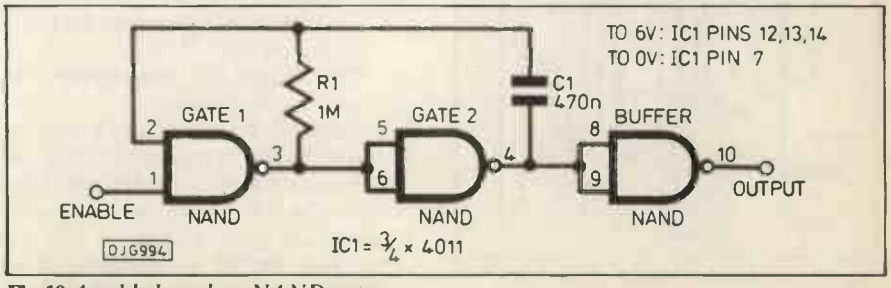


Fig.10 *Astable based on NAND gates.*

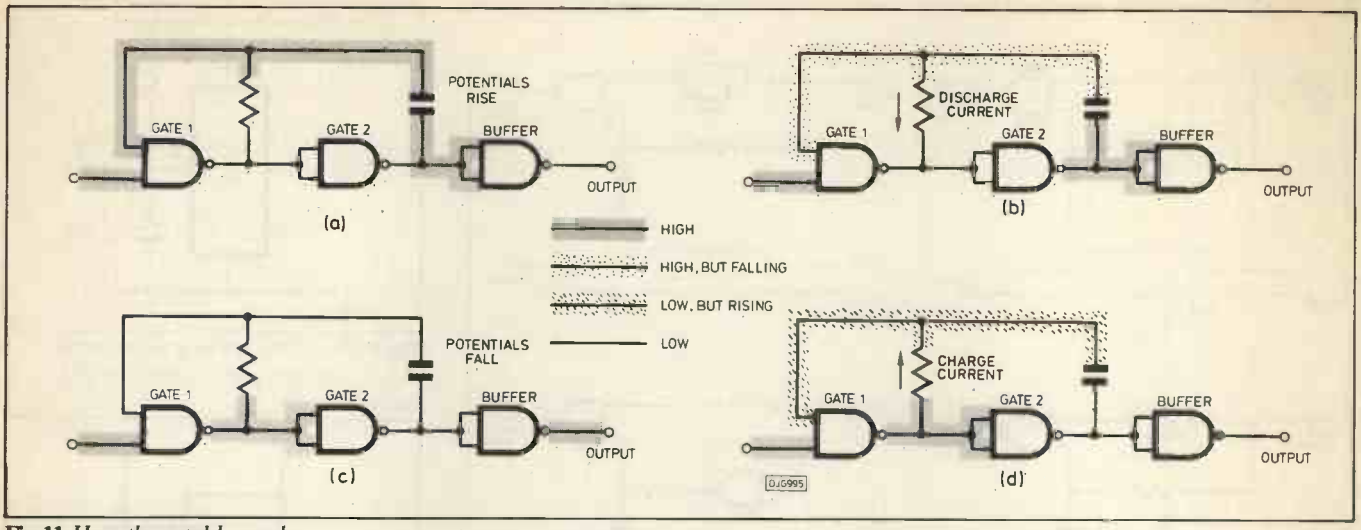


Fig.11 *How the astable works.*

opposite states, but does not remain stable in either state. Astables can be made using cross-connected gates, each linked to the other through capacitors. Using cmos logic, it is possible to simplify the circuit by omitting one of the capacitors. Fig. 10 shows a cmos astable, the action of which is explained in Fig. 11. As with the monostable circuit, we have a buffer gate to isolate the astable from the circuit to which it is connected. In this version of the astable, one of the inputs to gate 1 is used as an ENABLE input. If this is held high, the astable operates as described below. If the ENABLE is low, the circuit remains stable, with the buffer output high.

In Fig. 11a the astable has just changed state. The output of gate 1 has gone low and that of gate 2 has gone high. This raises the potential of *both* plates of the capacitor. The capacitor then loses charge through R1 (b), gradually raising the potential at the inputs of gate 2. Since this is a cmos gate, no current flows into the gate itself. When the input level has risen to 3V the astable changes state again (c). Both plates of the capacitor are at 0V and current flows through the resistor in the reverse direction to charge the capacitor (d). When the level on the capacitor plate and at the input of gate 1 has risen to 3V the astable changes state again, and we are back at diagram (a).

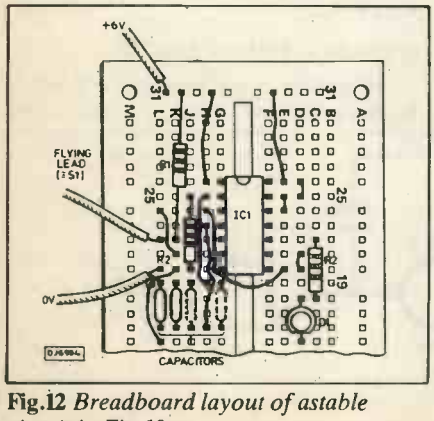


Fig.12 *Breadboard layout of astable circuit in Fig.10.*

INVESTIGATION 2

Astable action

In Fig. 12 the fourth (unused) gate has its inputs (pins 12, 13) connected to the 6V supply.

What to do

1. Set up the circuit as in Fig. 12, with the ENABLE input wire plugged into the 0V rail. There are two 470n capacitors connected in parallel, giving a total capacitance of 940n. There is room for two more such capacitors to be added in parallel, as shown. The state of the circuit is shown by the led, or you can use the logic probe described last month.

2. Connect the power supply. The led comes on and stays on, since ENABLE is at 0V.

3. Plug the ENABLE wire into the +6V rail. The led begins to flash. Count how many times the led flashes on during one minute. Divide this number by 60 to obtain the frequency in hertz.

4. Replace R1 by a 2M2 resistor. Count the number of flashes per minute. Divide by 60 to calculate the frequency.

5. Replace R1 by a 470k resistor. Measure the frequency.

6. Replace the original 1M resistor. Plug in two more 470n capacitors in parallel with the two that are already there. This gives a total capacitance of 1880n. Measure the frequency.

7. Remove three of the capacitors, leaving a single 470n capacitor. Measure the frequency.

8. Summarise the results of your investigation by filling in the blanks in these sentences:

The higher the resistance, the ..... the frequency.

The higher the capacitance, the ..... the frequency.

9. Replace R1 with a 1k resistor. Remove the led (or logic probe) from the circuit and connect a crystal earphone instead (Fig. 13). Switch on, *then* put the earphone to your ear. What can you hear? The astable is oscillating at an *audio* frequency, which can be heard as a note. Try other values of resistors and capacitors, to find how the frequency of the sound is affected. Write sentences similar to those at step 8 to summarise what you have found.

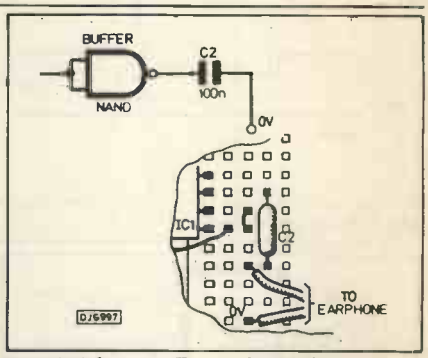


Fig.13 *Adapting Fig.12 for audio output.*

USING ASTABLES

There are two main uses for astables:

* clocks
* tone generators

Clocks generate pulses that are used to drive logic circuits. The clock acts like the conductor of an orchestra, beating out the time. The various parts of the circuit are like the members of the orchestra, each performing their own job at the correct time in relation to the clock and, therefore, to each other. In short, the clock *coordinates the operation* of the parts of the circuit, as well as *setting* the rate at which the system does its job.

An example to illustrate the use of a clock is a circuit to operate traffic lights (Fig. 14). The clock might run at 0.1Hz. The clock drives a counter, which counts the pulses from the clock. The counter continuously counts from 0 to 15, then resets to 0 again. Sub-circuits connected to the counter detect when the counter reaches different counts. These count-detecting sub-circuits switch on the coloured lamps. The count-detector which controls the red lamp switches on at count 0 and off at count 4. The lamp is on for four counts. At 0.1Hz (10 seconds per count), the lamp is on for 40 seconds. The other count-detectors act as shown in the figure to produce the familiar

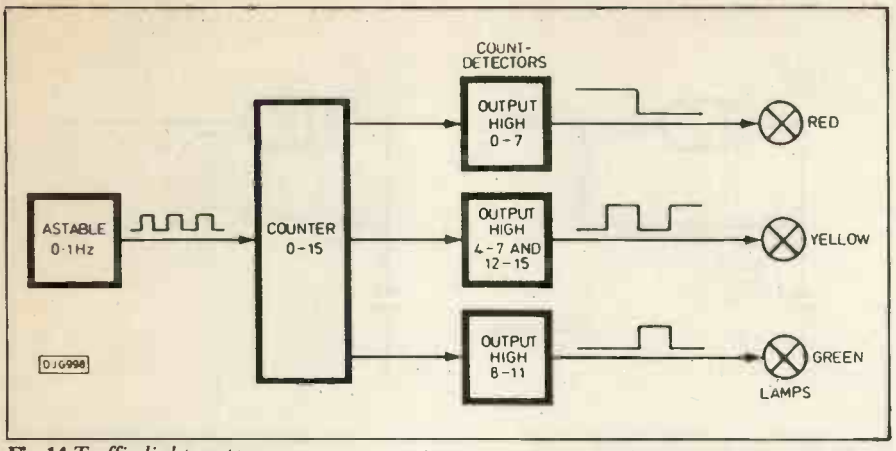


Fig.14 *Traffic light system.*

traffic-light sequence. By adjusting the frequency of the clock, we can make the lights go through their sequence faster or slower. By altering the times (=counts) at which the lamps are switched on and off, we can alter the timings of the individual stages of the operation. For example, if the lamps are where a minor road crosses a busy main road, we can arrange that the green lights on the main road are on for longer. Fig. 14 is a *system diagram*. It does not show how we wire up the counter, or the logic used for the count-detectors. Next month we look at this circuit in more detail.

A microcomputer is another example of a system that uses a clock. The clock is usually connected directly to the microprocessor, and possibly to other sections of the circuit – to synchronise the actions of all sections. The logic circuits inside the microprocessor are coordinated by the stream of pulses from the clock in a way similar to that described for the traffic light system. They perform their operations in a definite order: put an address on the address bus, read the data from memory after it has been put on the data bus, send this to the instruction register, carry out the instruction (several timed stages required for this), and so on. Many different operations have to be performed at high speed, in the correct order and at the right times (measured in microseconds!). Consequently, one difference between a microcomputer and a traffic lights system is that the clock of a microcomputer operates at very high speed, usually 1MHz or more.

TIMING PRECISION

You may have noticed that investigations with monostables and astables do not always give *precisely* the timings we would expect by calculating time constants. This is because resistances are usually precise only to 5% and capacitances only to about 10% (electrolytic capacitors may be precise only to 20%). The frequency of a resistor-capacitor astable is as precise as the *least* precise of its component – usually its capacitor. This is useless for many purposes – such as driving a digital clock. One way around the problem is to have a variable resistor in series with the fixed resistor so that the circuit can be 'tuned' to the required frequency. Even then, changes in temperature alter resistance and capacitance, and capacitance may change as the capacitor ages. Thus the frequency of the astable may alter after it has been tuned.

These changes are relatively small and do not matter for traffic-light controls and similar low-precision circuits. But for microcomputers (except perhaps the cheapest) and for other high-precision circuits (such as digital watches, the frequency-generation circuits of radio transmitters and receivers) it is essential to have much greater precision.

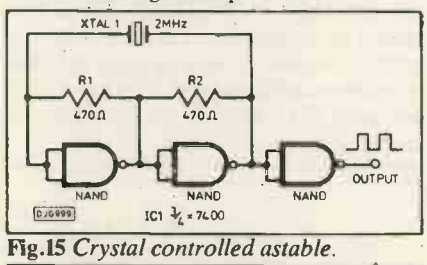


Fig.15 *Crystal controlled astable.*

One solution is an astable circuit such as that in Fig. 15. This relies on the frequency with which a specially-cut quartz crystal vibrates. Crystals can be cut and ground with a very high degree of precision at a relatively low cost. For example, for less than £1 you can buy a crystal cut to oscillate at precisely 32.768kHz. This particular frequency when divided by 2^{15}, gives a frequency of 1Hz, suitable for driving a digital watch or clock. When a voltage is applied to the crystal, it oscillates. When it oscillates an alternating voltage appears across it. The idea of the circuit is for the astable to supply an alternating voltage that is alternating at a frequency close to that of the crystal. The crystal vibrates, producing an alternating voltage *at its own precise frequency*. This *forces* the astable to vibrate at the same frequency.

We have said a lot about astables as clocks, but there is less to say about astables as tone generators. Perhaps the 'pips' we hear before a news bulletin are the simplest example of tone generation by an astable. Tone generation has obvious applications in synthesisers, electronic organs and similar musical instruments, as well as musical door-chimes and key-ring bleepers.

MARK AND SPACE

These terms have their origin in the signals sent by the earliest kinds of digital communications equipment, the teletype machines. When a signal consists of a series of pulses, the *mark* is the pulse itself, represented by a high level on the line. The interval between one pulse and the next is the *space*, represented by a low level (in some systems a negative voltage) on the line. The ratio between the length of a mark and the length of a space is the *mark/space ratio*. If mark and space are equal in length, the mark/space ratio is 1. There is more about this in the next section.

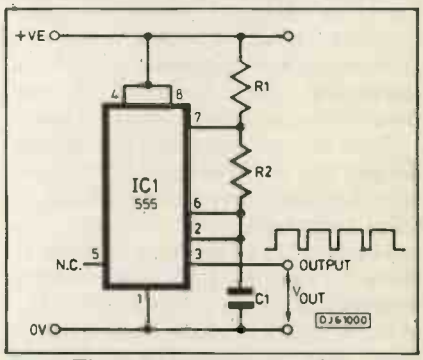


Fig.16 *The 555 wired as an astable.*

555 ASTABLE

The 555 timer ic can also be connected as an astable (Fig. 16). The differences in the wiring are that there is a resistor between pins 6 and 7, and that pin 2 (trigger) is connected to pin 6. The sequence of operations is similar to that of the monostable circuit (see above). The capacitor charges through R1 and R2, in series. Consequently, charging time (the length of the 'high' output period) depends on the total value of R1 and R2. When charging is complete, the output falls to 0V and discharging begins. Current flows from the capacitor, through R2 to pin 7. The discharging time (the length of the 'low' period') therefore depends on the value of R2, but not R1. When the capacitor is discharged and the voltage at pin 6 has fallen to V/3, this low voltage triggers the input at pin 2 and charging begins again. The circuit oscillates indefinitely.

The equations for charging and discharging are:

Charge time (output high) $t_1 = 0.693(R_1 + R_2)\,C$

Discharge time (output low) $t_2 = 0.693\,(R_2)\,C$.

Combining these to obtain the total period for charging and discharging we obtain:

$$t = 0.639\,(R_1 + 2.R_2)$$

So the frequency of the astable is given by:

$$f = \frac{1.44}{(R_1 + 2.R_2)\,C}$$

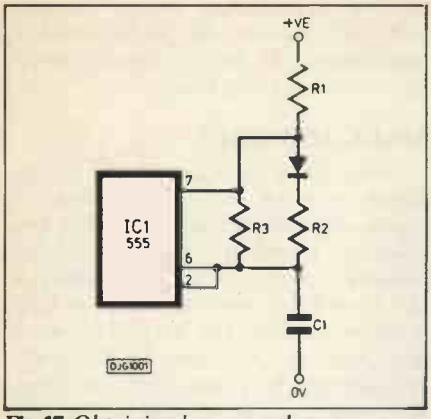


Fig.17 *Obtaining lower mark-space ratios.*

By suitable choice of resistors and capacitors the frequency of the astable can be set over a very wide range, from less than 1Hz to several hundred kilohertz. The 555 is equally useful as a clock or as a tone generator. As with the monostable, the frequency is not affected by variations in supply voltage.

Since the capacitor charges through R1 and R2, but discharges through R2 only, charging (high output) takes longer than discharging (low output). This makes it easy to set the mark/space ratio. However, since $R_1 + R_2$ is unavoidably greater than R_2 alone, the circuit of Fig. 16 must always produce a mark that is longer than the space. The mark-space ratio is always greater than 1. Using a diode in the circuit, as in Fig. 17, prevents discharge through R2. Instead, discharging occurs through R3, which has a higher resistance than R2. Discharging is prolonged and mark/space ratios of 1 or less may be obtained.

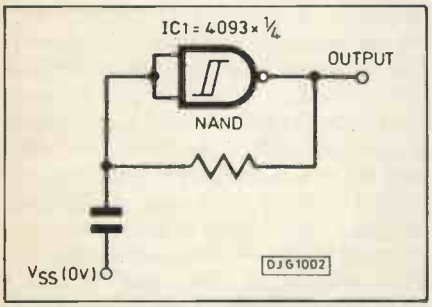


Fig.18 *Astable using a Schmitt trigger gate.*

SCHMITT TRIGGER ASTABLE

Fig. 18 shows a very simple but extremely useful astable based on the cmos 4093 Schmitt trigger ic. Its cost is low and it takes up little room on the circuit board. Fig. 19 shows how it works. At stage (a) the capacitor is discharged, inputs to the gate are low and its output is therefore high. Current flows through R1, gradually charging the capacitor. When the voltage across the capacitor reaches the upper threshold (see last month's article), the inputs to the gate are effectively high and the output suddenly goes low (b). Now the capacitor is discharged through the resistor. The voltage falls until it reaches the lower threshold. At this point the inputs to the gate are effectively low (a). The cycle is repeated indefinitely.

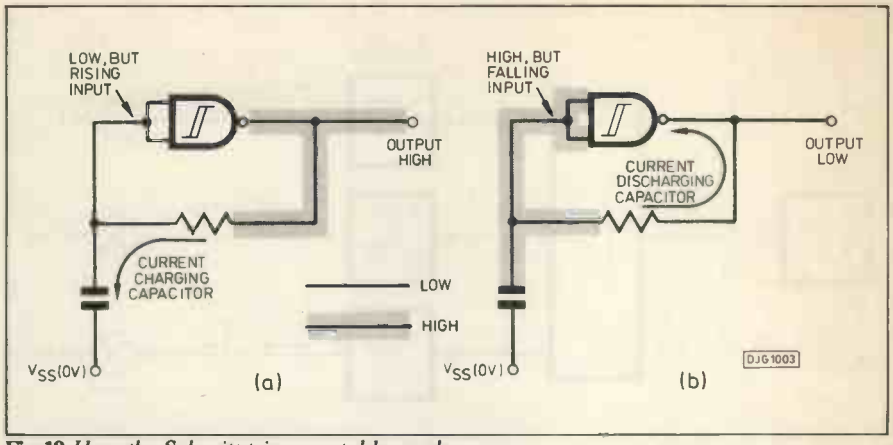


Fig.19 *How the Schmitt trigger astable works.*

QUESTIONS

1. Fig. 20 shows the circuit of a Schmitt trigger astable that has a mark-space ratio of 1/23. Explain how it works.
2. A monostable is to be built from a 555 ic to generate pulses approximately five seconds long. If the capacitor to be used has a capacitance of 1000μF, what value of resistor is required, to the nearest ohm? What is the nearest standard value of resistor obtainable? What period, in seconds, is obtained if this resistor is used?
3. The frequency of middle C on the musical scale is 256Hz. Calculate the values of capacitor to be used in a 555 astable circuit to give a note of this frequency, assuming that two resistors are each 56k. What is the mark/space ratio?

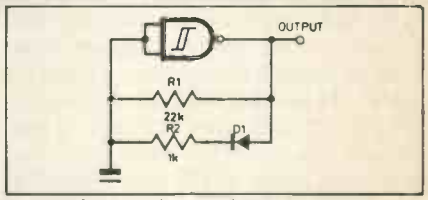


Fig.20 *Setting the mark-space ratio.*

MODULE OF THE MONTH

5. Programmable multivibrator

This module (Fig. 21), based on the 555 timer ic, is switched to operate either as a monostable or as an astable. Resistors and a capacitor of suitable value are inserted in the terminal sockets to set the timing. In monostable mode the circuit is triggered by a low input to the trigger socket. The reset button is used in monostable mode to return output to 0V. The module operates on any supply voltage in the range 4.5V to

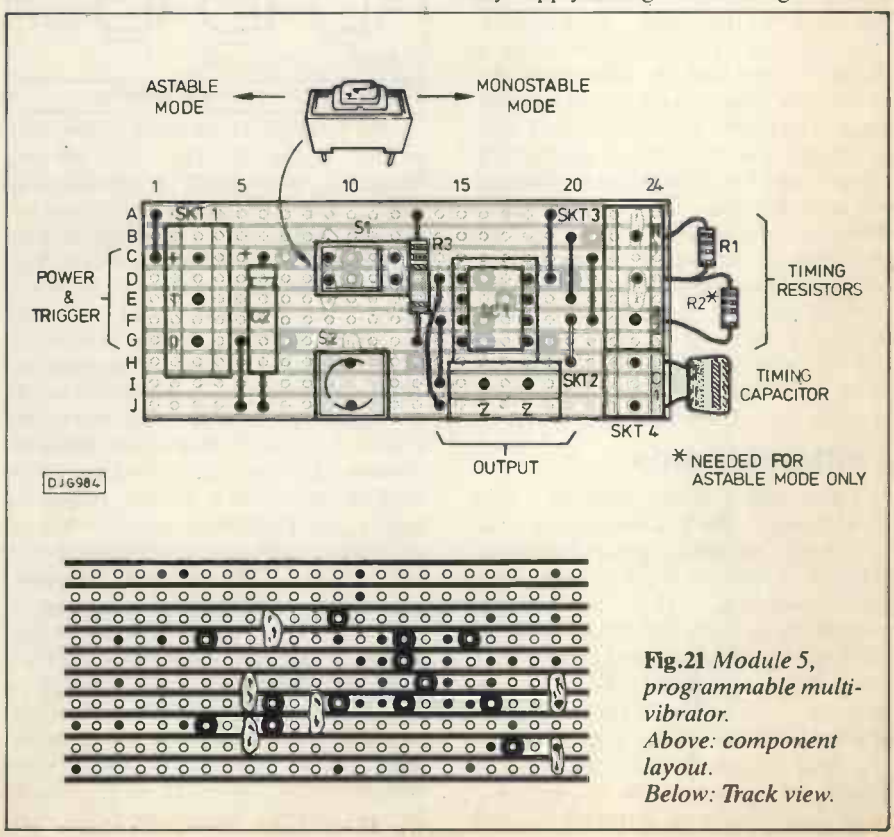


Fig.21 *Module 5, programmable multivibrator. Above: component layout. Below: Track view.*

16V. It requires 3mA when operated at 5V, or 10mA when operated at 15V.

Parts required

R1, R2 and C1, as required for timing
R3 100k, 0.25W
C2 22μ, 16V electrolytic
IC1 555 timer ic
8-pin dil socket
SKT1 pc terminal 3-way
SKT2 pc terminal 2-way
SKT3 pc terminal 3-way
SKT4 pc terminal 2-way
S1 dil switch single-pole double throw
S2 Push-to-make push-button, pcb type (pin spacing 5mm)
Stripboard Vero 14345

SYSTEM OF THE MONTH

Now that we have several modules to hand, we can start putting them together to build electronic systems. Fig. 22 shows a timer system that turns on a led for a specified period when the button is pressed. You don't *really* need a debounced key for this – a touch-plate wired to the trigger input of module 5 should do. Try designing and building your own system from the five modules now published.

Answers to questions

Don't look until you need to!

Pulse generator

Fig. 23(a) shows the normal state. At (b) the generator is triggered by a high-going input. Output goes briefly low until (c) when the capacitor has been discharged. Later when input goes low again, the capacitor becomes re-charged, but this does not affect the output. This is because low-low and low-high both give a high output from a NAND gate. The way the other pulse generators work follows a similar sequence.

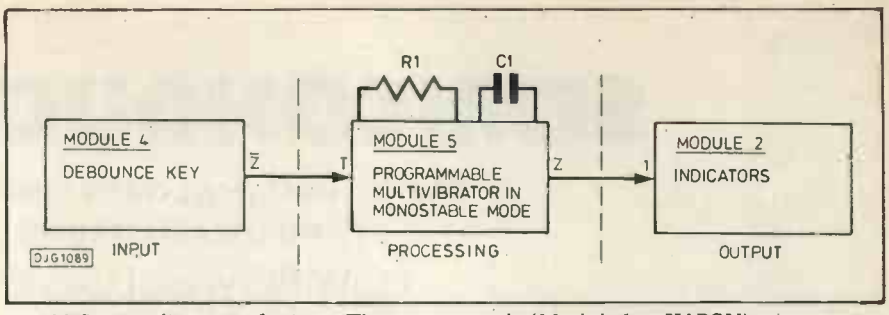


Fig. 22 *System diagram of a timer. The power supply (Module 1 or 5V PSU) goes to all three modules, but is not shown here.*

Questions on p.40

1. During charging (output high = mark) current flows through both R1 and R2. In parallel, these have a combined resistance of 957 ohms. During discharge (output low = space) current flows through R1 only, since the way through R2 is blocked by the reverse-biased diode, D1. The resistance of R1 is 22000 ohms, so discharging takes about 23 times (22000/957) as long as charging. The mark-space ratio is 1:23.

2. $R = t/(1.1 \times C) = 5/(1.1 \times 100 \times 10^{-6}) = 5/(1.1 \times 10^{-4}) = 45455$ ohms. Nearest standard (E12) is 47k. Frequency is $f = 1.1 \times 47 \times 10^{3} \times 100 \times 10^{-6} = 5.17$s.

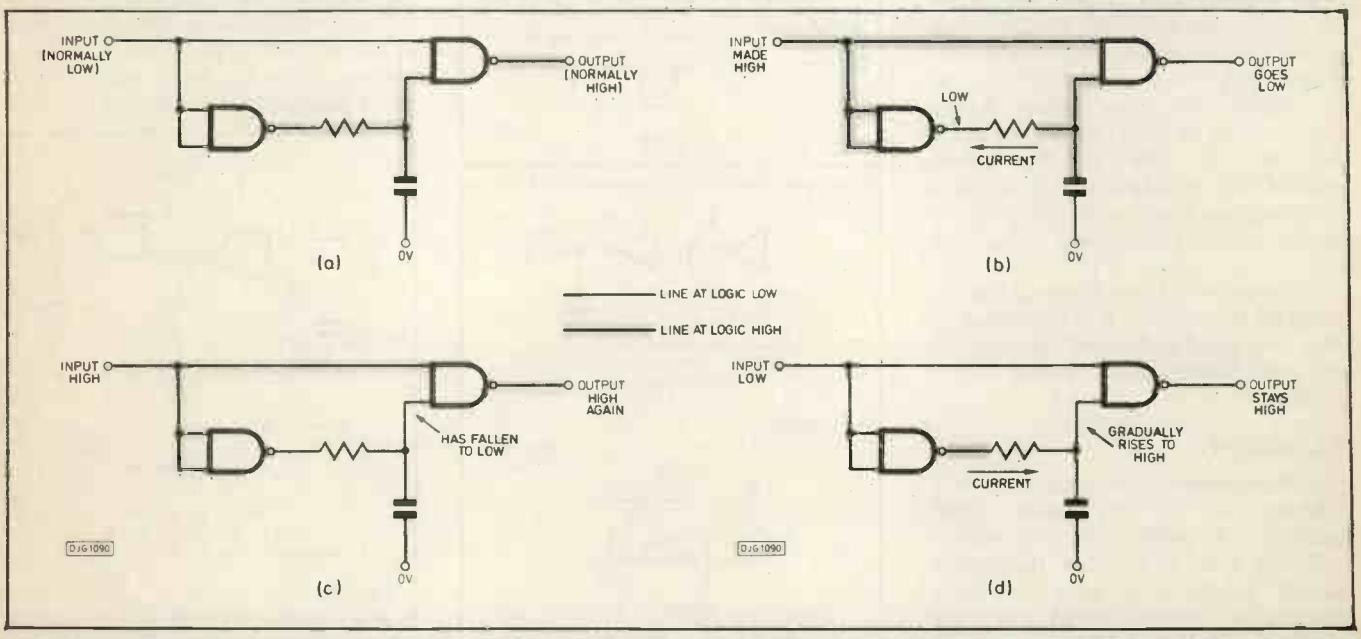
3.
$$C = \frac{1.44}{(R_1 + 2.R_2)f}$$

$$C = \frac{1.44}{168000 \times 256}$$

$$C = 33.5 \times 10^{-9}\text{F} = 33.5\text{nF}$$

Charging is through 112k, while discharging is through 56k. The mark-space ratio is 2:1.

Next month, in Part Four we examine digital circuits, their design problems, sequencing logic, counters and number systems.



STRIKING LOGIC

During the summer I spent a couple of weeks brushing up on my micros, omegas and the rest of the Greek alphabet. Simultaneously, while on the fascinating Aegean island of Thassos, I carried out healthy research into solar power and wave power. My wife thought I was just sunbathing and swimming.

Nonetheless, Greece does go in for alternative forms of energy harnessing, as is evident from the widespread use of roof-mounted solar panels for heating water in both domestic and business premises. So far, though, on none of my "research" trips to Greece have I seen evidence of electric power being generated by solar cells.

However, all their energy costs could surely be fractionalised if the dramatic power of the thunderstorms we witnessed could be harnessed. For five nights running, the sky was repeatedly ripped through to daylight conditions as strike after strike was flung by Zeus from one horizon to the other. Tourist mortals could only stand in awe at such mythological extravagance – except for a certain Ed who was wondering how many nicad PP3s could be recharged by each bolt.

My meteorological books don't give the answer directly, but it appears from various sources that storms have been known to consist of 7000 lightning strikes, and that the probable maximum figures for individual lightning discharges are 250,000 amps, 100 million volts, 400 coulombs, 25 billion watts, 32 million horsepower, 4 billion joules.

That's nearly as powerful as Ouzo! **Ed.**

SEMICONDUCTORS

PART 11: PRACTICAL CMOS DESIGNS

BY ANDREW ARMSTRONG

LOW POWER LOGIC

The characteristics of cmos logic make it ideal for many home construction projects. To get the best from cmos it is necessary to consider its electrical characteristics as well as its logic functions.

CMOS logic, and in particular 4000 series cmos, is a good type of logic for a wide range of applications. Historically ttl has been more widely used, partly because engineers were already familiar with it, and partly because it works at higher speed than 4000 series cmos. With the advent of the faster cmos families, this is less true. The 74HCT family is interchangeable with 74 series ttl in almost all situations. Only the specialised ttl families show any performance advantages over cmos, though 74LS ttl is still cheaper than the cmos alternative.

The 4000 series is a low cost cmos logic family which can do some things that other low cost logic families cannot. Its high input impedance and very low static power consumption make it ideal for battery powered low speed circuitry, or in applications where power supply consumption or heat dissipation are a problem. So low is the power consumption of 4000 series cmos that if an ic becomes warm to the touch there is almost certainly something wrong with it.

Table 8 shows the family characteristics for 4000 series cmos. It is interesting to note that speed and output drive current are both dependant on power supply voltage.

CLOCKING

Combinational logic circuits are very tolerant of different input signal qualities. A cmos gate will accept voltages well away from the power supply rail levels as logic 1 or 0 as appropriate, and little harm is done if the signal is slow in switching between the two levels. This is not so in the case of clocked circuits. For example, a normal cmos counter operating on a 12V supply cannot be successfully clocked by the output of a 741 running on the same supply, though a combinational circuit will cheerfully accept the output as one logic level or the other. The counter may count between some states and others, but may fail to count when, perhaps, the most significant bit should have switched.

There are several answers to this problem. One is to use a faster opamp or comparator to drive the counter. Many bifet opamps have fast enough slew rates to clock cmos counters, so this idea works well. Another possible solution is to use a Schmitt trigger gate to improve the waveform, and this is also an effective solution.

The third possible answer is to use cmos counters with Schmitt trigger inputs. These will accept very grotty signals and still work well. An example of this type of counter is the 4000.

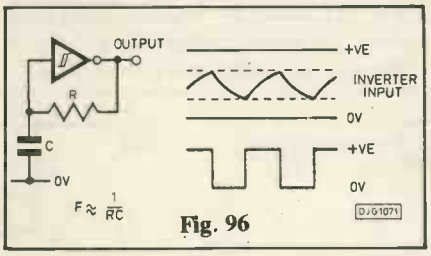


Fig. 96

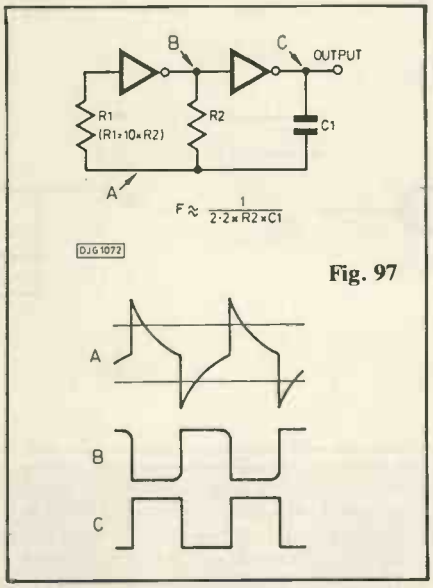


Fig. 97

APPLICATION CIRCUITS

One of the most common requirements in logic circuitry is to provide a clock signal. The circuit of Fig. 96 shows a simple design of clock oscillator, using a Schmitt inverter such as the 40106B. A Schmitt NAND gate such as the 4093 could be wired as an inverter to perform this function, in which case the second input could be used to gate the oscillator off. This simple design of oscillator suffers from the problem that its frequency depends substantially on the precise voltage of the Schmitt levels. This is a parameter which can vary from chip to chip, so the reproducibility of a design is not very good.

The design shown in Fig. 97 is more reproducible because the capacitor charges and discharges over a wider voltage range, while the uncertainty of switching point (controlled by the voltage gain of the gates) is small. The uncertainty is thus a small percentage of the total voltage swing, so the frequency of the oscillator is well defined. A gated version of this oscillator, using 2 input NOR gates, is shown in Fig. 98.

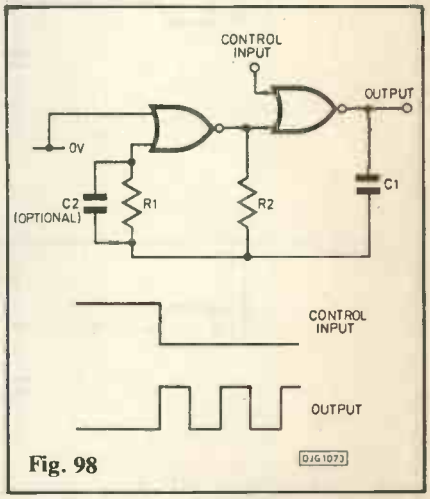


Fig. 98

An improved version of this oscillator is shown in Fig. 99. This circuit uses an extra inverter to provide a clean squarewave to drive the timing resistor, R2. The disadvantage associated with this is that there is one more propagation delay, which limits the maximum frequency.

In all the oscillator designs, except that of Fig. 99, the voltage on the capacitor swings outside the power supply rails. It is only able to do this because R1 prevents the input

D.C. CHARACTERISTICS V_{SS}=0V; for all devices unless otherwise specified

Parameter	V_{DD} V	Symbol	T_{amb} (°C) −40 min.	−40 max.	+25 min.	+25 max.	+85 min.	+85 max.	Unit	Conditions
Quiescent device current										
	5		–	1,0	–	1,0	–	7,5	μA	
Gates	10	I_{DD}	–	2,0	–	2,0	–	15,0	μA	
	15		–	4,0	–	4,0	–	30,0	μA	
	5		–	4,0	–	4,0	–	30	μA	
Buffers, flip-flops	10	I_{DD}	–	8,0	–	8,0	–	60	μA	
	15		–	16,0	–	16,0	–	120	μA	all valid input combinations;
	5		–	20	–	20	–	150	μA	$V_I=V_{SS}$ or V_{DD}
MSI	10	I_{DD}	–	40	–	40	–	300	μA	
	10		–	80	–	80	–	600	μA	
	5		–	50	–	50	–	375	μA	
LSI	10	I_{DD}	–	100	–	100	–	750	μA	
	15		–	200	–	200	–	1500	μA	
	5		–	0,05	–	0,05	–	0,05	V	
Output voltage LOW	10	V_{OL}	–	0,05	–	0,05	–	0,05	V	$V_I=V_{SS}$ or V_{DD}; I_O <1 μA
	5		4,95	–	4,95	–	4,95	–	V	
Output voltage HIGH	10	V_{OH}	9,95	–	9,95	–	9,95	–	V	$V_I=V_{SS}$ or V_{DD}; I_O <1 μA
	15		14,95	–	14,95	–	14,95	–	V	
	15		–	1,5	–	1,5	–	1,5	V	V_O=0,5V or 4,5V
Input voltage LOW	15	V_{IL}	–	3,0	–	3,0	–	3,0	V	V_O=1,0V or 9,0V I_O <1 μA
(buffered stages only)	15		–	4,0	–	4,0	–	4,0	V	V_O=1,5V or 13,5V
	5		3,5	–	3,5	–	3,5	–	V	V_O=0,5V or 4,5V
Input voltage HIGH	10	V_{IH}	7,0	–	7,0	–	7,0	–	V	V_O=1,0V or 9,0V I_O <1 μA
(buffered stages only)	15		11,0	–	11,0	–	11,0	–	V	V_O=1,5V or 13,5V
Input voltage LOW	5		–	1	–	1	–	1	V	V_O =0,5V or 4,5V
unbuffered stages	10	V_{IL}	–	2	–	2	–	2	V	V_O=1,0V or 9,0V I_O <1 μA
only)	15		–	2,5	–	2,5	–	2,5	V	V_O=1,5V or 13,5V
Input voltage HIGH	5		4	–	4	–	4	–	V	V_O=0,5V or 4,5V
(unbuffered stages	10		8	–	8	–	8	–	V	V_O=1,0V or 9,0V I_O <1 μA
only)	15		12,5	–	12,5	–	12,5	–	V	V_O=1,5V or 13,5V
Output (sink)	5		0,52	–	0,44	–	0,36	–	mA	V_O=0,4V; V_I=0 or 5V
current LOW	10	I_{OL}	1,3	–	1,1	–	0,9	–	mA	V_O=0,5V; V_I=0 or 10V
	15		3,6	–	3,0	–	2,4	–	mA	V_O=1,5V; V_I=0 or 15V
Output (source)	5		0,52	–	0,44	–	0,36	–	mA	V_O=4,6V; V_I=0 or 5V
current HIGH	10	$-I_{OH}$	1,3	–	1,1	–	0,9	–	mA	V_O=9,5V; V_I=0 or 10V
	15		3,6	–	3,0	–	2,4	–	mA	V_O=13,5V; V_I=0 or 15V
Output (source) current HIGH	5	I_{OL}	1,7	–	1,4	–	1,1	–	mA	V_O=2,5V; V_I=0 or 5V
Input leakage current	15	$\pm I_{IN}$	–	0,3	–	0,3	–	1,0	μA	V_O=0 or 15V
3-state output leakage; current	15	I_{OZH}	–	1,6	–	1,6	–	12,0	μA	output returned to V_{DD}
3-state output leakage current; LOW	15	$-I_{OZL}$	–	1,6	–	1,6	–	12,0	μA	output returned to V_{SS}

Table 8. *4000 series cmos data.*

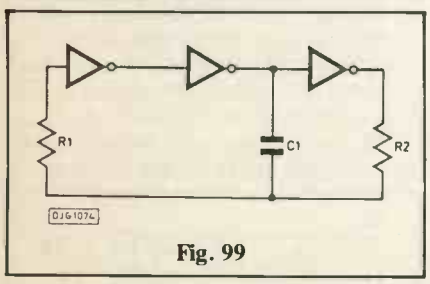


Fig. 99

protection diodes of the cmos chip from clamping the voltage to one diode drop either side of the power supply rails. The design may work without R1, but the reproducibility is poor.

Not that in Fig. 98 the unused input of the first NOR gate is connected to 0V, to allow the other part of the gate to work as an inverter. The circuit would also work if the two inputs were connected together, but the switching would be less symmetrical due to interaction between the input mosfets.

Note also in Fig. 98 the optional extra capacitor, C2. This is helpful if R1 has a high value, say greater than 220k. It is to prevent high frequent noise pickup on the gate input, which may cause oscillation on edges.

Sometimes there are no spare inverting gates in a design, but there are spare analogue switches because, perhaps, only one quarter of a 4016 has been used to make a sample and hold gate. In this case it can be useful to make an oscillator using analogue switch parts. Fig. 100 and Fig. 101 show two such circuits. The design in Fig. 100 has good current sink capability but poor current source, and the design in Fig. 101 has good source and poor sink capability.

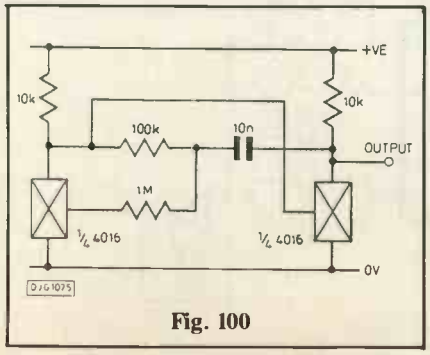


Fig. 100

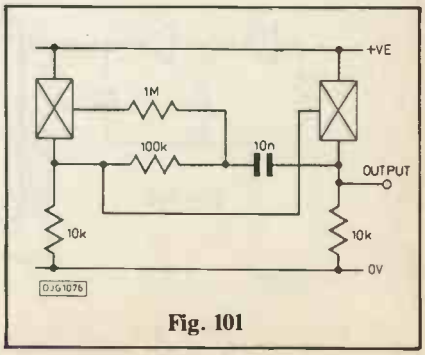


Fig. 101

CRYSTAL OSCILLATORS

In cases when the frequency accuracy or stability of normal r/c oscillators is not good enough, crystal oscillators are useful. CMOS gates can be used to make crystal oscillators. An example circuit is

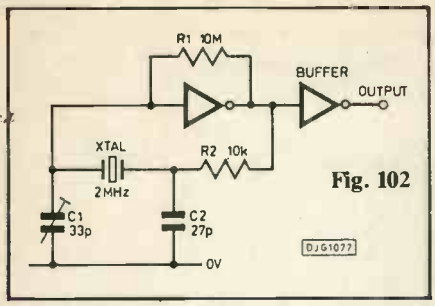


Fig. 102

shown in Fig. 102.

In this circuit, R1 biases the input of the inverter to prevent it drifting to logic 1 or logic 0 and just sitting there. C1 provides the standard loading for the crystal.. Many crystals are specified to oscillate at their resonant frequency when loaded by a capacitance of 20pF. If this capacitance is varied, the frequency may be adjusted *slightly* either side of the nominal frequency.

The purpose of R2 and C2 is to provide suitable loss and phase shift to make the pi network correctly, and to provide attenuation at higher frequencies or prevent the crystal oscillating at its third or fifth overtone. As a rule of thumb, R2 should be in the range 10k to 47k, and the phase shift given by R2 and C2 at the required operating frequency should be approximately 45° to 55°.

Some types of crystal do not work with ordinary buffered 4000 series cmos gates. Unbuffered A-series gates can sometimes work better in this application. This type of gate has also proved more appropriate in certain r/c clock oscillator applications, when required to run on a low supply voltage.

Certain ics, such as the 4060, incorporate inverters for use as oscillator elements on-chip. The circuitry of these inverters is optimised for use as an oscillator, and they normally work very well in this application.

SIMPLE DACS

Normally, a special ic is used for digital to analogue conversion, but because cmos outputs swing extremely close to the supply rails, it is possible to use them to switch resistors and perform digital to analogue conversion directly. Clearly, the resistor values used must not place too great a demand on the current sink or source capability of the cmos outputs, or else the voltage swing will be degraded.

The circuit of Fig. 103 shows a possible application circuit, which uses the R-2R-4R-8R principle to perform the digital to analogue conversion. The resistors are connected to the output of a counter which counts up, so the circuit shown will simply produce a staircase ramp.

A simple four-bit conversion can be useful in a number of applications, not demanding great precision or resolution. One example of this might be to provide the integral function in a two- or three-term control loop. Less esoteric applications would include providing indication on an analogue meter of the approximate value of the digital signal, and controlling a voltage controlled filter under digital control.

The principle shown in Fig. 103 is alright for up to four bits, but becomes impractical for larger numbers. An R-2R network as shown in Fig. 104 is suitable for more bits, and has the added advantage of equalising the loading on the cmos outputs. This avoids causing differences in the voltage swing due to different loading on the different outputs. A circuit of this type is certainly suitable for up to 6 bits.

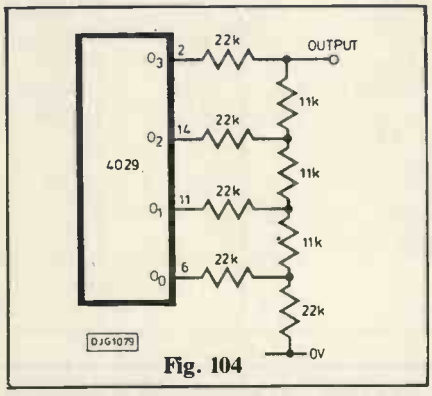


Fig. 104

CMOS is often used for small control circuits which receive disparate types of signal. One requirement which surfaces from time to time is to detect the presence of a clock edge and produce a short pulse. The circuit of Fig. 105 shows a completely reliable method of doing this, using an exclusive-OR gate, and a Schmitt trigger. The Schmitt trigger

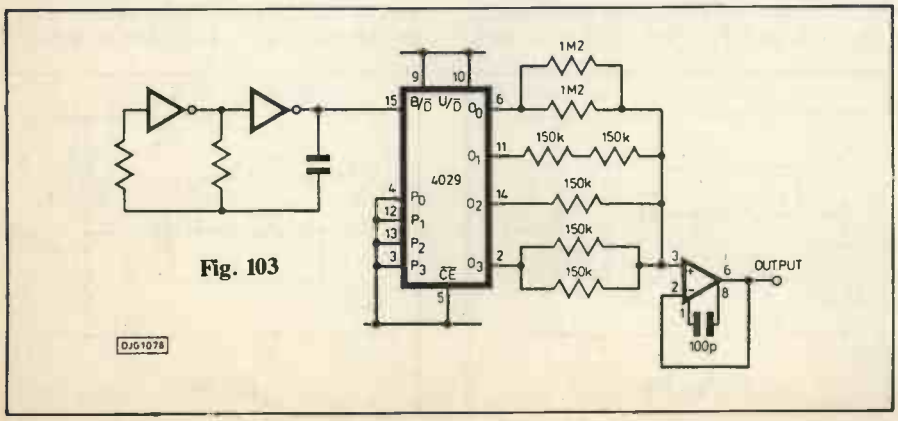


Fig. 103

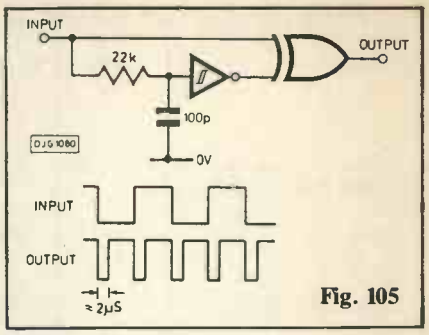


Fig. 105

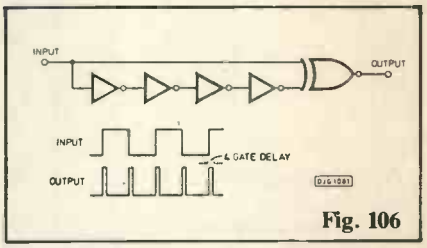


Fig. 106

switches cleanly when the r/c time constant charges to a certain level, so that if a clean input pulse is provided then a clean output pulse will be generated. If necessary the input signal could be cleaned up by a Schmitt trigger.

The circuit of Fig. 106 shows a means of doing this edge-detection without work very well, but it will produce an output pulse of a length equal to the extra gate delays imposed. Such a short pulse can be difficult to view on an oscilloscope when testing the circuit, and it can be degraded by the wiring in a system which is generally designed to work at low frequencies. It does however have the advantage that no passive components are required, and spare gate portions are used instead. For most hobbyist applications, the circuit of Fig. 105 is to be preferred.

The use of Schmitt triggers and r/c networks leads to the provision of a timed series of pulses on receipt of a clock edge. Such a system may be used, for example, to make sure that a control circuit has been reset and allowed to commence operation before its control outputs are enabled. The circuit in Fig. 107 performs this timing function.

This circuit does look rather clumsy, and a slightly purer design would use a

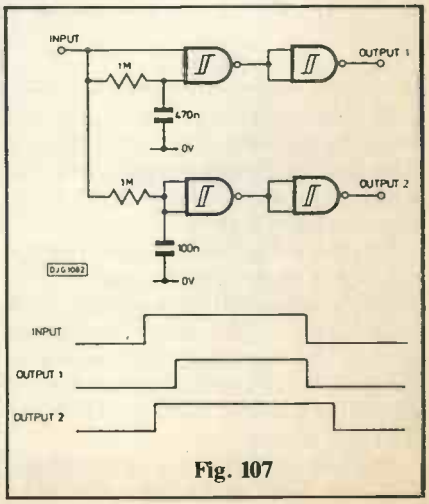


Fig. 107

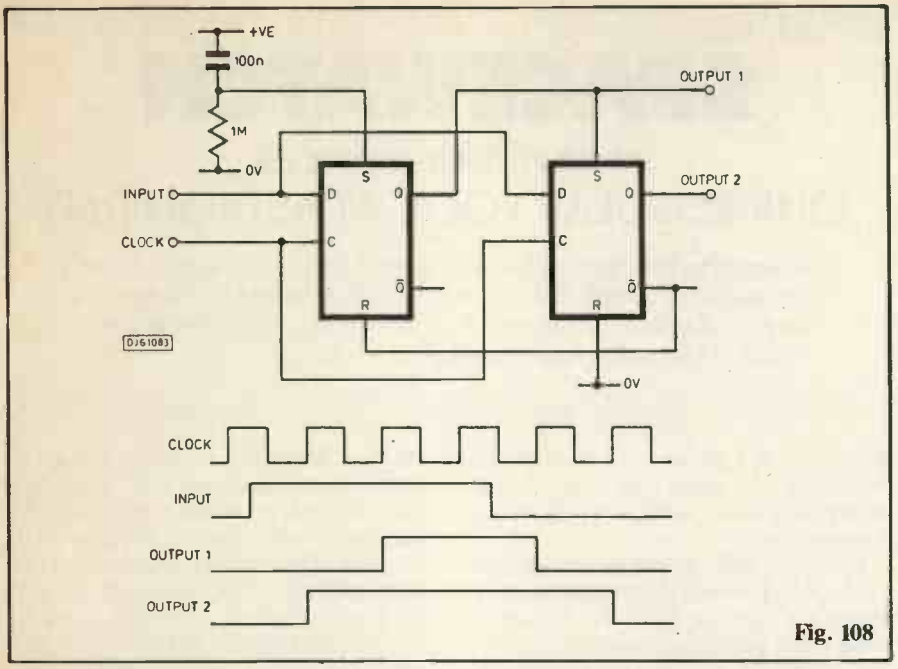


Fig. 108

system clock to clock flip-flops wired as shown in Fig. 108. This circuit costs no more than the one using passive components, and would take up less board space on a printed circuit board containing mostly ics. It is also less vulnerable to faults caused by incorrect component placement.

BATTERY BACKUP

Because of its low power consumption, cmos lends itself to battery operated and battery backed circuitry. In the circuit of Fig. 109, a rechargeable battery is shown, connected so as to retain the state of a counter when the main equipement power is removed. In this sort of application, it is envisaged that the main equipment power would operate current-hungry devices such as displays, which the battery could not power for very long.

In this circuit, shown only in outline, the display drives are powered by the battery as well to prevent the battery trying to power the rest of the circuit via the input protection diodes of the 4511s. The outputs of the 4511s do not source current, they only sink it, so the displays can be supplied from the main power supply and not be inconvenienced when it is switched off.

In this particular circuit I have provided some protection for the counter from clock pulses occurring when the main power is switched off, but spurious pulses could be allowed through as the power disappears. The unit in which this was used was under operator control, and the operator could not reach the count switches at the same time as switching off the mains, so that more extreme protection measures were not needed.

CMOS gates can give spurious outputs at power supply levels of just under three volts, so that if better protection from spurious pulses was required the input of the AND gate, which is running on the battery supply, should be driven from a comparator made with discrete components. The comparator should be designed to switch its output to logic 0 when the main power supply goes down to (say) 6V.

I have employed this circuit in practice, and it works very effectively. In the development stage, it provided me with an unintended demonstration of the static sensitivity of cmos gates. At first, the current consumption of the prototype was too low to measure conveniently. After some modification work, carried out in a hurry, and without using the anti-static wrist strap, the current consumption increased to the point where it would have flattened a rechargeable battery over a period of 24 hours. This level of current consumption would have passed unremarked in many designs, but it indicates the presence of damage which may later cause failure.

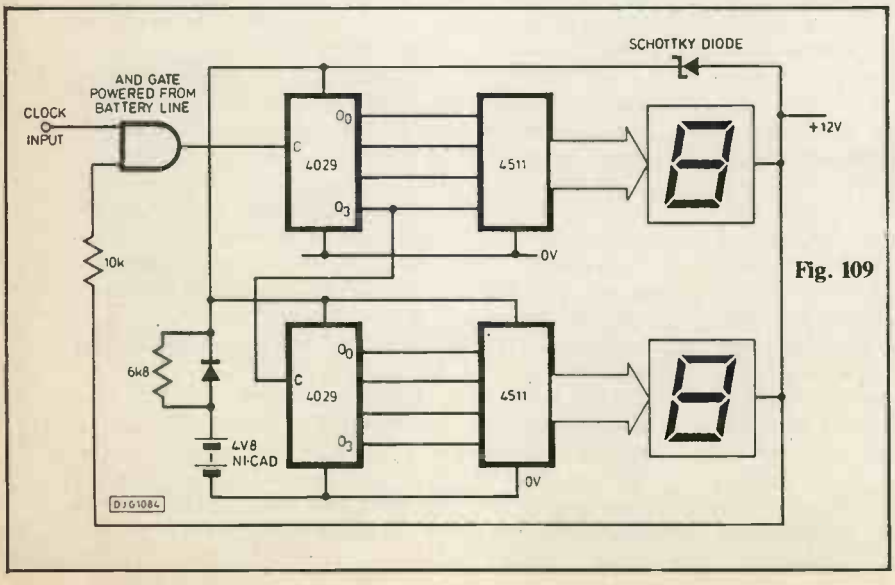


Fig. 109

HIGH SPEED CMOS

Possibly the standard logic of the future will be the 74HC range of cmos. This logic family has the cmos advantage of low power consumption at low frequencies, but it operates much faster than 4000 series cmos. No advantage is completely without drawbacks, of course, and in the case of 74HC the drawback is that it requires a 5V power supply. This restriction may not be convenient in low speed circuits incorporating analogue and digital circuitry but it is not usually a problem.

Most of the design considerations which relate to 4000 series cmos will apply to 74HC. In addition, more attention should be given to the routing of ground connections, and to decoupling at regular intervals on a pcb. The edge speeds of 74HC are much greater than with 4000 series cmos, and interference may be radiated from high speed circuits unless they are in screened boxes.

74HC cmos is less likely to be used by the home constructor, so I shall not delve too deeply into the subject. Briefly, the operating speed is similar to that of LS-ttl, but the power consumption is much lower, at .003μW for a quiescent gate as against 8mW for an LS-ttl gate in quiescent state. The current consumption of a 74HC gate is about the same as that for the LS-ttl gate at a frequency of approximately 20MHz.

The output sink and source current for high speed cmos is typically 4mA. Output rise and fall times are around 20ns and propagation delays are around 23ns. These figures are similar to those for LS-ttl, and, as the pin outs of these and 74LS ttl are identical, 74HC logic is beginning to take up where 74LS left off.

That's your lot for this month, folks. Next month I will be looking at some of the design criteria for using emitter coupled logic (ecl). PE

SPACEWATCH

BY DR PATRICK MOORE CBE

OUR REGULAR LOOK AT ASTRONOMY

Among the space probes sending back images or due to do so are several old ones which could be reactivated for further service. Remember Giotto? And the supernova in the large cloud of Magellan has acquired a pair of halos.

So far there continues to be good news from Voyager 2, on its way to Neptune; images of both Neptune and Triton have been obtained, though as yet they do not show much, and the spacecraft remains in its 'cruise mode'. Long before it reaches Neptune, the Russian Phobos probes will have arrived at their target, and we hope for exciting news early in the New Year.

The great Australia Telescope has been officially opened by the Prime Minister of Australia, Mr Hawke, at a ceremony at Culgoora, in New South Wales. This is a major step forward, and shows that radio astronomy has really 'come of age'.

Everyone will remember the Giotto probe, which passed through the head of Halley's Comet in 1986 and obtained close-range pictures of the nucleus. The space-craft survived, and should be back in the vicinity of the Earth by the middle of 1990. It has been announced that the ESA team now plans to try to re-activate it, and according to the Project Scientist, Rüdeger Reinhard, it may be that most of the experiments are still functional. The attempt at re-activation will be made early in 1990. If all goes well, Giotto will pass within 22,000 km of the Earth on 2 July 1990, and may be sent off for a second comet encounter, possibly with Comet Grigg-Skjellerup on 10 July 1992. The two Japanese comet probes, Sakigake and Suisei, are in working order, and may also be used for further missions; Sakigake to Comet Honda-Mrkos-Pajdusaková on 3 February 1996,

The Sky This Month

The nights are darker now (remember that Summer Time ends on October 23) and the stars are coming into their own. This is also rather a good time for the planets.

Mercury is at inferior conjunction on October 11, but then moves rapidly away from the Sun to western elongation on the 26th. This is the best morning apparition of the year so far as we are concerned, and Mercury should be a naked-eye object from about the 18th through to the first week in November. At its brightest, on the 26th, the magnitude is −0.5, brighter than any star visible from Britain with the exception of Sirius. Against its light background Mercury can be elusive, but once you have identified it you will wonder how you could have overlooked it!

Venus, magnitude −4, is also in the dawn sky but is quite impossible to miss. It is now past dichotomy, and the phase increases from 70 per cent to 80 per cent during October, so that the shape is clearly gibbous – as binoculars will show, even though no telescope will show much on the planet's cloud-hidden disk.

Mars is only just past opposition, and remains excellently placed in the evening sky throughout October, though it fades from magnitude −2.6 to −1.9 – still considerably brighter than Sirius. It is virtually 'full' at the start of the month, but by the end of October has changed to 95 per cent, so that telescopes show it to be appreciably gibbous. The apparent diameter decreases from 23 seconds of arc to 19 seconds of arc, so that even a small telescope will continue to show the main dark markings such as the V-shaped Syrtis Major – unless, of course, clouds in the Martian atmosphere obscure the surface, which is quite possible. If you use say a 6-inch telescope under good conditions, and see very little, you may be sure that a dust-storm is in progress.

Jupiter, too, is an evening object, moving slowly westward in Taurus; the magnitude is −2.7, so that it now slightly outshines Mars. It remains visible throughout the hours of darkness. Saturn passed opposition in June, and is now past its best for this year, but remains visible for some time after sunset, though it is inconveniently low down. The variable satellite Iapetus is east of the planet, and not too easy to see, but small telescopes will show Titan, which is at eastern elongation on the 6th and 22nd and at western elongation on the 14th and 30th.

The Moon is new on October 10, and full on October 25. There are no solar or lunar eclipses this month.

The main meteor shower during October is that of the Orionids, associated with Halley's Comet. They are active between October 16 and 27, and reach maximum on the 22nd, with a zhr of about 25, but obviously the Moon will interfere, and it is not likely that the Orionids will put on a really impressive display this year. Some Taurids are also on view; these are slow meteors, associated with Encke's Comet. They are at their best early in the month, when the Moon is not obstrusive.

The evening sky is dominated by Pegasus, with its famous Square; look for the Andromeda Galaxy, M.31, which is a naked-eye object, though even with a moderate-sized telescope it is not spectacular. It is just over 2,000,000 light-years away. Novæ have often been seen in it, and there has been one supernova, S Andromedæ, which blazed out in 1885 and reached the fringe of naked-eye visibility. Unfortunately we do not know a great deal about it. In 1885 it was not even known that M.31 is an external system, and most astronomers tended to agree with J.E. Gore that in all probability 'the star had nothing to do with the Nebula'. How wrong he was!

Ursa Major is at its lowest in the north, so that the W of Cassiopeia is almost overhead. The "Summer Triangle" is still on view, but Altair at least is becoming low. In the east we can now see some members of Orion's retinue, including the Pleiades star-cluster, and Orion itself has risen by the early hours of the morning.

and Suisei to Comet Giacobini-Zinner on 24 November 1998. Of the Halley armada, only the two Russian probes, Vega 1 and Vega 2, appear to have come to the end of their careers.

SUPERNOVA LIGHT ECHOES

The appearance of the supernova 1987A, in the Large Cloud of Magellan, was one of the most important astronomical events for many years; it was indeed the first naked-eye supernova since Kepler's Star of 1604. At its brightest, around May, it dominated the whole area of the sky, and it was intensively studied from all observatories from which it could be seen; it was sad that it was too far south to be seen from anywhere in Britain or Europe. It was remarkable inasmuch as the progenitor was not a red supergiant, but a blue star – which explained why it was underluminous by supernova standards; there was less surface area to expand.

Recent photographs, taken by David Malin with the 3.9-metre Anglo-Australian Telescope at Siding Spring in New South Wales, show 'light echoes' in the form of two complete luminous rings surrounding the old star. They have been known for several months, but Malin's pictures are the first to show them as complete circles. They are caused by two thin sheets of gas, each of which contains 'dust', lying between the supernova and ourselves; the dust-grains are being illuminated by light which left the supernova at the height of the outburst. In other words, they are not new – we just could not see them before, because there was nothing to make them shine. The inner ring is about 476 light-years in front of the supernova, and the outer ring about 1200 to 1350 light-years in front of the supernova; the outer ring is tilted, and seems to be centred on the Tarantula Nebula, 30 Doradûs, a region where many massive stars have formed, and previous supernovæ must have flared up every few centuries. The Tarantula Nebula is truly impressive; if it were as close to us as the Orion Nebula (around 1500 light-years) it would cast shadows.

What will happen to the supernova now? It will presumably form either a pulsar or a black hole; we do not yet know. A pulsar may be the more likely candidate, but as yet no signs of it have been detected, and we must simply wait and see. At least astronomers have cause for satisfaction. If we cannot have a supernova in our own Galaxy, then at least a supernova in the Large Cloud is the next best thing. **PE**

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BLAME APORSCHIONED

I for one would have expected that an upmarket car like a Porsche would be free from electronic problems.

Not so, it seems from the story being laughed at around the office. My reliable informant knows of a certain Porsche owner who took the car into the West End of London on a shopping spree around stores of the Arabian Empire. The only car park available was one into which cars are crammed, and the keys left in the ignition so that attendants can move cars if necessary.

This posh parks the car, leaves the keys in, but by habit activates the door locks by means of the remote control sported by such elite jalopies. Unkown to owner though, the system appears to have a design fault and other Porsches were nearby. Neither expecting, nor hearing, the muted clunk-click of other Porsche locks engaging in the no-entry position, the owner departs the scene.

Only on returning some hours later is our friend made aware of the chaos caused to the harassed attendants and other Porsche owners who had also left their keys, and their own remote controls, very safely in the cars.

Only a few days after being regaled by the Porsche problem I overheard a conversation in another car park.

"... that learner", says one man to the other, "then slammed into the back of me. So I jumped out to give him a mouthful and accidentally pressed the central door locking button. It's one of those that automatically winds up the windows as well. So up goes the window, and traps my hand. Pulling free, I stumbled onto the door, closing it. And my keys were inside - with all the doors now locked!"

Technology still needs taming... **Ed.**

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BD13	12	30 watt reed switches, it's surprising what you can make with these – burglar alarms, secret switches, relay etc etc.
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BD34	48	2 meter length of connecting wire all colour coded
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BD45	1	24hr time switch, ex-Electricity Board, automatically adjust for lengthening and shortening day. Original cost £40 each
BD49	10	neon valves, with series resistors, these make good night lights
BD56	1	mini uniselector, one use is for an electric jigsaw puzzle, we give circuit diagram for this. One pulse into motor, moves switch through one pole
BD59	2	flat solenoids – you could make your multi-tester read AC amps with this
BD67	1	suck or blow operated pressure switch, or it can be operated by any low pressure variation such as water level in water tanks
BD91	2	mains operated motors with gearbox. Final speed 16rpm. 2 watt rated
BD103A	1	6 750MA power supply, nicely cased with input and output leads
BD120	2	stripper boards each contains a 400v 2A bridge rectifier and 14 other diodes and rectifiers as well as dozens of condensers etc
BD122	10m	twin screened flex with white pvc cover
BD128	10	very fine drills for p.c.b. boards etc. Normal cost about 80p each
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BD139	6	microphone inserts – magnetic 400 ohm also act as speakers
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BD268	1	mini 1 watt amp for record player. Will also change speed of record player motor
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BD283	3	mild steel boxes approx. 3" x 3" x 1" deep – standard electrical
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24 HOUR TIMESWITCH 16A C/O contacts, up to 6 on-offs per day. Nicely cased for wall-mounting. Price £8.00. Ref. 8P6.

CAPICITOR BARGAIN Axial ended 4700 uf at 25V. Superior Jap make, normally 50p each, you get 4 for £1.00 Ref. 613.

PIEZO ELECTRIC FAN an unusual fan, more like the one used by Madame Butterfly, than the conventional type, it does not rotate. The air movement is caused by two vibrating arms. It is American made, mains operated, very economical and cause's no interference. So it is ideal for computer and instrument cooling. Price is only £1 each. Ref BD598

SPRING LOADED TEST PRODS – heavy duty, made by the famous Bulgin company. Very good quality. Price four for £1. Ref. BD597.

CASE WITH 13A PRONGS – to go into 13A socket, nice size and suitable for plenty of projects such as car battery trickle charger, speed controller, time switch, night light, noise suppressor, dimmers etc. Price – 2 for £1. Ref. BD565

ALPHA-NUMERIC KEYBOARD – this keyboard has 73 keys arranged in two groups, the main area is a QWERTY array and on the right is a 15 key number pad, board size is approx. 13" x 4" – brand new but offered at only a fraction of its cost namely £3 plus £1 post Ref: 3P27.

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3-30V VARIABLE VOLTAGE POWER SUPPLY UNIT – with 1 amp DC output. Intended for use on the bench for experimenters, students, inventors, service engineers etc. This is probably the most important piece of equipment you can own. (After a multi range test meter). It gives a variable output from 3-30 volts and has an automatic short circuit and overload protection, which operates at 1.1 amp approximately. Other features are very low ripple output, a typical ripple is 3mV pk-pk, 1mV rms. Mounted in a metal fronted plastic case, this has a voltmeter on the front panel in addition to the output control knob and the output terminals. Price for complete kit with full instructions is £15. Ref. 15P7.

TRANSMITTER SURVEILLANCE (BUG) – tiny, easily hidden, but which will enable conversation to be picked up with FM radio. Can be housed in a matchbox, all electronic parts and circuit. Price £2. Ref. 2P52.

PLD PROGRAMMER

Part One By Chris Kelly and Steve Pattinson

AN OFFER YOU CAN'T RE-FUSE

This programmable logic device programmer works with the Philips PLS103 pld, one of the most straightforward logic devices to program. This is a good low-cost starting point for understanding plds and their uses.

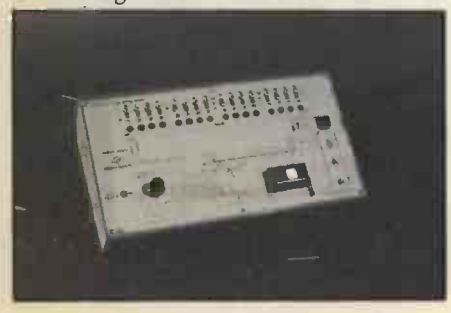
Programmable logic devices, plds, are logic ics which can be programmed by the user to perform a wide variety of logic functions. Two introductory articles (PE OCT-NOV 87) described their use for *combinational logic* such as memory decoders and random logic, and for *sequential logic* such as counters and sequencers. These devices promise huge advantages in minimising logic circuit design time, reducing chip count for a given application and simplifying printed circuit boards.

Commercial programming equipment can cost between £1200 and £2000, far out of reach of the average hobbyist. Here we describe the construction and operation of a simple pld programmer which is only a fraction of the commercial cost but with the limitation of using only one type of pld, the Philips PLS103. However, this is one of the simplest programmable devices to understand and to program, with many useful applications.

THE PLS103

The PLS103 (Fig. 1) is a 28-pin field programmable gate array (fpga) with sixteen inputs I0 to I15 which, through true/complement buffers, present logic signals to a fuse array with nine AND gates.

The polarities of the nine outputs, F0 to F8, can be high or low and are individually determined by fuses X0 to X8 at the inputs of exclusive-or gates. When any Xn fuse is blown, the appropriate exclusive-or gate inverts the logic state from the true/complement array. The output buffer at ouput Fn inverts again so the overall function



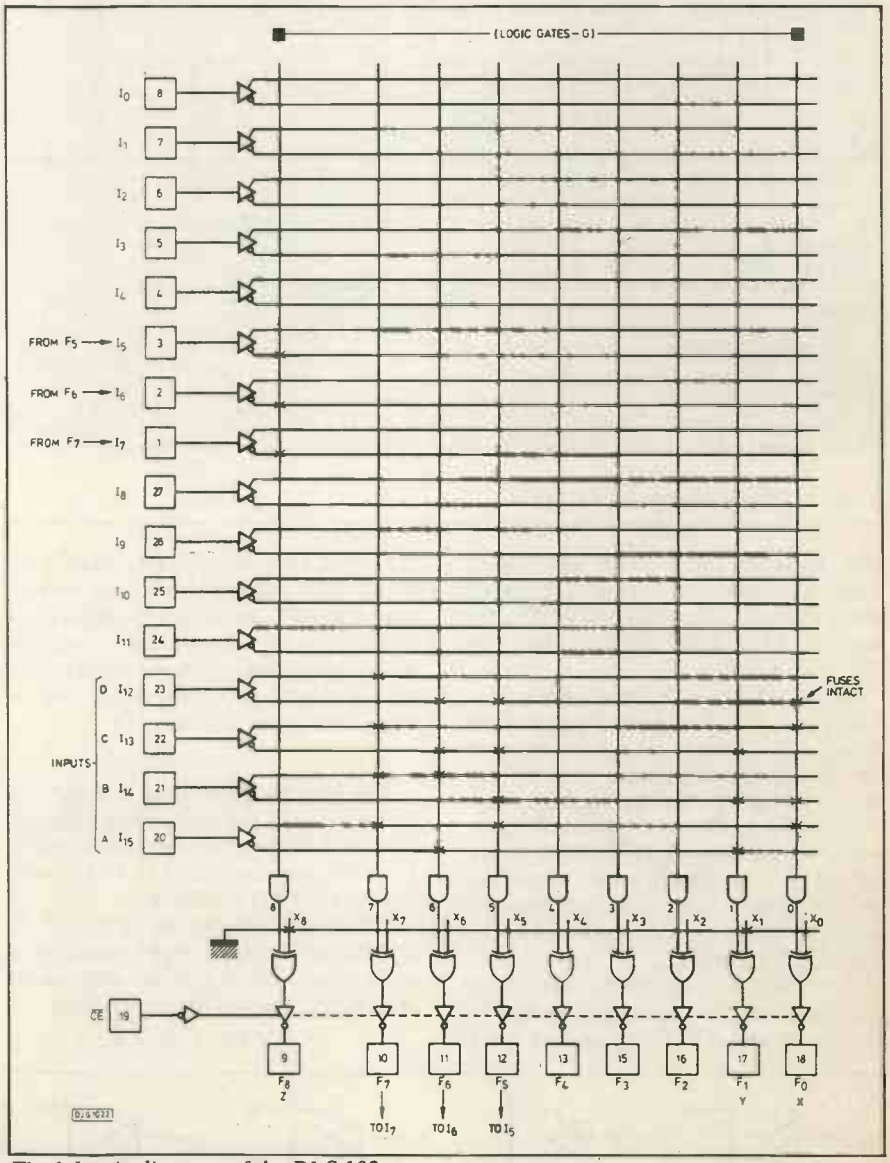


Fig.1 *Logic diagram of the PLS 103.*

between input and output appears as AND. This is referred to as a 'high output polarity', with code 'H' used on a program table as shown in Table 1.

With the appropriate X fuse intact the exclusive-or does not invert, yet the following output buffer does invert. Therefore, an AND function in the array can appear as a NAND function at the output. This is referred to as a 'low output polarity' with code 'L'.

Each input, let's call it In where n is a number between 1 and 15, is programmed individually for an output Fp, where p is between 1 and 8. Fig. 2 shows the four possible combinations of

$F_0(18)$	X	=	$A.\bar{B}.C.\bar{D}$	
$F_1(17)$	Y	=	$\bar{A}.\bar{B}.\bar{C}.$	= A+B+C
$F_2(16)$		=		
$F_3(15)$		=		
$F_4(13)$		=		
$F_5(12)$	F_5	=	$A.\bar{B}.\bar{C}.\bar{D}.$	
$F_6(11)$	F_6	=	$\bar{A}.B.\bar{C}.\bar{D}.$	
$F_7(10)$	F_7	=	A.B.C.D.	
$F_8(9)$	Z	=	$F_5+F_6+F_7$	= $F_5.F_6.F_7.$

GATE		INPUT															
POLARITY		I_{15}	I_{14}	I_{13}	I_{12}	I_{11}	I_{10}	I_9	I_8	I_7	I_6	I_5	I_4	I_3	I_2	I_1	I_0
F_0	H	H	L	H	L	–	–	–	–	–	–	–	–	–	–	–	–
F_1	L	L	L	L	–	–	–	–	–	–	–	–	–	–	–	–	–
F_2	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O
F_3	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O
F_4	O′	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O	O
F_5	H	H	L	L	L	–	–	–	–	–	–	–	–	–	–	–	–
F_6	H	L	H	L	L	–	–	–	–	–	–	–	–	–	–	–	–
F_7	H	H	H	H	H	–	–	–	–	–	–	–	–	–	–	–	–
F_8	L	–	–	–	–	–	–	–	–	L	L	L	–	–	–	–	–
PIN NO.		20	21	22	23	24	25	26	27	1	2	3	4	5	6	7	8
VARIABLE NAME		A	B	C	D					F_7	F_6	F_5					

NOTES:
1. The FPGA is shipped with all links intact. Thus a background of entries corresponding to states of virgin links exists in the table, shown BLANK for clarity.
2. Unused inputs are normally programmed Don't Care (—).
3. Unused Gates can be left blank.

PROGRAM TABLE ENTRIES	AND		CONTROL	
	INACTIVE	O	HIGH	H
	I	H	LOW	L
	I	L	(POL.)	
	Don't Care	–		
	(I)			

Table 1: Program table for PLS103

fuse patterns for a given gate input. Unprogrammed plds have all fuses intact and the inputs of any unused AND gate are left in this state, identified by code 'O'.

Suppose the simple Boolean relationship $X=A.\bar{B}.C.\bar{D}$ is to be programmed where A,B,C and D are applied to input I15 to I12 and X is taken from F0 (as shown in Fig. 2). A and C are high input variables and $\bar{B}$ and $\bar{D}$ are low input variables. A high input variable (code 'H' in Fig. 2) at input In requires one fuse In intact and the other fuse In blown. An active low input variable (code 'L') has In intact with In blown.

$X=A.\bar{B}.C.\bar{D}$ uses only four input variables so the remaining twelve In inputs for that AND gate are not used. However, they might carry signals for other AND gates which would corrupt our wanted relationship if the other fuses remained intact. The unused inputs of an AND gate must become 'don't care' inputs by blowing both In and In fuses. A 'don't care' input has the code '–'.

The fpga program table in Table 1, is partly filled in for this simple example. Note that all the inputs are still available for remaining outputs F1 to F8 for other AND or NAND equations.

OR functions can be achieved by DeMorgan's Theorem which states that if each input to a NAND is individually inverted the overall function is OR:

$$Y = A + B + C = \overline{\bar{A}.\bar{B}.\bar{C}}$$

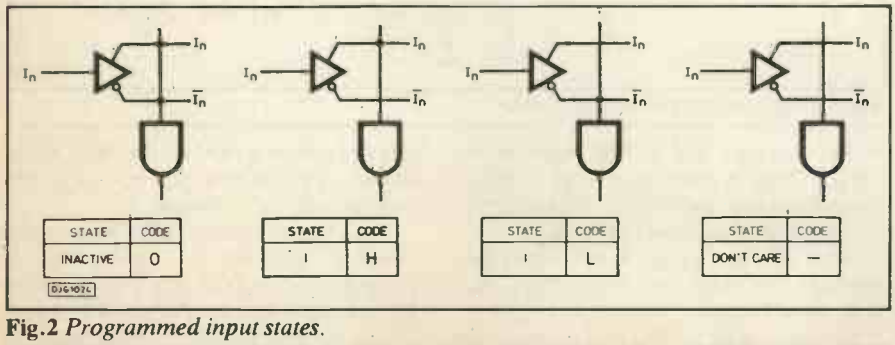


Fig.2 *Programmed input states.*

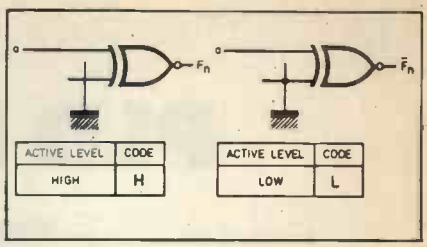


Fig.3 *Programming output polarity.*

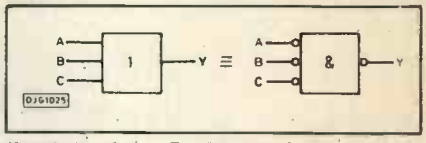


Fig.4 *Applying De Morgan's theorem to make a nand gate function as a nor gate.*

This is shown in Fig. 4. Implementing this on the PLS103 is simply a matter of leaving the complement fuses intact with the true fuses blown for input variables A,B and C and leaving fuse X1 of F1 intact. This is shown on Table 1 for output F1 which is programmed with an output polarity 'L', and inputs I15, I14 and I15 are programmed 'L'.

Sum of product terms such as

$$Z = \bar{A}.B.\bar{C}.\bar{D} + A.\bar{B}.\bar{C}.\bar{D} + A.B.C.D$$

can be implemented by feeding three separate AND outputs back to unused inputs and ORing them using DeMorgan's Theorem. This again is shown in Fig. 1 and Table 1. The term $A.\bar{B}.\bar{C}.\bar{D}$ is programmed to output F5, $\bar{A}.B.\bar{C}.\bar{D}$ to F6 and A.B.C.D to F7. These outputs are then externally connected to inputs I5, I6 and I7 respectively and complemented by blowing each In fuse. Output F8 is programmed 'L' to complete the equation

$$Z = \overline{\bar{F5}.\bar{F6}.\bar{F7}}$$

Note that with these examples three different logic functions are shown programmed on the same chip, leaving three outputs and nine inputs 'inactive' and therefore shown as 'O' in Table 1.

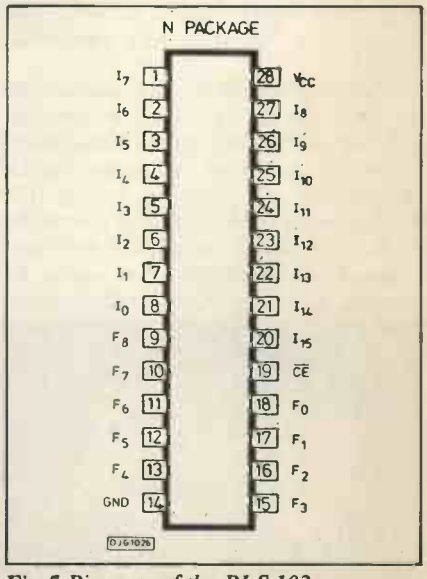
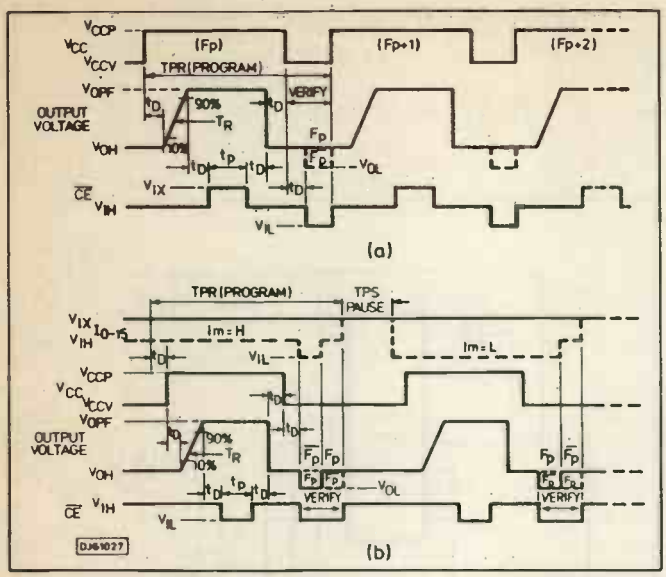


Fig.5 *Pin-outs of the PLS 103.*



V_{CCV}	5.00 ± 0.25 V	Verify supply voltage. Adjust VR2 and measure 5 V STAB output
V_{CCP}	8.75 ± 0.25 V	Programming supply voltage. Adjust VR3 and measure 8.75 V output.
V_{OPF}	17.00 ± 1.00 V	Forced programming output voltage. Adjust VR4 and measure 17 V output.
V_{IX}	10.00 ± 0.5 V	$\overline{CE}$ program enable voltage. Adjust VR5 and measure 10 V output.
V_{OL}	< 0.45 V	Output low voltage.
V_{OH}	> 2.4 V	Output high voltage.
V_{IL}	0 to 0.8 V	Input low voltage.
V_{IH}	2.4 to 5.5 V	Input high voltage.
t_p	0.4 + 0.1 ms	Programming pulse width.

Table 2: Voltage and timing values.

Fig.6 *(Left) Programming waveforms.*

Even after programming a chip partially, the inactive or unused portions can be programmed at a later time for other functions.

PROGRAM AND VERIFY WAVEFORMS

The PLS103 is programmed in two stages, with reference to the pin configuration of Fig. 5. First the output polarity program-verify sequence is shown in Fig. 6a. The second stage is the input program-verify sequence as shown in Fig. 6b. The voltage and timing values are given in Table 2.

To program an output for a high polarity requires the appropriate X fuse to be blown by the following sequence. All inputs, In, are disabled by raising the applied voltages to V_{IX} which is nominally 10V. $\overline{CE}$ (pin 19) is raised to V_{IH}=5V, V_{CC} (pin 28) is raised from V_{CCV}=5V to programming voltage V_{CPP}=8.75V, followed by the selected output pin being applied with a rising voltage from V_{OH}=5V to V_{OPF}=17V. $\overline{CE}$ then receives a program pulse of V_{IX}=10V for a duration of between 0.3 and 0.5 milliseconds.

The verify voltages are V_{CCV}=5V, V_{OH}=5V and $\overline{CE}$ goes to V_{IL}=0V.

The input program sequence for blowing either In or $\bar{I}n$ fuses is very similar to that for output polarity, differing only in that the selected input is switched to V_{IH}=5V for blowing fuse In, or V_{IL}=0V for blowing fuse $\bar{I}n$ and the

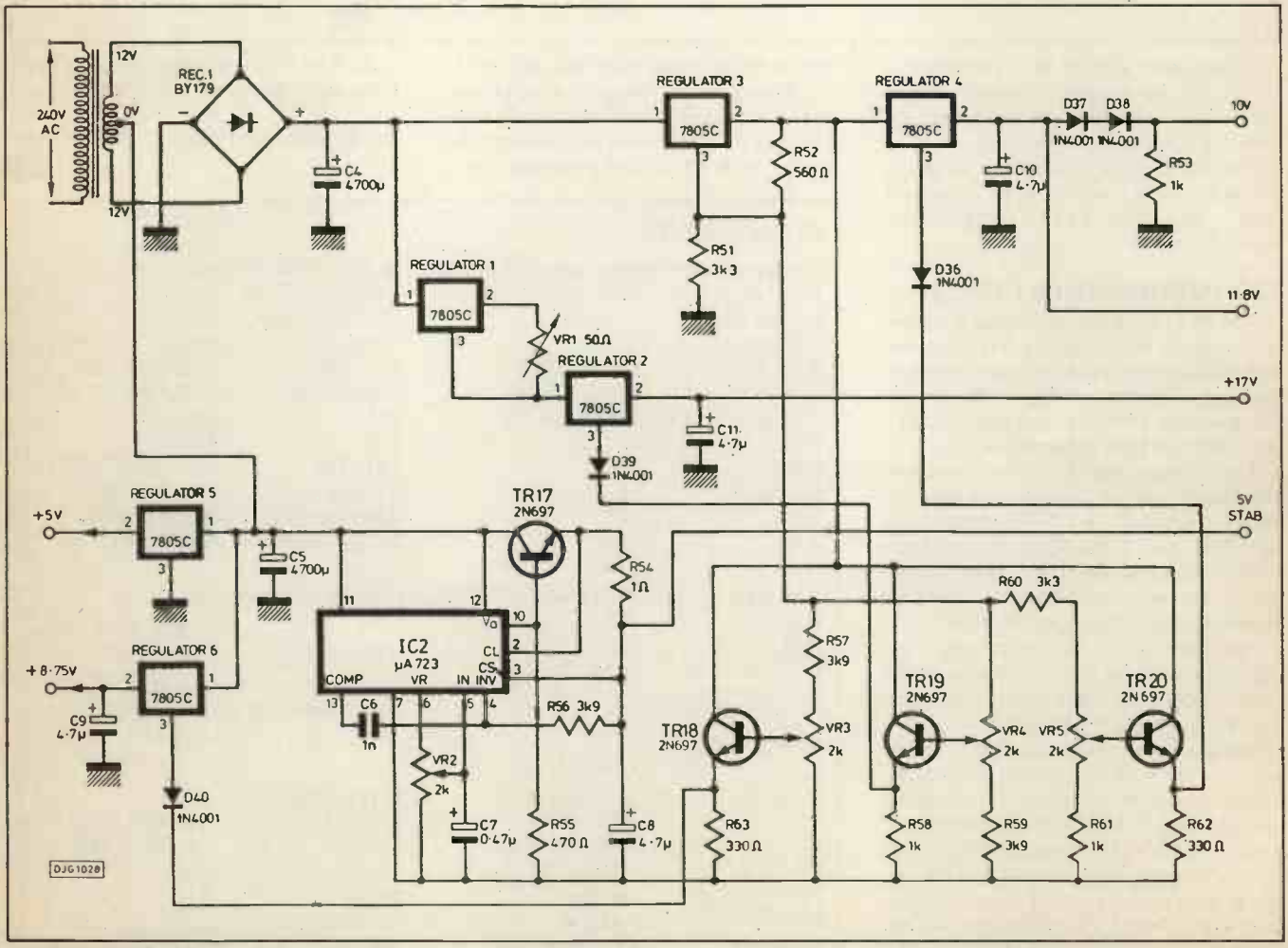


Fig.7 *Voltage regulation circuit.*

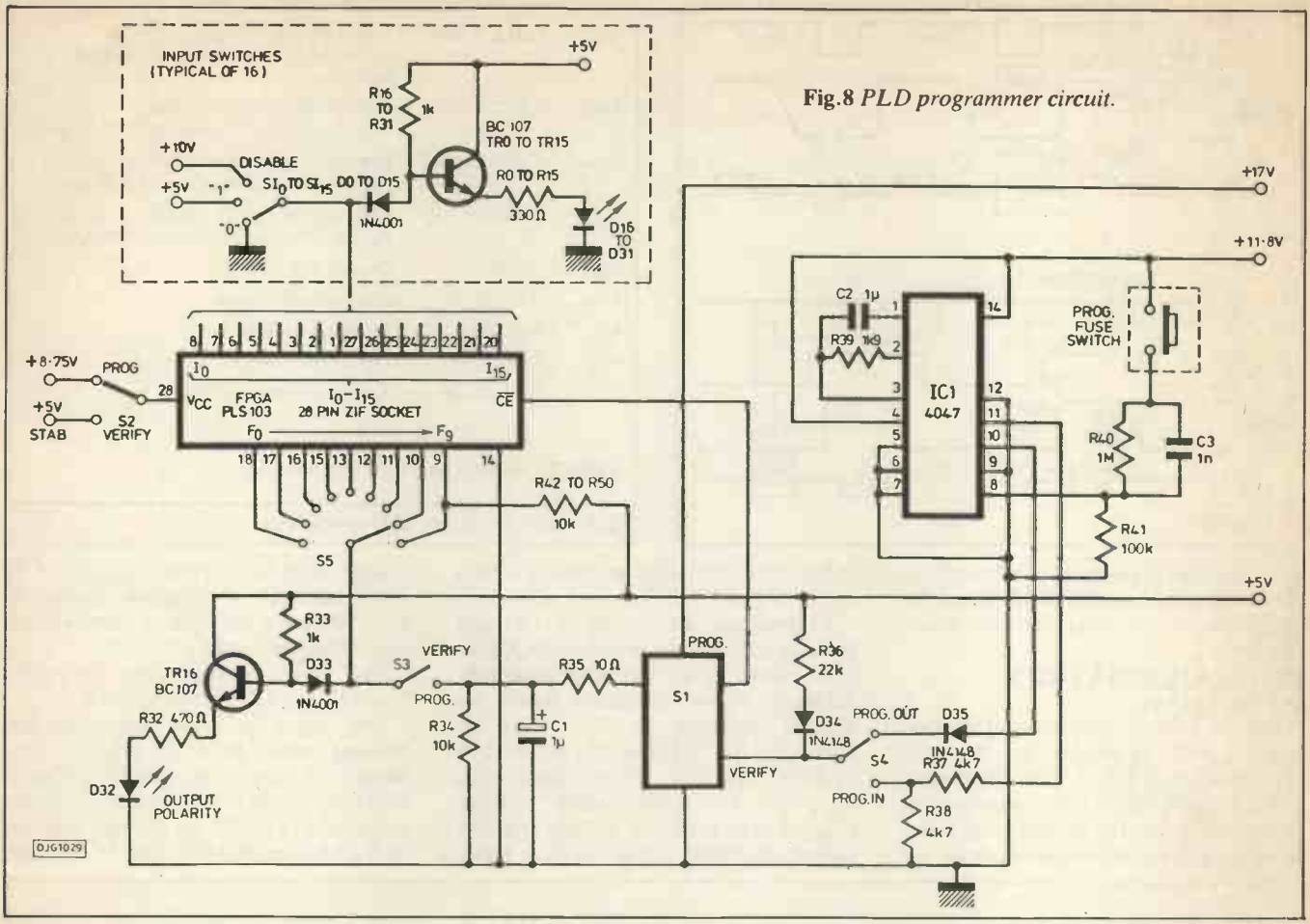


Fig.8 *PLD programmer circuit.*

program pulse applied to CE is from V_{IH} to V_{IL} for the required duration. Don't care states are then programmed by blowing both input fuses.

The input verify sequence requires reference to a truth table as described later in the PLD Programmer instructions.

THE PROGRAMMER CIRCUIT

The PLD Programmer circuit is shown in two parts for simplicity. Fig. 7 shows the voltage regulation and current limiting circuitry and Fig. 8 shows the programmer switches, led displays and the 'program fuse' pulse circuitry.

The voltage supply components form the biggest part of this circuit. Using six 7805C regulators, a µA723 operational amplifier and a number of transistor buffers, the required programming and verify voltages are realised. Table 2 shows all the voltage specifications.

Switches S1 to S4 are arranged to apply the voltages to the fpga pins in the correct sequence when the switches are operated in numerical order.

Switch S1 prepares $\overline{CE}$ (pin 19) for program or verify potentials. Switch S2 applies program voltage V_{CCP} or verify voltage V_{CCV} to V_{CC} (pin 28). Switch S3 applies +17V (when programming) to a selected output via rotary switch RS, or is left open circuit for the selected output potential to be verified. Switch S4 selects the 10V pulse path (applied through S1 to $\overline{CE}$) for programming outputs, or selects the 0V pulse path (also applied to $\overline{CE}$ through S1) for programming inputs.

The inputs I0 to I15 have individual switches to apply +10V for disabling the input, open circuit for programming a logic 1, or 0V for programming a logic 0. Each input has a transistor/led display.

TO BE CONTINUED NEXT MONTH

PE

COMPONENTS

Resistors 0.25W carbon except R0 to R15

R0 to R15	330 x 8 dil (2 OFF)
R16 to R31, R33, R53, R58, R61	1k (36 OFF)
R32, R55	470 (2 OFF)
R34, R42 to R50	10k (10 OFF)
R35	10
R36	22k
R37, R38	4k7 (2 OFF)
R39	1k5
R40	1M
R41	100k
R51, R60	3k3 (2 OFF)
R52	560
R54	1ohm
R56, R57, R59	3k9 (3 OFF)
R62, R63	330 (2 OFF)

CAPACITORS

C1	1µF 63V elect
C2	1µF 100V elect
C3	1nF
C4	4700µF 63V elect
C5	4700µF 25V elect
C6	1nF 100V
C7	0.047µF
C8 to C11	4.7µF 63V elect

SEMICONDUCTORS

D0 to D15, D33, D34, D37, D38, D39, D40	1N4001 (22 OFF)
D34, D35	1N4148 (2 OFF)
D16 to D32	CQX 51-5 (17 OFF)
IC1	4047
IC2	µA723
T0 to T16	BC107 (17 OFF)
T17 to T21	2N697 (5 OFF)
REG1 to REG6	7805 0.5 amp
Bridge rectifier	BY179

POTENTIOMETERS

VR1	50ohms multi-turn
VR2 to VR5	2k multi-turn

TRANSFORMER 12V + 12V 30VA toroidal

SWITCHES

SI0 to SI15	3 position break before make
S1	dpdt
S2, S4	spdt
S3	spst
RS	1-pole 9-way rotary

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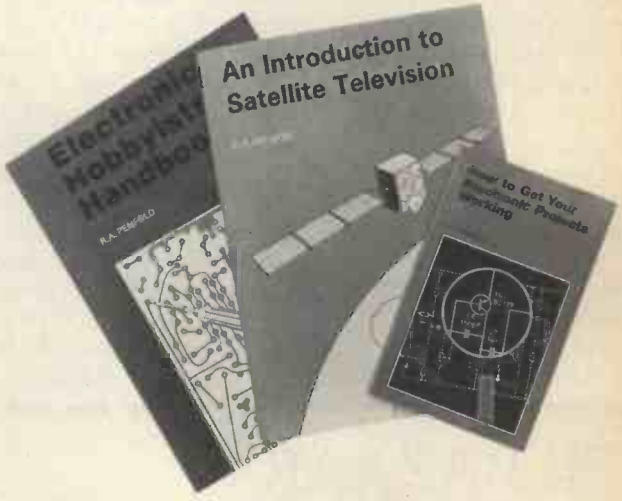

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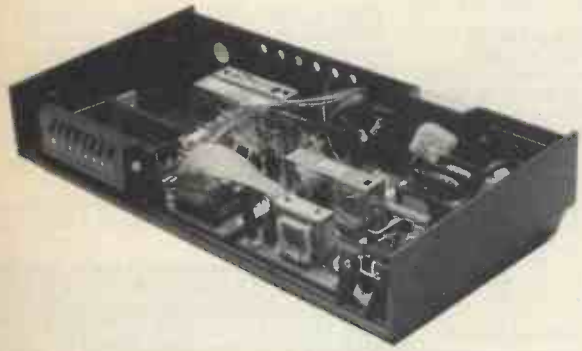

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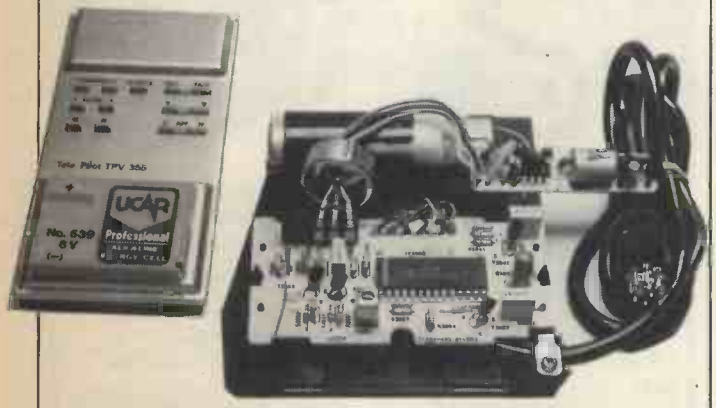

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THE LISTENING MACHINE

BY TOM IVALL

SPEAK SLOWER, MASTER

Speech recognition machines are not yet developed enough to write a letter, but they do fine for checking pigs.

Consider the two phrases: "A tax on shipping" and "Attacks on shipping". They mean different things, but when spoken out loud at normal conversational speed they sound the same.

This is one of the characteristic problems facing researchers in electronic speech recognition. How can an automatic listening machine distinguish between such ambiguous streams of sound? The answer certainly doesn't lie in electronic processing of the initial audio signals. Somehow the machine must be given more than just the power to recognise sounds. It must be able to *understand* speech, at least to a limited extent.

Brian Oakley, the retiring director of the UK's Alvey Programme on information technonlogy, drew attention to this problem in a valedictory survey of what Alvey had achieved. He said he would reduce r&d effort on signal processing but increase it on natural language processing.

The Alvey scheme ended its five-year run this year and is now being replaced by another national, collaborative r&d programme in information technology. Continuing Alvey projects, plus ideas for new work recently invited by the Government, are to be supervised by a new body called the Information Engineering Directorate. This is controlled and funded by the Department of Trade & Industry and the Science & Engineering Research Council. The IED national programme will concentrate on three main areas of work – vlsi, systems architecture and systems engineering. Speech recognition r&d will come into the system architecture area.

But the fact that speech recognition is still being researched doesn't mean that it hasn't already produced some practical results. By working within its present limitations, such as the one mentioned above, we can still make use of it in a modest kind of way.

A number of banks, for example, are trying out speech recognition as a possible way of handling simple, routine telephone enquiries from customers about their accounts. In the UK the National Westminster Bank and the Royal Bank of Scotland are among the pioneers. In Japan some banks have been providing this kind of service for five years or more.

In industry the technique is beginning to be used in certain situations where workers have to continuously provide information on the tasks they're performing but can't punch keyboards etc, as their hands are fully occupied. Here the procedure is called voice data entry. In general it has applications where entering data by manual methods would be difficult or inconvenient or would slow the operative's work to an unacceptable level of productivity.

Another situation is where the worker has to move about a lot in the course of the job. Using a headset type of microphone – connected by a long flexible cable or portable radio link to the speech recognizer – avoids having to keep going back to a computer terminal or other hand-operated data input device.

So far the industrial tasks catered for seem to be mainly in quality control, inspection, sorting, stock control and the checking and logging of incoming goods. In the USA, for example, the glass manufacturer Owens Illinois uses voice data entry in quality control of glass faceplates for television tubes. General Electric has found it useful for the recording of incoming replacement components at their New Concord distribution centre for appliance parts.

In Japan a meat auctioning firm called Gunma Meat Wholesale Market has installed a system to allow operatives to handle, examine and simultaneously describe hanging carcases of pigs and cattle. The Japan Publication States Company, a books distribution agent, uses the techinque for sorting about 180,000 books a day according to destination. All these users justify the cost of the electronic equipment by the savings in time, labour and paperwork relative to previous methods of working.

How has this sort of progress been achieved? Mainly by scaling down the performance that you are asking the speech recognition system to deliver. By deliberately applying operational constraints, the development engineer can make the technology simpler, more reliable in operation and cheaper to manufacture.

For example, if you ask the user to introduce short pauses between the words spoken in a phrase – say about a quarter of a second – it relieves the listening machine from having to pick out these individual words from the sound stream. So speech recognizers working on isolated words become simpler and cheaper than machines for continuous speech. Natural speech, of course, tends to run all the words together into an unbroken stream. But when only a few words have to be spoken at a time, as in the voice data entry systems mentioned above, it's not putting too much of a strain on the factory worker to introduce the short pauses.

Practical speech recognisers are anything from single printed circuit boards, which can be plugged into personal computers or other electronic equipment, to desk-top or floor-standing units housed in their own cabinets. Most of them work on the automatic recognition of whole words or short groups of words as units of meaning – as distinct from, say, phonemes.

Their basic principle of operation is to compare an input acoustic pattern – the spoken utterance – against an already stored set of reference acoustic patterns. This means, of course, that at some point the device has to be given the necessary set of reference patterns to form its 'vocabulary'. If the manufacturer does this, the machine is called a speaker-independent recognizer. But if the user does it, in a setting-up process called training, the device is classed as speaker-dependent.

Voice data entry doesn't usually need a very large vocabulary. Up to about 100 words is probably enough for most industrial applications. This makes voice data entry a much simpler technique than the so-called speech-to-text systems for office automation, which may well need vocabularies of 5000 words or more. So, by being allowed to limit the size of the vocabulary, the manufacturer can simplify and reduce the cost of the equipment. PE

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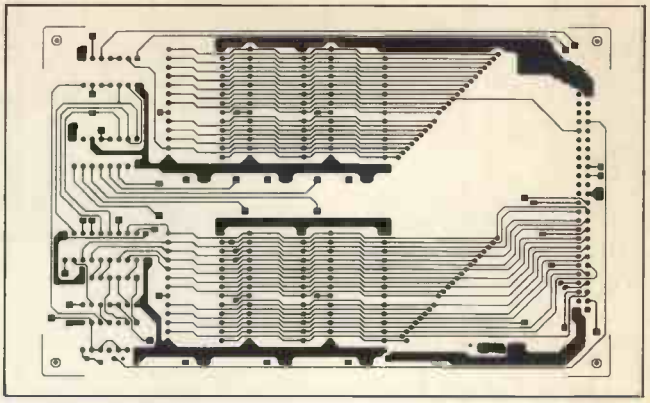

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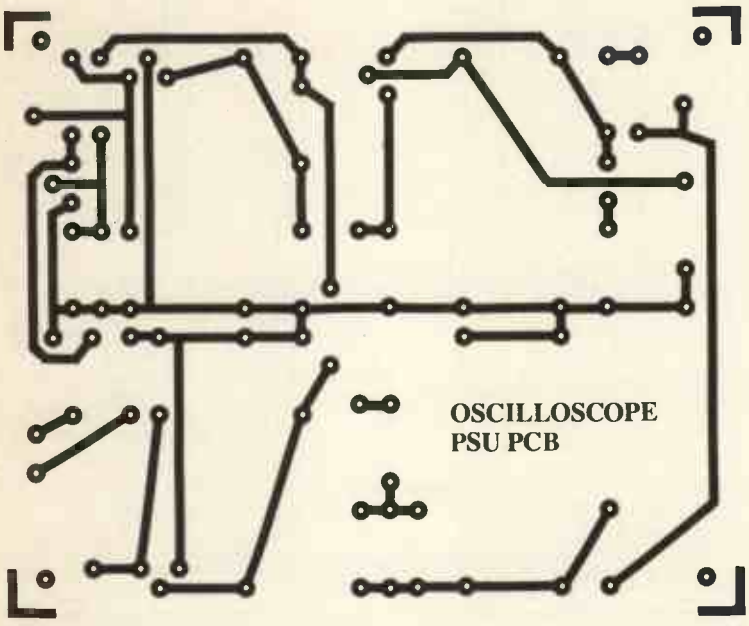

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NEXT PRINT ONTO PCB

Place positive transparency onto photosensitised copper clad fibre glass, cover with glass to ensure full contact. Expose to Ultraviolet light for several minutes (experiment to find correct time – depends on UV intensity).

Develop PCB in Sodium Hydroxide (available from chemists) until clean track image is seen, wash in warm running water. Etch in hot Ferric Chloride, frequently withdrawing PCB to allow exposure to air. Wash PCB in running water, dry, and drill holes, normally using a 1mm drill bit.

(PCB materials and chemicals are available from several sources – study advertisements.)

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4086B	0.32
4089B	0.80
4093B	0.18
4094B	0.46
4095B	0.58
4096B	0.88
4097B	1.25
4098B	0.50
4099B	0.45
4500B	8.10
4501UB	0.30
4502B	0.38
4503B	0.32
4504B	0.98
4505B	1.98
4506	0.60
4506UB	0.88
4507B	0.33
4508B	0.70
4510B	0.36
4511B	0.39
4512B	0.40
4513B	1.10
4514B	0.72
4515B	0.72
4516B	0.38
4517B	1.15
4518B	0.38
4519B	0.30
4520B	0.38
4521B	0.90
4522B	0.48
4526B	0.38
4527B	0.44
4528B	0.38
4529B	0.70
4530B	0.90
4531B	0.60
4532B	0.52
4534B	2.63
4536B	1.38
4538B	0.59
4539B	0.65
4541B	0.65
4543B	0.84
4544B	1.50
4547B	1.41
4549B	3.88
4551B	0.80
4553B	1.60
4554B	1.94
4555B	0.35
4556B	0.38
4557B	1.45
4558B	0.99
4559B	3.40
4560B	1.10
4561B	1.02
4562B	3.40
4566B	1.55
4568B	2.50
4569B	1.72
4572B	0.32
4580B	1.82
4581B	1.18
4582B	0.68
4583B	0.60
4584B	0.30
4585B	0.42
4597B	2.82
4598B	3.10
4599B	1.70
40097	0.45
40098	0.42
40100	1.72
40101	0.84
40102	1.10
40103	1.10
40104	1.10
40105	1.44
40106	0.38
40107	0.52
40108	2.90
40109	0.85
40110	1.95
40114	1.95
40116	8.10
40117	2.25
40147	2.25
40160	1.10
40161	1.10
40162	1.10
40163	0.75
40174	0.48
40175	0.54
40181	2.10
40182	0.85
40192	0.58
40193	0.56
40194	0.66
40195	0.80
40208	2.95
40240	1.36
40244	1.36
40245	1.36
40257	1.35
40373	1.10
40374	1.10
43403	3.58
45000	15.14
45026	3.09
45027	4.10
45028	4.10
45029	4.10
45040	7.44
45041	7.88
45106	5.40
45109	3.08
45138	1.48
45145	9.25
45146	10.44
45151	15.00
45152	16.50
45156	13.50
45157	11.06
45158	11.94
45159	23.00
45406	3.32
45411	6.44
45414	10.50
45428	9.25
45429	6.94
45433	18.46
45440	12.75
45441	11.31
45453	6.94

74 SERIES

7400	0.22
7401	0.32
7402	0.20
7403	0.16
7404	0.25
7405	0.24
7406	0.38
7407	0.38
7408	0.15
7410	0.15
7413	0.35
7414	0.30
7416	0.35
7417	0.35
7420	0.25
7425	0.18
7427	0.20
7430	0.24
7432	0.29
7437	0.18
7438	0.39
7442	0.40
7446	0.60
7451	0.20
7470	0.30
7472	0.20
7473	0.25
7474	0.36
7475	0.30
7476	0.25
7483	0.60
7485	0.60
7486	0.35
7490	0.50
7492	0.45
7493A	0.55
74104	0.44
74107	0.30
74121	0.45
74123	0.60
74125	0.48
74126	0.35
74128	0.52
74160	0.55
74161	0.60
74163	0.84
74164	0.98
74174	0.65
74175	0.55
74245	1.47
74257	0.76
74265	0.80
74283	0.70
74298	1.24
74365	0.44
74367A	0.80

74LS SERIES

74LS00	0.13
74LS01	0.14
74LS02	0.15
74LS03	0.15
74LS04	0.15
74LS05	0.15
74LS06	0.42
74LS07	0.42
74LS08	0.15
74LS09	0.17
74LS10	0.15
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74LS12	0.15
74LS13	0.24
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74LS33	0.16
74LS37	0.18
74LS38	0.17
74LS40	0.16
74LS42	0.32
74LS47	0.56
74LS48	0.56
74LS49	0.64
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74LS55	0.17
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74LS77	0.75
74LS78	0.28
74LS83	0.42
74LS85	0.44
74LS86	0.28
74LS90	0.32
74LS91	0.75
74LS92	0.36
74LS93	0.30
74LS95	0.46
74LS96	0.60
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74LS109	0.32
74LS112	0.32
74LS113	0.32
74LS114	0.32
74LS122	0.40
74LS123	0.42
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74LS126	0.35
74LS132	0.35
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74LS136	0.35
74LS137	0.76
74LS138	0.35
74LS139	0.35
74LS145	0.76
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74LS154	0.88
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74LS162	0.46
74LS163	0.46
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74LS166	0.68
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74LS170	0.78
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74LS183	1.60
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74LS195	0.52
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74LS197	0.52
74LS221	0.56
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74LS245	0.50
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74LS249	0.82
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74LS273	0.52
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74LS293	0.32
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74LS298	0.65
74LS299	1.40
74LS321	5.64
74LS322	1.80
74LS323	1.80
74LS347	1.20
74LS348	1.28
74LS352	0.90
74LS353	0.90
74LS363	1.40
74LS364	1.40
74LS365	0.37
74LS366	0.37
74LS367	0.35
74LS368	0.37
74LS373	0.55
74LS374	0.55
74LS375	0.55
74LS377	0.82
74LS378	0.72
74LS379	1.10
74LS380	5.63
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74LS386	0.48
74LS390	0.50
74LS393	0.45
74LS395	0.78
74LS396	1.70
74LS398	1.24
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74LS549	4.20
74LS566	4.65
74LS567	4.65
74LS568	1.95
4LS569	1.57
74LS593	8.48
74LS620	1.55
74LS621	1.55
74LS622	1.55
74LS623	1.55
74LS624	1.55
74LS625	2.30
74LS626	2.20
74LS627	2.35
74LS628	2.20
74LS629	1.10
74LS640	0.98
74LS641	0.90
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74LS644	1.10
74LS645	1.10
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74LS649	5.60
74LS651	6.32
74LS652	5.60
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74LS654	5.60
74LS668	0.85
74LS669	0.78
74LS670	0.72
74LS673	3.40
74LS674	3.40
74LS682	2.40
74LS683	2.40
74LS684	2.40
74LS685	2.40
74LS686	3.30
74LS687	2.70
74LS688	1.85
74LS793	3.28
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